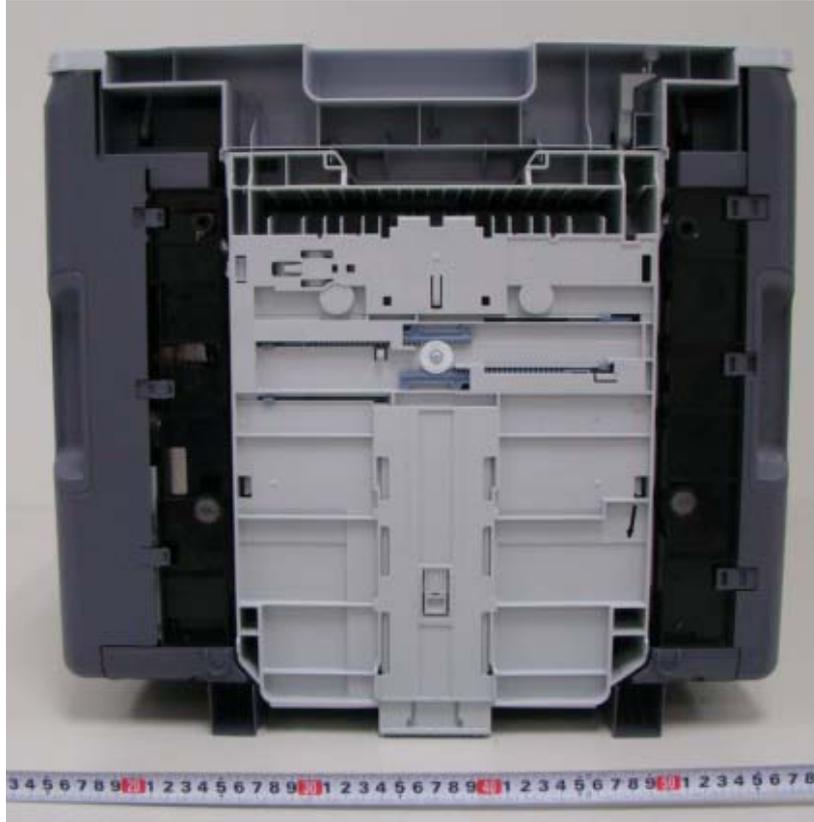
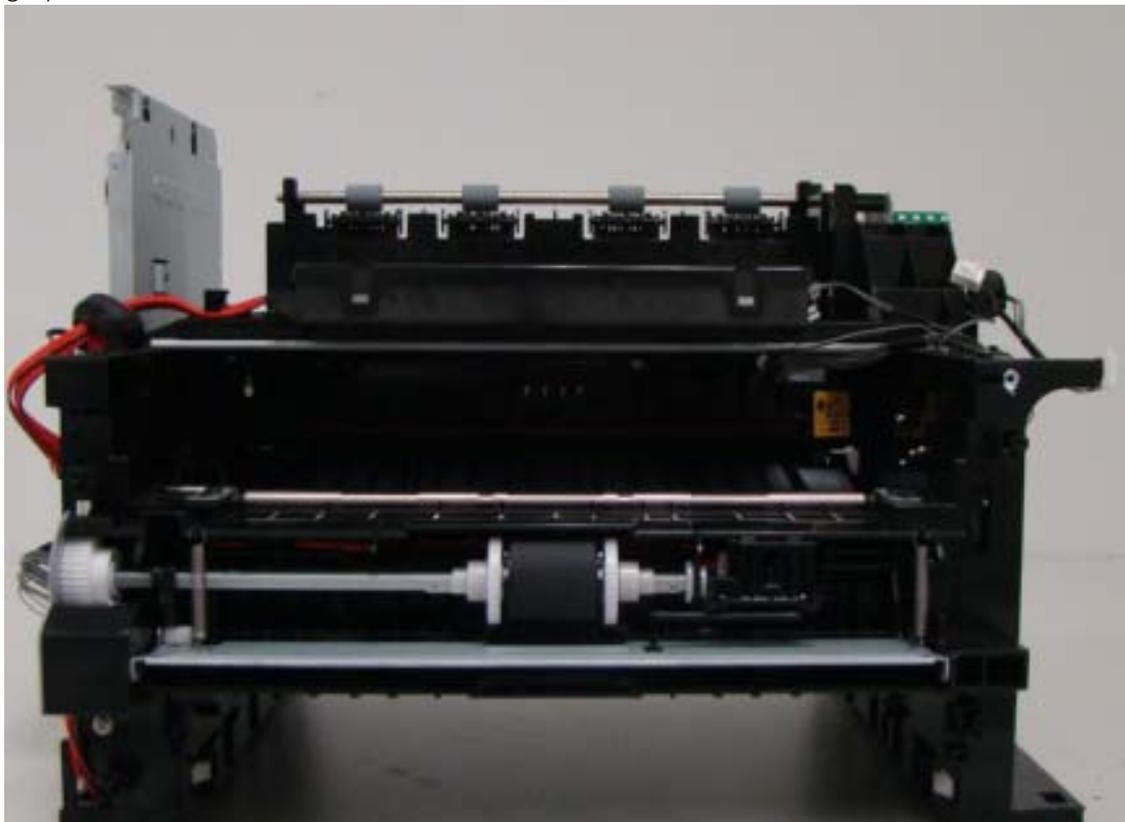


Remark #2: Circuit diagram

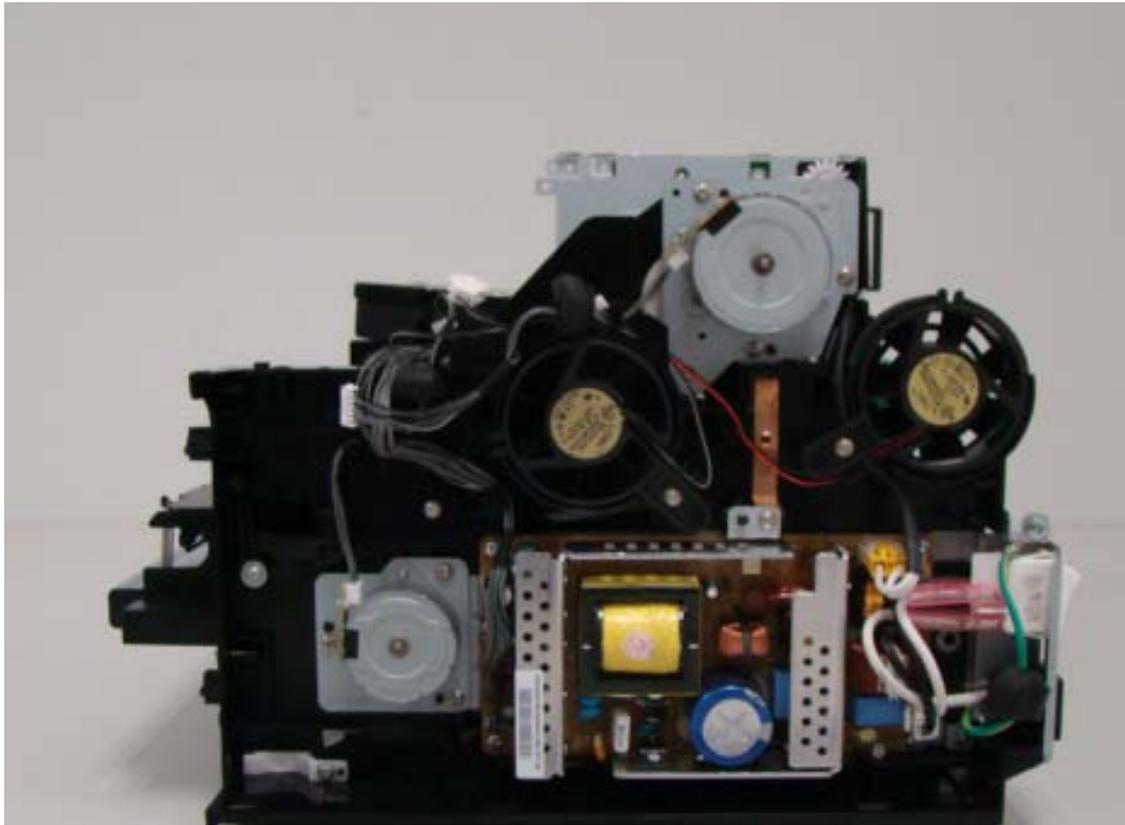
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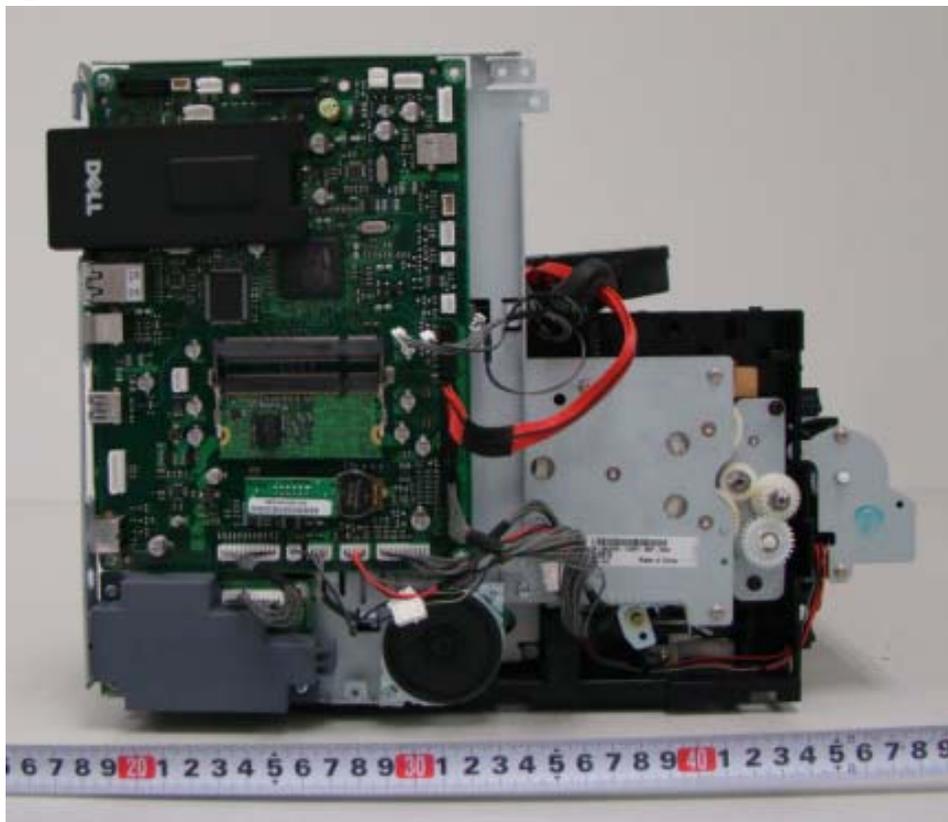
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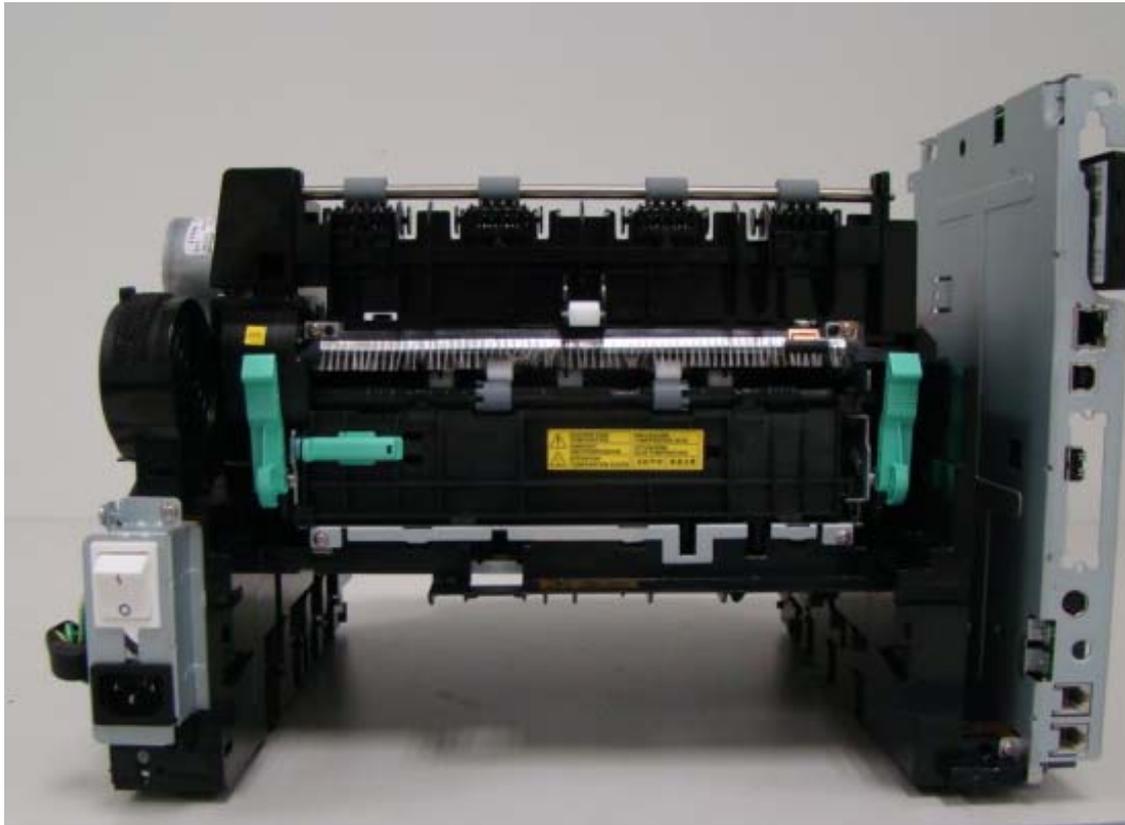
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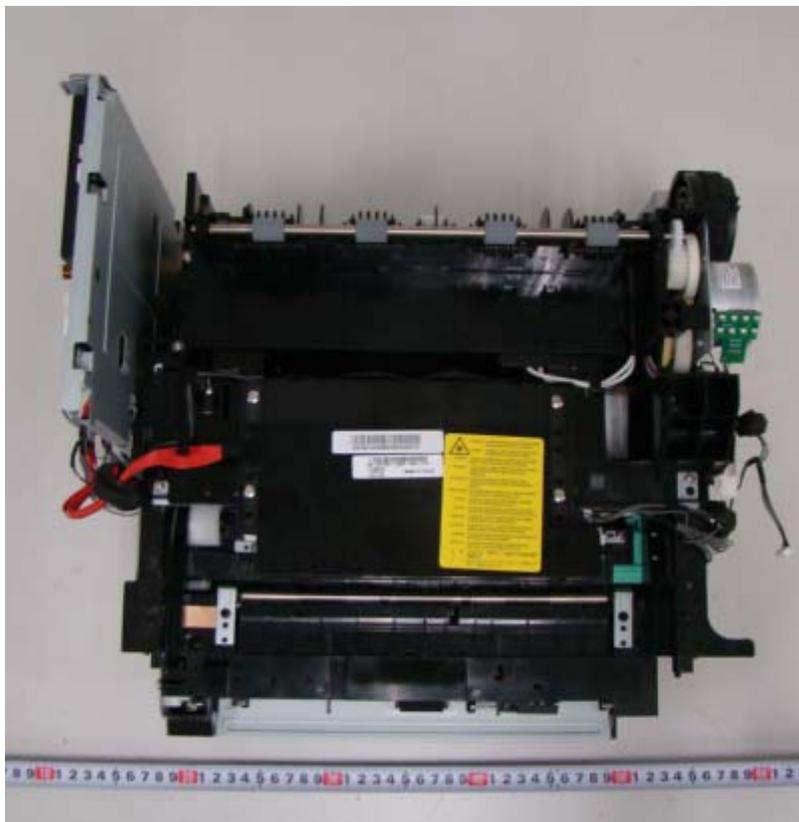
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Remark #2: Circuit diagram  
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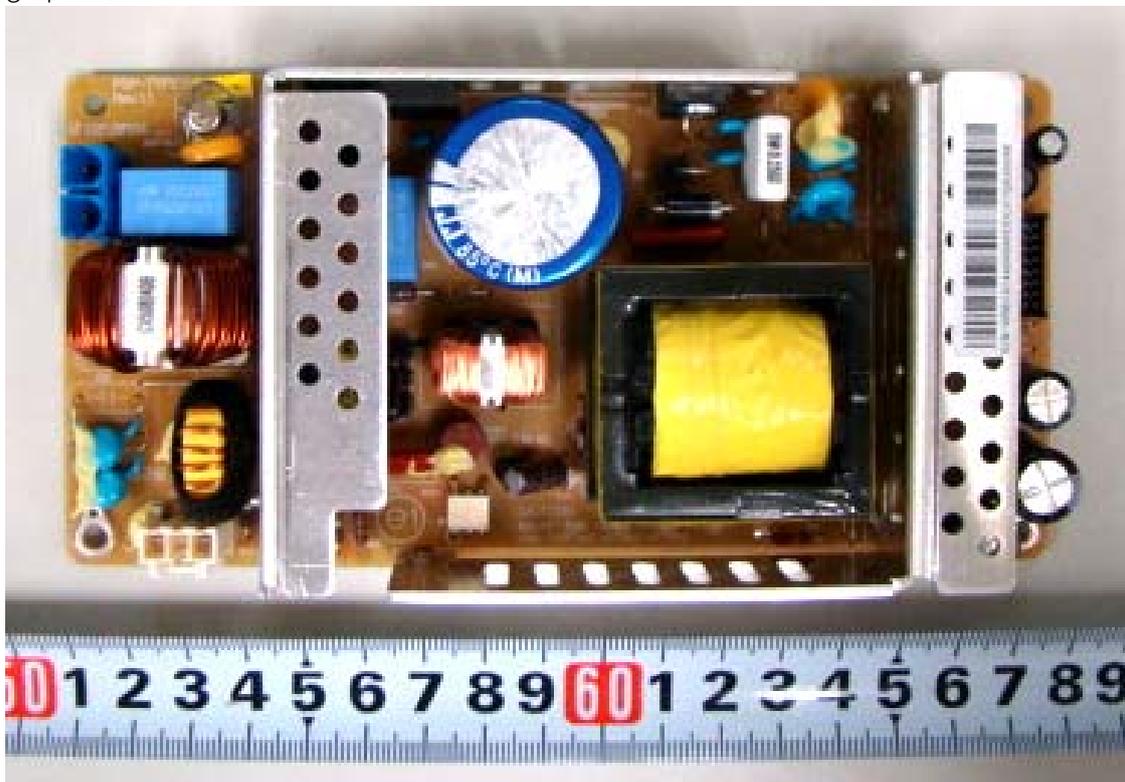
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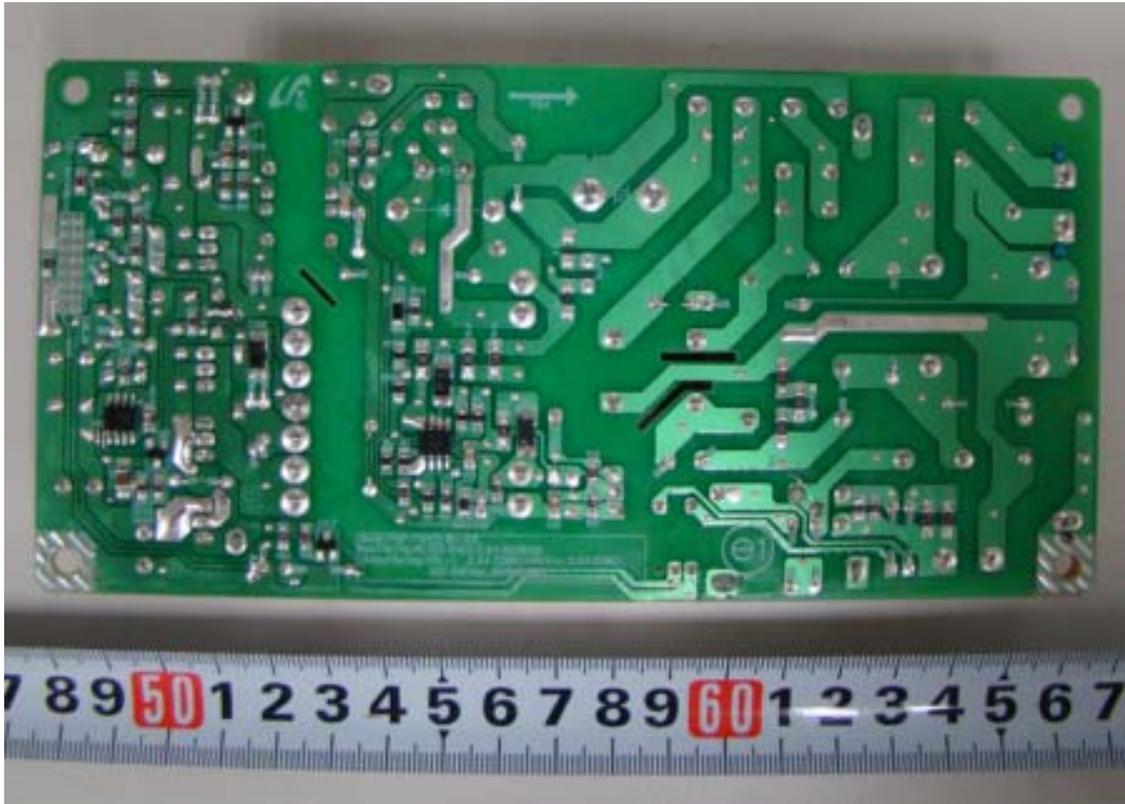
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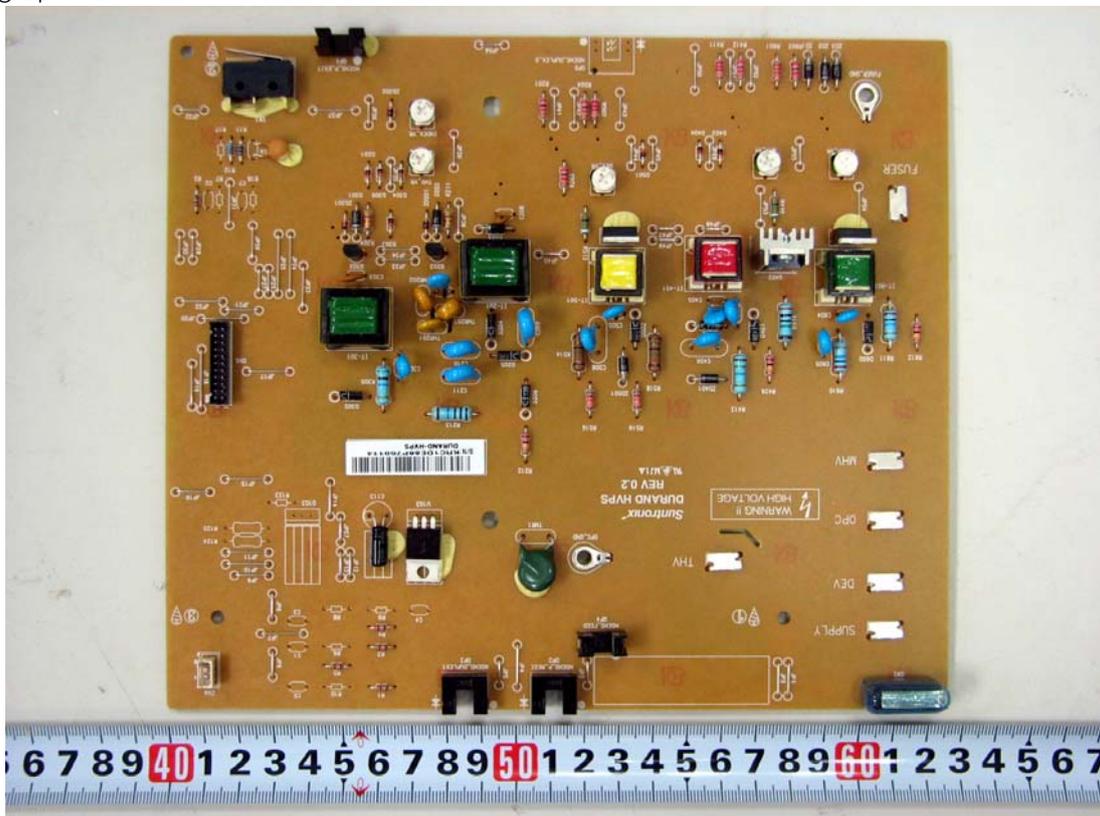
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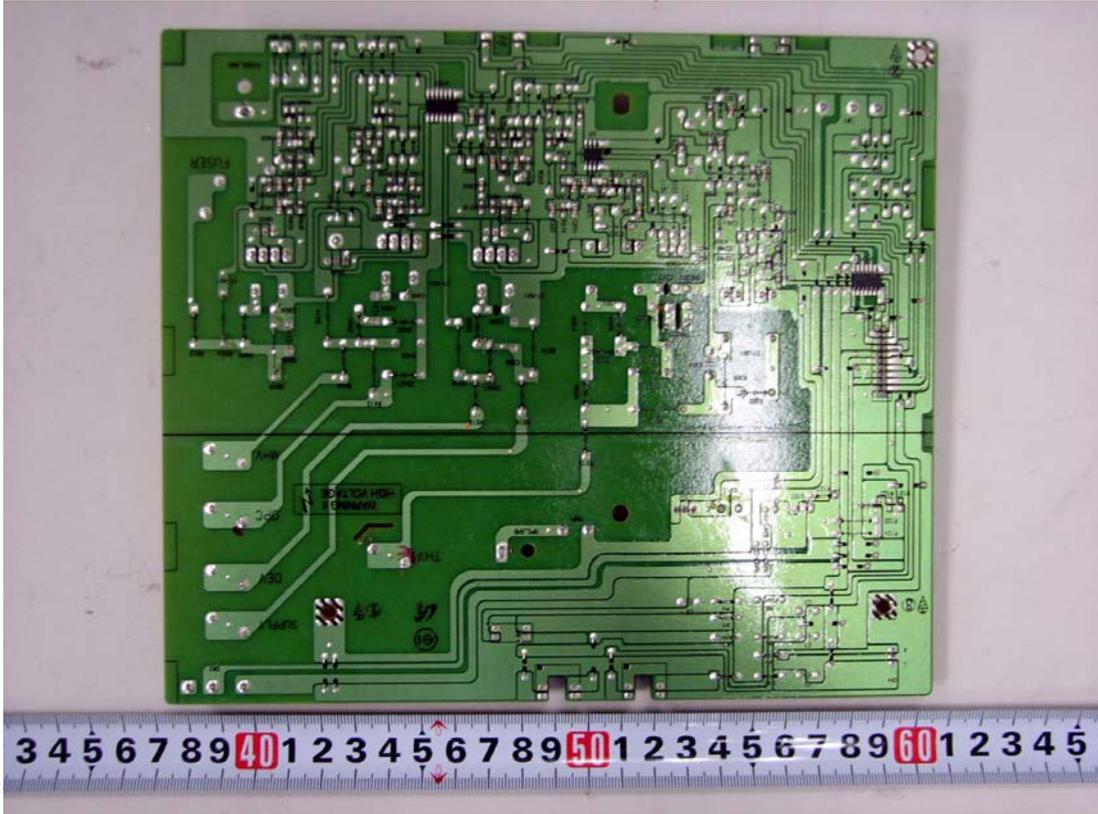
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<Photograph 18 >



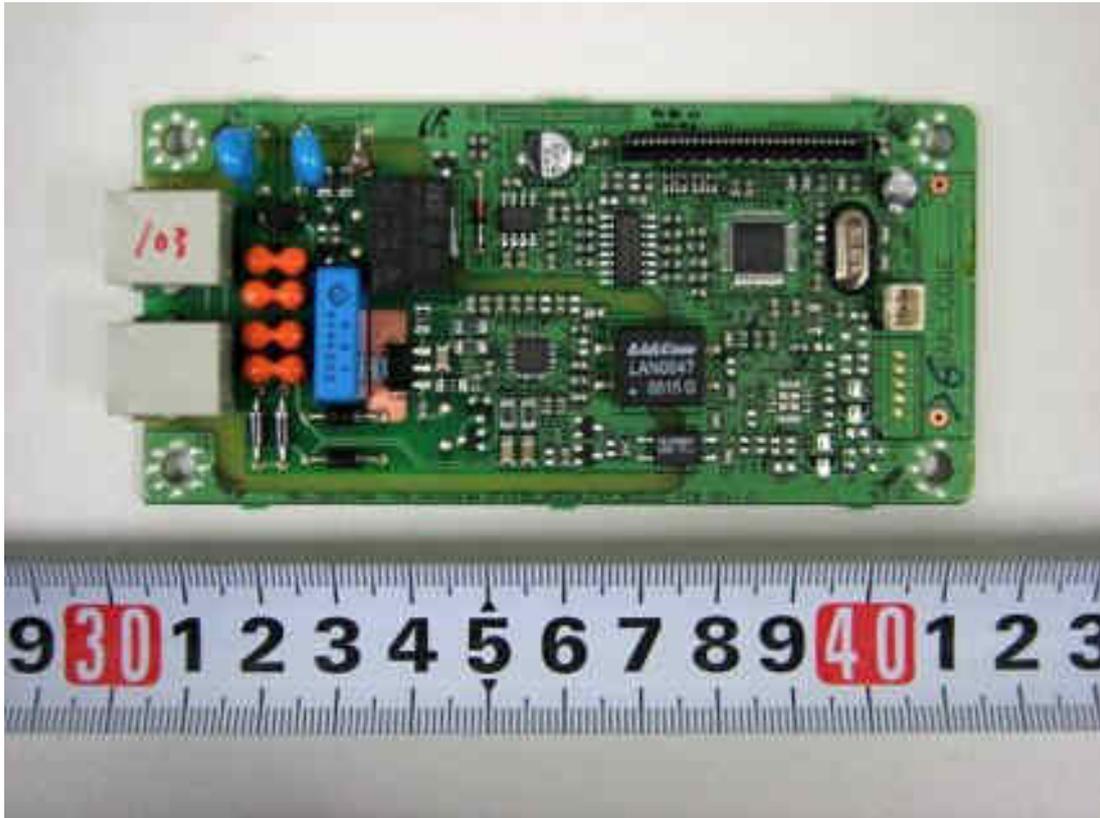
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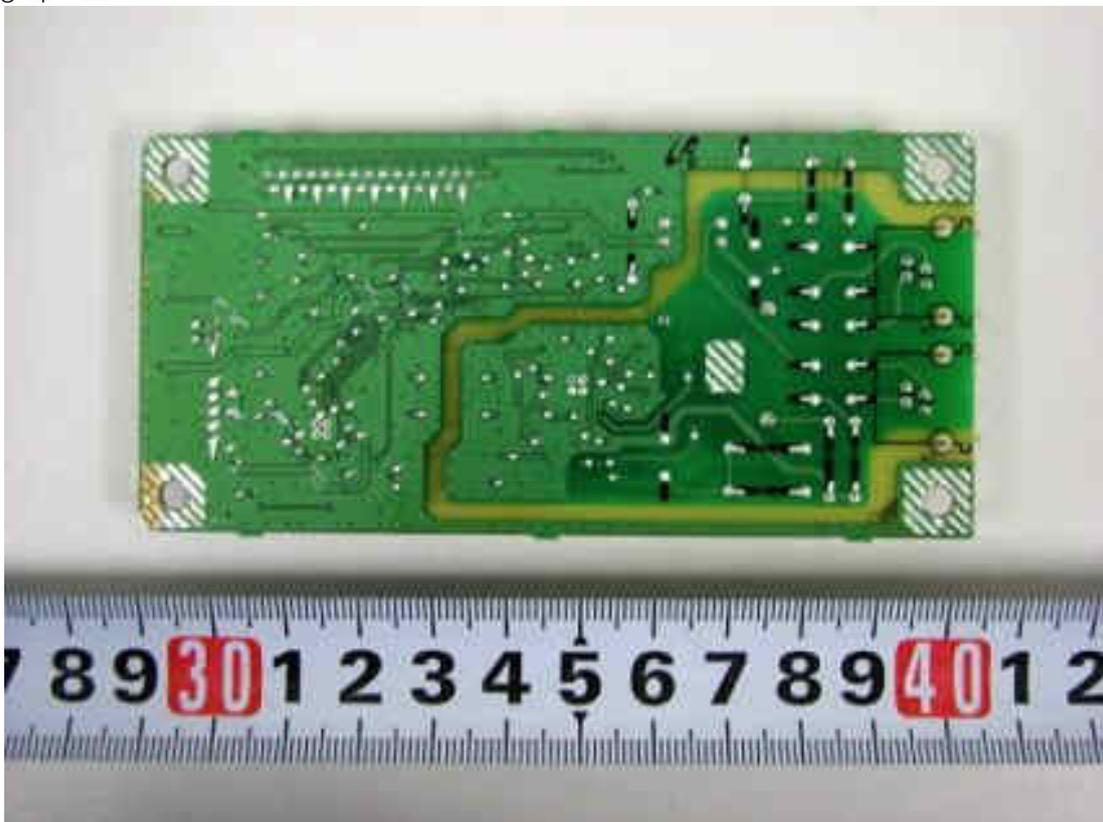
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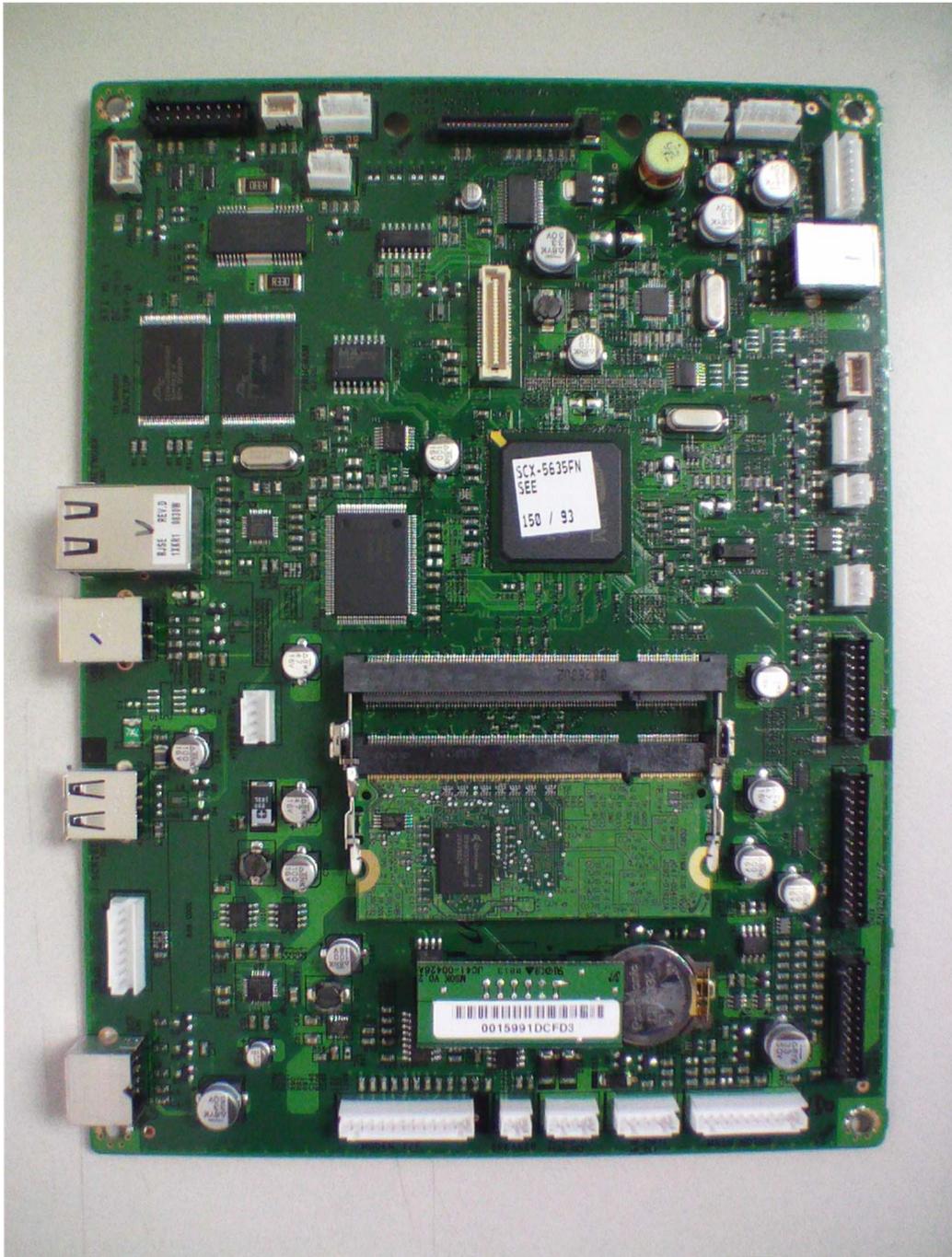
Remark #2: Circuit diagram  
<Photograph 21 >



<Photograph 22 >



Top of the Main PBA



Bottom of the Main PBA

