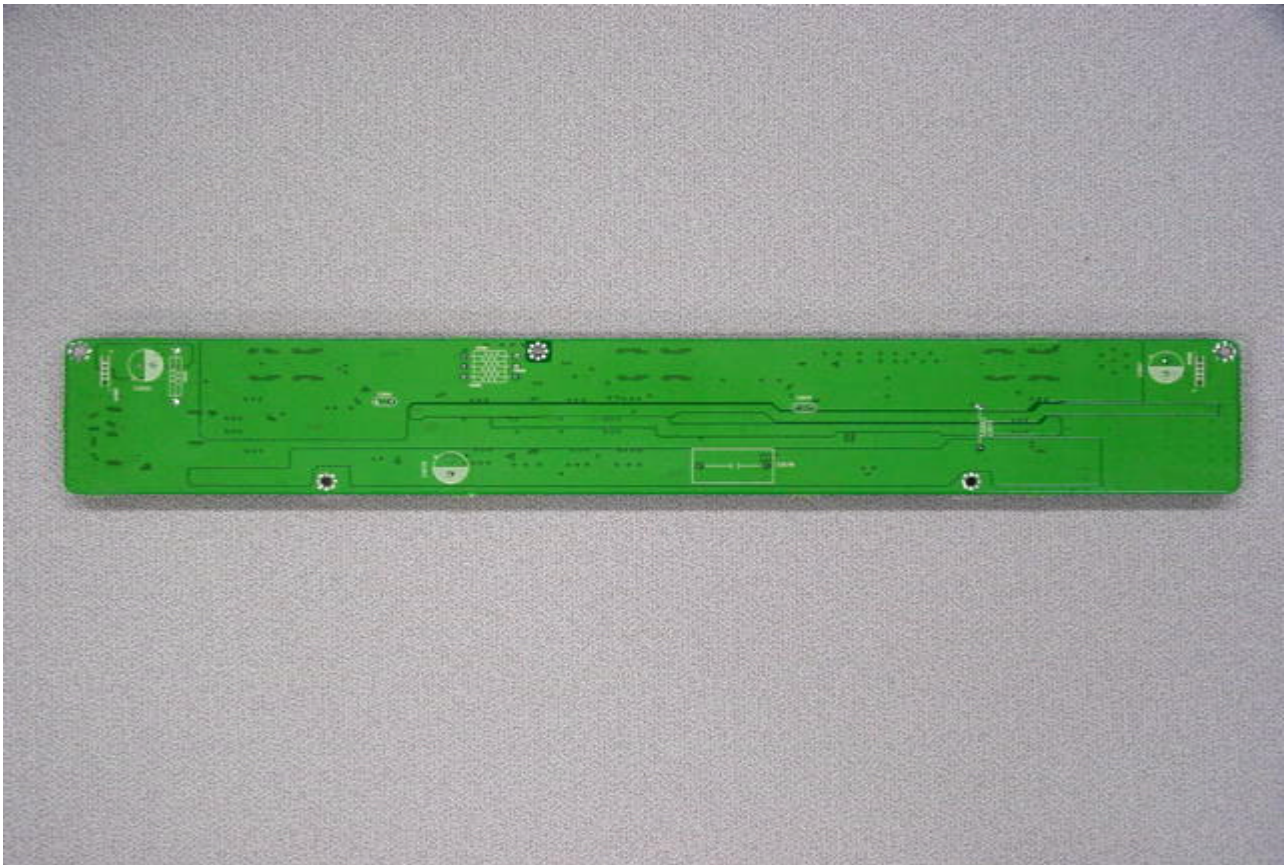
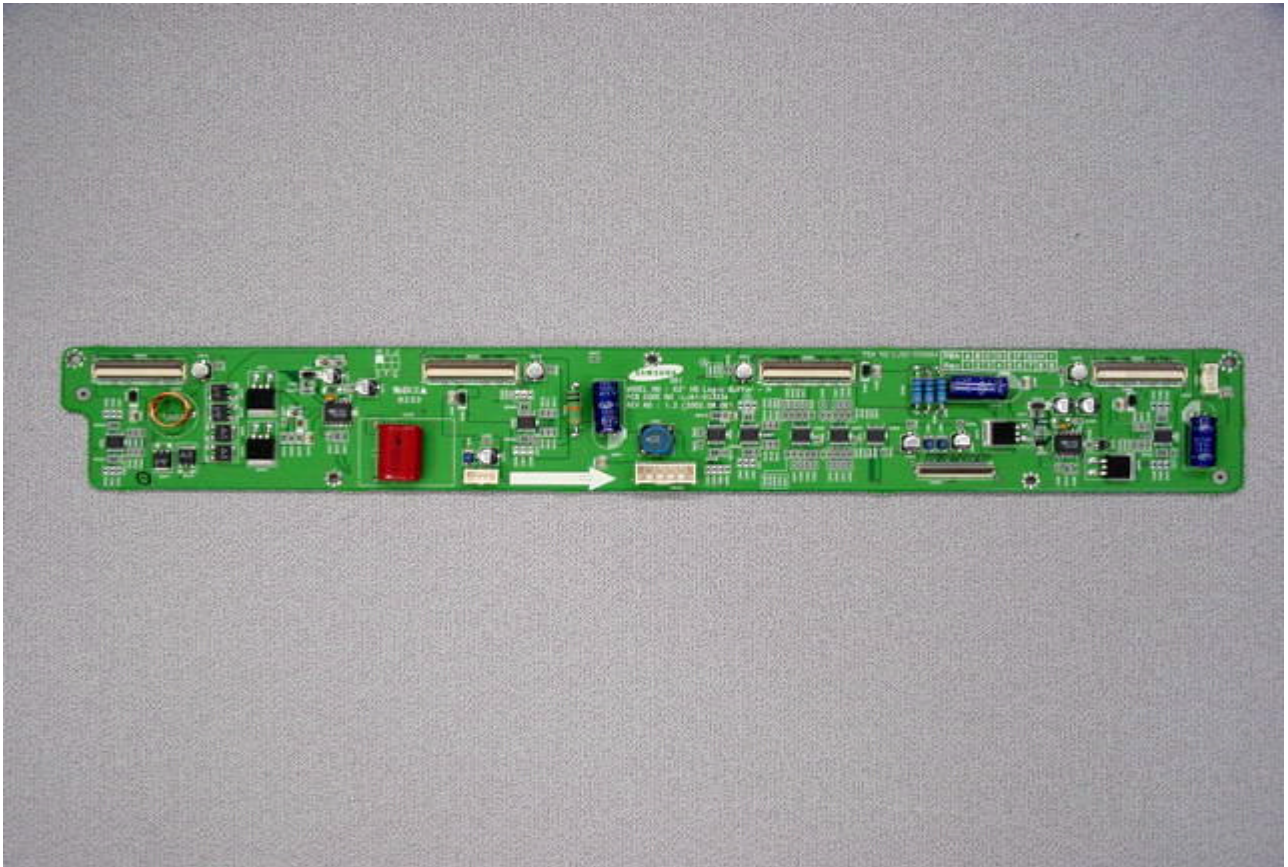


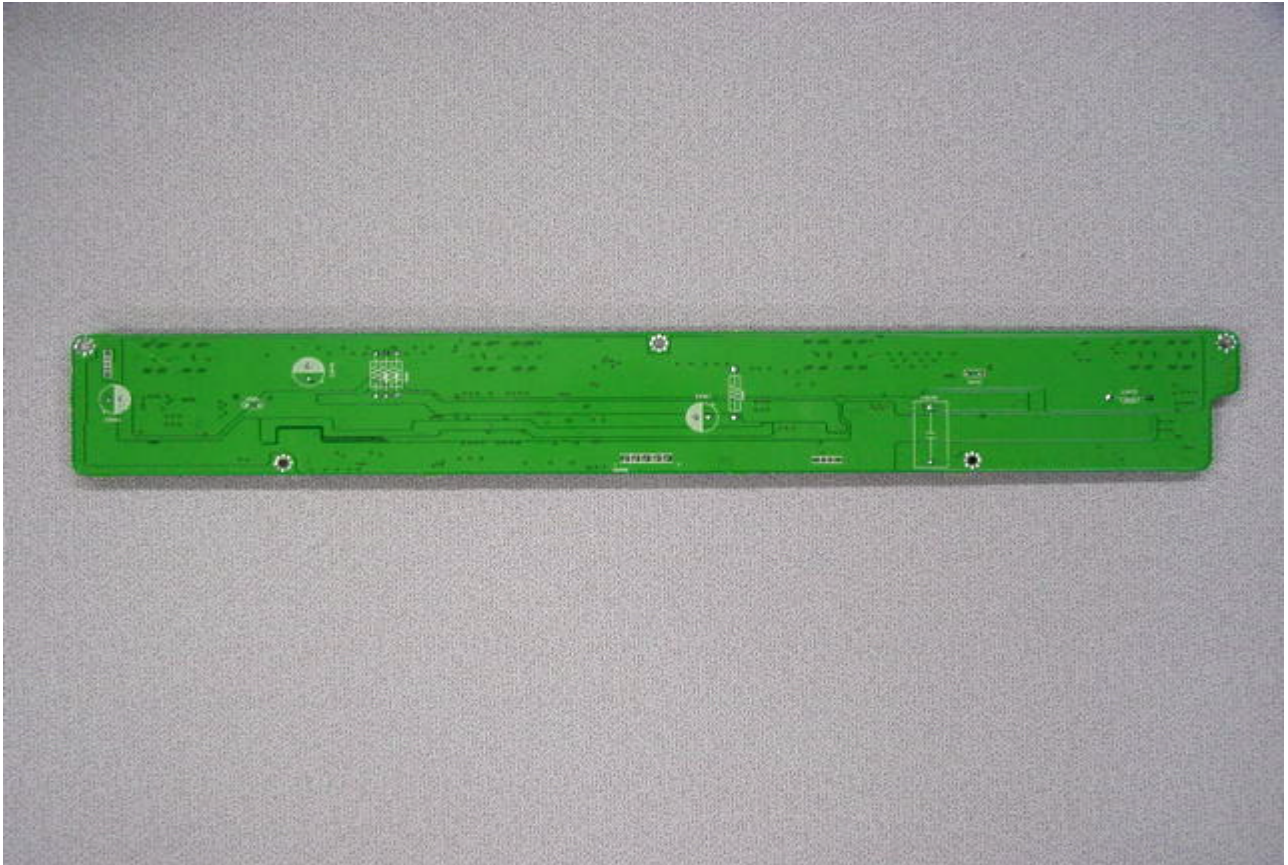
Logic buffer I board top view



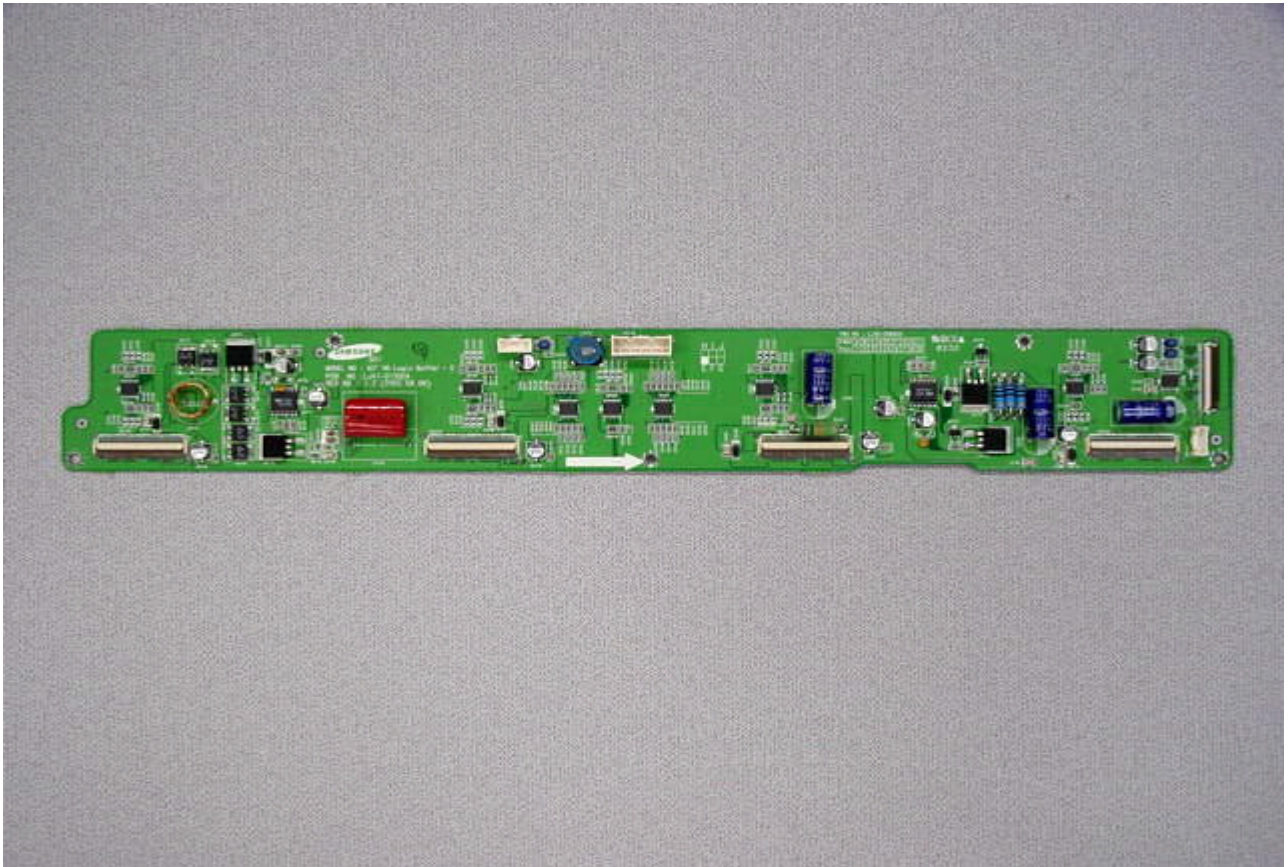
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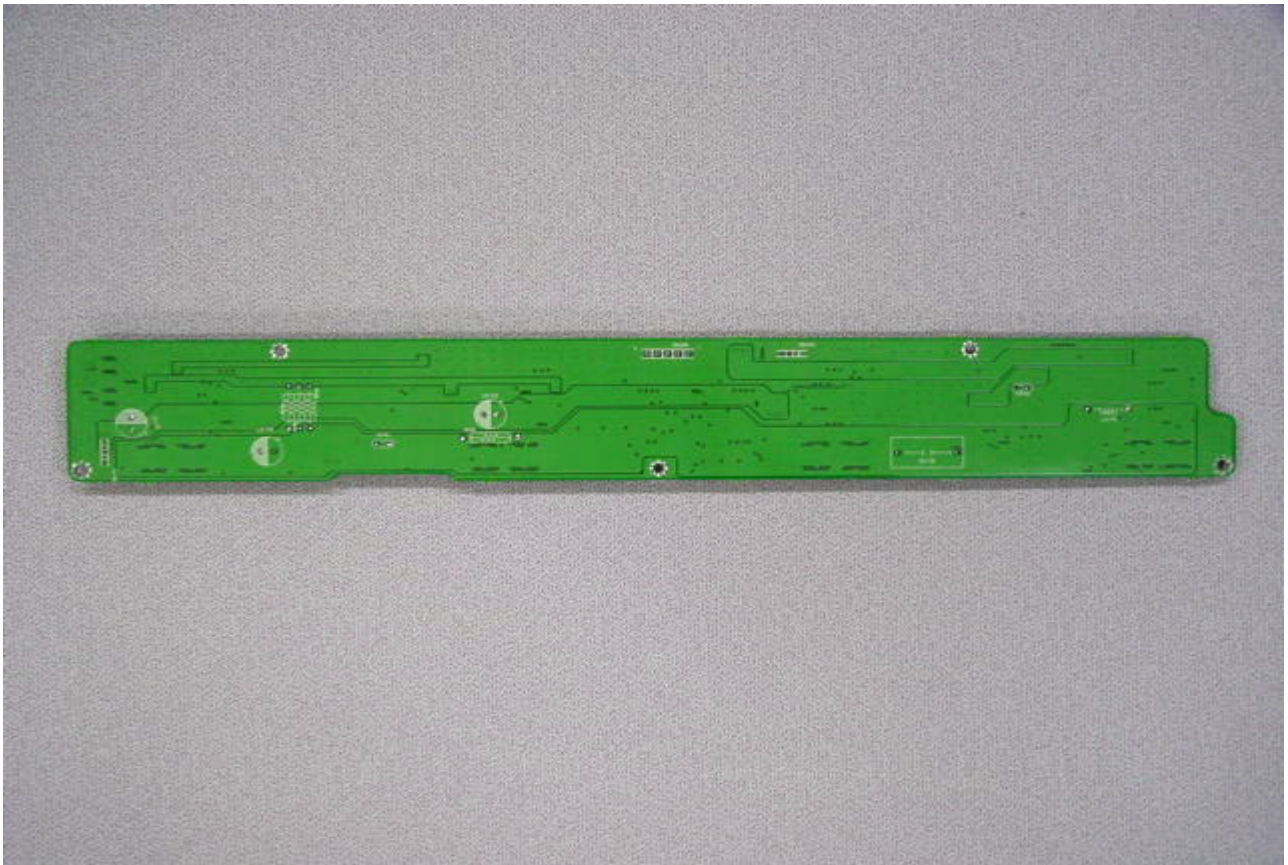
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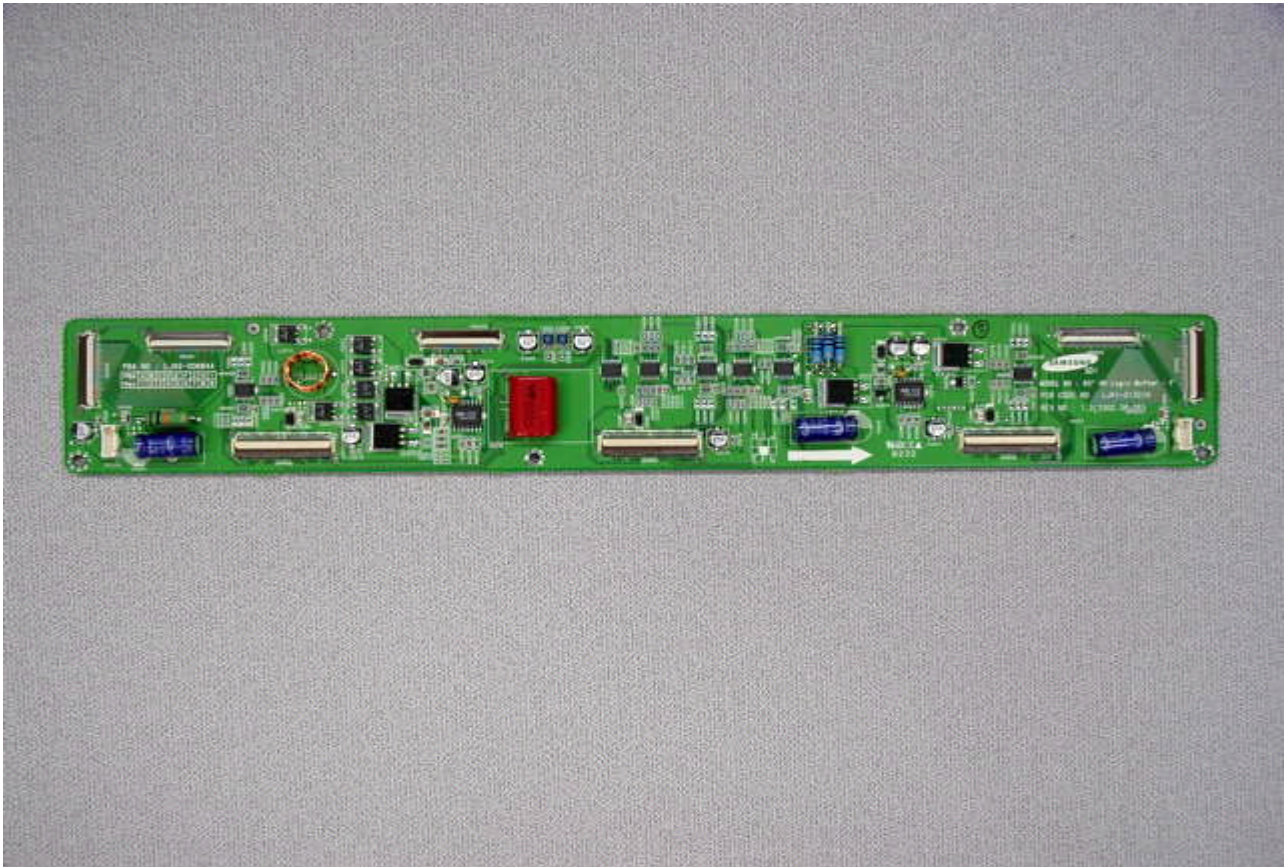
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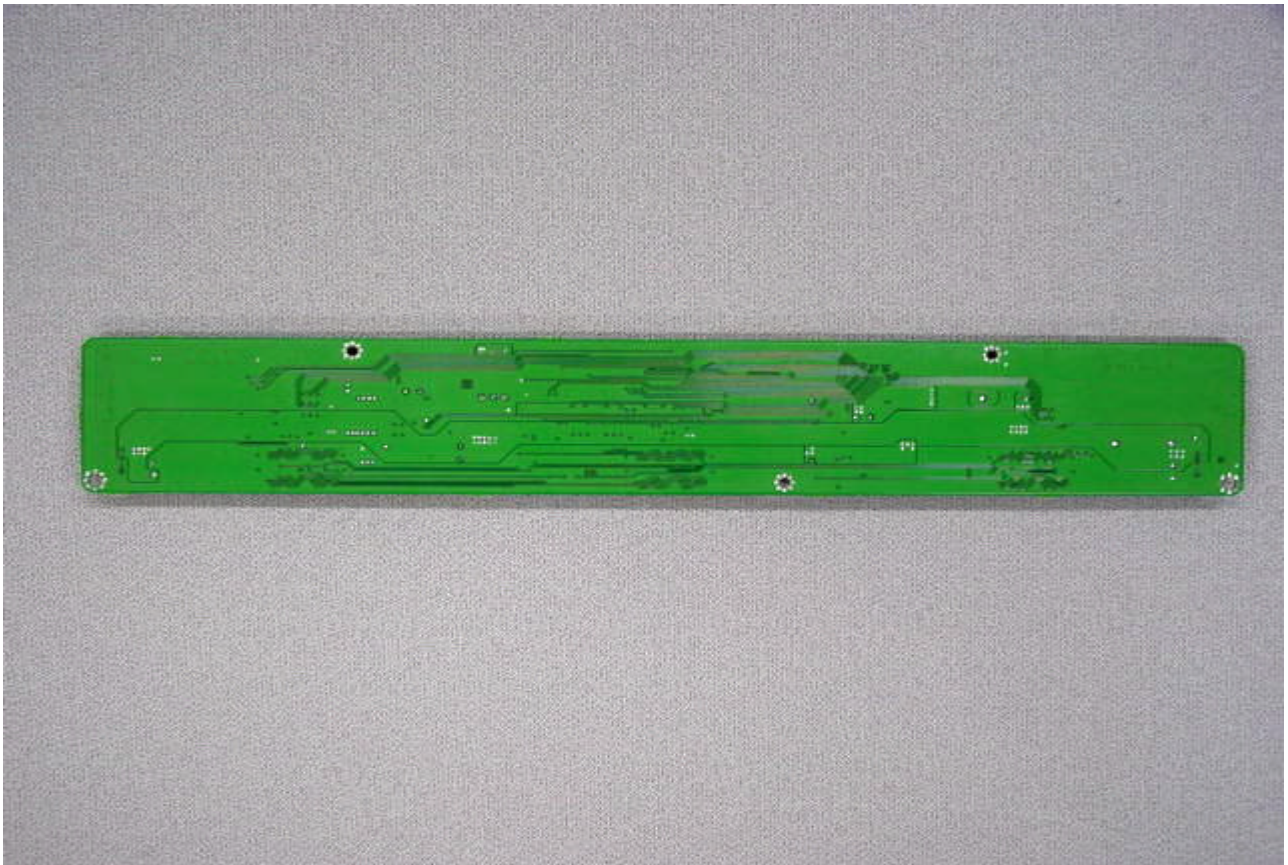
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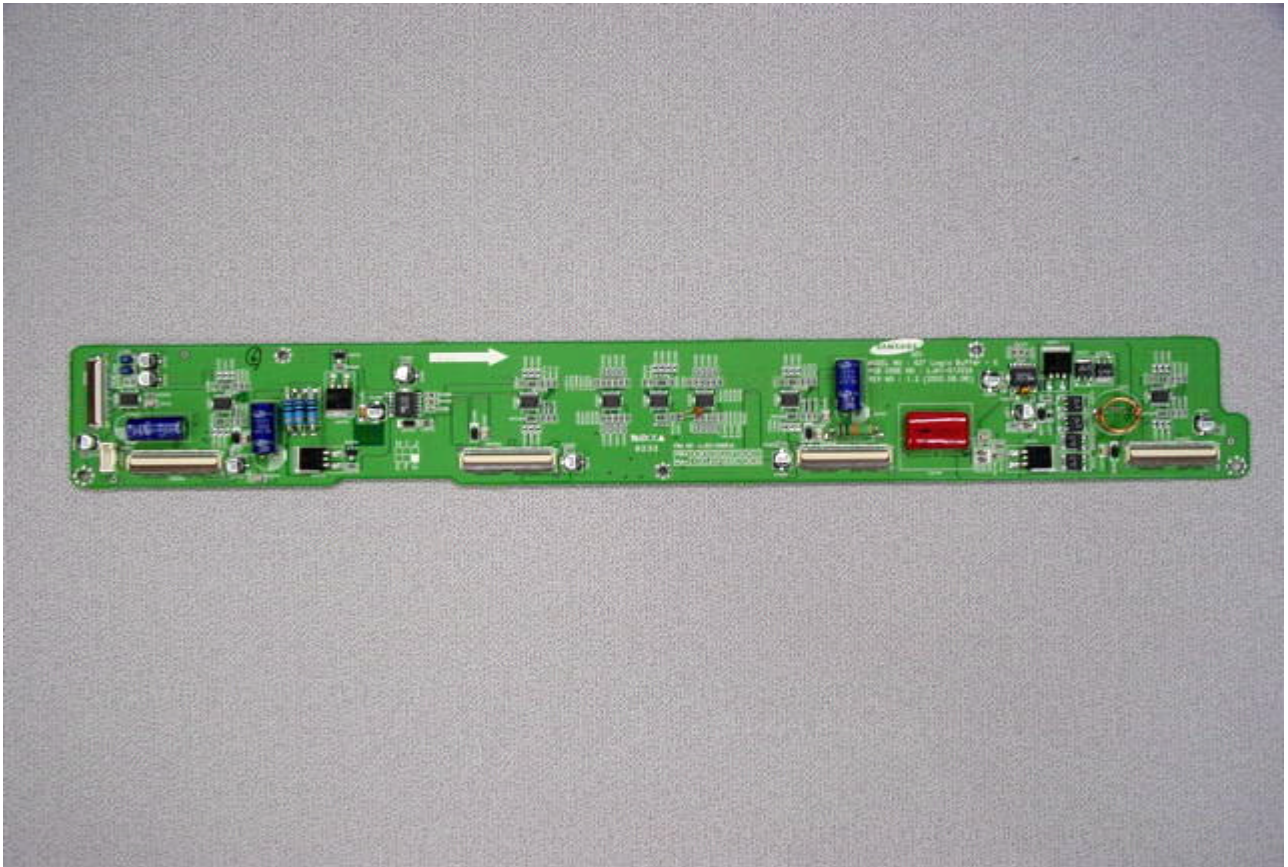
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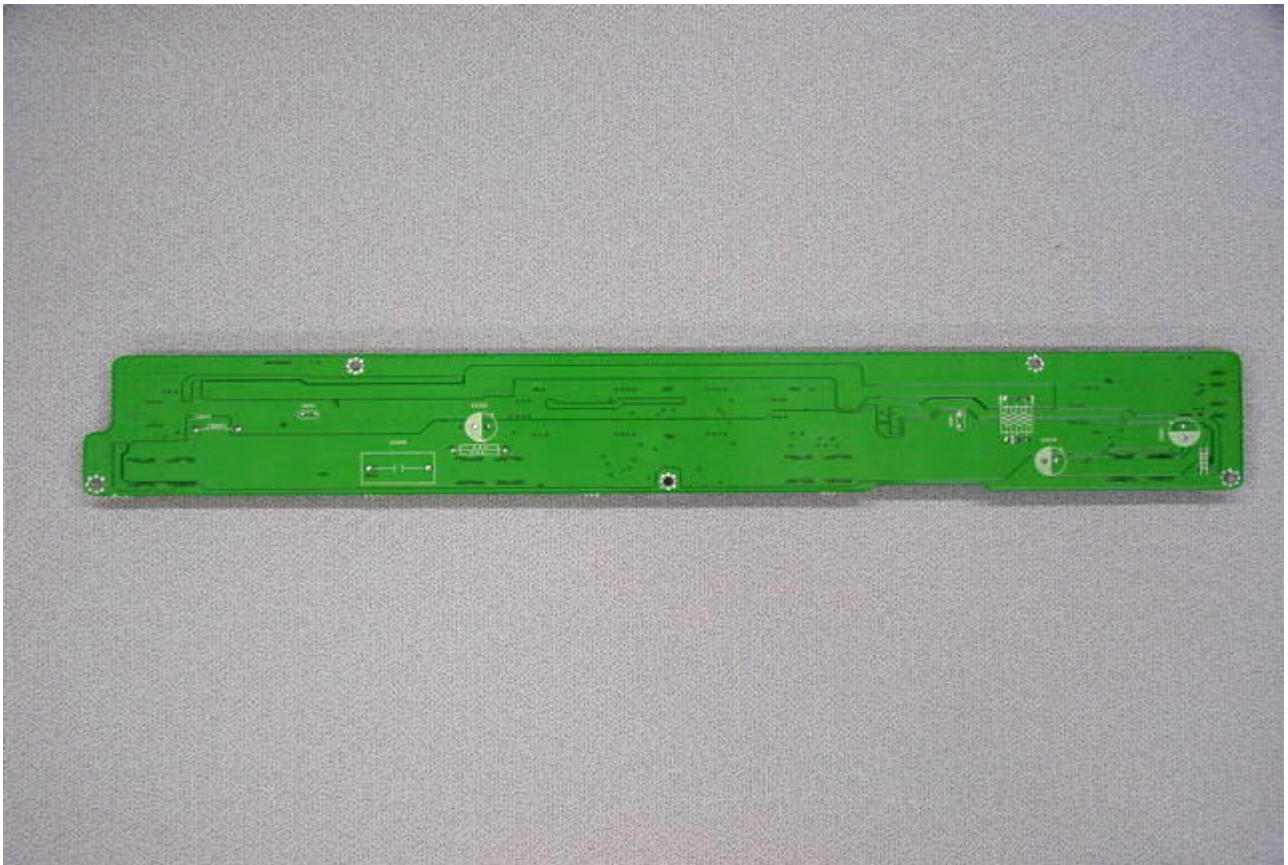
Logic buffer F board top view



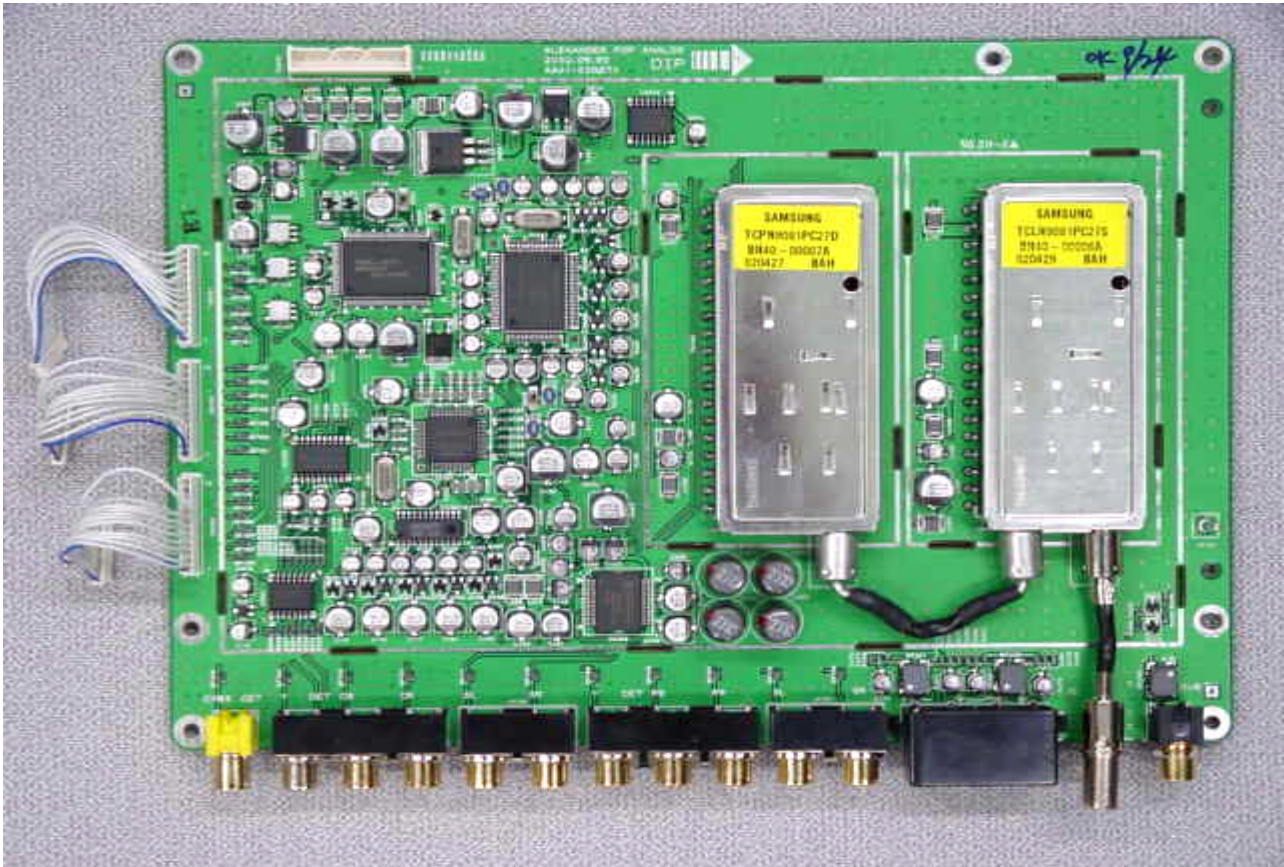
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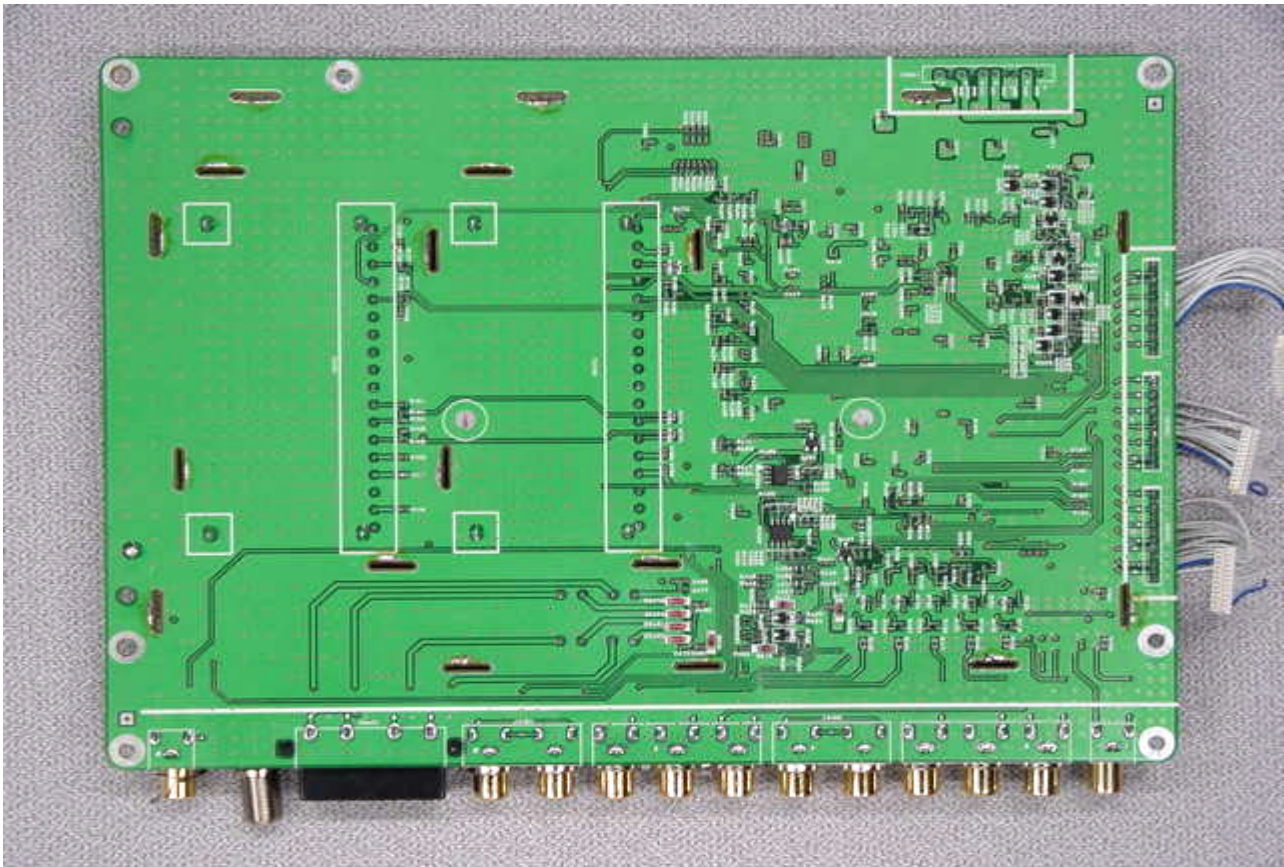
Logic buffer G board top view



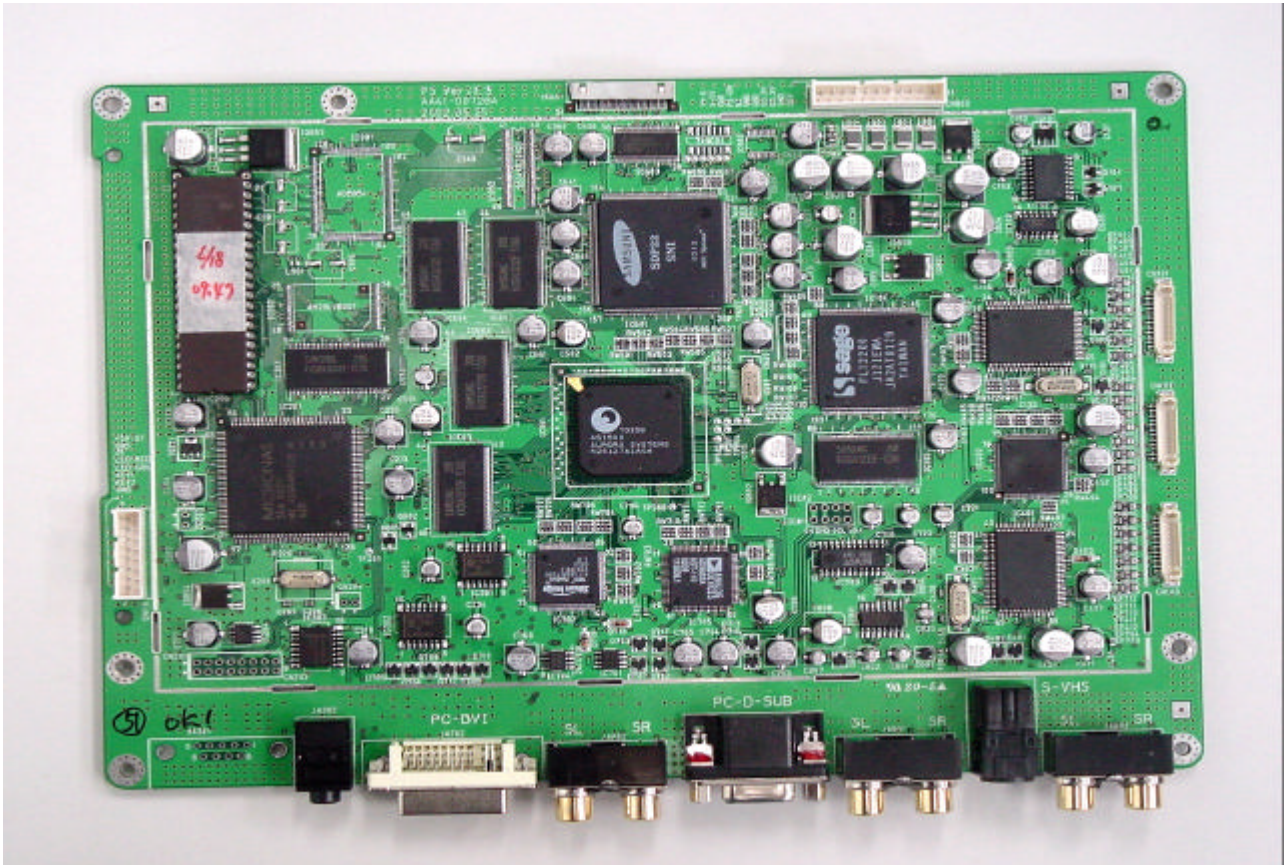
Logic buffer G board bottom view



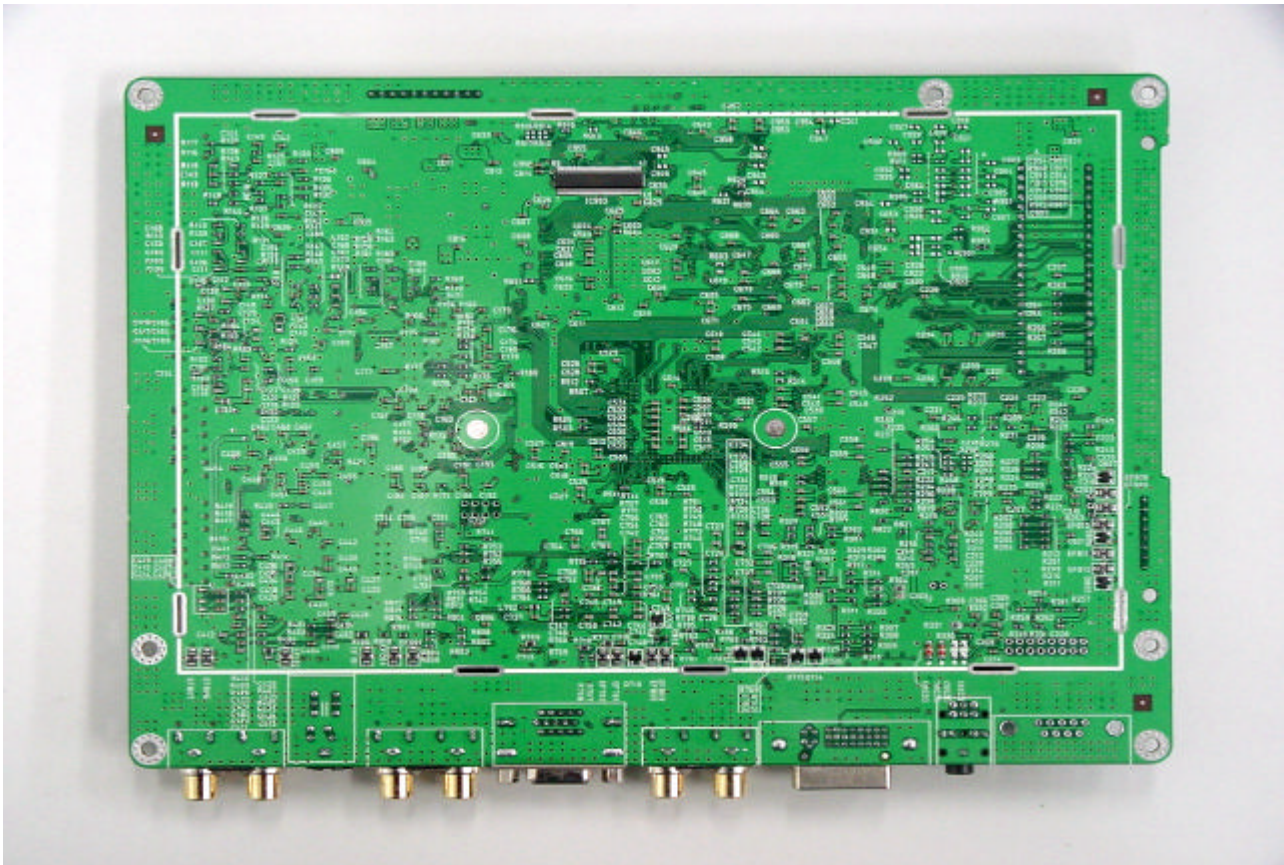
Visual board top view



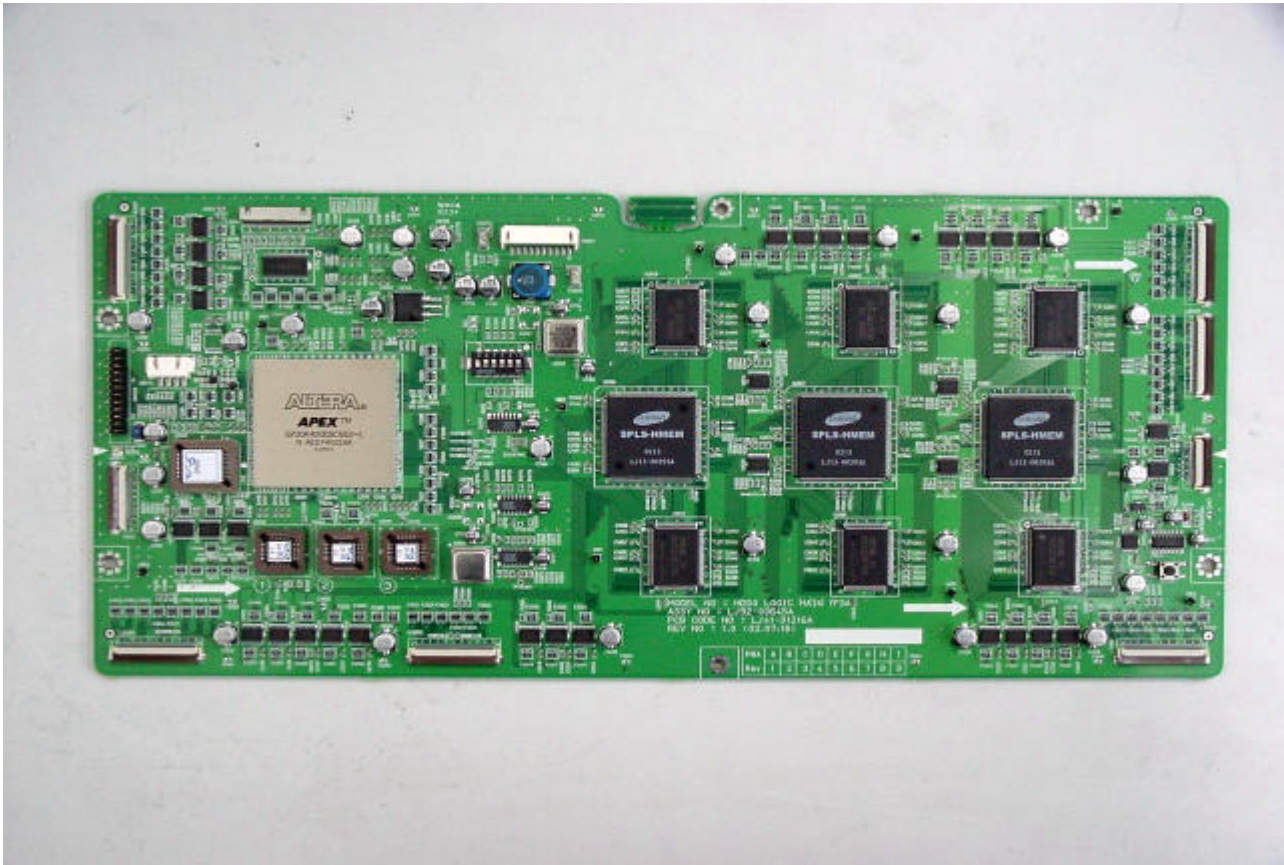
Visual board top view



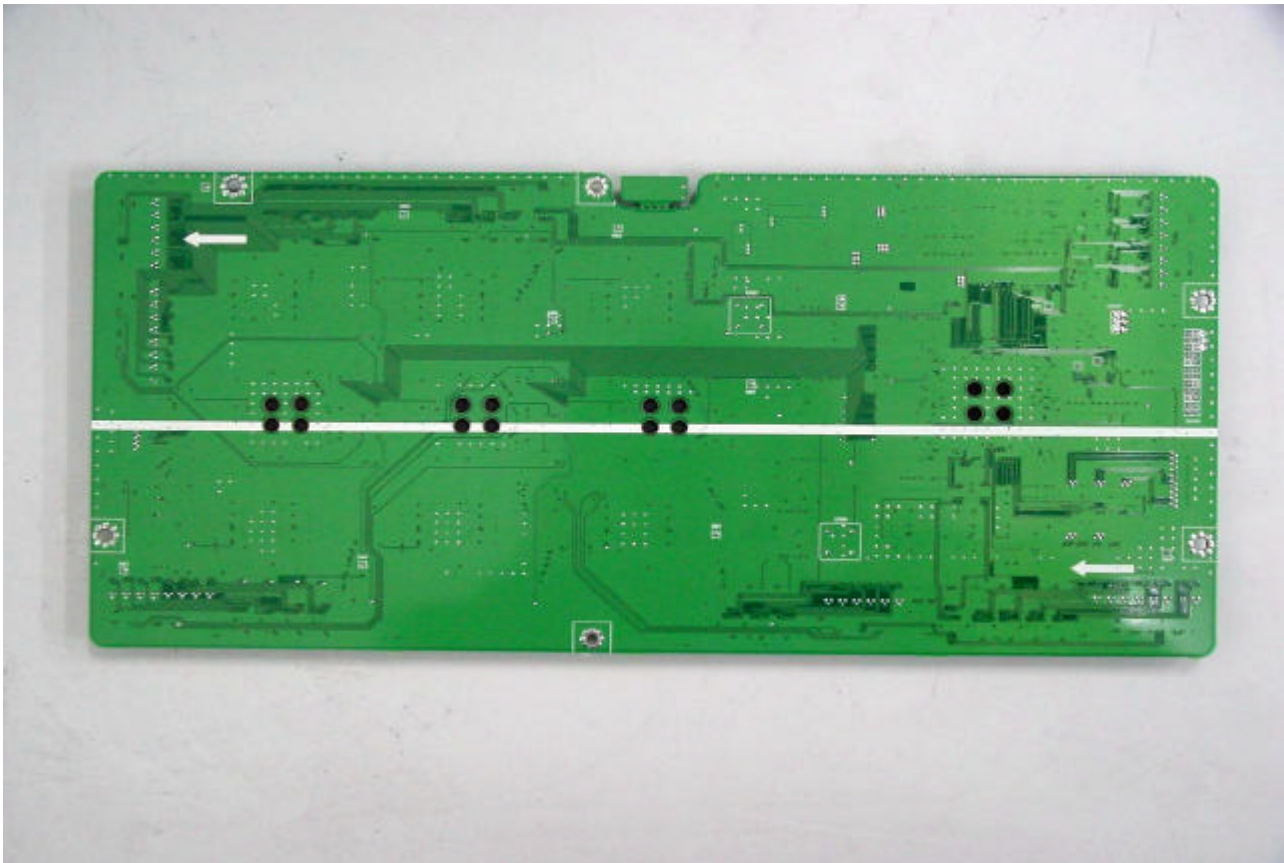
Digital board top view



Digital board bottom view



Logic board top view



Logic board bottom view