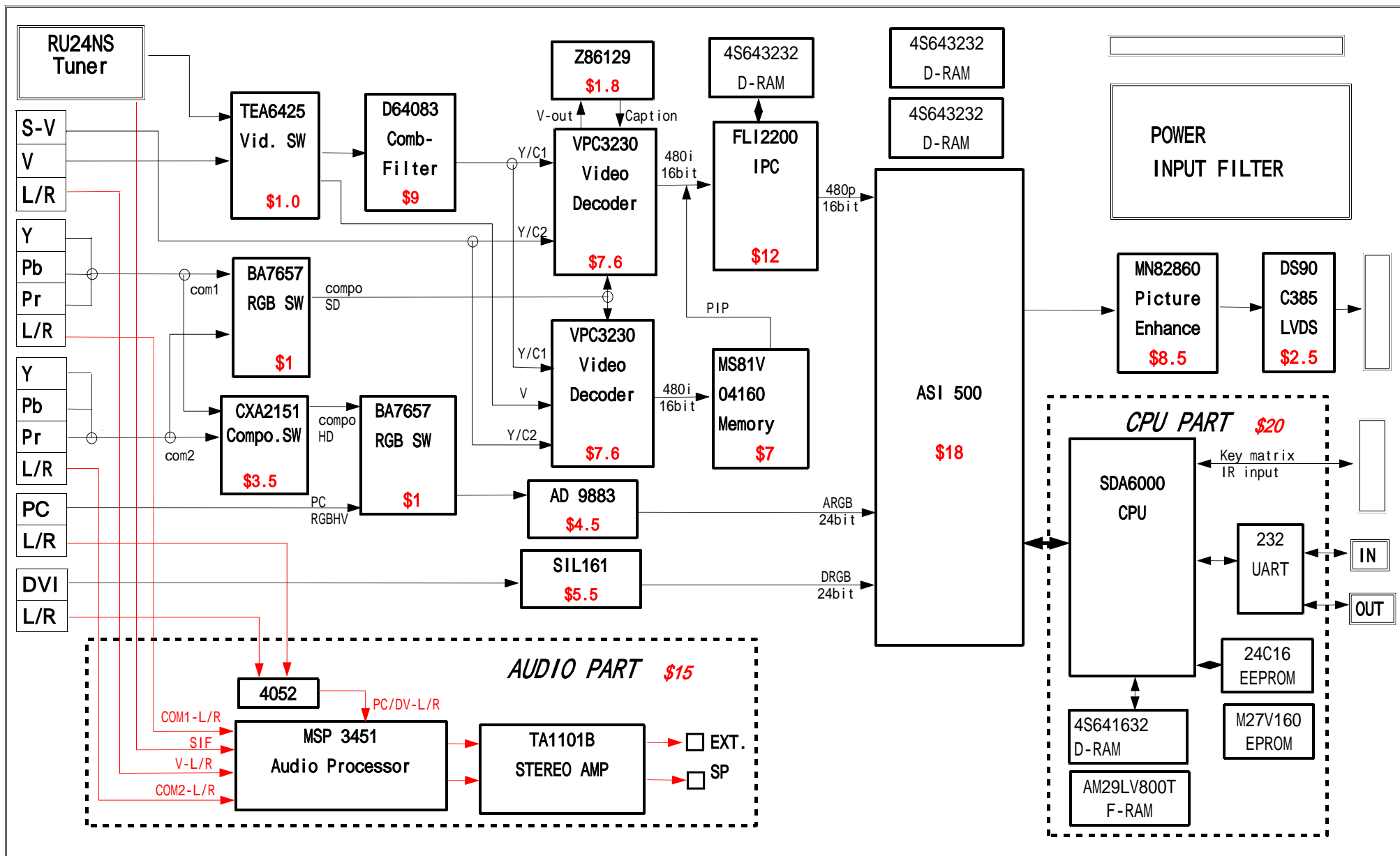
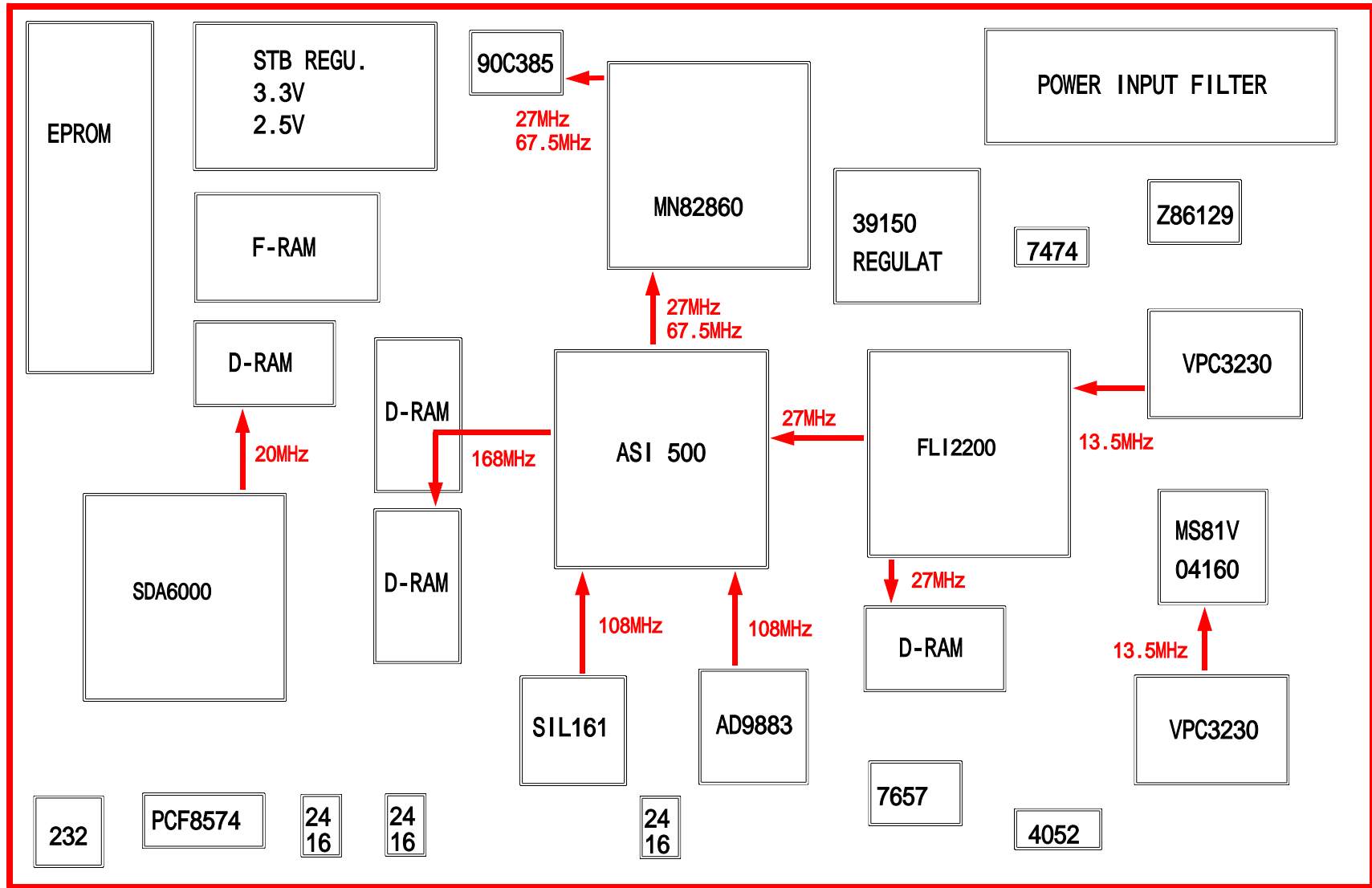


Visual Board Block Diagram



Digital Visual Board Clock Diagram



2-1. LOGIC BOARD Block Diagram (●→: I/O, —→: Internal Signal, □: LOGIC, ▣: Memory)

