Technical Description

The Equipment Under Test (EUT) is Sound Bar with Bluetooth which contains a pair of built-in stereo loudspeaker and power amplifier. It can accept both analog input sources (RCA aux-in and 3.5mm phone-jack line-in) and wireless Bluetooth device. The Bluetooth module in the EUT operating in the frequency range from 2402MHz to 2480MHz (79 channels with 1MHz channel spacing). The EUT is powered by 13VDC from an AC/DC adaptor. The AC/DC adaptor can accept 120VAC only.

Bluetooth Module

Modulation Type: GFSK RF output power setting = -8dBm

Antenna Type: Integral, Internal (PCB Trace)

Frequency Range: 2402MHz - 2480MHz, 1MHz channel spacing, 79 channels

The functions of main ICs are mentioned below.

1. BlueTooth module (BT3GMD-A30P):

- 1) U100 (BCM20771) acts as the 2.4GHz radio core of Bluetooth module (BT3GMD-A30P) which is integrating with audio CODEC.
- 2) C6, C5, L4, C3, L2, C1 and C7 act as antenna matching network.
- 3) Y2 provides system clock (oscillation frequency 26MHz).

2. Pre-amplifier Module:

- 1) IC3 (CSC2314) acts as the pre-amplifier which manipulate/control the input audio signals for the power amplifier. The pre-amplifier has the features of:
 - input selector for switching audio sources (RCA, line-in and Bluetooth)
 - Volume control
 - 2 Band Tone control (bass, treble)

3. Amplifier Module:

1) IC1 (TDA7491) acts as the 2X10W class-D power amplifier for the built-in stereo speakers.

4. MCU module:

1) IC6 (BJ8F101M20A) acts as main MCU for system control.

5. AC/DC Adaptor

- 1) U1 acts as the PWM controller.
- 2) Q1 (4N60) acts as the switching element.
- 3) U2 (817C) acts as the optocoupler providing feedback signal.
- 4) T1 acts as high frequency transformer.

Channel Frequency Table of Bluetooth Module

CH1 2403 CH2 2404 CH3 2406 CH4 2406 CH5 2407 CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2418	02MHz 03MHz 04MHz 05MHz 06MHz 07MHz 08MHz 09MHz 10MHz 11MHz	0 1 2 3 4 5 6 7 8 9	CH26 CH27 CH28 CH29 CH30 CH31 CH32 CH33 CH34 CH35	2428MHz 2429MHz 2430MHz 2431MHz 2432MHz 2433MHz 2434MHz 2435MHz 2436MHz	1A 1B 1C 1D 1E 1F 20 21 22	CH52 CH53 CH54 CH55 CH56 CH56	2455MHz 2456MHz 2457MHz 2458MHz 2459MHz 2460MHz	34 35 36 37 38 39 3A 3B	CH78	2480MHz	4E
CH2 2404 CH3 2405 CH4 2406 CH5 2407 CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	04MHz 05MHz 06MHz 07MHz 08MHz 09MHz 10MHz 11MHz	3 4 5 6 7 8 9	CH28 CH29 CH30 CH31 CH32 CH33 CH34	2430MHz 2431MHz 2432MHz 2433MHz 2434MHz 2435MHz 2436MHz	1C 1D 1E 1F 20 21	CH54 CH55 CH57 CH57 CH58	2456MHz 2457MHz 2458MHz 2459MHz 2460MHz	36 37 38 39 3A			
CH3 2405 CH4 2406 CH5 2407 CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2418	05MHz 06MHz 07MHz 08MHz 09MHz 10MHz 11MHz	3 4 5 6 7 8 9	CH29 CH30 CH31 CH32 CH33 CH34	2431 MHz 2432 MHz 2433 MHz 2434 MHz 2435 MHz 2436 MHz	1D 1E 1F 20 21	CH55 CH56 CH57 CH58	2457MHz 2458MHz 2459MHz 2460MHz	37 38 39 3A			
CH4 2406 CH5 2407 CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2418	06MHz 07MHz 08MHz 09MHz 10MHz 11MHz	4 5 6 7 8 9	CH30 CH31 CH32 CH33 CH34	2432MHz 2433MHz 2434MHz 2435MHz 2436MHz	1E 1F 20 21	CH56 CH57 CH58	2458MHz 2459MHz 2460MHz	38 39 3A			
CH5 2407 CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2418	07MHz 08MHz 09MHz 10MHz 11MHz	5 6 7 8 9	CH31 CH32 CH33 CH34	2433MHz 2434MHz 2435MHz 2436MHz	1F 20 21	CH57 CH58 CH59	2459MHz 2460MHz	39 3A			
CH6 2408 CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2418	08MHz 09MHz 10MHz 11MHz 12MHz	6 7 8 9	CH32 CH33 CH34	2434MHz 2435MHz 2436MHz	20 21	CH58	2460MHz	3A			
CH7 2409 CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	09MHz 10MHz 11MHz 12MHz	7 8 9	CH33 CH34	2435MHz 2436MHz	21	CH59	N DECEMBER OF STREET				
CH8 2410 CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	10MHz 11MHz 12MHz	8 9	CH34	2436MHz	100000	1 0.506 (0.00)	2461MHz	3B			
CH9 2411 CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	11MHz 12MHz	9			22	CHICA				3	- 23
CH10 2412 CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	12MHz		CH35	04271411		CH60	2462MHz	3C			Į.
CH11 2413 CH12 2414 CH13 2415 CH14 2416 CH15 2413 CH16 2418	77.77E	Α		2437MHz	23	CH61	2463MHz	3D			
CH12 2414 CH13 2415 CH14 2416 CH15 2417 CH16 2418	121/11-	***	CH36	2438MHz	24	CH62	2464MHz	3E			Î
CH13 2415 CH14 2416 CH15 2417 CH16 2418	TOMHZ	В	CH37	2439MHz	25	CH63	2465MHz	3F			
CH14 2416 CH15 2417 CH16 2418	14MHz	С	CH38	2440MHz	26	CH64	2466MHz	40		Sc	Į.
CH15 2417 CH16 2418	15MHz	D	CH39	2441MHz	27	CH65	2467MHz	41			
CH16 2418	16MHz	Е	CH40	2442MHz	28	CH66	2468MHz	42			Î
The state of the s	17MHz	F	CH41	2443MHz	29	CH67	2469MHz	43			
C446	18MHz	10	CH42	2444MHz	2A	CH68	2470MHz	44		Se N	
CH17 2419	19MHz	11	CH43	2445MHz	2B	CH69	2471MHz	45			
CH18 2420	20MHz	12	CH44	2446MHz	2C	CH70	2472MHz	46			
CH19 2421	21MHz	13	CH45	2447MHz	2D	CH71	2473MHz	47			
CH20 2422	22MHz	14	CH46	2448MHz	2E	CH72	2474MHz	48			
CH21 2423	23MHz	15	CH47	2449MHz	2F	CH73	2475MHz	49			
CH22 2424	24MHz	16	CH48	2450MHz	30	CH74	2476MHz	4A			
CH23 2425	25MHz	17	CH49	2451MHz	31	CH75	2477MHz	4B			
CH24 2426	26MHz	18	CH50	2452MHz	32	CH76	2478MHz	4C			
CH25 2427	27MHz	19	CH51	2453MHz	33	CH77	2479MHz	4D			



Features:

Bluetooth 3.0 Audio Module, Class 2

The BT3GMD-A30P offers the following features:

Bluetooth Qualified

A2DP1.2 using SBC decoder for streaming audio over

Version: V1.0

Bluetooth and AVRCP 1.4 for remote control functionality

Mar 2012

• Configurable seven-band speaker equalization as well as ten

presets allowing multiple music listening styles

- High quality 96 dB SNR DACs with 44.1 and 48 kHz sample rates for high-fidelity playback
- Single-chip Bluetooth 3.0 transceiver supporting

Bluetooth 2.1 + Enhanced Data Rate (EDR) and Bluetooth 2.0, 1.2, and 1.1 backward compatibility

- Best-in-class Bluetooth radio with up to 8 dBm transmit power and -91dBm receive sensitivity
- Support for side tone and digital microphones
- Supports microphone and speaker HW equalization
- automatic volume control (AVC)
- Switching regulator, battery charger, and power management unit
- Supports fast charging, power dissipation monitoring, and optional charger voltage regulation
- Dual high quality 8 kHz and 16 kHz audio MIC inputs
- Multilanguage voice prompt
- Voice command recognition

Product Description:

The BT3GMD-A30P is a Bluetooth 3.0 Module solution integrating common components required for cost and performance-optimized stereo headset designs.

The BT3GMD-A30P also delivers differentiating features including enhanced audio quality, reduced charging times, A2DP, and multipoint connections through the integration of various noise suppression technologies, noise and echo reduction headset, for high-end

and cost and performance-optimized stereo headsets.

The BT3GMD-A30P supports Bluetooth SIG-compliant wideband speech implementation to greatly enhance the audio quality with both PCs and cell phones.

The BT3GMD-A30P supports the Bluetooth 3.0 standard, adding enhanced power control, simple and secure pairing, and enhanced inquiry response as value-added features for Bluetooth headsets. All major functional blocks required for a Bluetooth stereo headset, including switcher, charger, and stereo audio codec are

The module includes EEPROM, crystal, and PCB antenna.

Applications:

- High-End Stereo Wireless Headsets
- High-END Mono Headsets
- Hands-Free Car Kits
- Wireless Speakers

Functional Block Diagram:

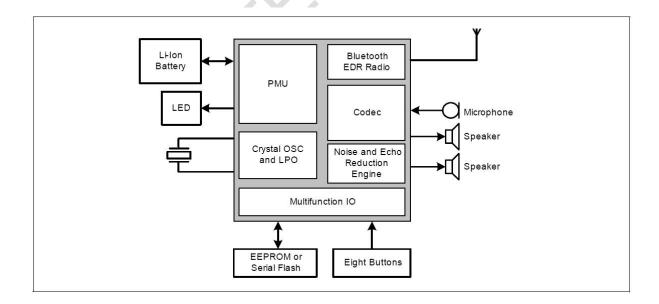


Figure 1: BT30MD-A30P Block Diagram



Physical Description:

The BT30MD-A30P is a 13.5mm×22mm FR4 PCB with 30 pads located around the perimeter.

Table 1 shows the pinout diagram of the module.

PIN	Signal	PIN	Signal	PIN	Signal	PIN	Signal
1	GND	2	MICBAIS	3	MIC1_P	4	MIC1_N
5	NC	6	NC	7	SPKL_N	8	SPKL_P
9	SPKR_N	10	SPKR_P	11	RST	12	TXD
13	RXD	14	REV	15	FWD	16	VOUT
17	VBATT	18	NPNCNTL	19	VCHGAUX	20	VCHG
21	WAKEUP	22	LED2	23	LED1	24	Shutdown
25	VOL-	26	VOL+	27	LED3	28	MFB
29	PLAY	30	GND				

Table 1 Pin Location

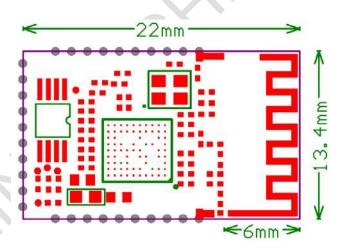


Figure 2: Module PCB Top View



Table 2 Pin Function Descriptions

in Number	Pin Name	I/0	Power Domain	Description
1	GND	I	GND	Digital radio ground.
2	MICBAIS	0	MICAVDD	Microphone bias output.
3	MIC1_P	I	AUD_AVDD	Audio codec microphone differential positive input channel. Mic1 P.
4	MIC1_N	I	AUD_AVDD	Audio codec microphone differential negative input channel. Mic1 N.
5	NC			
6	NC	G.		
7	SPKL_N	0	SPKAVDD	Speaker differential negative output channel 1.
8	SPKL_P	0	SPKAVDD	Speaker differential positive output channel 1.
9	SPKR_N	0	SPKAVDD	Speaker differential negative output channel 2.
10	SPKR_P	0	SPKAVDD	Speaker differential positive output channel 2.
11	RST	I	VDDO	Power-on reset, active low.
12	TXD	I/0	VDDO	General-purpose I/O.
13	RXD	I/0	VDDO	General-purpose I/O.
14	REV	I/0	VDDO	General-purpose I/O.
15	FWD	I/0	VDDO	General-purpose I/O.
16	VOUT	0	AVDD	3.3V Voltage output.
17	VBATT	I	VBAT	3.1-4.2V Input voltage.
18	NPNCNTL	0	VCHG	Base control for external PNP driver transistor through an NPN transistor,
19	VCHGAUX	I	VCHG	Power to the charger control system.
20	VCHG	I	VCHG	Charger supply input.
21	WAKEUP	I	AVDD_OUT	PMU wake-up and shut-down pin. MIA-LITE wakeup/system power-down signal.
22	LED2	0	VBAT	Connect the cathode of LED2. Anode can be connected to HVLDO.
23	LED1	0	VBAT	Output driver for LED. Connect the cathode of LED1. Anode can be connected to HVLDO
24	Shut down	I/0	VDDO	General-purpose I/O.
25	VOL-	I/0	VDDO	General-purpose I/O.
26	VOL+	I/0	VDDO	General-purpose I/O.
27	LED3	I/0	VDDO	General-purpose I/O.
28	MFB	I/0	VDDO	General-purpose I/O.
29	PLAY	I/0	VDDO	General-purpose I/O.
30	GND	I	GND	Digital radio ground.



Supporting Documentions:

Reference Schematic:

The most recent schematic, bill of materil, and layout file are available from the ITON Technology Limit. Contact your ITON representative for details.

Layout Considerations:

The BT30MD-A30P module is placed at the location where the antenna is away from the power supply(i.e.,BT1 Battery contacts) and any digital signal traces.. The antenna keep-out area which is 5mm around the parameter of the module region is shown in the red dotted box. PCB material and signal traces should not be placed within the antenna keep-out area to assure optimum antenna performance.

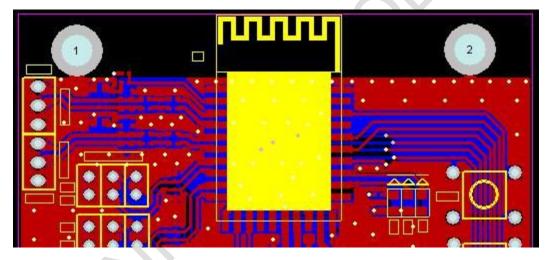


Figure 3: Design reference

Electrical Characteristics:

Table 3: Maximum Electrical Rating

Rating	Symbol	Value	Unit
Maximum DC supply voltage for I/O	VDDO	3.8	٧
Maximum DC supply voltage for charger	VCHG	6.5	V
Maximum voltage on input or output pin	Vimax	Domain supply voltage a + 10%	
Maximum transient voltage on input or output pin, 10% maximum duty time	Vimaxt	4.1	V
Minimum voltage on input or output pin	Vimin	VSS - 0.3v	V
Maximum voltage on LED	VLED-max	4.1	V
Storage temperature range	Tstg	-40 to +125	°C
Maximum battery input voltage	VBAT	4.5	V
Maximum charger power dissipation	Pmax (VCHG - VBAT)	390	mW

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Taiwan H/Q Add.: No.46, Aly. 108-3, Ln. 445, Sec. 2, Meishi Rd., Yangmei City, Taoyuan County 326, Taiwan (R.O.C.)



Table 4:Power Supply Current (with a Nominal 3.7V Battery Voltage)

Operating Mode	Typical	Unit
Narrowband Speech Active mode (with 500 ms sniff interval)		
• HV3	9.1	mA
• 2EV3	8.0	mA
• EV3	8.9	mA
A2DP Active mode		
 44.1kHz sampling rate, SBC (stereo, 8 sub bands, 16 blocks, 53 bit pool), 2DH5 packet type with 118 byte frame size 	11.3	mA
Standby mode		
Single HFP Sniff (640 ms interval)	200	μA
Single HFP Sniff (500 ms interval)	207	μA
Dual HFP Sniff (640 ms interval)	327	μA
Dual HFP Sniff (500 ms interval)	332	μA
Deep Sleep (off) mode	3.0	μA

Notes:

- The currents are measured without an audio signal present.
- The currents are measured with Broadcom generic MMI, and LEDs are off.
- The standby current is measured with the device operating in Slave mode.

Table 5: Audio DAC Path Performance Specifications, 8 kHz and 16 kHz Sample Rate

Property	Conditions	Minimum	Typical	Maximum	Unit
Full-scale output signal level	0 dB driver gain 1 kHz tone at 0 dBFS	0-	3.2	-	Vppc
	32Ω line load				
Output driver capability	0 dB driver gain	82	30	=	mW
	1 kHz tone at 0 dBFS				(rms
	32Ωload				
Output load impedance	Nominal speaker load	16	32	(News)	Ω
Driver gain range	Adjustable gain	-18	79 4	0	dB
Driver step sizes	HI:	() -	3	(S=	dB
Absolute gain error	Over 0 to -18 dB driver gain	(1 11-1)	1	(Mag)	dB
	1 kHz tone				
Idle channel tone	0 dB driver gain, no signal	9. 5=)	100 E	-105	dBc
	32Ωload				
SNR	0 dB driver gain	90	96	(19 54)	dB
	A-weight				
	20 kHz BW				
	32Ω load				
Dynamic range	0 dB driver gain	-90	-96		dB
	A-weight, 20 kHz BW				
	1 kHz tone at -60 dBFS				
	32Ωload				
Total harmonic distortion (THD) + N	Po= 24 mW	-		-70	dB
	0 dB driver gain				
	A-weight, 20 kHz BW				
	32Ωload				
	Po= 3 mW, 0 dB driver gain	94 5 .	-	-62	dB
	A-weight				
	20 kHz BW				
	32Ωload				



RF Specification:

Table 6 : Receiver RF Specifications

Property	Minimum	Typical ^a	Maximum ^b	Unit
Receiver Section				
Frequency range	2402	-	2480	MHz
Rx Sensitivity				
GFSK, 0.1% BER, 1 Mbps	\$2 <mark>00</mark>	-89.5	(<u>-</u>	dBm
pi/4-DQPSK, 0.01% BER, 2Mbps	124	- 91.5		dBm
8-DPSK, 0.01% BER, 3 Mbps	-	-85.5	(-	dBm
Maximum input	8 <u>00</u>	<u></u>	-10.0 ^c	dBm
Interference Performance				
C/I co-channel (GFSK, 0.1% BER)	-	-	11.0	dB
C/I 1 MHz adjacent channel (GFSK, 0.1% BER)	8 <u>00</u>	17169 10	0.0	dB
C/I 2 MHz adjacent channel (GFSK, 0.1% BER)			-30.0	dB
C/I ≥ 3 MHz adjacent channel (GFSK, 0.1% BER)	-	-	-40.0	dB
C/I image channel (GFSK, 0.1% BER)	82 <u>00</u>	(*)40 (*)	-9.0	dB
C/I 1 MHz adjacent to image channel (GFSK,0.1% BER)			-20.0	dB
C/I co-channel (pi/4-DQPSK, 0.1% BER)	18	-2 4	13.0	dB
C/I 1 MHz adjacent channel (pi/4-DQPSK, 0.1% BER)	\$ <u>**</u>	2450	0.0	dB
C/I 2 MHz adjacent channel (pi/4-DQPSK, 0.1% BER)	120		-30.0	dB
C/I ≥ 3 MHz adjacent channel (8-DPSK, 0.1% BER)	-	 4	-40.0	dB
C/I image channel (pi/4-DQPSK, 0.1%BER)	\$2 <u>66</u>	10369 10	- 7.0	dB
C/I 1 MHz adjacent to image channel (pi/4-DQPSK,0.1% BER) -		-20.0	dB
C/I co-channel (8-DPSK, 0.1% BER)	-	-	21.0	dB
C/I 1 MHz adjacent channel (8-DPSK, 0.1% BER)	82 <u>00</u>	<u> </u>	5.0	dB
C/I 2 MHz adjacent channel (8-DPSK, 0.1% BER)		-	-25.0	dB
C/I ≥ 3 MHz adjacent channel (8-DPSK, 0.1% BER)	-	=	-33.0	dB
C/I image channel (8-DPSK, 0.1% BER)	()	<u></u>	0.0	dB
C/I 1 MHz adjacent to image channel (8-DPSK,0.1% BER)			-13.0	dB



Table7: Transmitter RF Specifications

Property	Minimum	Typical	Maximum	Unit
Transmitter Section				
Frequency range	2402	3 8	2480	MHz
Maximum output power (Class 2 with V12 pin power to VDDT pin, with TCA and TSSI)	F - 3	2	4	dBm
Maximum output power (Class 1 with 3.3V to VDDTF pin, wit TCA and TSSI) ^D	h 5	8	12	dBm
In-Band Spurious Emission				
±500 kHz	0=	- 1	-20.0	dBc
1.0 MHz < M - N < 1.5 MHz (EDR only)	-		-26.0	dBc
1.5 MHz < M - N < 2.5 MHz (EDR only)	2	100 M	-20.0	dBm
M - N > 2.5 MHz (EDR only)	-	₩ 1	-40.0°	dBm
Out-of-Band Spurious Emission				
30 MHz to 1 GHz	2	-80.0	-36.0 ^d	dBm
1 GHz to 12.75 GHz		= 13	-30.0 ^e	dBm
1.8 GHz to 1.9 GHz	(F)	-80.0	-4 7.0	dBm
5.15 GHz to 5.3 GHz	~	-90.0	- 47.0	dBm
GPS Band Spurious Emissions and Noise Floor ^f				
1572.92 MHz to 1577.92 MHz (without SAW filter)	-	-150	-124	dBm/Hz
1572.92 MHz to 1577.92 MHz (with SAW filter)	24	-162	-146	dBm/Hz
Out-of-Band Noise and Spurious Emission without Band-pa	ss Filter at F	ront End ^f		
746 MHz to 764 MHz (CDMA)	-	- 78) (=)	dBm
851 MHz to 894 MHz (CDMA)	1	- 68	112	dBm
925 MHz to 960 MHz (GSM)	() ()	- 68	-	dBm
1805 MHz to 1880 MHz (GSM)	-	- 70	9.55	dBm
1930 MHz to 1990 MHz (CDMA)	<u> </u>	- 73	11,2	dBm
2110 MHz to 2170 MHz (WCDMA)	(-)3	- 73	188	dBm
Out-of-Band Spurious Emission Noise Floor ^f				
746 MHz to 764 MHz	22	- 140	-130	dBm/Hz
851 MHz to 894 MHz	(-)	- 140	-130	dBm/Hz
925 MHz to 960 MHz	NET 1	- 140	-130	dBm/Hz
1805 MHz to 1880 MHz	4	- 140	-130	dBm/Hz
1930 MHz to 1990 MHz	-	-140	-130	dBm/Hz



Application Examples:

- ·Stereo Headphones
- ·Wireless stereo speakers
- Soundbars
- ·Mono Headsets
- ·Handsets
- ·and more...

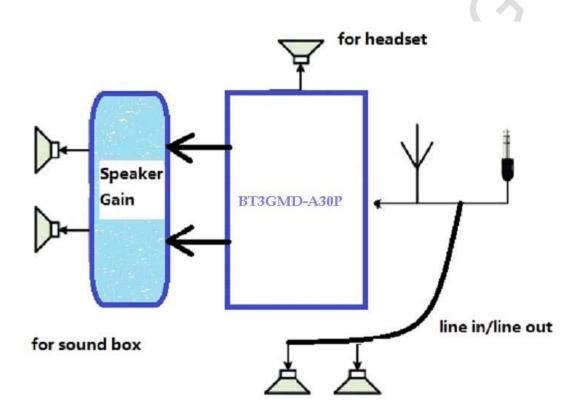


Figure 4 : Application



Mechanical Specification:

Weight:

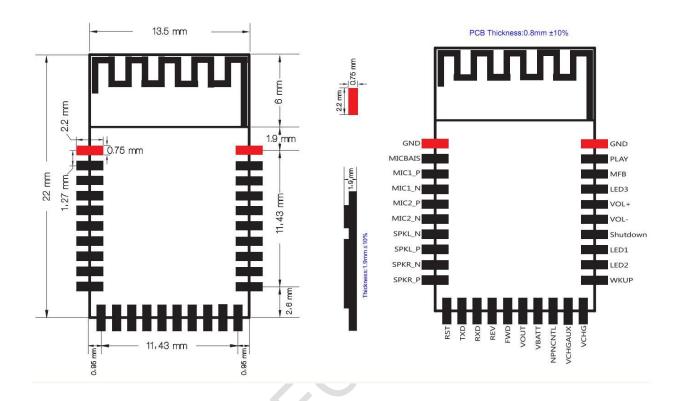


Figure 5: BT3GMD-A30P Module PCB Layout