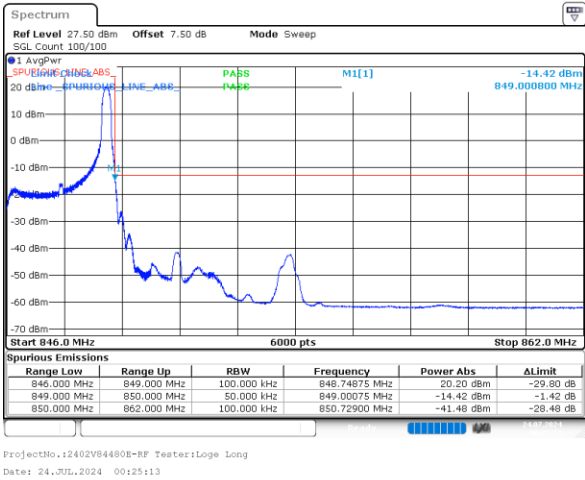
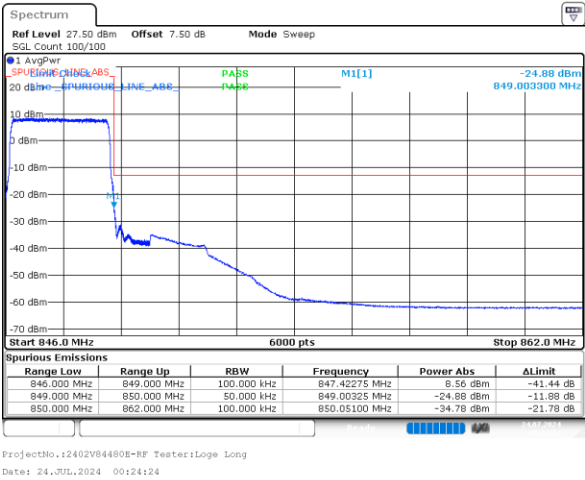


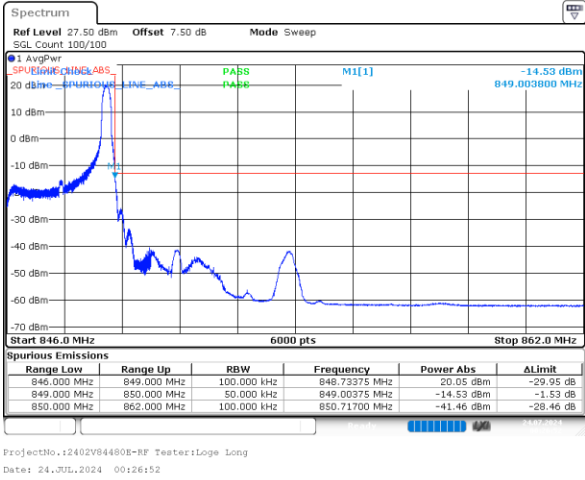
3MHz_High_QPSK_1@14



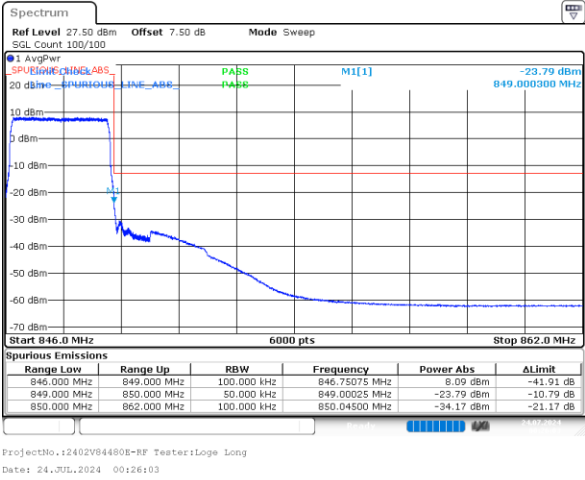
3MHz_High_QPSK_15@0



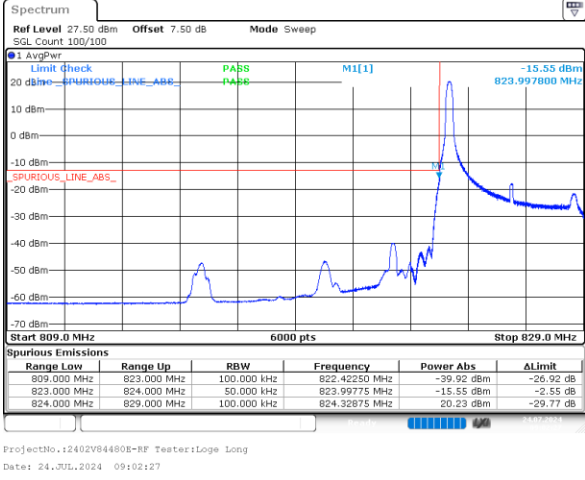
3MHz_High_16QAM_1@14



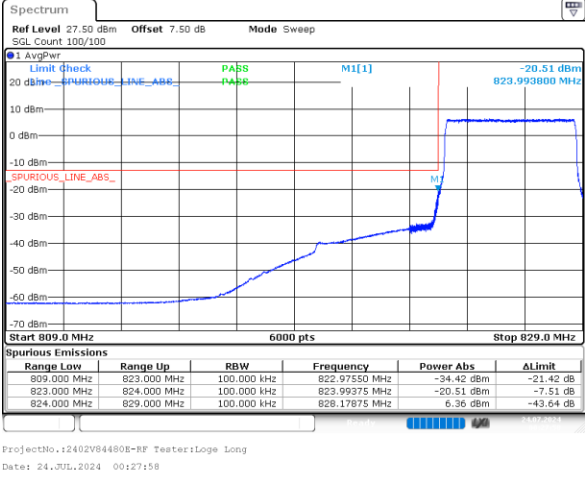
3MHz_High_16QAM_15@0



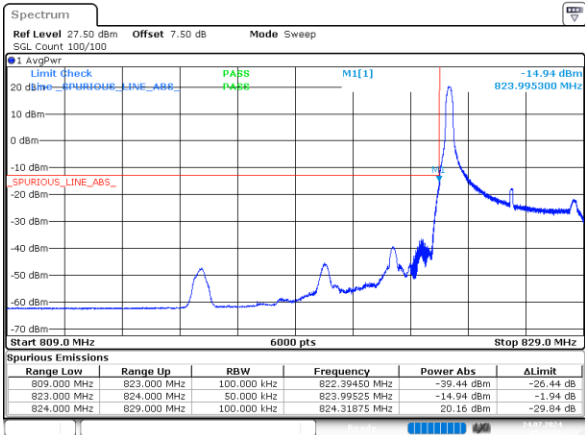
5MHz_Low_QPSK_1@0



5MHz_Low_QPSK_25@0

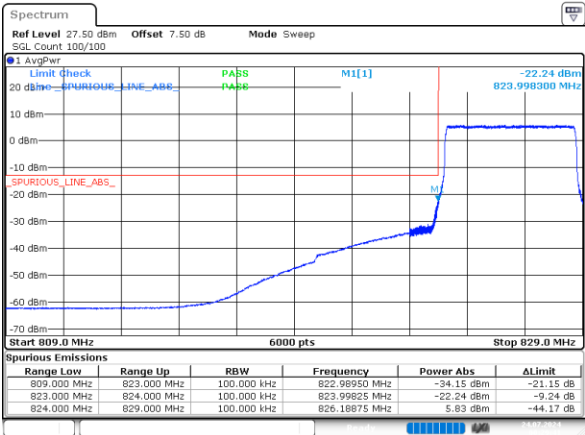


5MHz_Low_16QAM_1@0



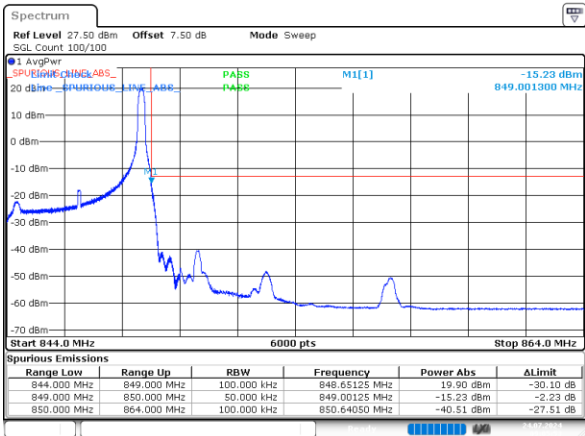
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 09:03:26

5MHz_Low_16QAM_25@0



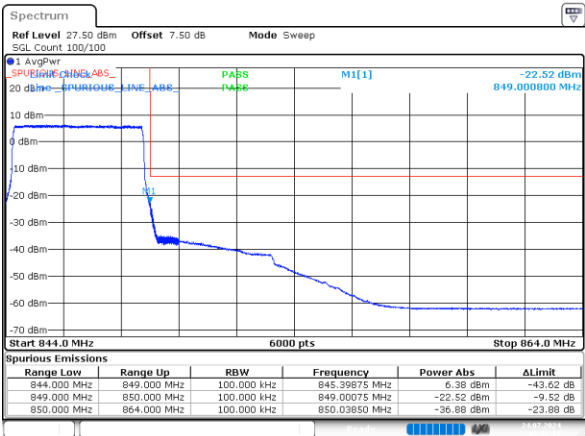
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 Date: 24.JUL.2024 00:29:41

5MHz_High_QPSK_1@24



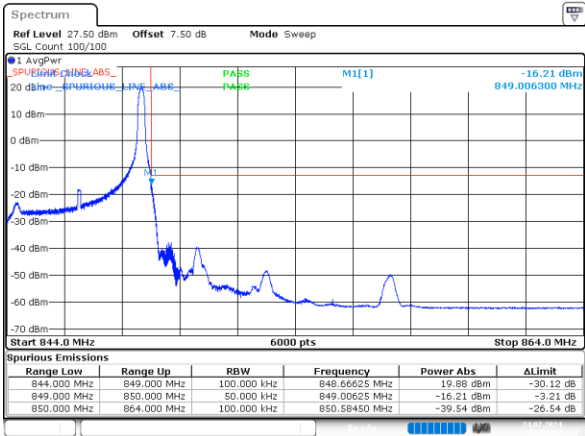
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 Date: 24.JUL.2024 09:05:53

5MHz_High_QPSK_25@0



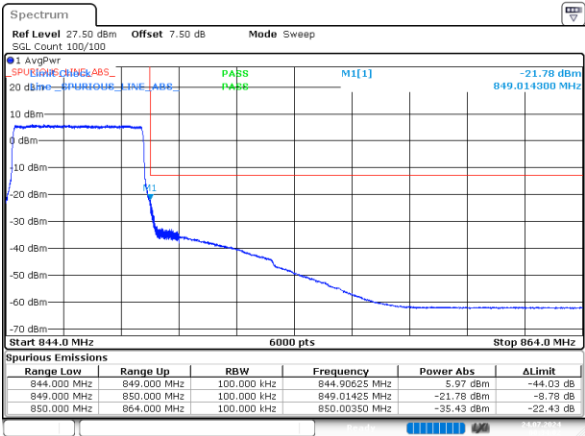
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 Date: 24.JUL.2024 00:32:17

5MHz_High_16QAM_1@24



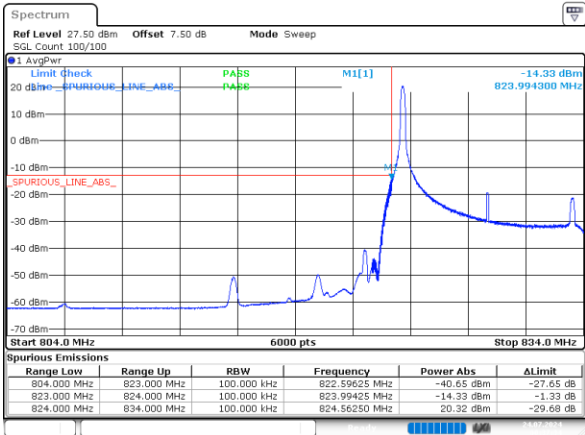
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5MHz_High_16QAM_25@0



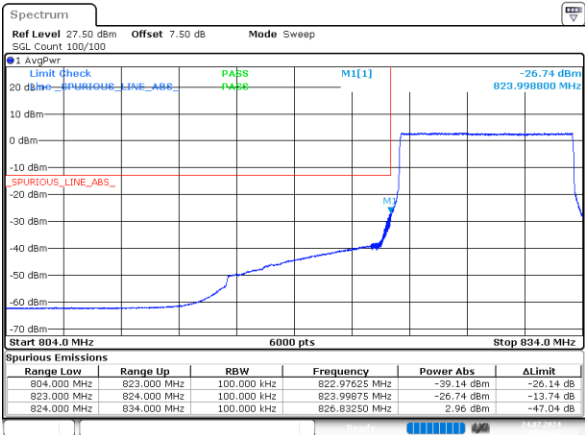
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 Date: 24.JUL.2024 00:34:01

10MHz_Low_QPSK_1@0



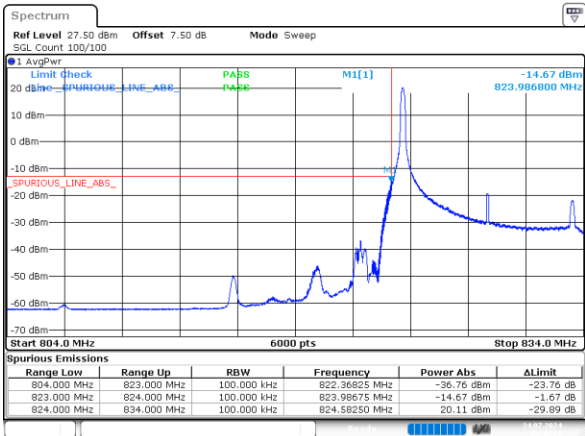
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 Date: 24.JUL.2024 00:37:34

10MHz_Low_QPSK_50@0



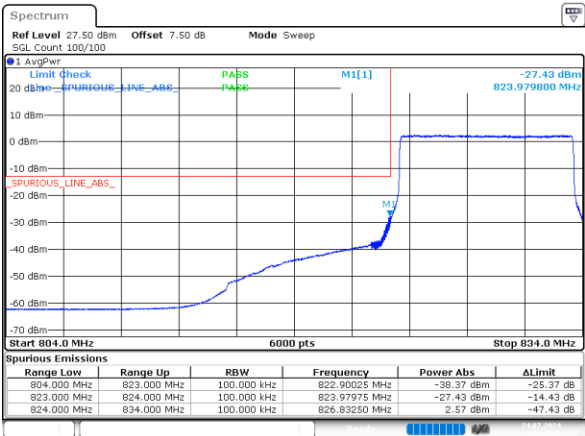
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 Date: 24.JUL.2024 00:36:21

10MHz_Low_16QAM_1@0



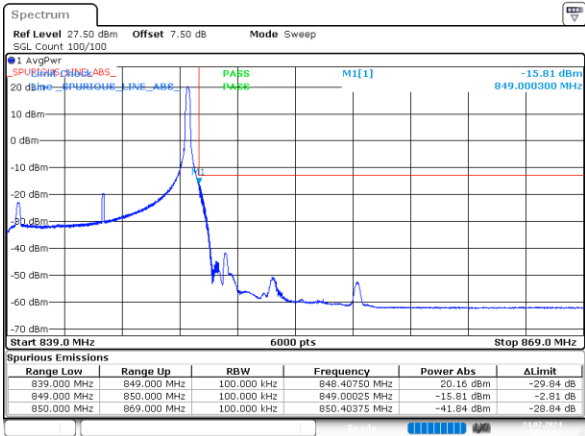
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10MHz_Low_16QAM_50@0



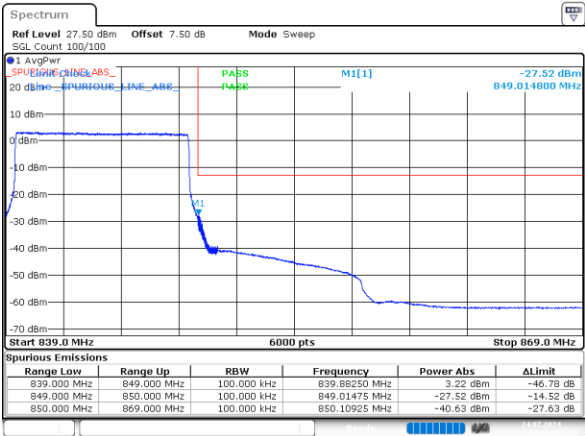
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 Date: 24.JUL.2024 00:38:48

10MHz_High_QPSK_1@49



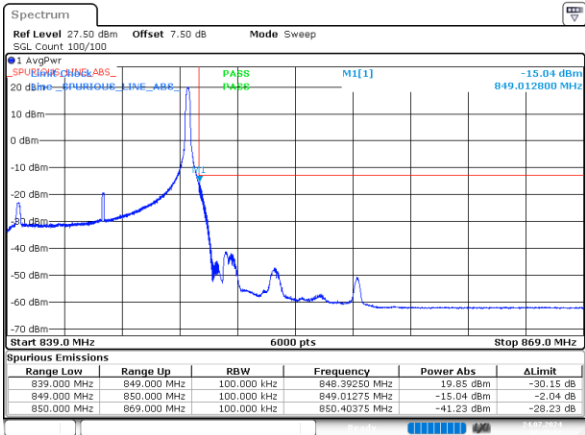
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10MHz_High_QPSK_50@0



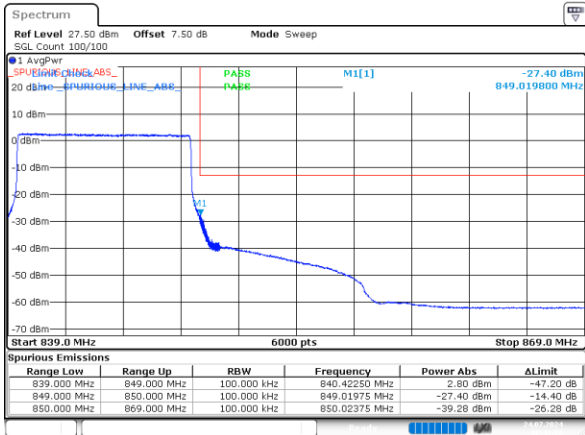
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:41:24

10MHz_High_16QAM_1@49



ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 00:45:02

10MHz_High_16QAM_50@0

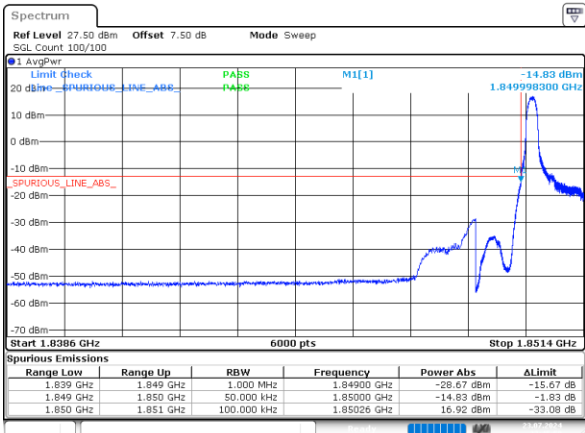


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Date: 24.JUL.2024 00:43:50

FCC Part 24E

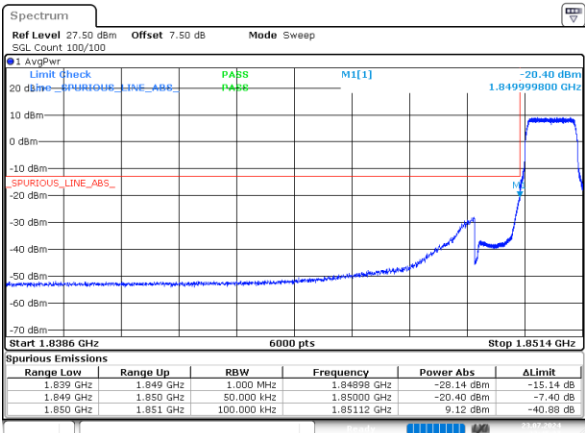
B2 , Normal

1.4MHz_Low_QPSK_1@0



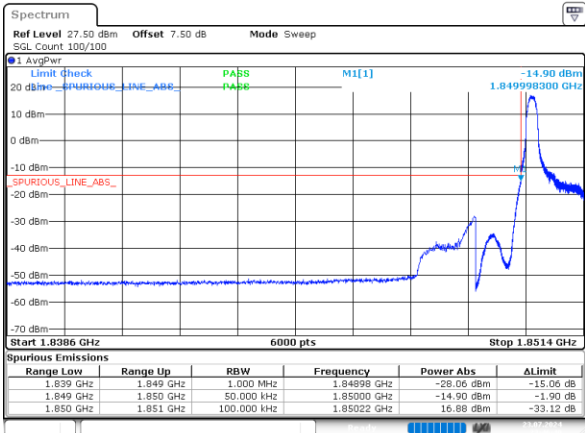
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 Date: 23.JUL.2024 23:45:58

1.4MHz_Low_QPSK_6@0



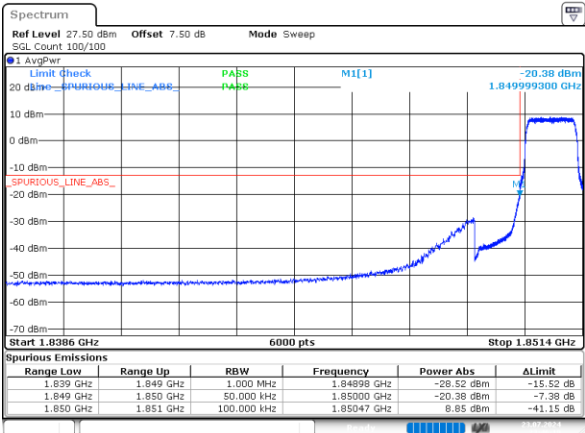
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 Date: 23.JUL.2024 23:45:17

1.4MHz_Low_16QAM_1@0



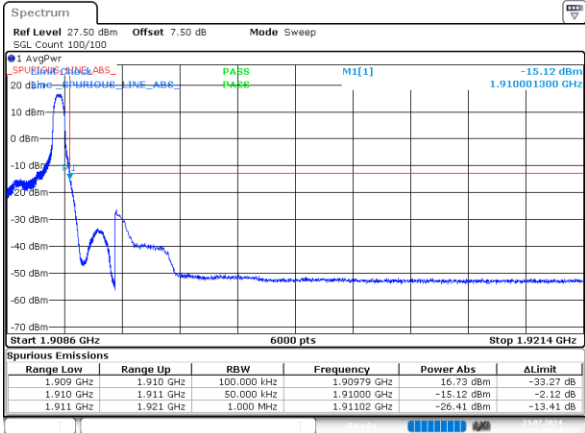
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 Date: 23.JUL.2024 23:46:40

1.4MHz_Low_16QAM_6@0



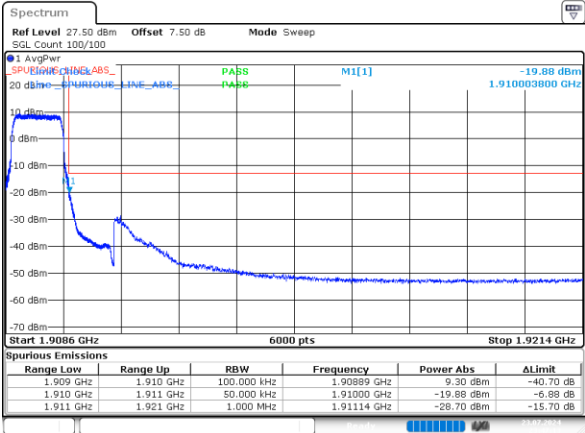
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 Date: 23.JUL.2024 23:46:19

1.4MHz_High_QPSK_1@5



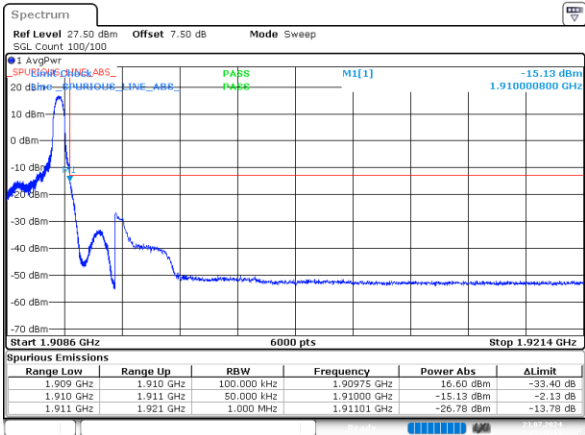
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 Date: 23.JUL.2024 23:47:34

1.4MHz_High_QPSK_6@0



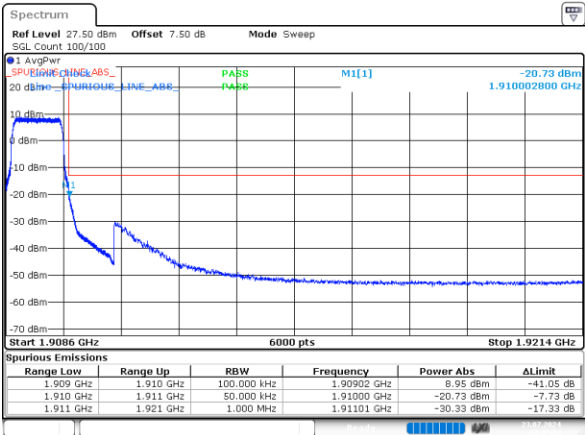
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 Date: 23.JUL.2024 23:47:13

1.4MHz_High_16QAM_1@5



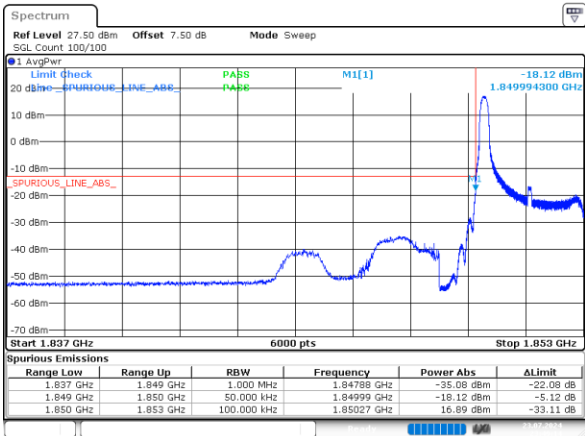
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Date: 23.JUL.2024 23:48:14

1.4MHz_High_16QAM_6@0



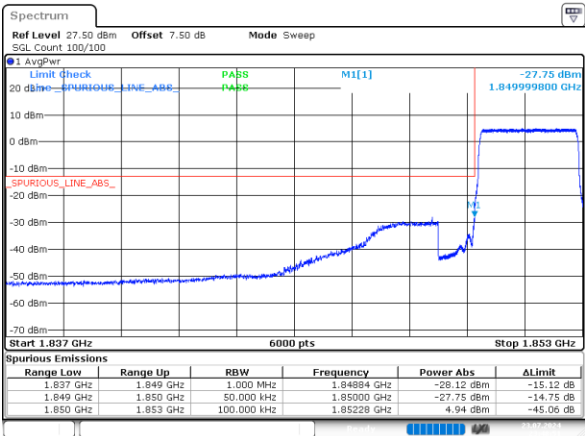
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Date: 23.JUL.2024 23:47:54

3MHz_Low_QPSK_1@0



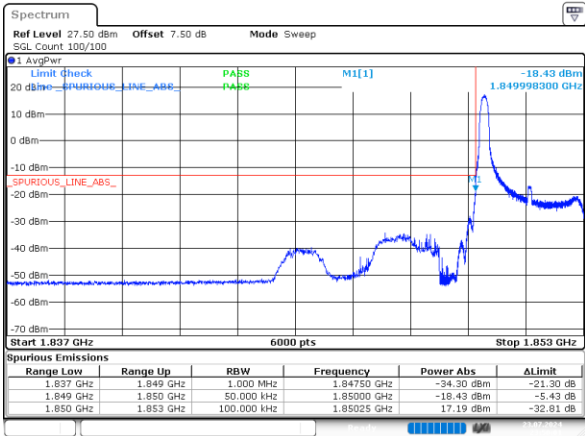
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Date: 23.JUL.2024 23:49:14

3MHz_Low_QPSK_15@0



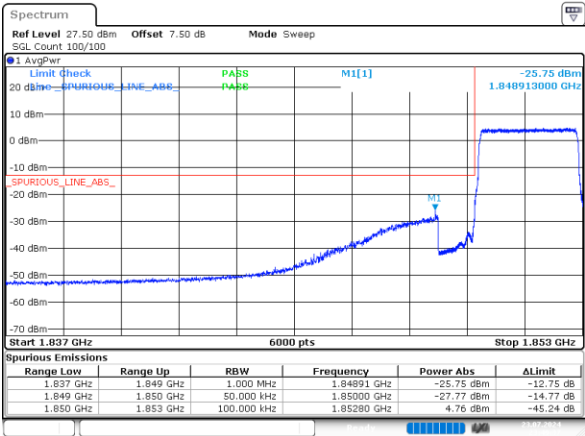
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Date: 23.JUL.2024 23:48:51

3MHz_Low_16QAM_1@0



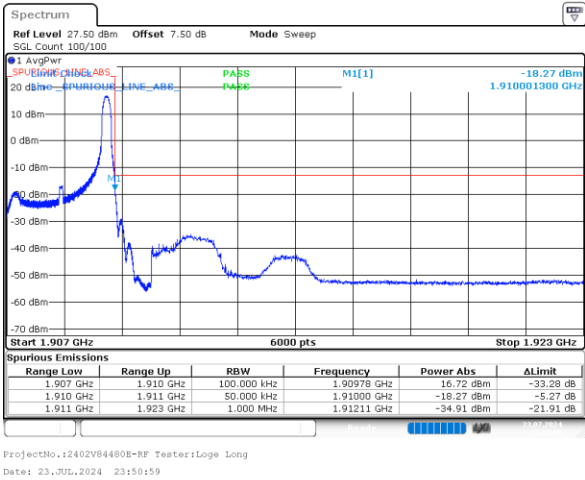
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Date: 23.JUL.2024 23:50:01

3MHz_Low_16QAM_15@0

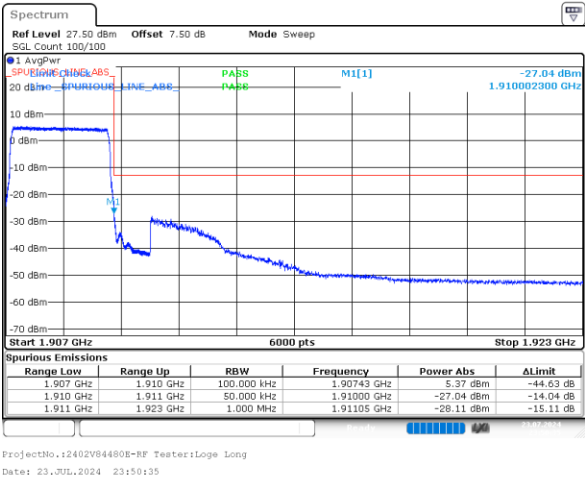


ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 23.JUL.2024 23:49:37

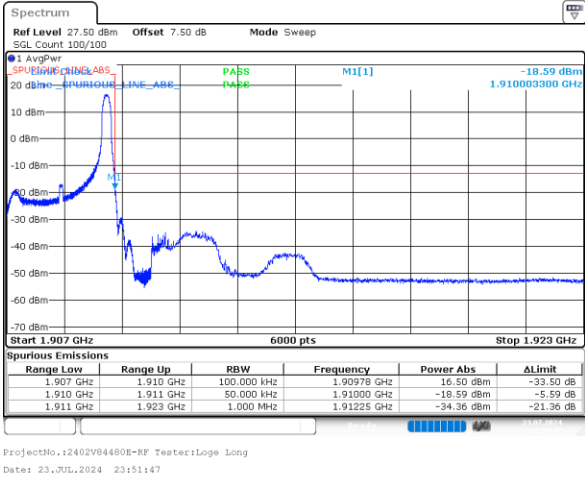
3MHz_High_QPSK_1@14



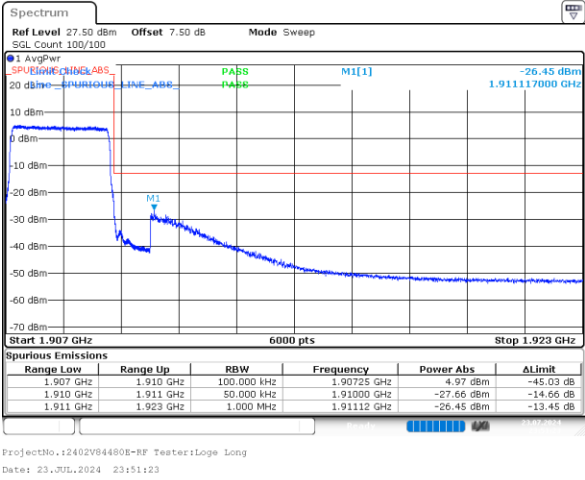
3MHz_High_QPSK_15@0



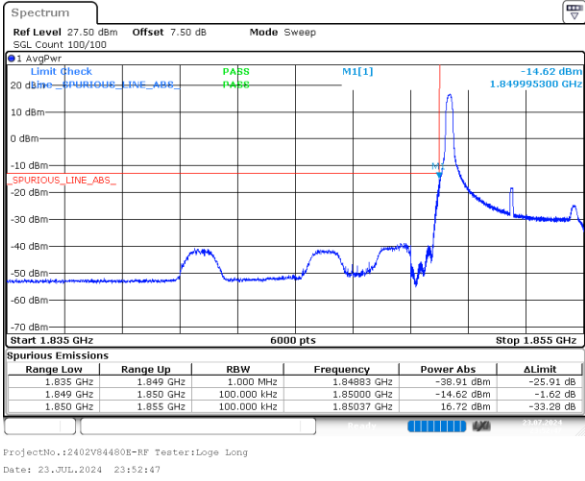
3MHz_High_16QAM_1@14



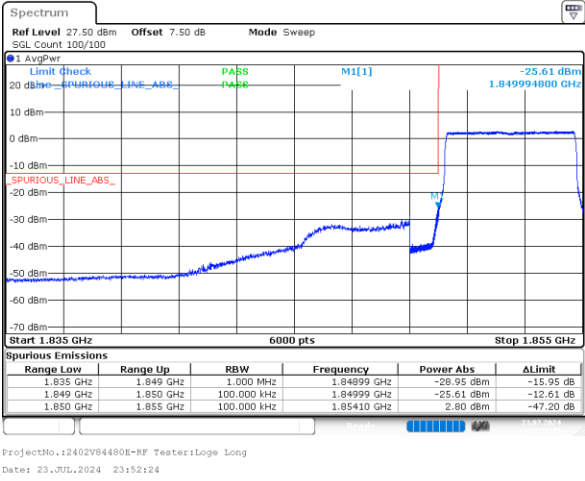
3MHz_High_16QAM_15@0



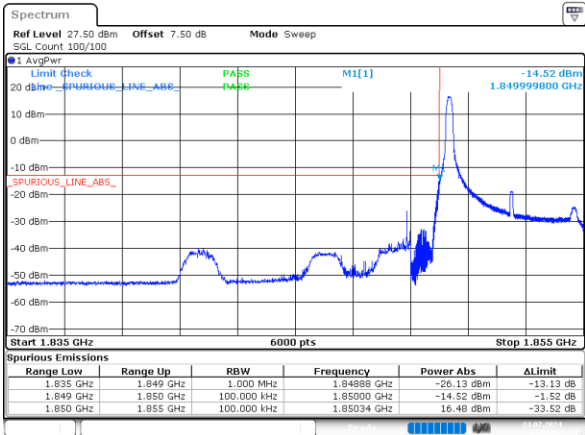
5MHz_Low_QPSK_1@0



5MHz_Low_QPSK_25@0

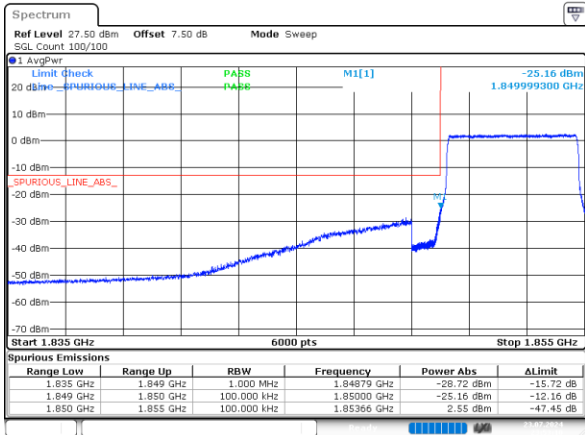


5MHz_Low_16QAM_1@0



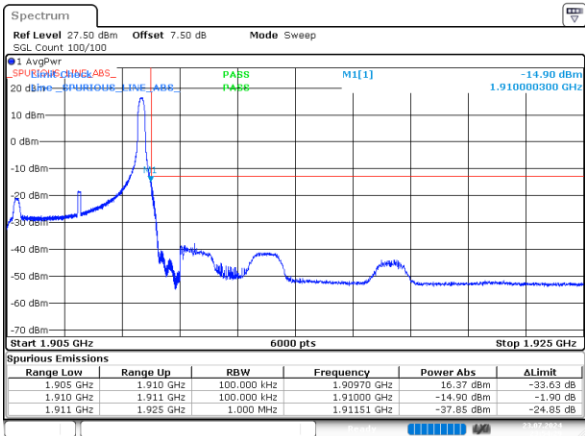
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Date: 23.JUL.2024 23:53:33

5MHz_Low_16QAM_25@0



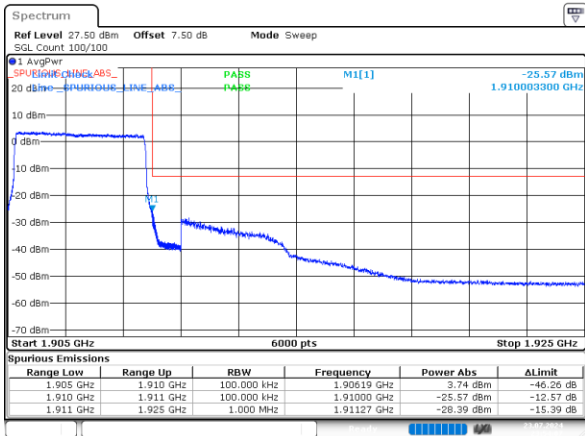
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Date: 23.JUL.2024 23:53:10

5MHz_High_QPSK_1@24



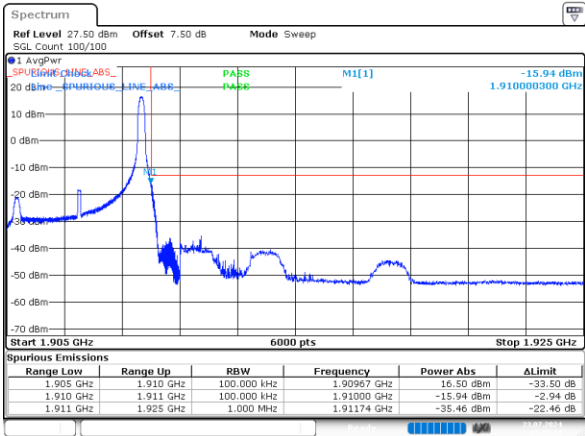
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 23.JUL.2024 23:54:29

5MHz_High_QPSK_25@0



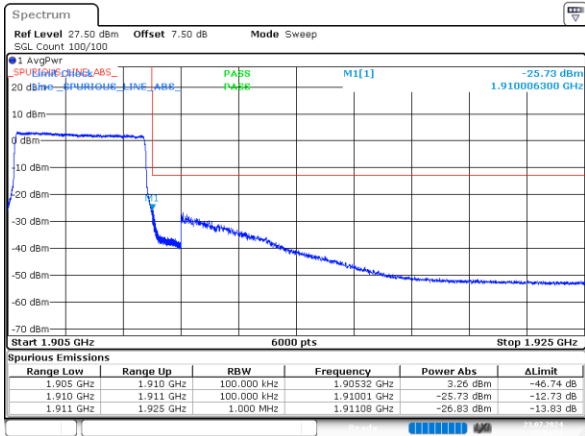
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Date: 23.JUL.2024 23:54:06

5MHz_High_16QAM_1@24



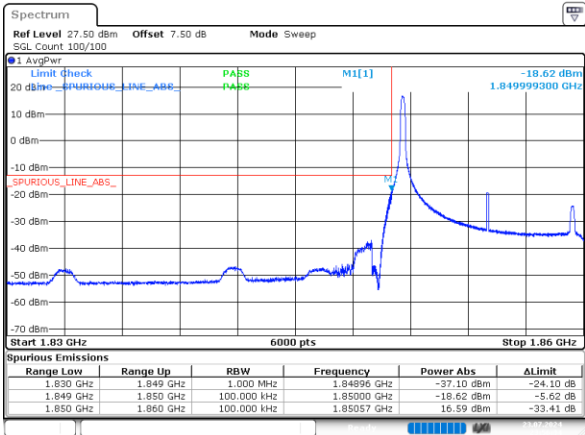
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Date: 23.JUL.2024 23:55:14

5MHz_High_16QAM_25@0



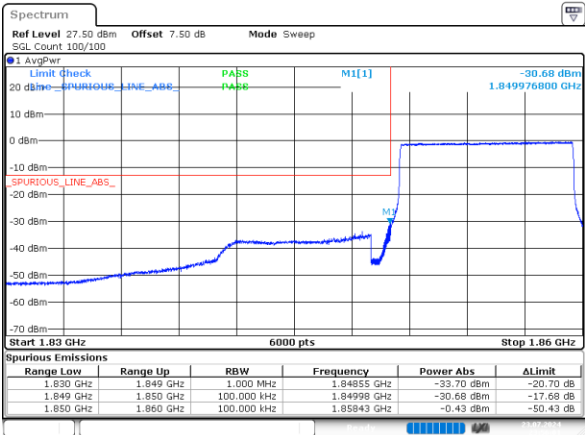
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Date: 23.JUL.2024 23:54:52

10MHz_Low_QPSK_1@0



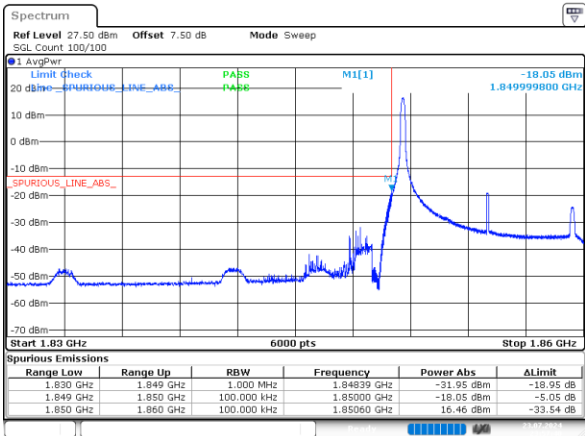
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 23.JUL.2024 23:56:34

10MHz_Low_QPSK_50@0



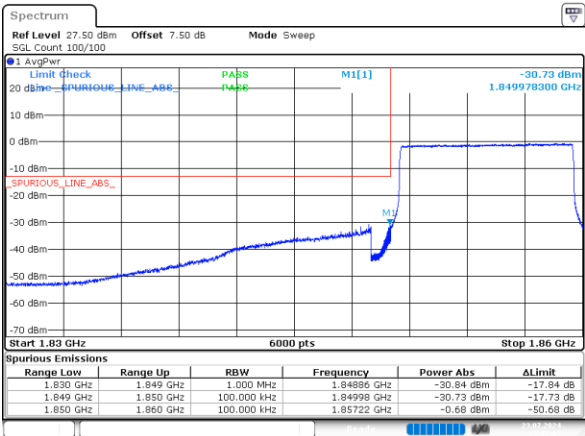
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 Date: 23.JUL.2024 23:56:01

10MHz_Low_16QAM_1@0



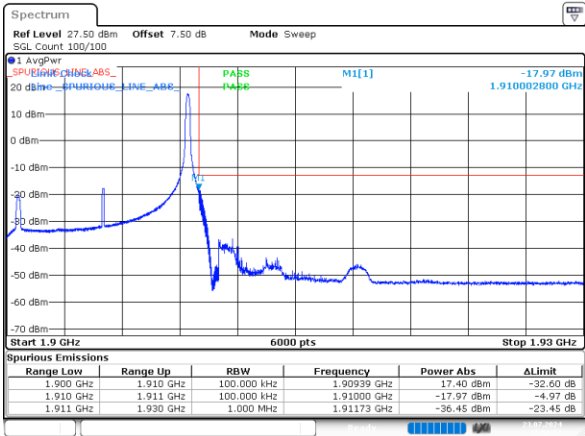
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 Date: 23.JUL.2024 23:57:40

10MHz_Low_16QAM_50@0



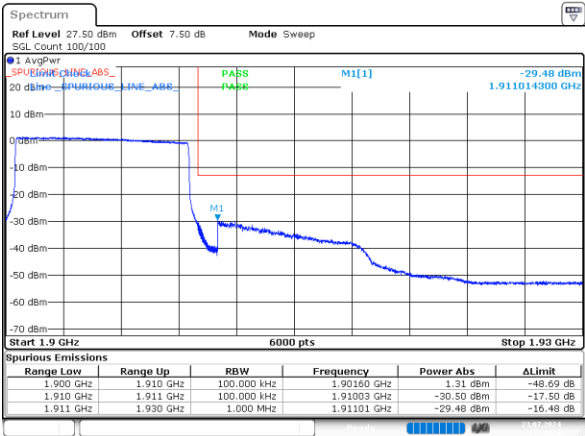
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 Date: 23.JUL.2024 23:57:07

10MHz_High_QPSK_1@49



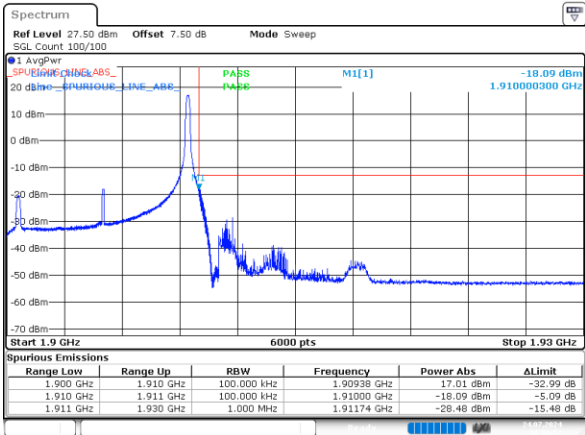
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 Date: 23.JUL.2024 23:58:57

10MHz_High_QPSK_50@0



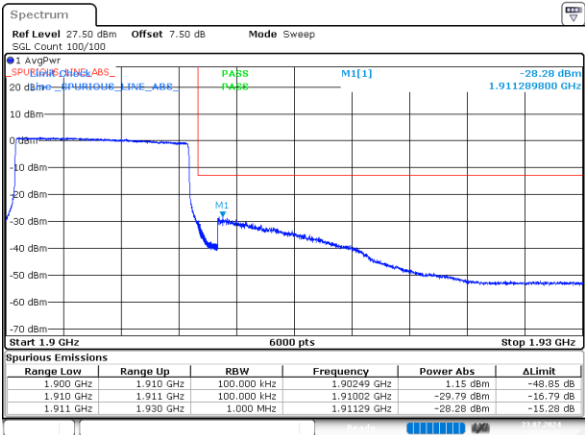
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 Date: 23.JUL.2024 23:58:24

10MHz_High_16QAM_1@49



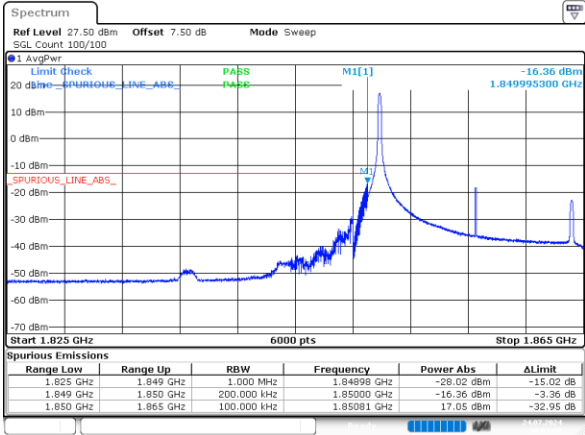
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:00:02

10MHz_High_16QAM_50@0



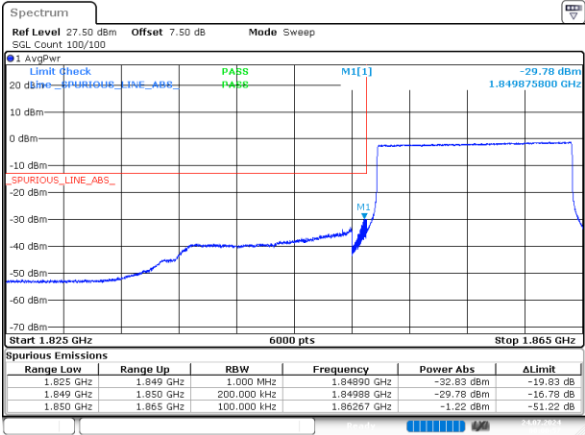
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15MHz_Low_QPSK_1@0



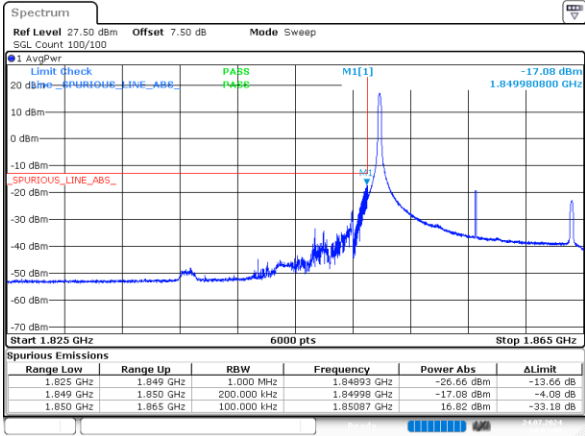
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:01:37

15MHz_Low_QPSK_75@0



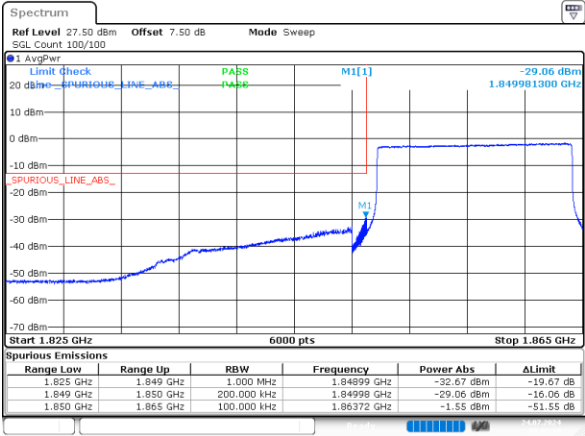
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15MHz_Low_16QAM_1@0



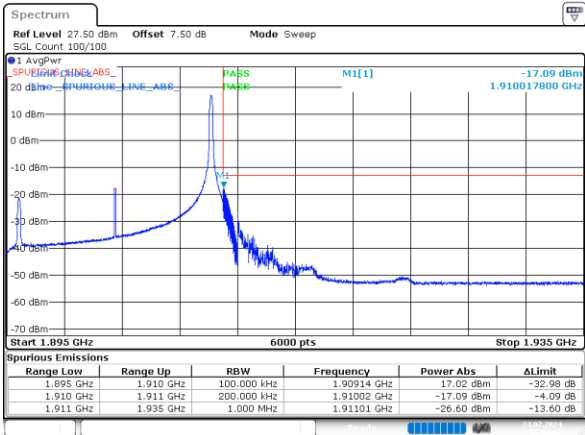
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:03:00

15MHz_Low_16QAM_75@0



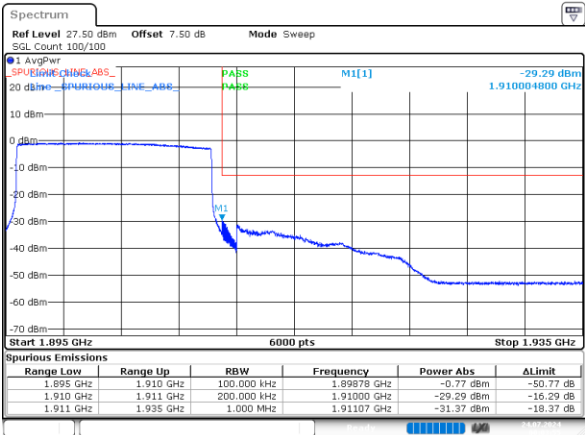
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 Date: 24.JUL.2024 00:02:18

15MHz_High_QPSK_1@74



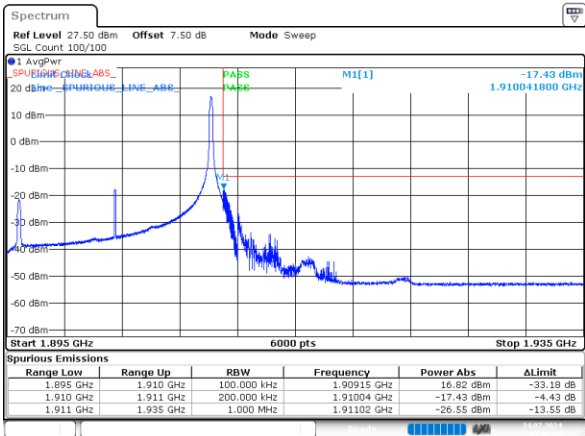
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 Date: 24.JUL.2024 00:04:37

15MHz_High_QPSK_75@0



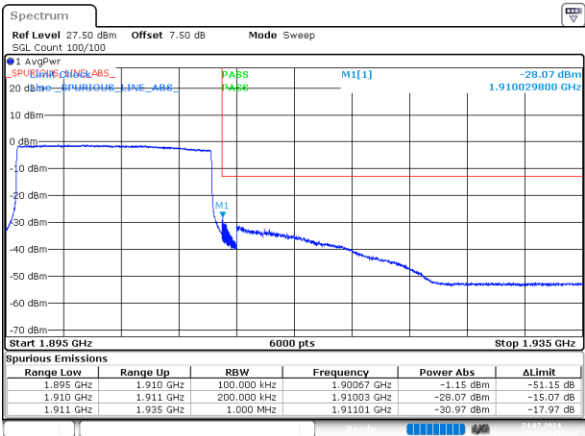
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15MHz_High_16QAM_1@74



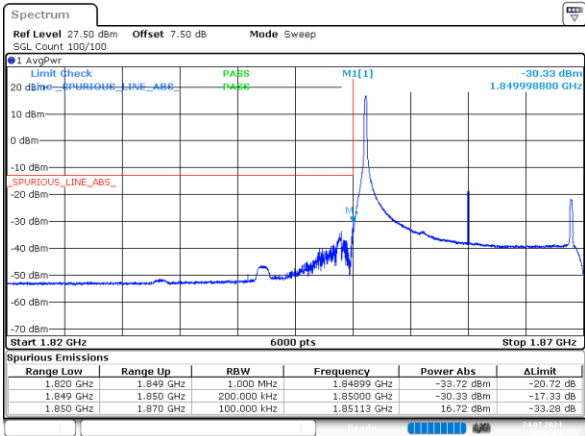
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15MHz_High_16QAM_75@0



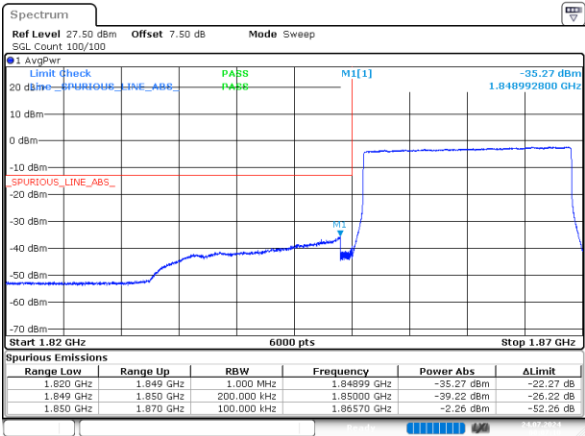
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:05:19

20MHz_Low_QPSK_1@0



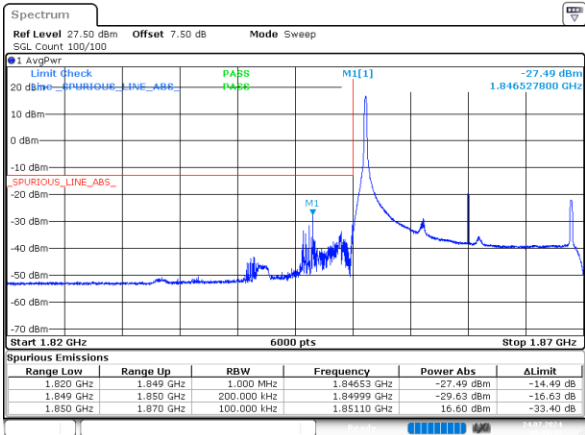
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 Date: 24.JUL.2024 00:08:03

20MHz_Low_QPSK_100@0



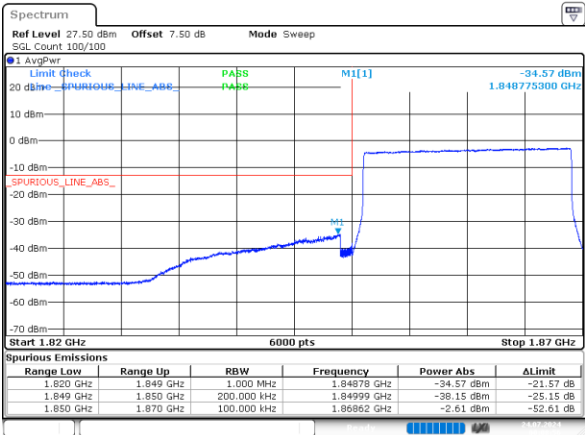
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 Date: 24.JUL.2024 00:07:10

20MHz_Low_16QAM_1@0



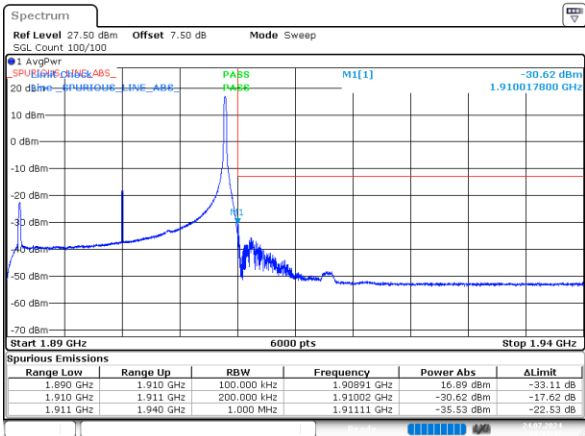
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:09:49

20MHz_Low_16QAM_100@0



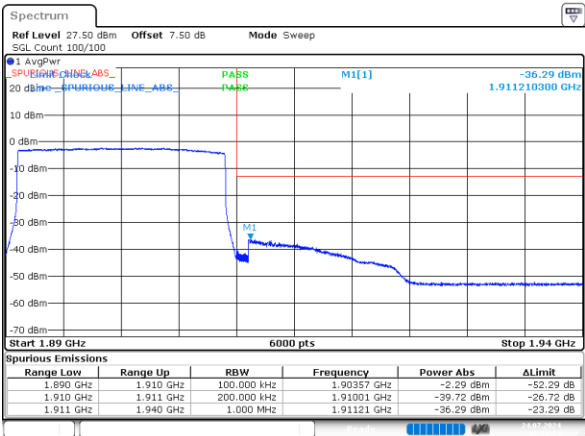
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:08:55

20MHz_High_QPSK_1@99



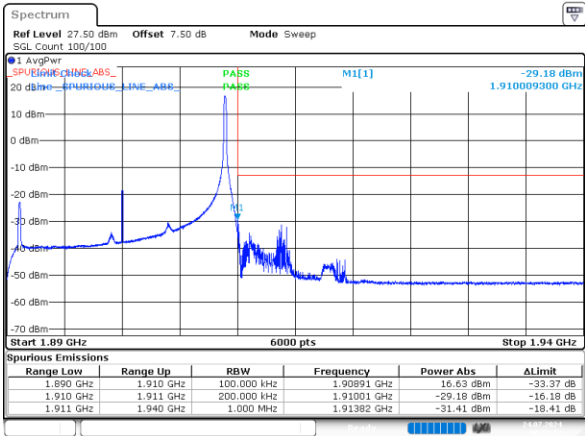
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:11:47

20MHz_High_QPSK_100@0



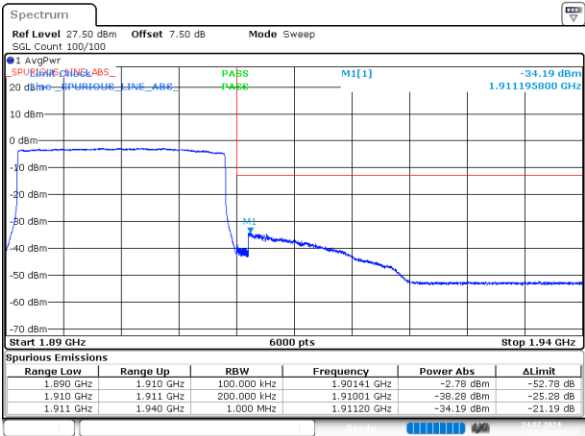
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:10:54

20MHz_High_16QAM_1@99



ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:13:32

20MHz_High_16QAM_100@0

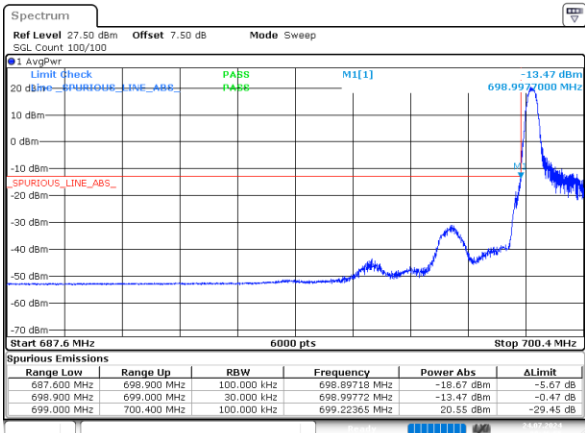


ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:12:39

FCC Part 27

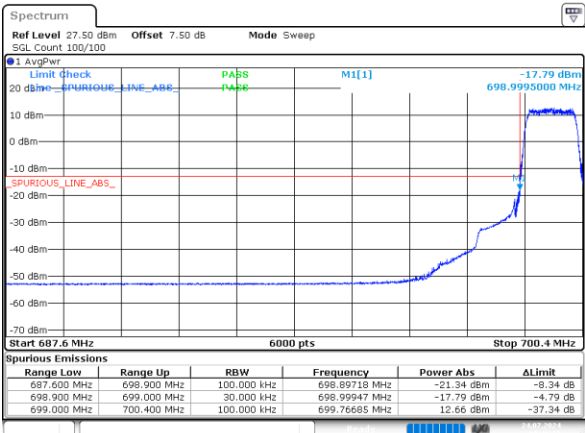
B12 , Normal

1.4MHz_Low_QPSK_1@0



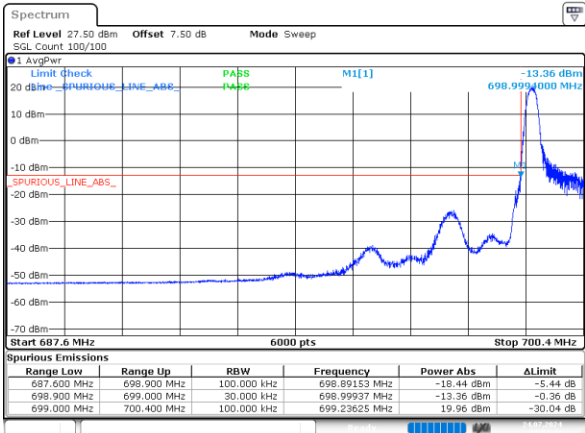
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 Date: 24.JUL.2024 00:46:55

1.4MHz_Low_QPSK_6@0



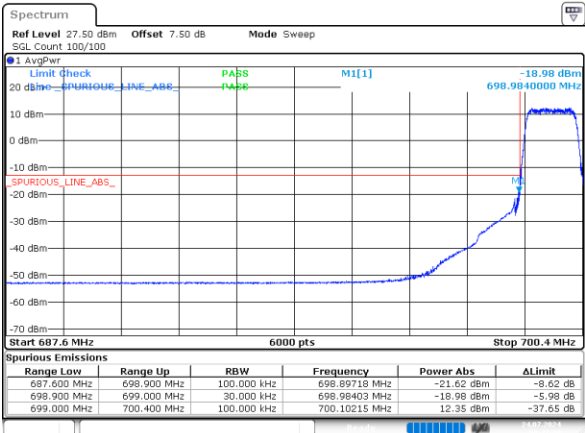
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1.4MHz_Low_16QAM_1@0



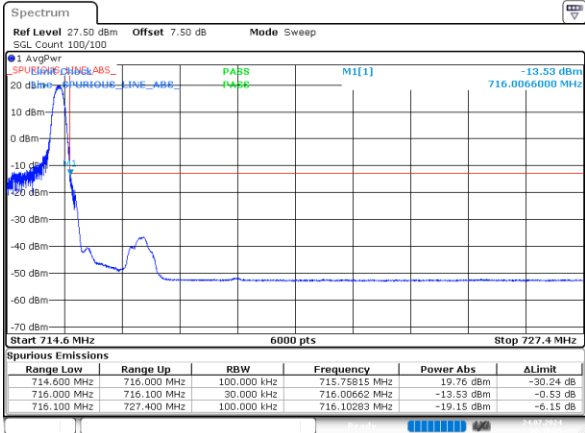
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 Date: 24.JUL.2024 09:07:42

1.4MHz_Low_16QAM_6@0



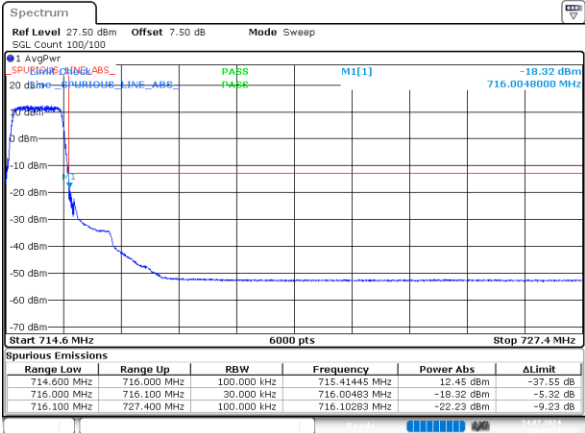
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 Date: 24.JUL.2024 00:47:29

1.4MHz_High_QPSK_1@5



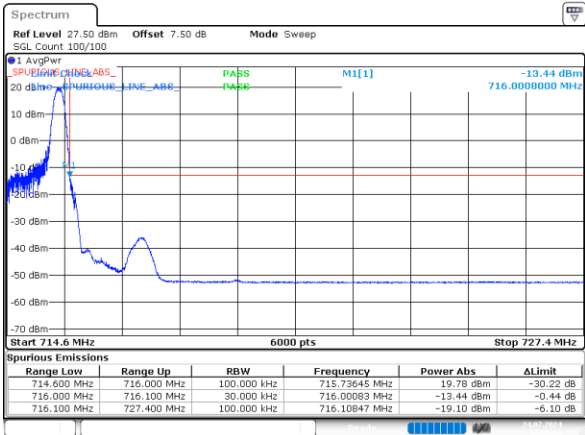
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 24.JUL.2024 00:49:25

1.4MHz_High_QPSK_6@0



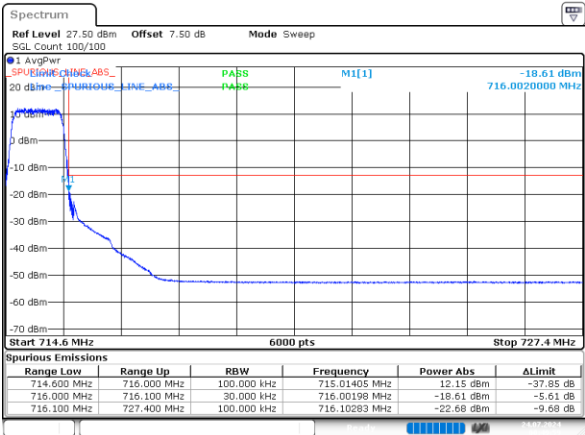
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 Date: 24.JUL.2024 00:48:51

1.4MHz_High_16QAM_1@5



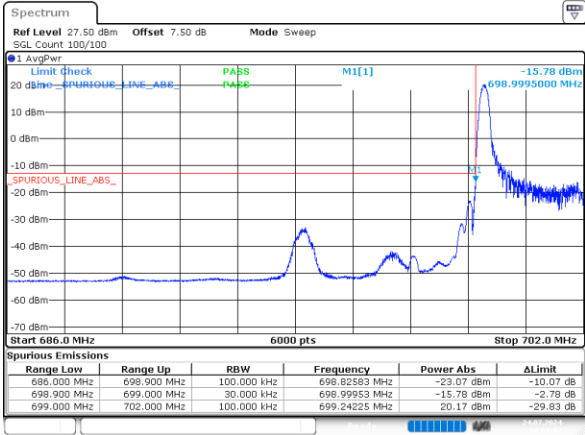
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Date: 24.JUL.2024 00:50:33

1.4MHz_High_16QAM_6@0



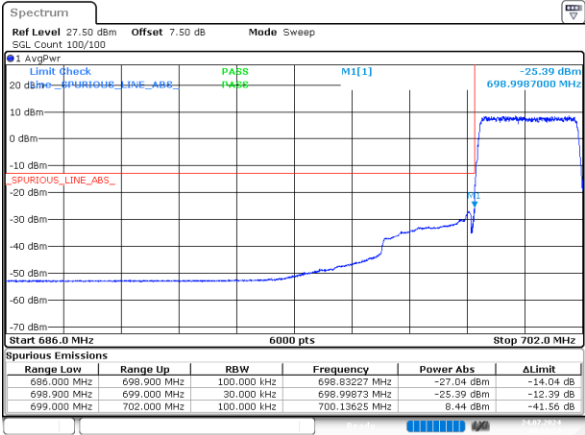
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Date: 24.JUL.2024 00:49:59

3MHz_Low_QPSK_1@0



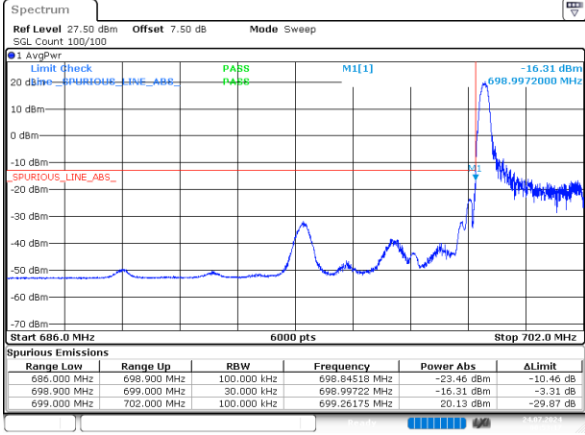
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 00:52:02

3MHz_Low_QPSK_15@0



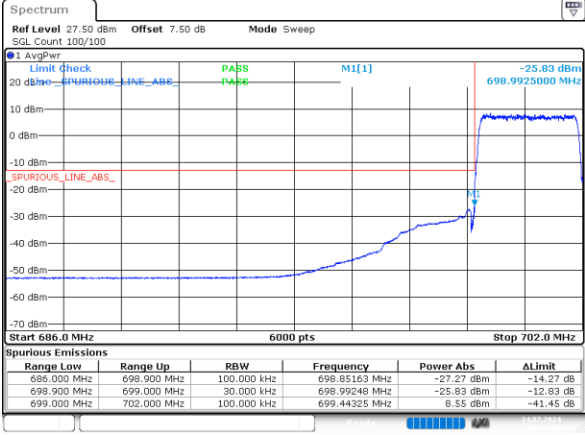
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Date: 24.JUL.2024 00:51:24

3MHz_Low_16QAM_1@0



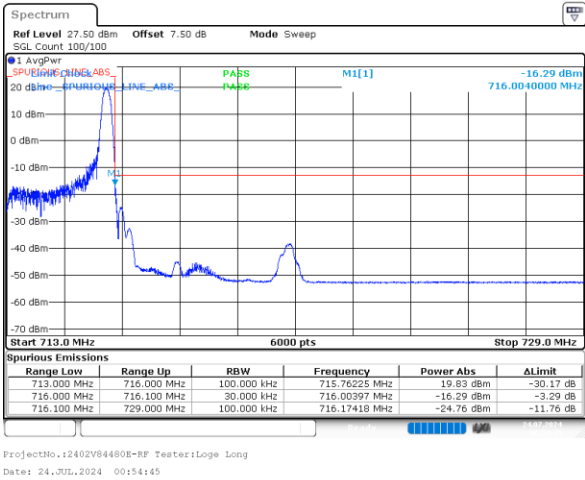
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Date: 24.JUL.2024 00:53:17

3MHz_Low_16QAM_15@0

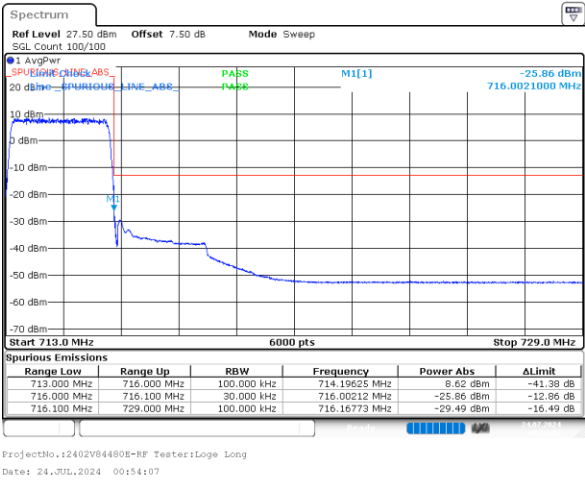


ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 00:52:39

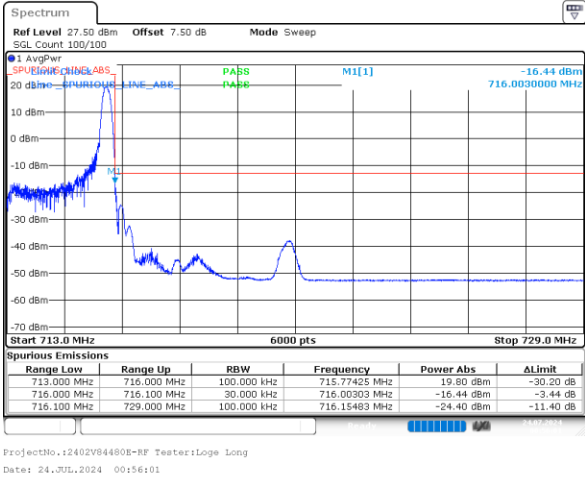
3MHz_High_QPSK_1@14



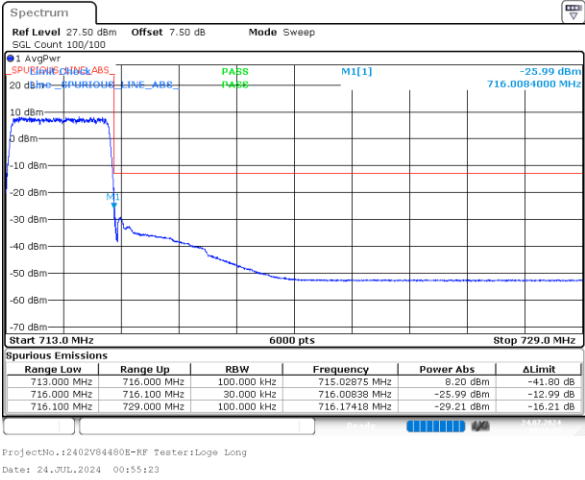
3MHz_High_QPSK_15@0



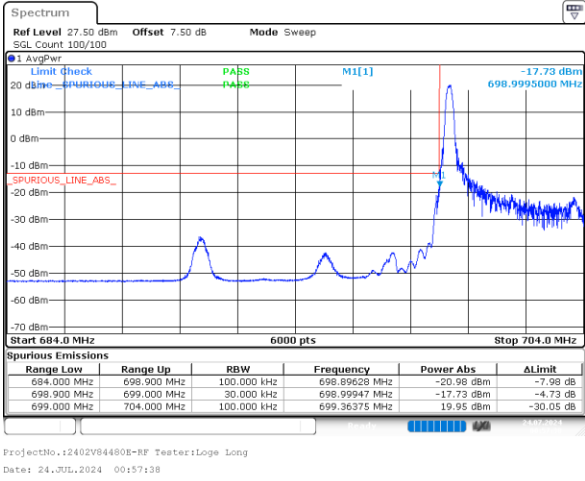
3MHz_High_16QAM_1@14



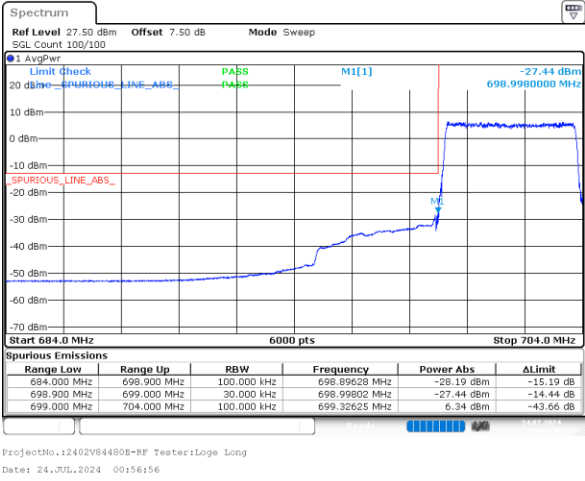
3MHz_High_16QAM_15@0



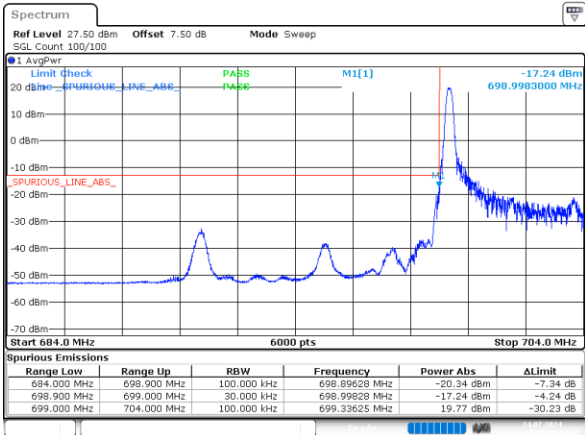
5MHz_Low_QPSK_1@0



5MHz_Low_QPSK_25@0

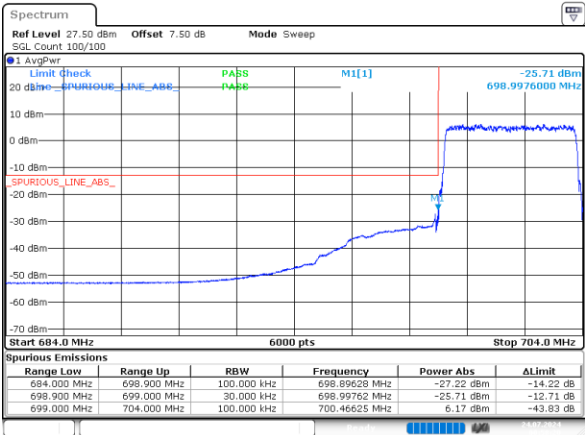


5MHz_Low_16QAM_1@0



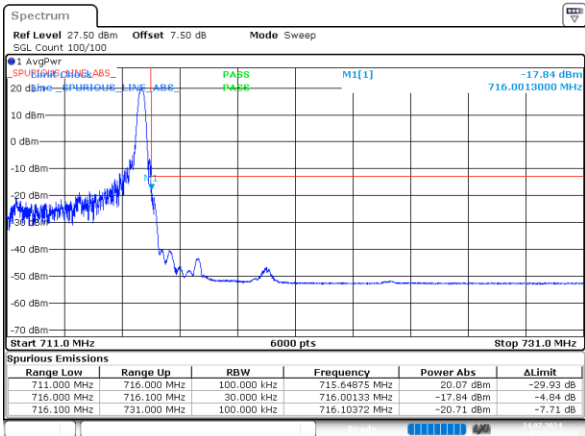
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Date: 24.JUL.2024 00:59:02

5MHz_Low_16QAM_25@0



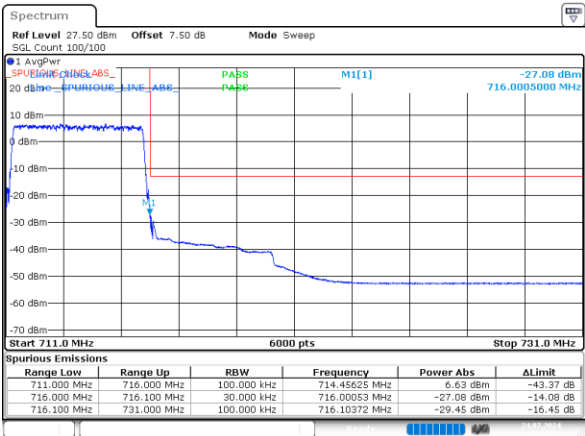
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Date: 24.JUL.2024 00:58:20

5MHz_High_QPSK_1@24



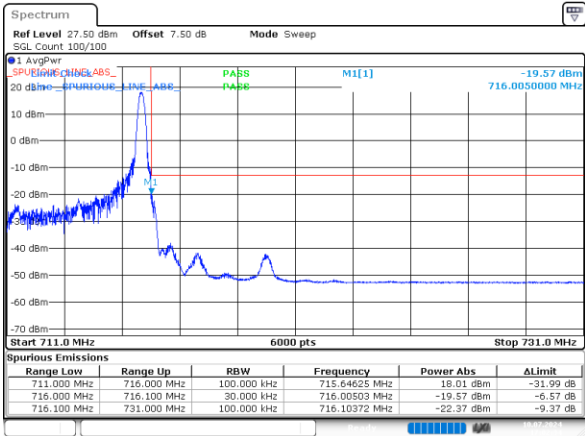
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 01:00:39

5MHz_High_QPSK_25@0



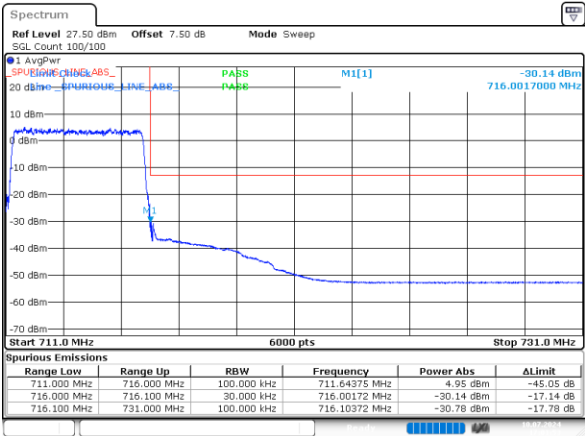
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Date: 24.JUL.2024 00:59:57

5MHz_High_16QAM_1@24



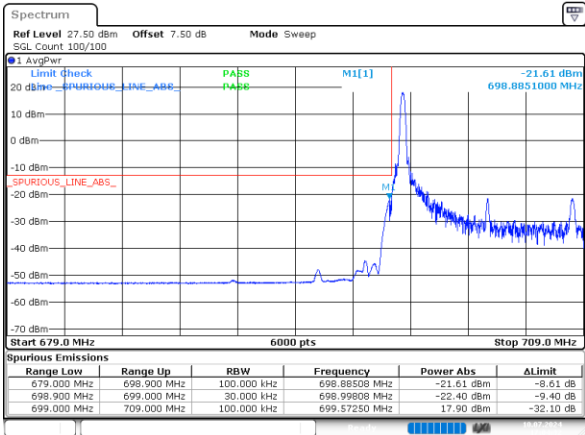
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Date: 10.JUL.2024 17:02:34

5MHz_High_16QAM_25@0



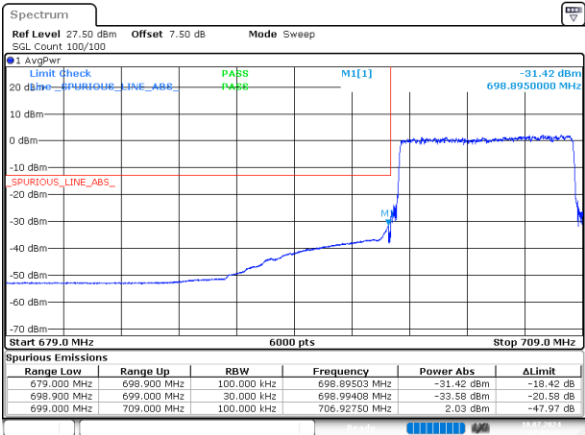
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Date: 10.JUL.2024 17:01:53

10MHz_Low_QPSK_1@0



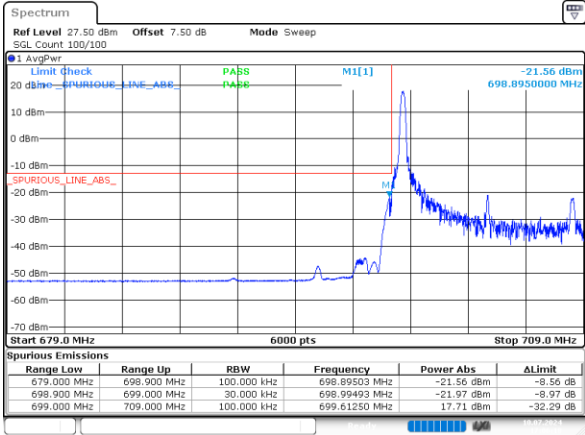
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 10.JUL.2024 17:04:32

10MHz_Low_QPSK_50@0



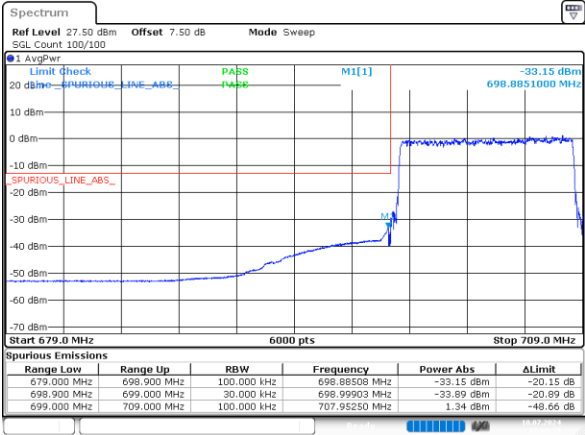
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Date: 10.JUL.2024 17:03:39

10MHz_Low_16QAM_1@0



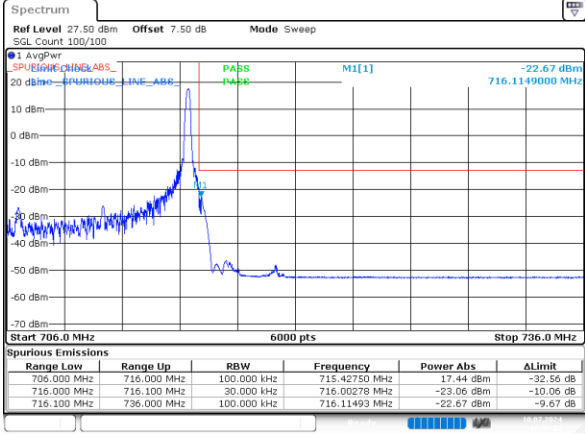
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Date: 10.JUL.2024 17:06:17

10MHz_Low_16QAM_50@0



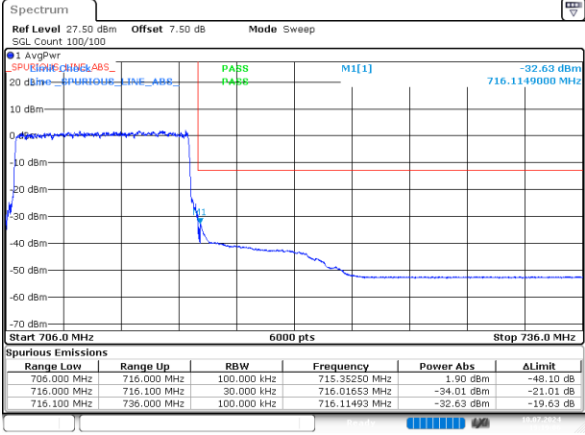
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Date: 10.JUL.2024 17:05:25

10MHz_High_QPSK_1@49



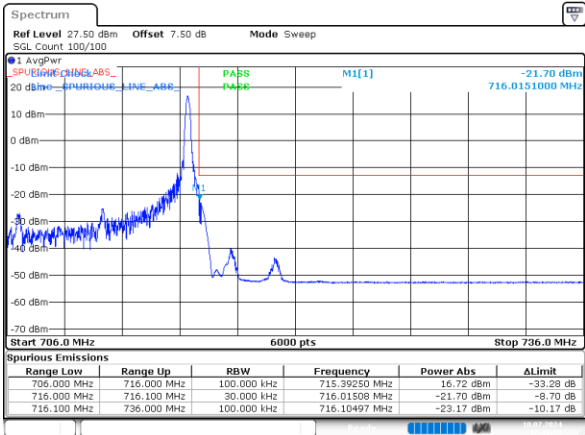
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Date: 10.JUL.2024 17:08:11

10MHz_High_QPSK_50@0



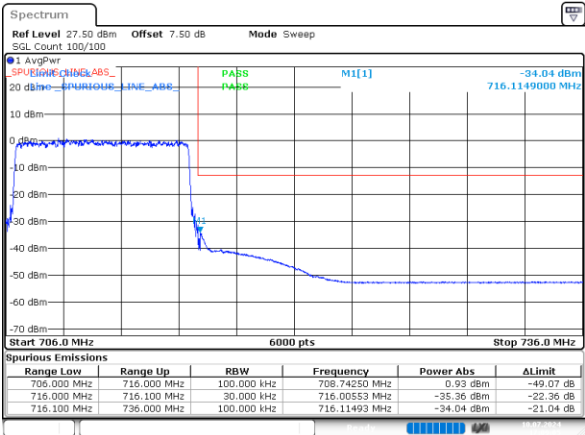
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Date: 10.JUL.2024 19:19:06

10MHz_High_16QAM_1@49



ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 10.JUL.2024 17:09:58

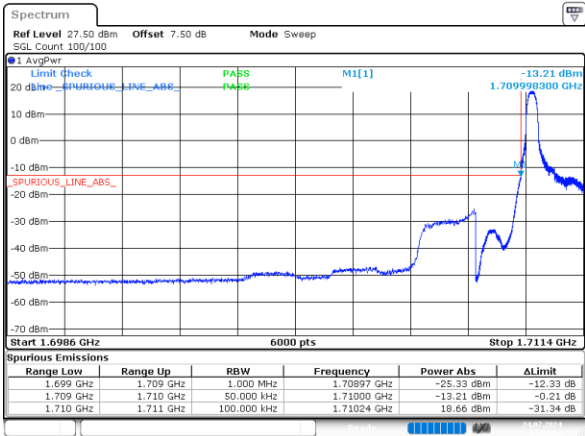
10MHz_High_16QAM_50@0



ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 10.JUL.2024 17:09:05

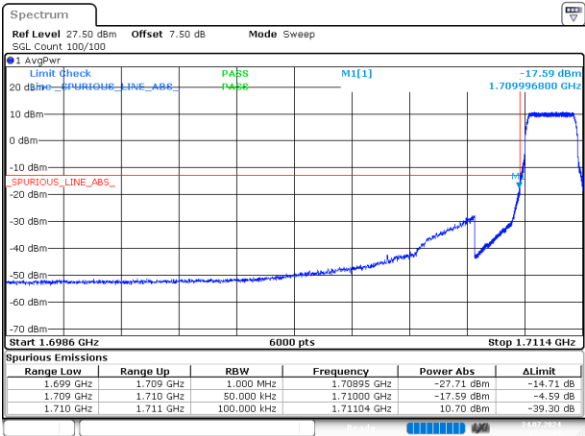
B66 , Normal

1.4MHz_Low_QPSK_1@0



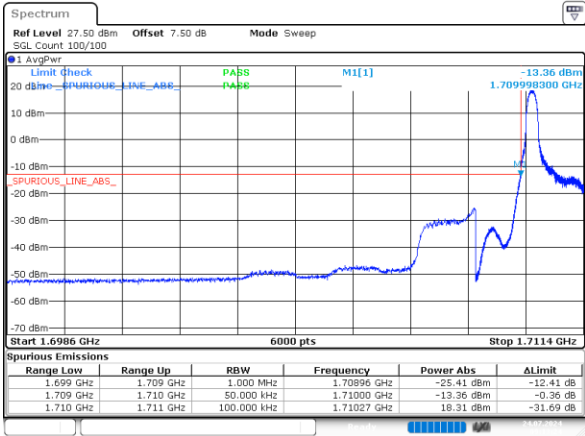
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Date: 24.JUL.2024 09:30:04

1.4MHz_Low_QPSK_6@0



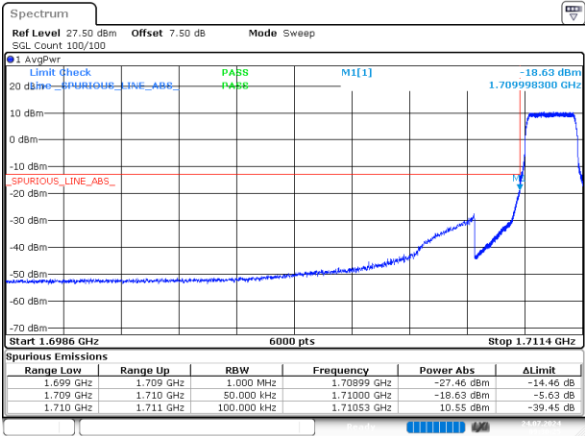
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Date: 24.JUL.2024 09:29:44

1.4MHz_Low_16QAM_1@0



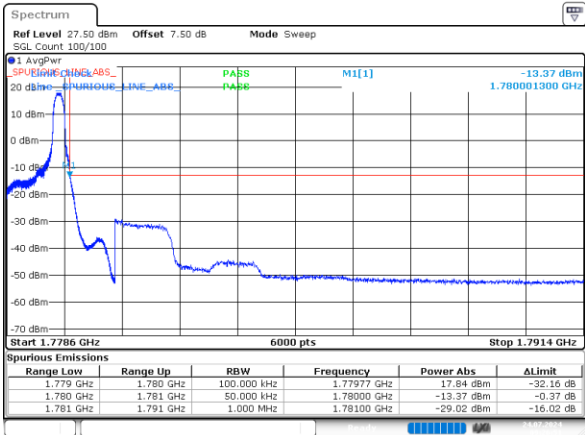
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 09:13:24

1.4MHz_Low_16QAM_6@0



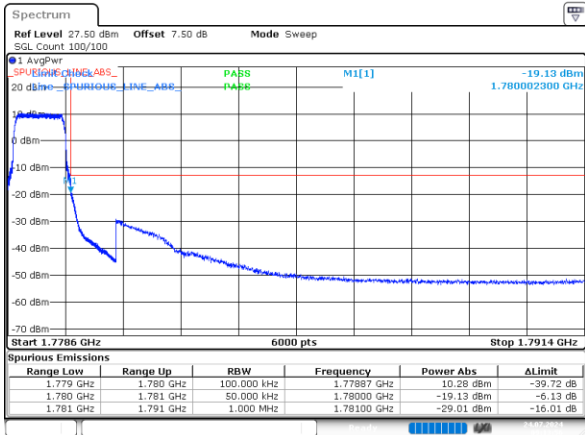
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Date: 24.JUL.2024 09:10:25

1.4MHz_High_QPSK_1@5



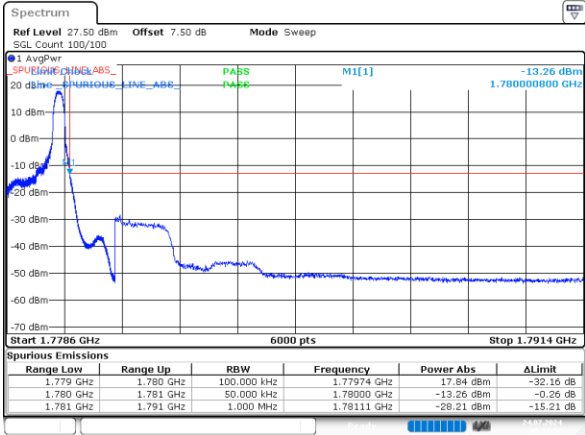
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Date: 24.JUL.2024 09:26:51

1.4MHz_High_QPSK_6@0



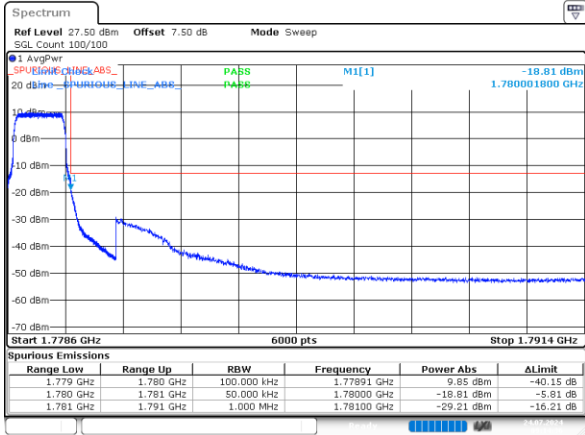
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Date: 24.JUL.2024 09:13:55

1.4MHz_High_16QAM_1@5



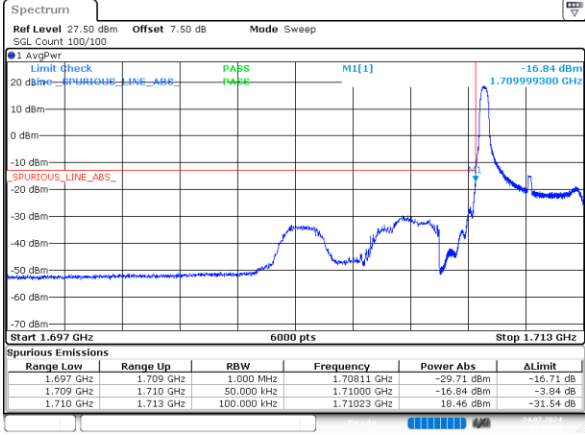
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Date: 24.JUL.2024 09:14:57

1.4MHz_High_16QAM_6@0



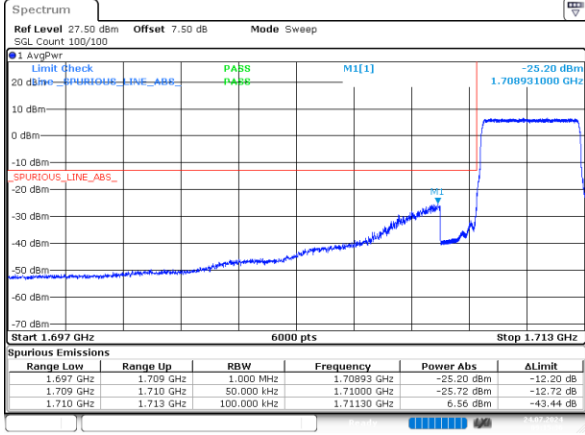
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Date: 24.JUL.2024 09:14:36

3MHz_Low_QPSK_1@0



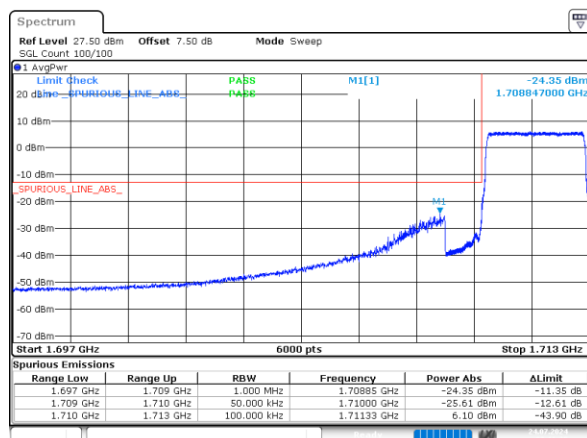
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Date: 24.JUL.2024 09:16:00

3MHz_Low_QPSK_15@0



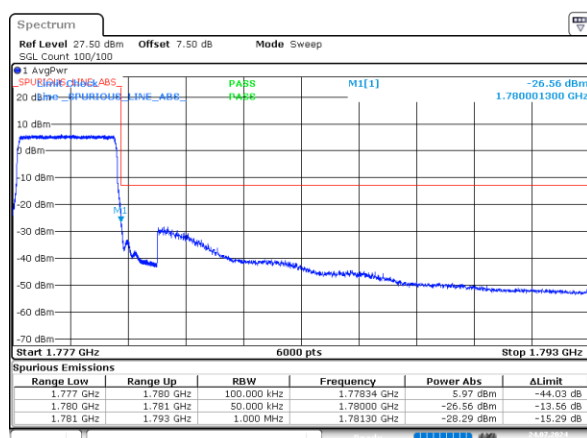
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Date: 24.JUL.2024 09:15:36

3MHz_Low_16QAM_15@0



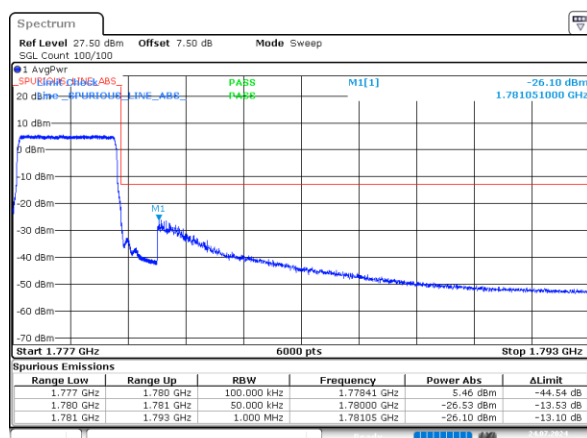
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 09:16:25

3MHz_High_QPSK_15@0



ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24.JUL.2024 09:17:26

3MHz_High_16QAM_15@0



ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 24..JUL..2024 09:18:16