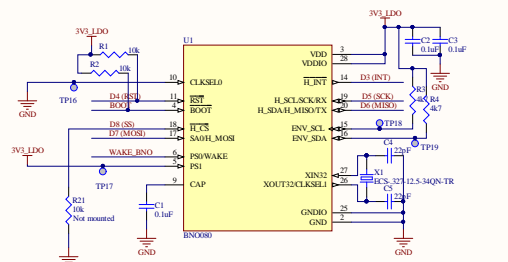


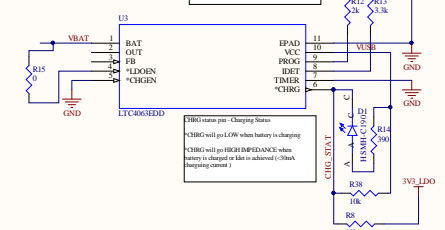
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EN	8	1	GPIO4	ADCL_CH4	ITC
EN	8	1	GPIO5	ADCL_CH5	ITC
EN	8	1	GPIO6	ADCL_CH6	ITC
EN	8	1	GPIO7	ADCL_CH7	ITC
EN	8	1	GPIO8	ADCL_CH8	ITC
EN	8	1	GPIO9	ADCL_CH9	ITC
EN	8	1	GPIO10	ADCL_CH10	ITC
EN	8	1	GPIO11	ADCL_CH11	ITC
EN	8	1	GPIO12	ADCL_CH12	ITC
EN	8	1	GPIO13	ADCL_CH13	ITC
EN	8	1	GPIO14	ADCL_CH14	ITC
EN	8	1	GPIO15	ADCL_CH15	ITC
EN	8	1	GPIO16	ADCL_CH16	ITC
EN	8	1	GPIO17	ADCL_CH17	ITC
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EN	8	1	GPIO24	ADCL_CH24	ITC
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EN	8	1	GPIO91	ADCL_CH91	ITC
EN	8	1	GPIO92	ADCL_CH92	ITC
EN	8	1	GPIO93	ADCL_CH93	ITC
EN	8	1	GPIO94	ADCL_CH94	ITC
EN	8	1	GPIO95	ADCL_CH95	ITC
EN	8	1	GPIO96	ADCL_CH96	ITC
EN	8	1	GPIO97	ADCL_CH97	ITC
EN	8	1	GPIO98	ADCL_CH98	ITC
EN	8	1	GPIO99	ADCL_CH99	ITC
EN	8	1	GPIO100	ADCL_CH100	ITC

GPIO	Analog Function	Comment
GPIO0	ADCL_CH0	ITC
GPIO1	ADCL_CH1	ITC
GPIO2	ADCL_CH2	ITC
GPIO3	ADCL_CH3	ITC
GPIO4	ADCL_CH4	ITC
GPIO5	ADCL_CH5	ITC
GPIO6	ADCL_CH6	ITC
GPIO7	ADCL_CH7	ITC
GPIO8	ADCL_CH8	ITC
GPIO9	ADCL_CH9	ITC
GPIO10	ADCL_CH10	ITC
GPIO11	ADCL_CH11	ITC
GPIO12	ADCL_CH12	ITC
GPIO13	ADCL_CH13	ITC
GPIO14	ADCL_CH14	ITC
GPIO15	ADCL_CH15	ITC
GPIO16	ADCL_CH16	ITC
GPIO17	ADCL_CH17	ITC
GPIO18	ADCL_CH18	ITC
GPIO19	ADCL_CH19	ITC
GPIO20	ADCL_CH20	ITC
GPIO21	ADCL_CH21	ITC
GPIO22	ADCL_CH22	ITC
GPIO23	ADCL_CH23	ITC
GPIO24	ADCL_CH24	ITC
GPIO25	ADCL_CH25	ITC
GPIO26	ADCL_CH26	ITC
GPIO27	ADCL_CH27	ITC
GPIO28	ADCL_CH28	ITC
GPIO29	ADCL_CH29	ITC
GPIO30	ADCL_CH30	ITC
GPIO31	ADCL_CH31	ITC
GPIO32	ADCL_CH32	ITC
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GPIO35	ADCL_CH35	ITC
GPIO36	ADCL_CH36	ITC
GPIO37	ADCL_CH37	ITC
GPIO38	ADCL_CH38	ITC
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GPIO40	ADCL_CH40	ITC
GPIO41	ADCL_CH41	ITC
GPIO42	ADCL_CH42	ITC
GPIO43	ADCL_CH43	ITC
GPIO44	ADCL_CH44	ITC
GPIO45	ADCL_CH45	ITC
GPIO46	ADCL_CH46	ITC
GPIO47	ADCL_CH47	ITC
GPIO48	ADCL_CH48	ITC
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GPIO50	ADCL_CH50	ITC
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GPIO64	ADCL_CH64	ITC
GPIO65	ADCL_CH65	ITC
GPIO66	ADCL_CH66	ITC
GPIO67	ADCL_CH67	ITC
GPIO68	ADCL_CH68	ITC
GPIO69	ADCL_CH69	ITC
GPIO70	ADCL_CH70	ITC
GPIO71	ADCL_CH71	ITC
GPIO72	ADCL_CH72	ITC
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GPIO74	ADCL_CH74	ITC
GPIO75	ADCL_CH75	ITC
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GPIO79	ADCL_CH79	ITC
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GPIO81	ADCL_CH81	ITC
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GPIO83	ADCL_CH83	ITC
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GPIO90	ADCL_CH90	ITC
GPIO91	ADCL_CH91	ITC
GPIO92	ADCL_CH92	ITC
GPIO93	ADCL_CH93	ITC
GPIO94	ADCL_CH94	ITC
GPIO95	ADCL_CH95	ITC
GPIO96	ADCL_CH96	ITC
GPIO97	ADCL_CH97	ITC
GPIO98	ADCL_CH98	ITC
GPIO99	ADCL_CH99	ITC
GPIO100	ADCL_CH100	ITC



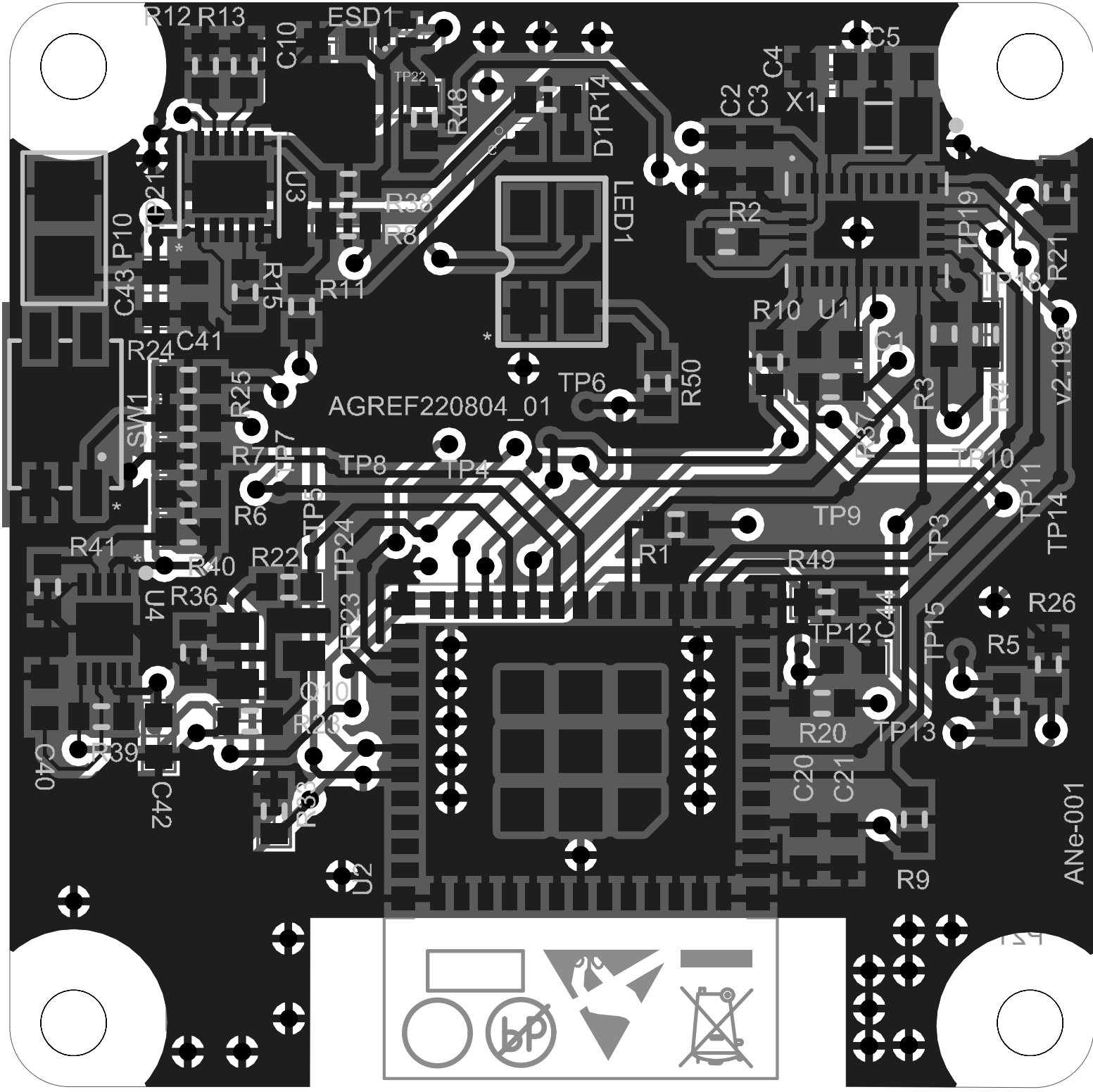
*R24 is connected to ADCL_CH0 (GPIO0)
 R6 and R7 is a voltage divider to match VBAT to voltage that can be used by the ADC.
 Voltage at IOCT1 (IOCT1-IOCT0) is VBAT.
 Assuming VBAT min is 4.2V, max IOCT1 is 3.97V.
 Pinout:
 1. Input I/OCT1 using ADC.
 2. calculate VBAT using VBAT - IOCT1 (pin 2 and R7).
 3. Check if value is within battery LOW or HIGH levels.

Programming Current
 Range = 1000 Iping
 R12 at 2k will give 50mAh of charging current
 Current Detection
 Icc = 100 Ibits
 R13 at 1k will give 50mAh of current detection
 Charging will start at 50mAh and end when current reaches 10mAh
 Battery at 450mAh = 500/450 = 1.1C



TP16 (near pin - Charging Status)
 *CHRG will go LOW when battery is charging
 *CHRG will go HIGH IMPEDANCE when battery is charged or Idle is achieved (50mAh charging current)

- 14 Dec 2021
- 15 Dec 2021
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- 1 Jan 2022
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