

客戶名稱 CUSTOMER	:	
客户料號 CUSTOMER'S P/N	:	
料號 PART NUMBER	:	WAN3216F245H08
規格 DESCRIPTION	:	Chip Antenna 3216 L Ant 2.45G Type H08
版本 VERSION	:	V1.2
日期 ISSUE DATE	:	2020/03/13



	エ 程 部 R&D CENTER	
承 認 APPROVAL	確認 CHECKED	製 作 DRAWN
Ray	Tennyson	Snow



萬誠科技股份有限公司

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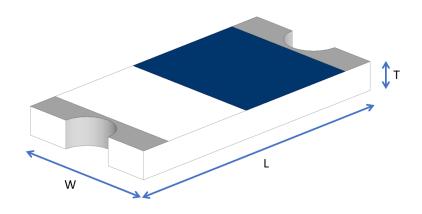
OneWave Electronic Co., Ltd.

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3216 Chip antenna

For Bluetooth / WLAN Applications





	Dimension (mm)			
L	3.23 ± 0.20			
W	1.66 ± 0.20			
Т	0.45 ± 0.20			

Part Number Information

WAN	<u>3216</u>	<u>F</u>	<u>245</u>	H	<u>08</u>
Α	В	С	D	Е	F

Α	Product Series	Antenna		
B	Dimension L x W	3.2 x 1.6mm (±0.2mm)		
С	Material	High K material		
D	Working Frequency	2.4 ~ 2.5GHz		
Ε	Feeding mode	PIFA & Single Feeding		
F	Antenna type	Type = 08		

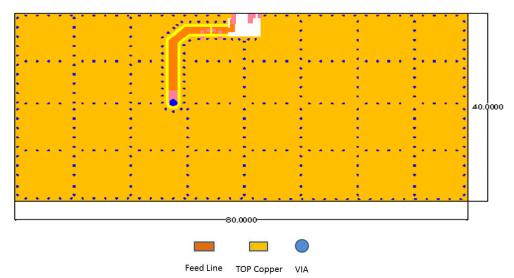
1. Electrical Specification

Specification					
Part Number	WAN3216F245H08				
Central Frequency	2450	MHz			
Bandwidth	120 (Min.)	MHz			
Return Loss	-6.5 (Max)	dB			
Peak Gain	1.75	dBi			
Impedance	50	Ohm			
Operating Temperature	-40~+110	°C			
Maximum Power	4	W			
Resistance to Soldering Heats	10 (@ 260°C)	sec.			
Polarization					
Azimuth Beamwidth	Omni-directional				
Termination Ni / Au (Leadless)					

Remark : Bandwidth & Peak Gain was measured under evaluation board of next page

2. Recommended PCB Pattern

Recommended PCB Pattern 若未參照我司規格書上Layout建議做設計· Evaluation Board Dimension 進而造成後續生產上的天線特性與品質差異問題· 我司將不負責處理。



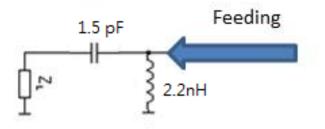
2nd Evaluation Board Dimension

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Suggested Matching Circuit

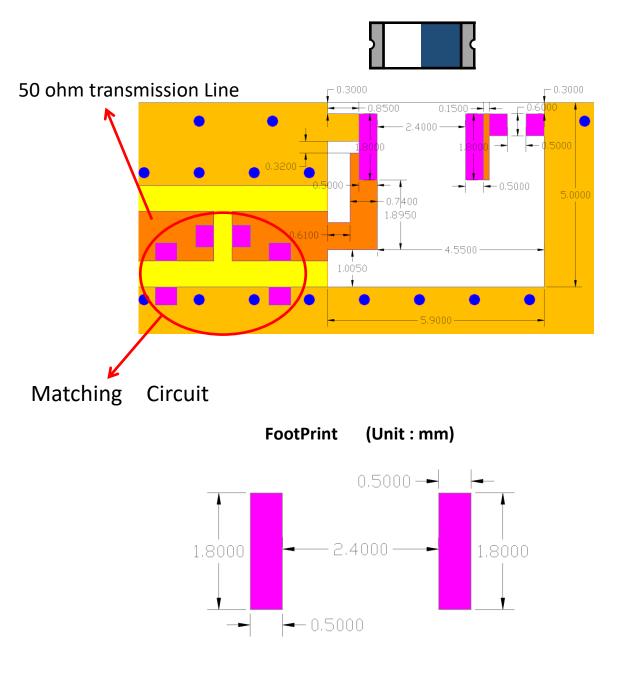
重要資訊:

匹配元件建議使用精準度高的電感±0.1~0.3nH、電容±0.1pF

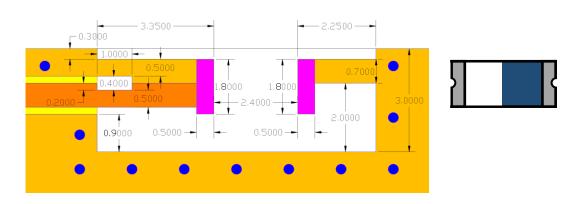




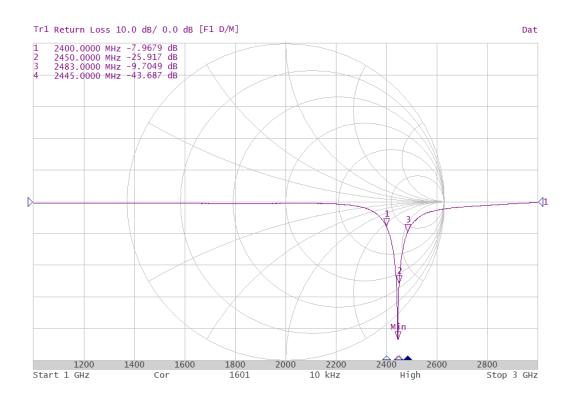
Layout Dimensions in Clearance area(Size=5.9*5.0mm)



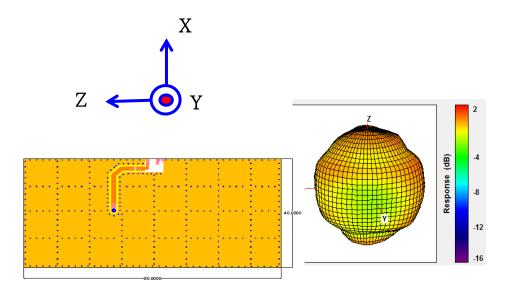
• 2nd Layout Dimensions in Clearance area(Size=8.0*3.0mm)



3. Measurement Results Return Loss

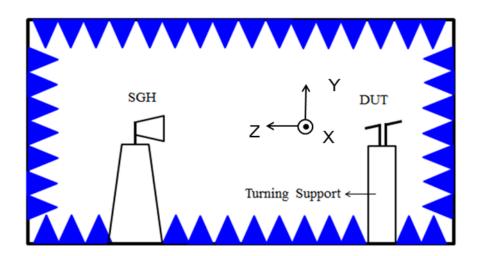


Radiation Pattern



	Efficiency	Peak Gain	Directivity
2400MHz	81.46%	1.67 dBi	2.56 dBi
2450MHz	84.75%	1.75 dBi	2.46 dBi
2500MHz	82.68%	1.70 dBi	2.52 dBi

Chamber Coordinate System





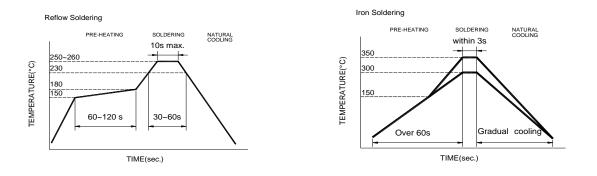
4.Reliability and Test Condictions

ITEM	REQUIREMENTS	TEST CONDITION
Solderability	1. Wetting shall exceed 90% coverage	Pre-heating temperature:150°C/60sec.
Colderability	2. No visible mechanical damage	Solder temperature:230 \pm 5 $^{\circ}$ C
		Duration:4±1sec.
	TEMP (°C)	Solder:Sn-Ag3.0-Cu0.5
	4±1 sec.	Flux for lead free: rosin
	230°C 41 Sec.	
	150°C	
	60sec	
	ý 00000	
0.11.1.1		
Solder heat Resistance	 No visible mechanical damage Central Freq. change :within ± 6% 	Pre-heating temperature:150°C/60sec.
Redictarioo		Solder temperature:260±5°C
	TEMP (°C)	Duration:10±0.5sec.
	260°C 10±0.5 sec.	Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin
	150°C	
	60sec	
Component	1. No visible mechanical damage	The device should be reflow
Adhesion (Push test)		soldered(230 \pm 5°C for 10sec.) to a tinned
(1 0311 (031)		copper substrate A dynometer force
		gauge should be applied the side of the component. The device must with-ST-F
		0.5 Kg without failure of the termination
		attached to component.
Component	1. No visible mechanical damage	Insert 10cm wire into the remaining open
Adhesion		eye bend ,the ends of even wire lengths
(Pull test)		upward and wind together.
,		Terminal shall not be remarkably
		damaged.
Thermal shock	1. No visible mechanical damage	+110°C=>30±3min
	2. Central Freq. change :within ±6%	-40°C=>30±3min
		Test cycle:10 cycles
	Phase Temperature(°C) Time(min)	The chip shall be stabilized at normal
	1 +110±5℃ 30±3	condition for 2~3 hours before
	2 Room Within	measuring.
	Temperature 3sec	
	3 -40±2℃ 30±3	
	4 Room Within	
	Temperature 3sec	
Destat		Temperature: +110±5°C
Resistance to	1. No visible mechanical damage	Duration: 1000±12hrs
High Tama anatuma	2. Central Freq. change :within ±6%	
Temperature	3. No disconnection or short circuit.	The chip shall be stabilized at normal
		condition for 2~3 hours before
Desist		measuring. Temperature:-40±5℃
Resistance to	1. No visible mechanical damage	Duration: 1000±12hrs
Low	2. Central Freq. change :within ±6%	
Temperature	3. No disconnection or short circuit.	The chip shall be stabilized at normal
		condition for 2~3 hours before
Librar 1997		measuring.
Humidity	1. No visible mechanical damage	Temperature: $40\pm 2^{\circ}$ C
	2. Central Freq. change :within ±6%	Humidity: 90% to 95% RH Duration: 1000±12hrs
	3. No disconnection or short circuit.	
		The chip shall be stabilized at normal
		condition for 2~3 hours before
		measuring.

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5.Soldering and Mounting

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.



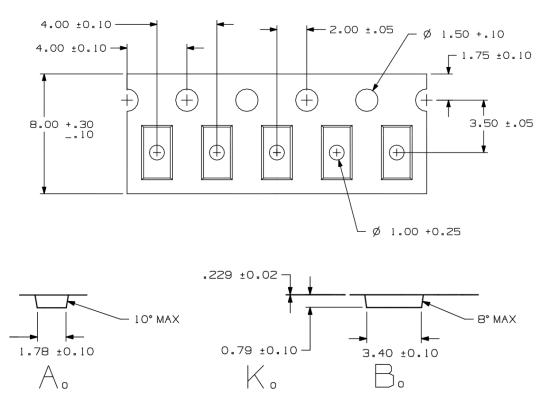
Recommended temperature profiles for re-flow soldering in Figure 1.

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

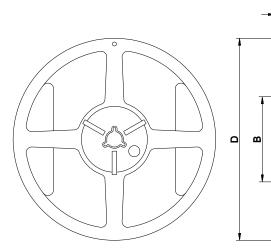
- Preheat circuit and products to 150° C
- Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm
- 280°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 3 sec.

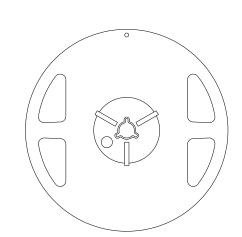
6.Packaging Information

Tape Specification:









7" x 8 mm

C

Tape Width(mm)	A(mm)	B(mm)	C(mm)	D(mm)	Chip/Reel(pcs)
8	9.0±0.5	60±2	13.5±0.5	178±2	3000

7. Storage and Transportation Information

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~ 40° C and 30~70% RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation Conditions

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.