

Appendix for 12A_n25A

Product Name: 5G CPE

Model No: C150

Appendix A: Average Power Output for NSA

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	21.50	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Inner_Full	23.00	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.51	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	21.48	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	21.49	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Inner_Full	22.48	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.21	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.61	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	21.46	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Inner_Full	23.03	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.59	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.43	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	21.47	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Inner_Full	22.58	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.60	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.50	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	21.62	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Inner_Full	23.26	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.70	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.63	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	21.82	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Inner_Full	22.92	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.43	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.73	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	21.47	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Inner_Full	23.01	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.33	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.76	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	21.53	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Inner_Full	22.64	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	21.49	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.45	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	21.58	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Inner_Full	23.14	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.77	PC3	PASS

DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.62	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	21.52	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Inner_Full	22.53	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.83	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.45	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	21.74	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Inner_Full	23.21	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.74	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.73	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	21.78	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Inner_Full	22.79	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.84	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.82	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	21.64	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Inner_Full	23.22	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.80	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.81	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	21.72	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Inner_Full	22.65	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	21.42	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.32	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	21.58	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Inner_Full	23.06	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.84	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.46	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	21.66	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Inner_Full	22.56	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.42	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.82	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	21.70	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Inner_Full	23.16	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.59	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.74	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	21.77	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Inner_Full	22.90	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.47	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.73	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	21.77	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Inner_Full	23.29	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.62	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.84	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	21.65	PC3	PASS

DC_12A_n25A	15	5+20	CP-16QAM	M+L	Inner_Full	22.91	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.51	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	22.18	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	21.58	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Inner_Full	23.04	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.86	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.29	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	21.67	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Inner_Full	22.54	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.37	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.39	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	21.80	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Inner_Full	23.30	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.56	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.67	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	21.76	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Inner_Full	22.80	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.45	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.62	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

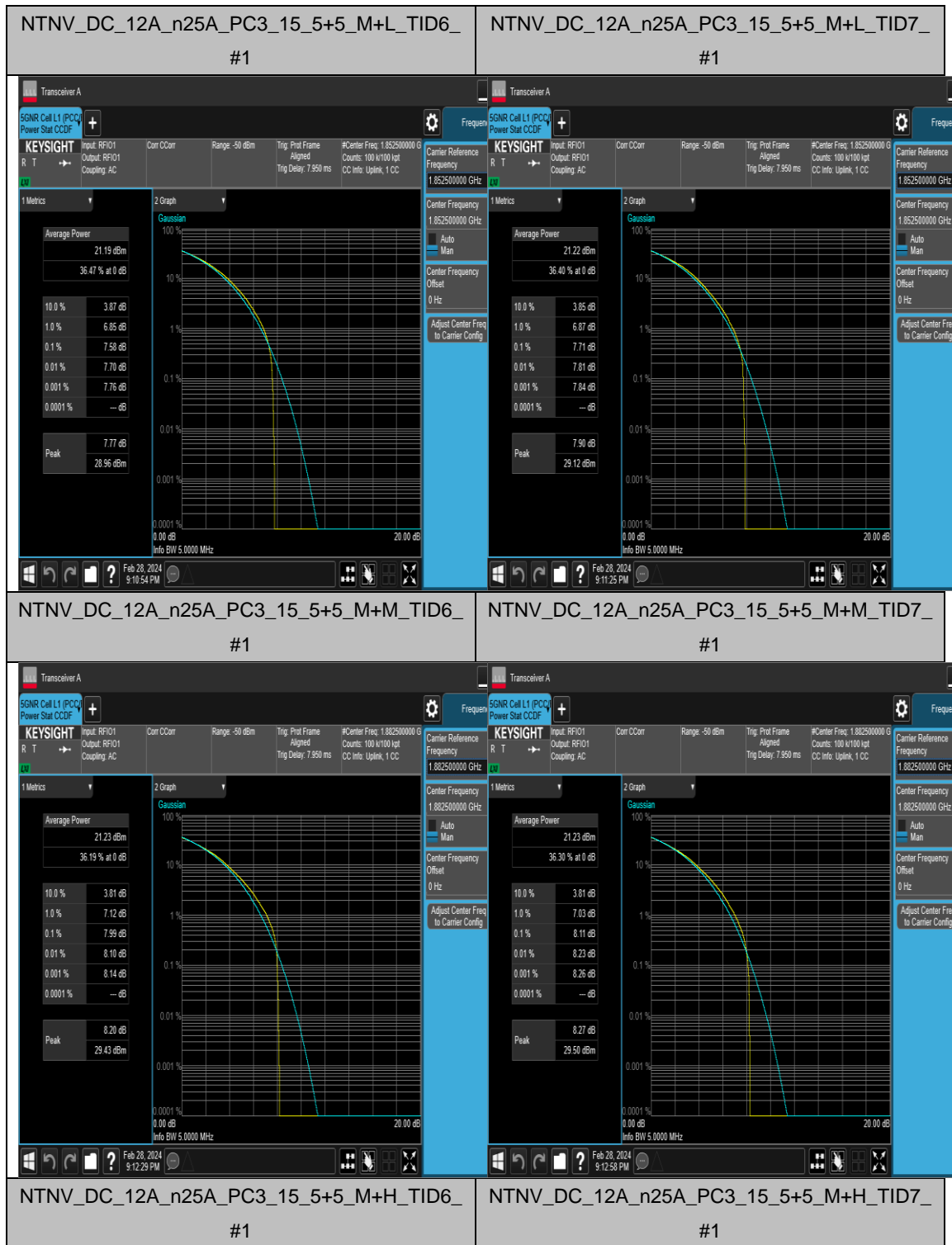
Peak-to-Average Ratio(CCDF)

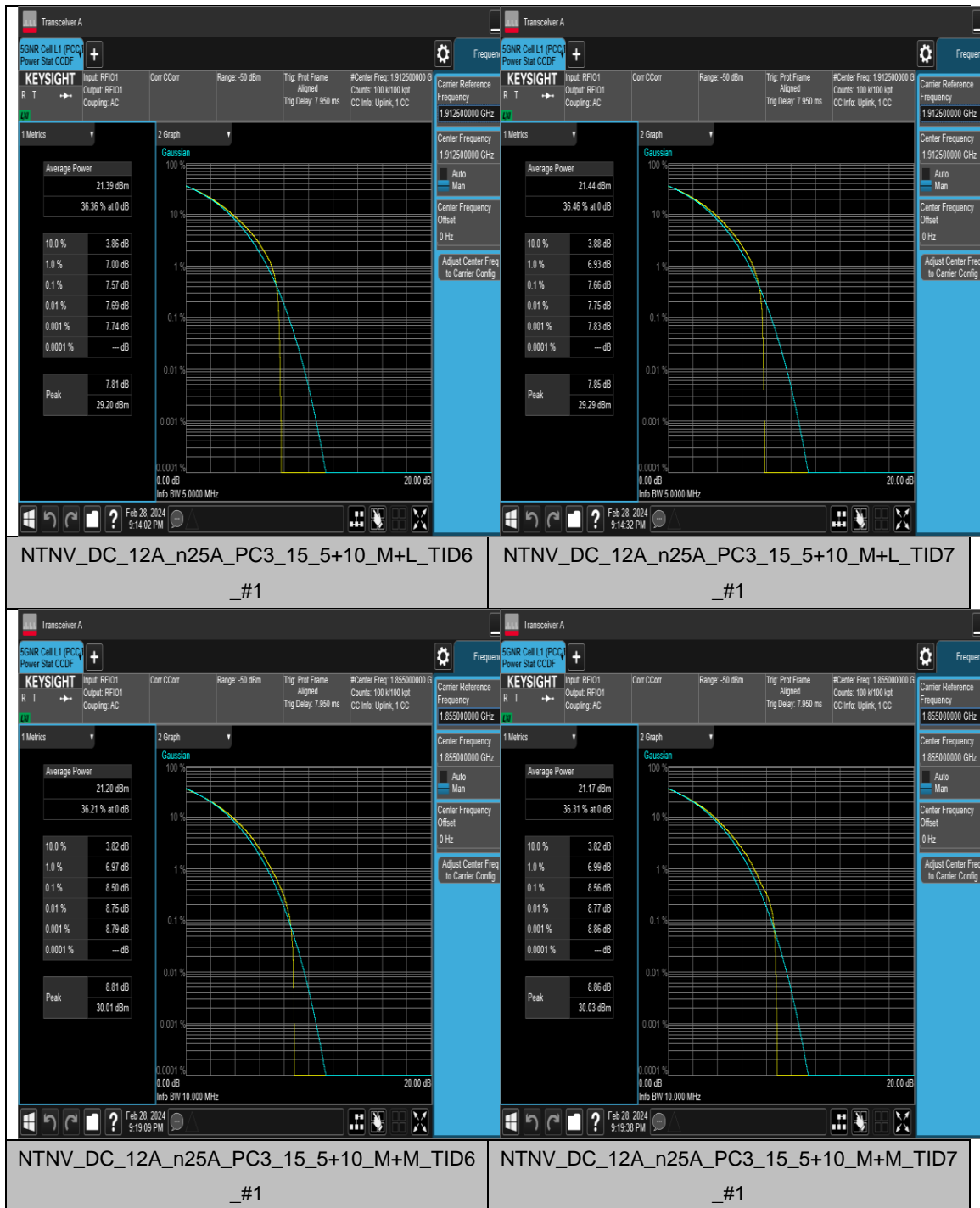
Test Result

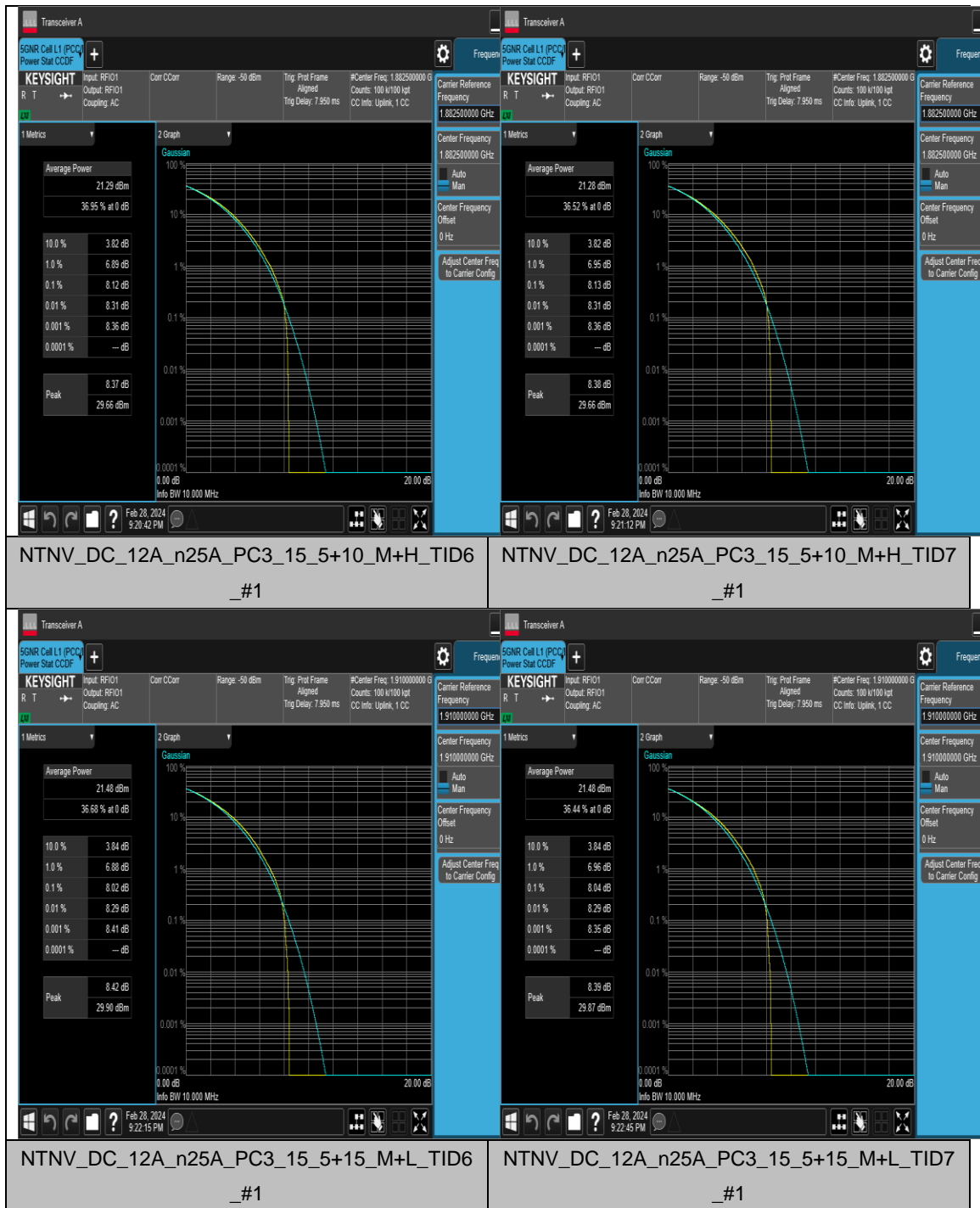
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_12A_n 25A	15	5+5	CP-QPSK	M+L	Outer_Full	7.58	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+L	Outer_Full	7.71	≤13	PASS
DC_12A_n 25A	15	5+5	CP-QPSK	M+M	Outer_Full	7.99	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+M	Outer_Full	8.11	≤13	PASS
DC_12A_n 25A	15	5+5	CP-QPSK	M+H	Outer_Full	7.57	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+H	Outer_Full	7.66	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+L	Outer_Full	8.50	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+L	Outer_Full	8.56	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+M	Outer_Full	8.12	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+M	Outer_Full	8.13	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+H	Outer_Full	8.02	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+H	Outer_Full	8.04	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+L	Outer_Full	7.59	≤13	PASS
DC_12A_n 25A	15	5+15	CP-16QAM	M+L	Outer_Full	7.59	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+M	Outer_Full	7.88	≤13	PASS
DC_12A_n 25A	15	5+15	CP-16QAM	M+M	Outer_Full	7.92	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+H	Outer_Full	8.08	≤13	PASS

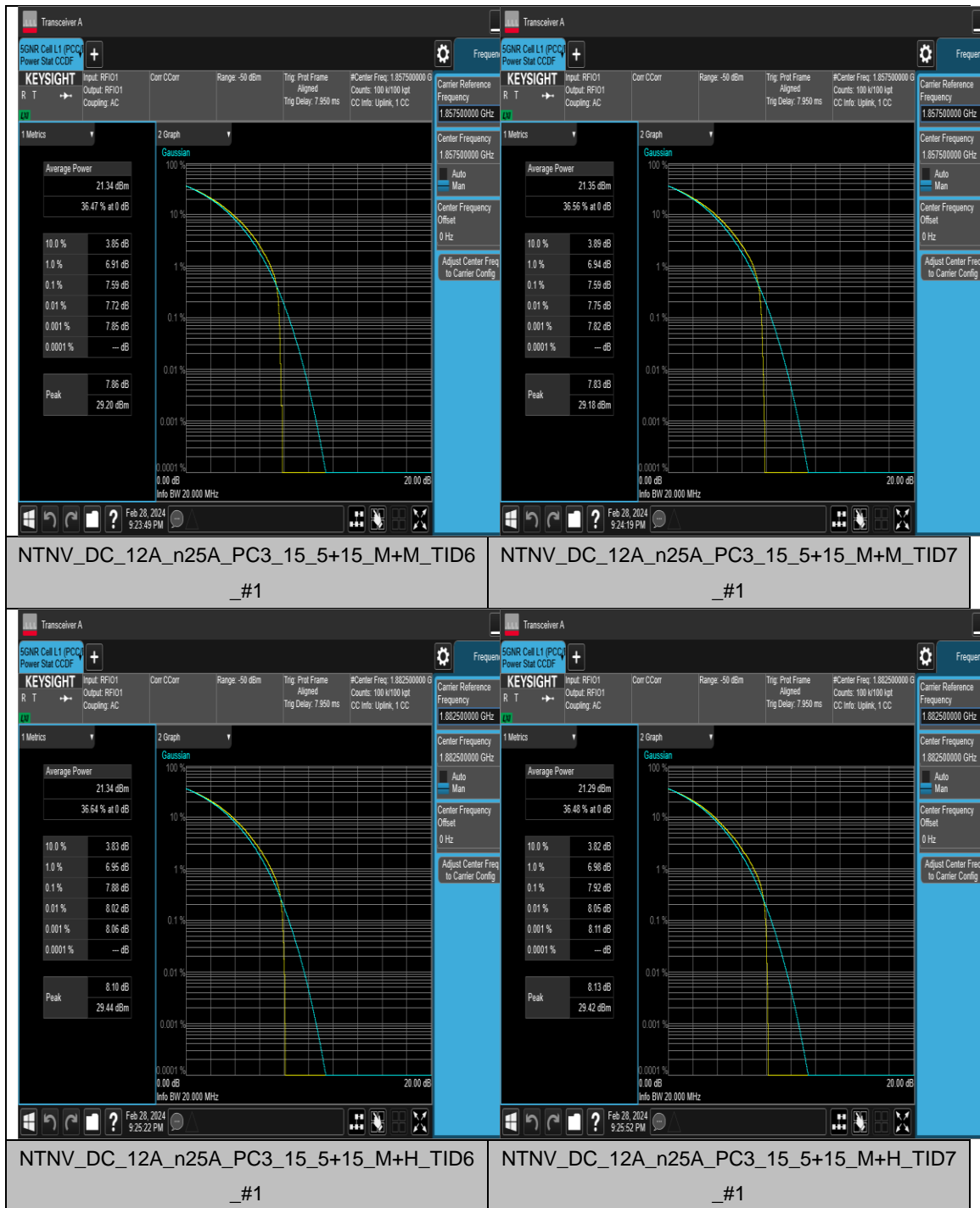
DC_12A_n 25A	15	5+15	CP-16QAM	M+H	Outer_Full	8.04	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+L	Outer_Full	8.09	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+L	Outer_Full	8.12	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+M	Outer_Full	7.64	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+M	Outer_Full	7.65	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+H	Outer_Full	8.03	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+H	Outer_Full	7.97	≤13	PASS

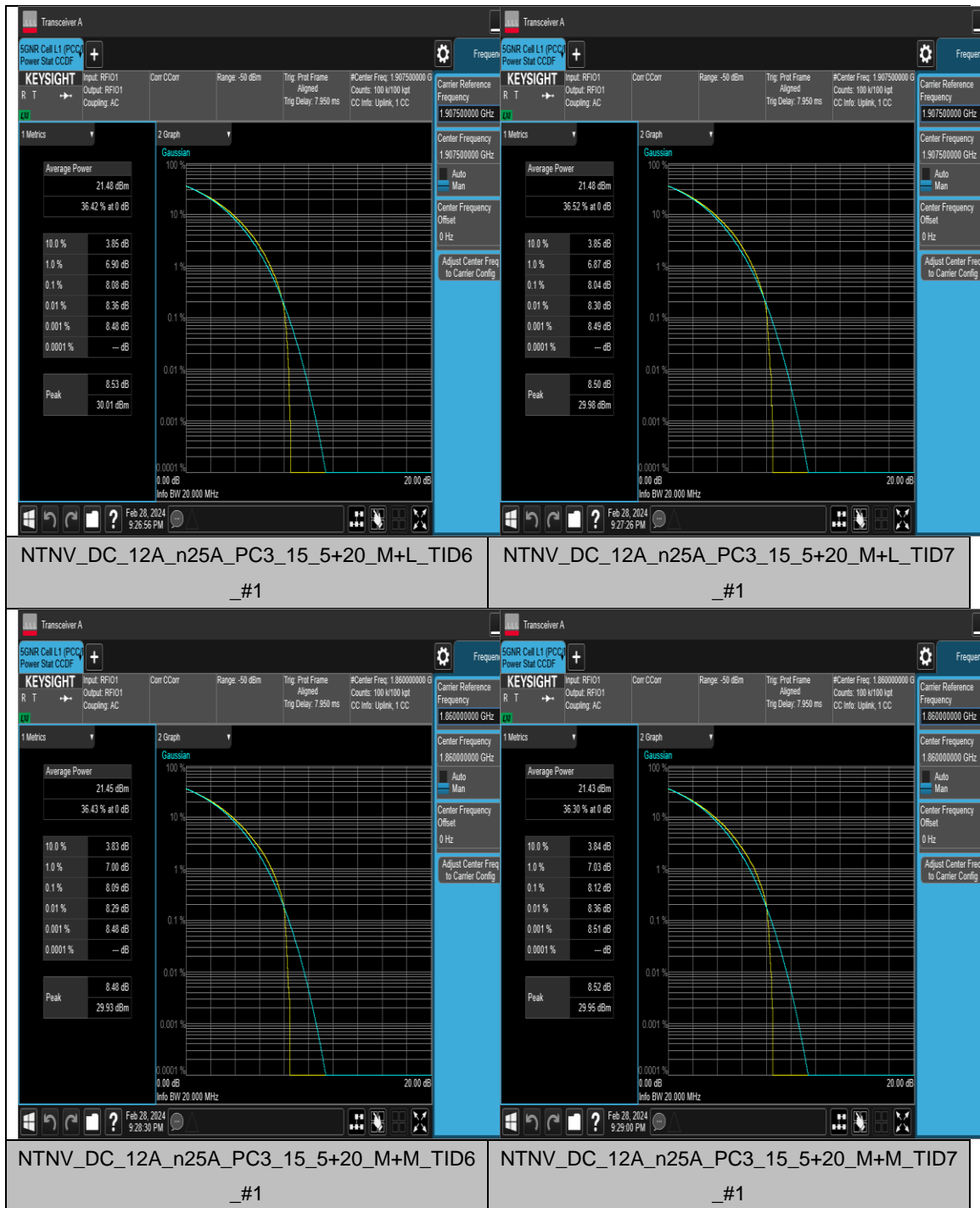
Test Graphs

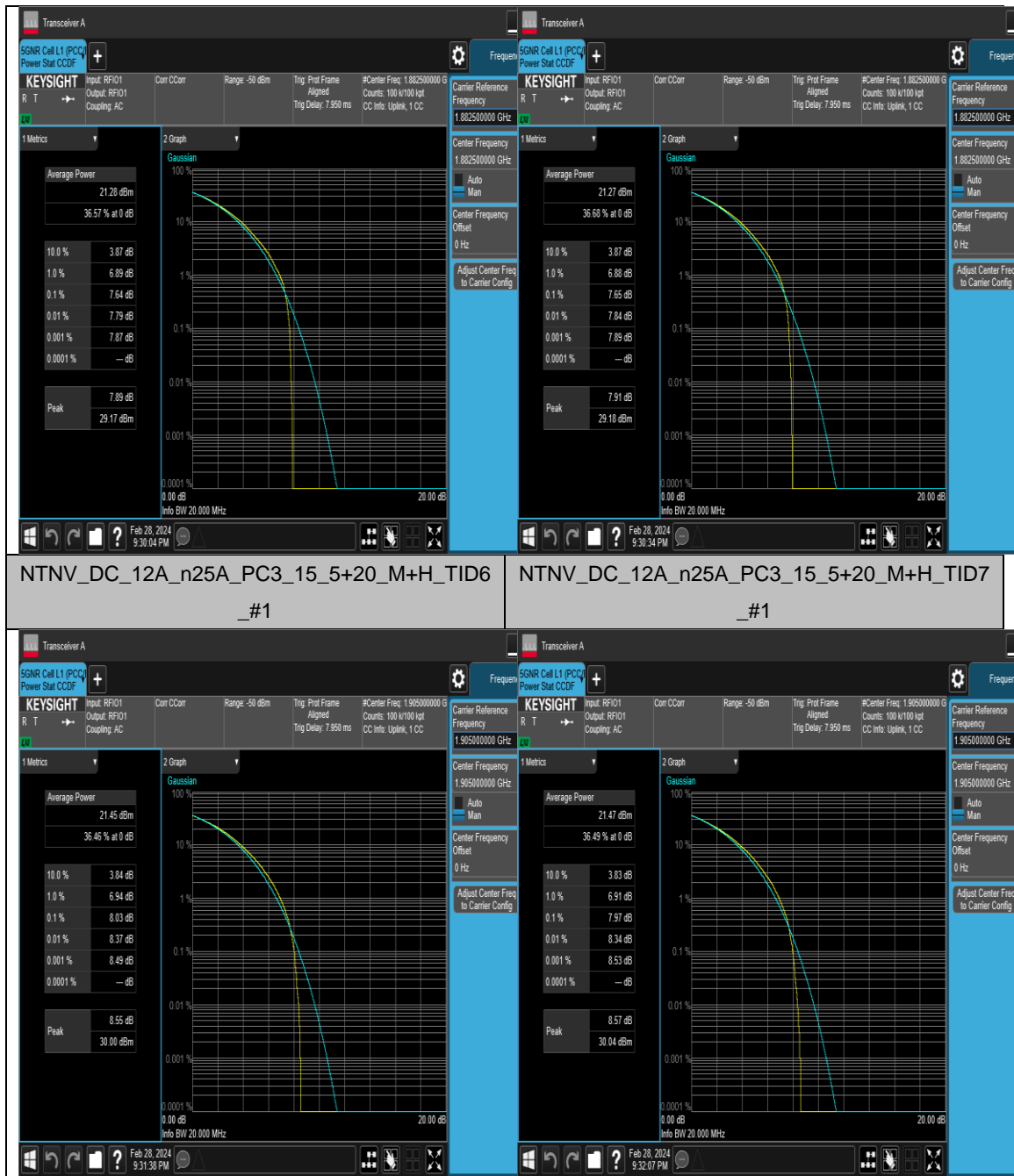












NTNV_DC_12A_n25A_PC3_15_5+20_M+H_TID6

_#1

NTNV_DC_12A_n25A_PC3_15_5+20_M+H_TID7

_#1

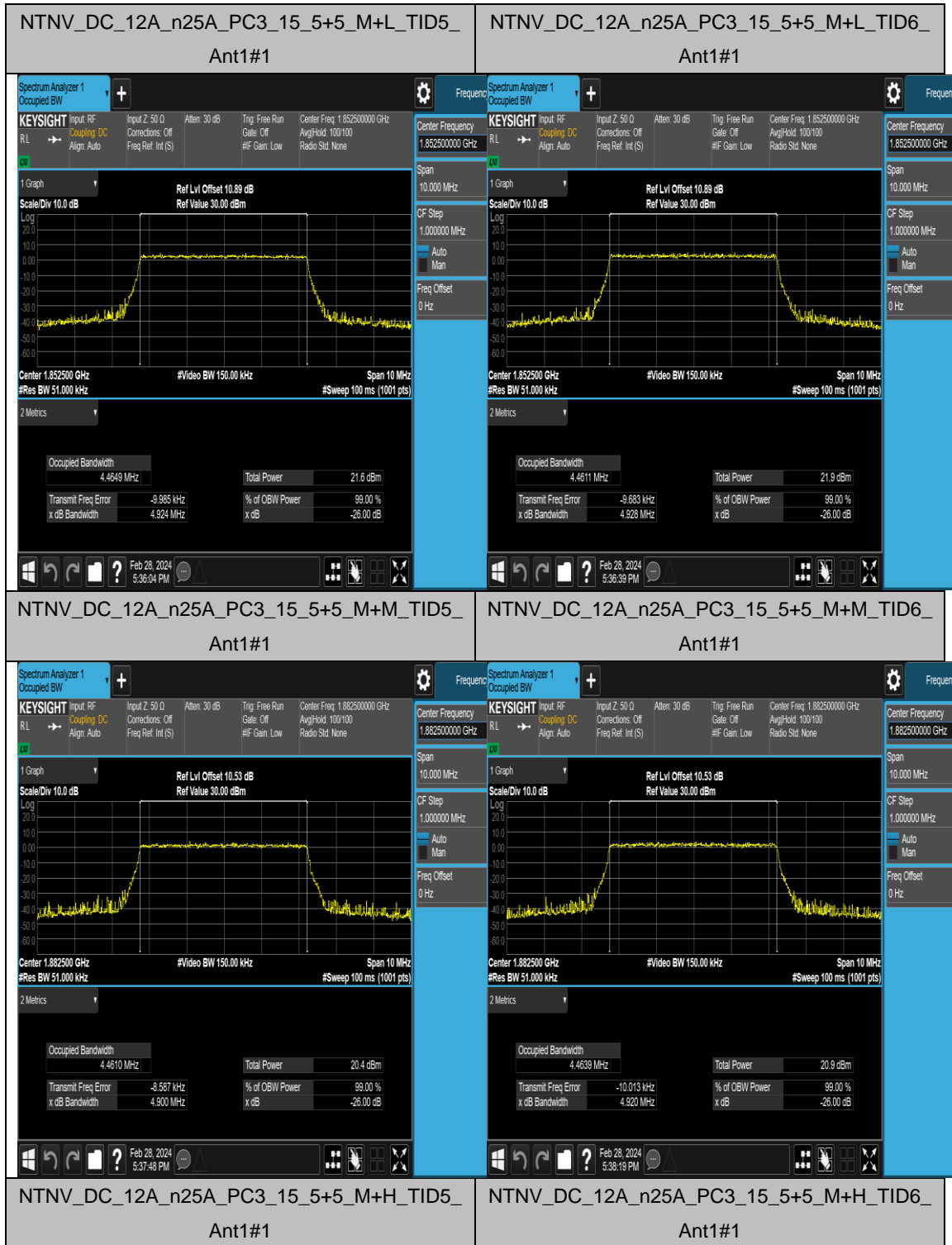
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

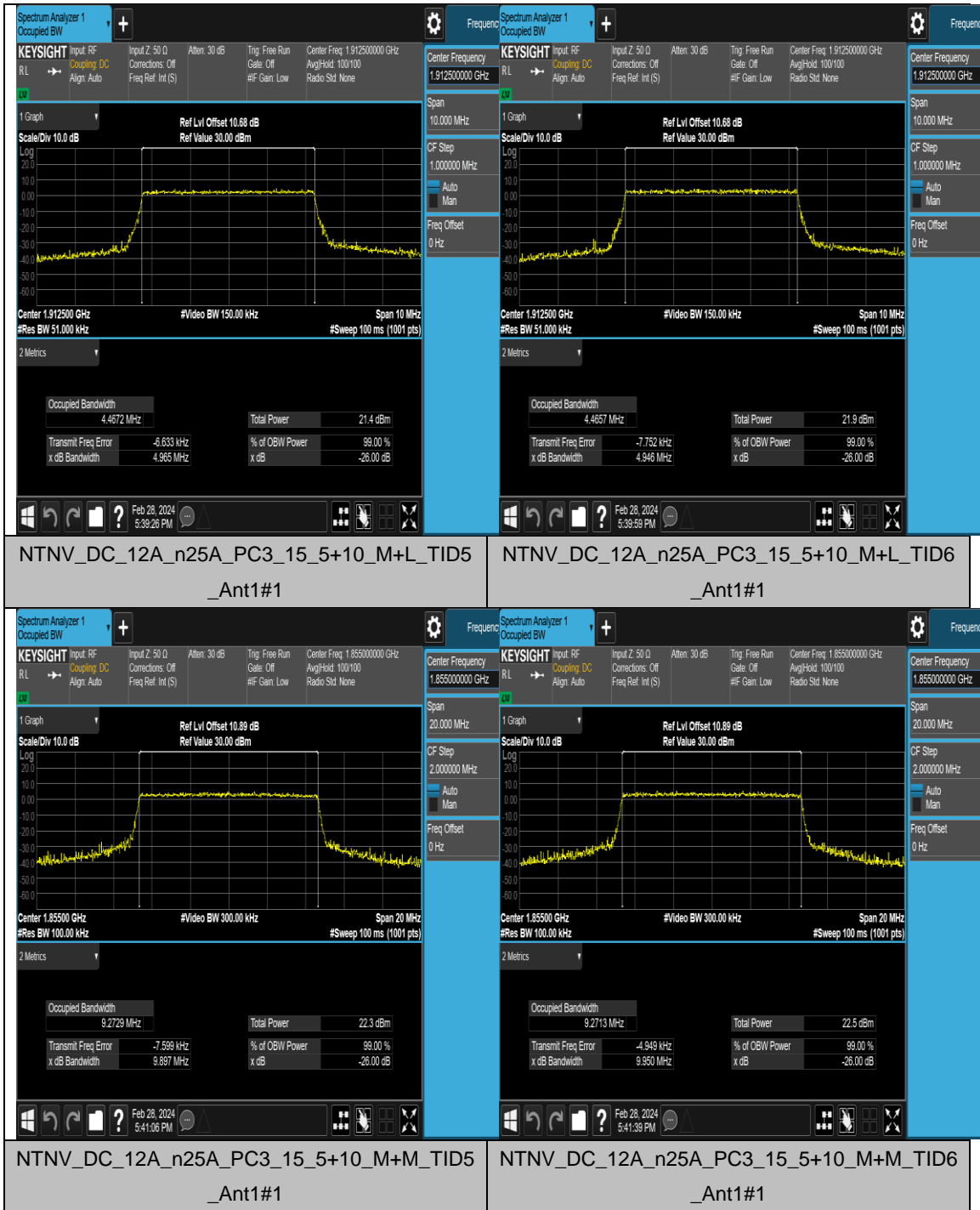
NSA

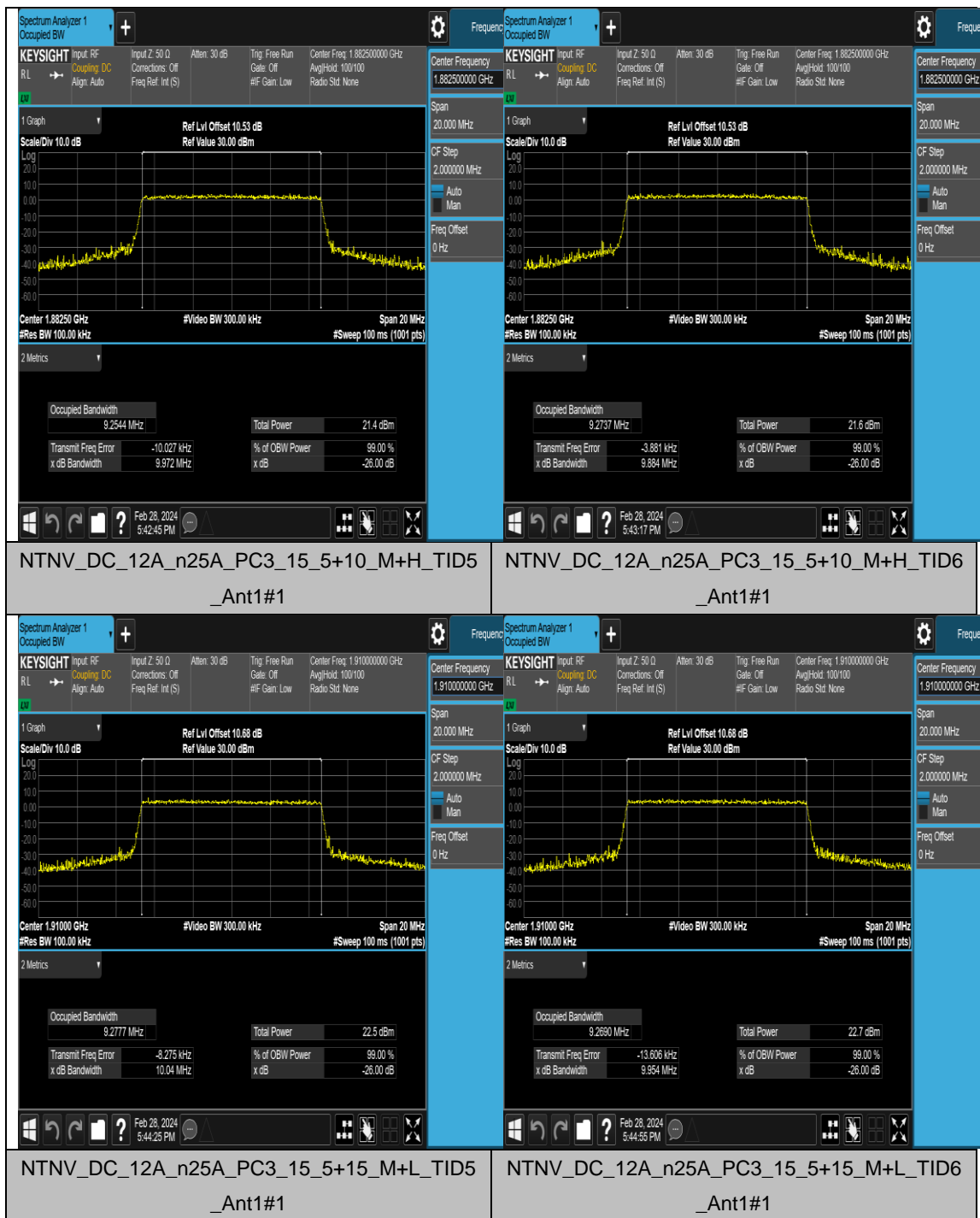
Test Result

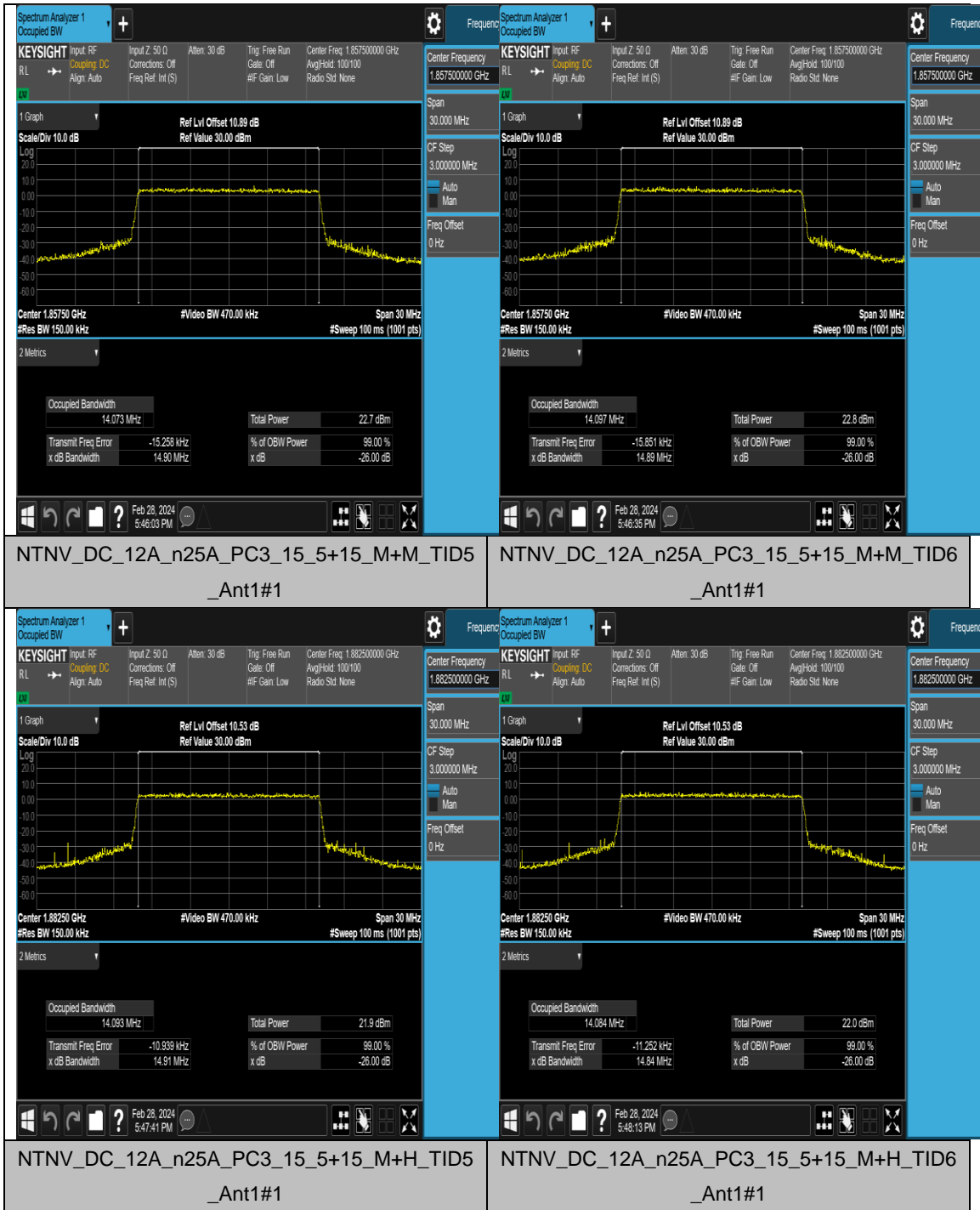
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4649	4.924	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4611	4.928	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4610	4.900	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4639	4.920	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4672	4.965	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4657	4.946	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2729	9.897	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2713	9.950	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2544	9.972	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2737	9.884	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2777	10.04	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2690	9.954	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	14.073	14.90	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	14.097	14.89	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	14.093	14.91	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	14.084	14.84	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	14.086	14.85	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	14.065	14.84	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	18.900	19.88	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	18.897	19.79	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	18.879	19.73	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	18.851	19.77	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	18.861	19.79	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	18.886	19.79	PASS

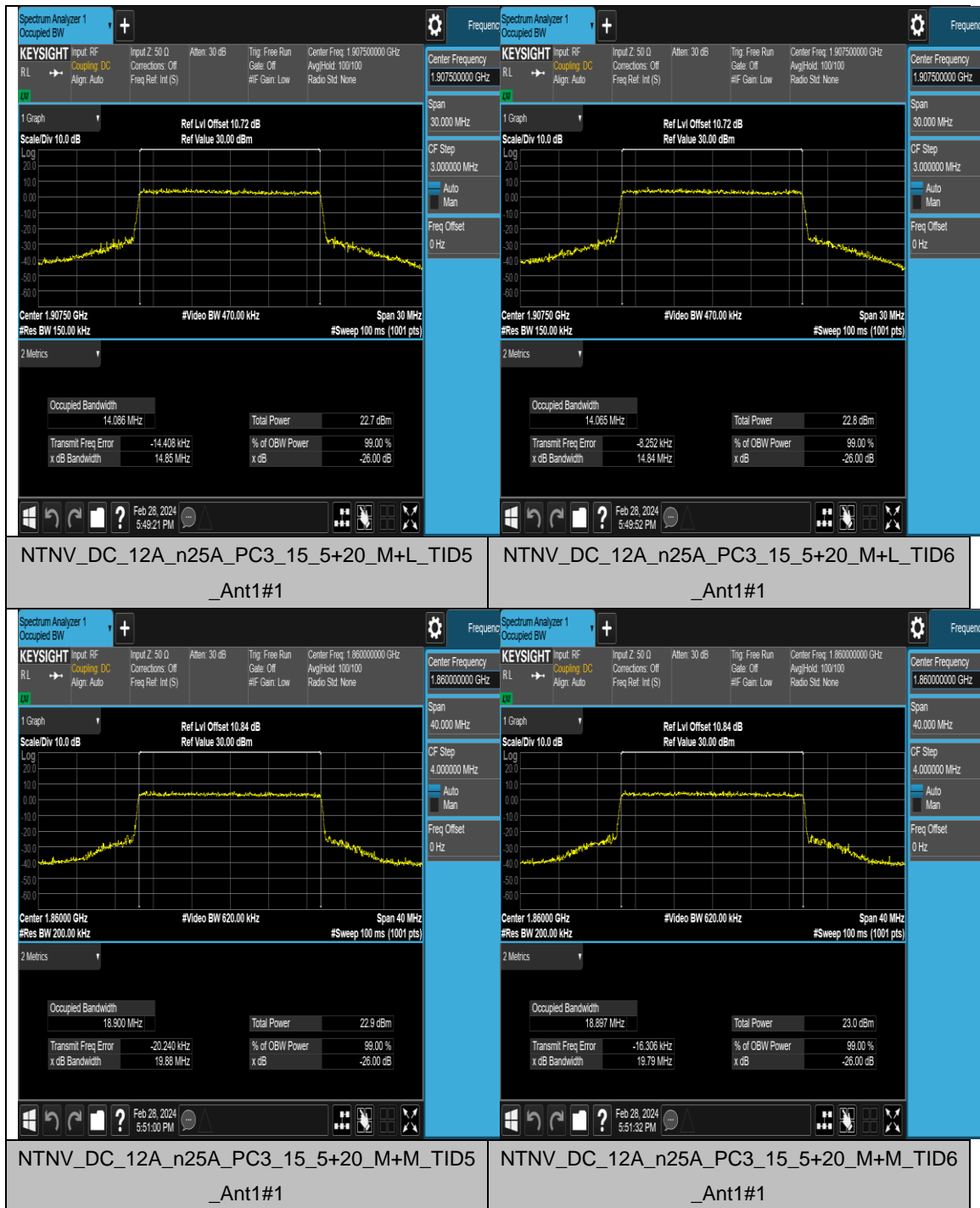
Test Graphs

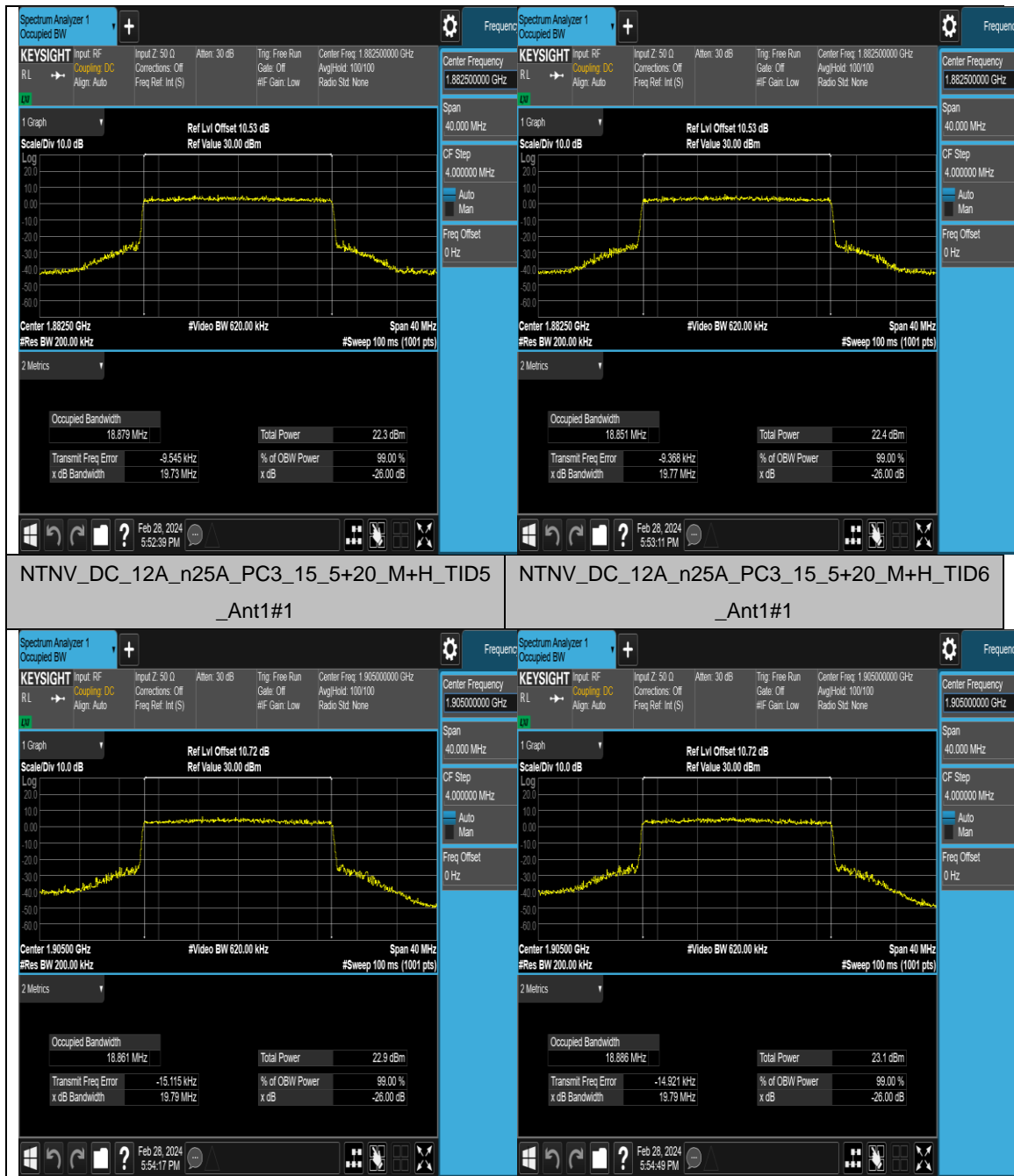










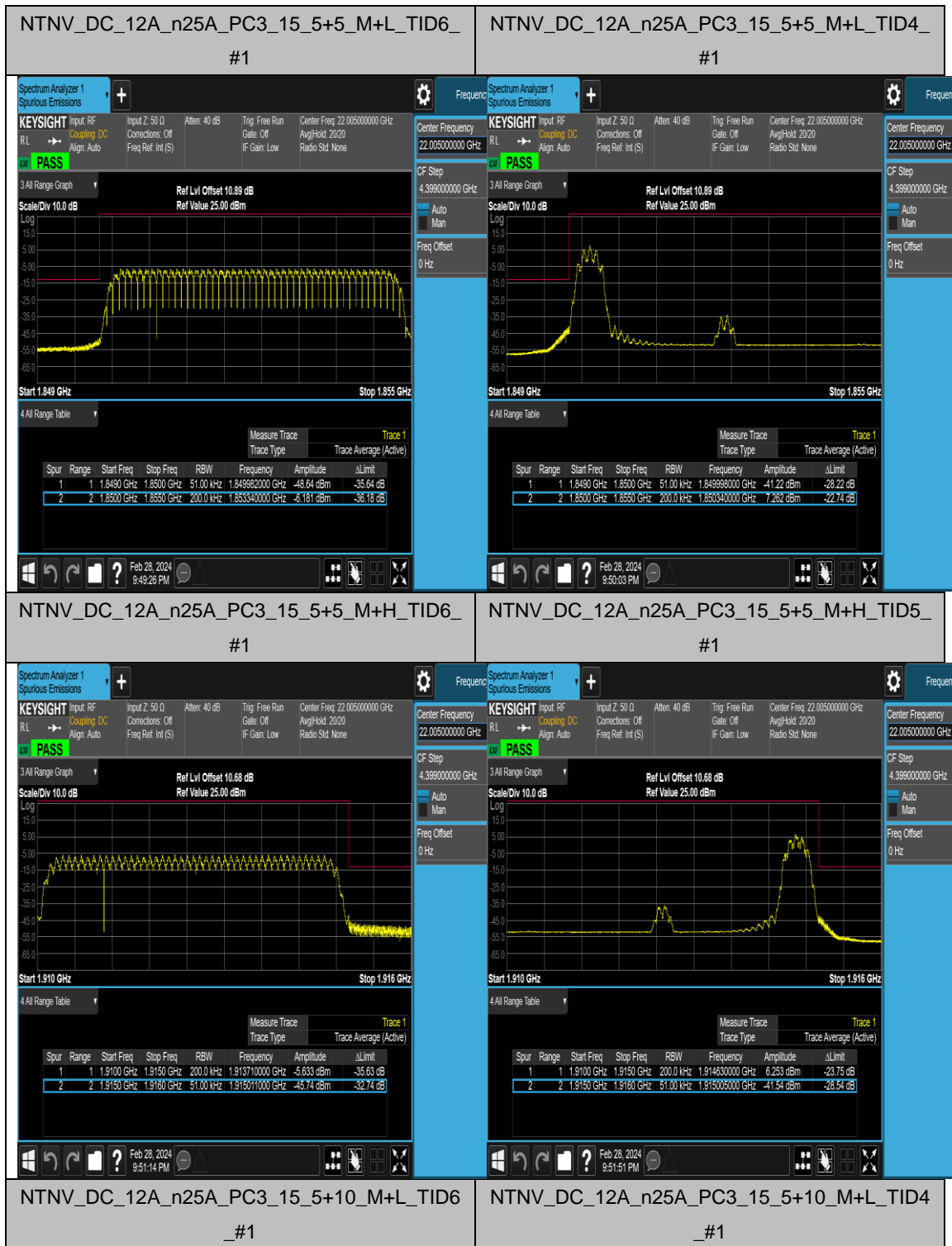


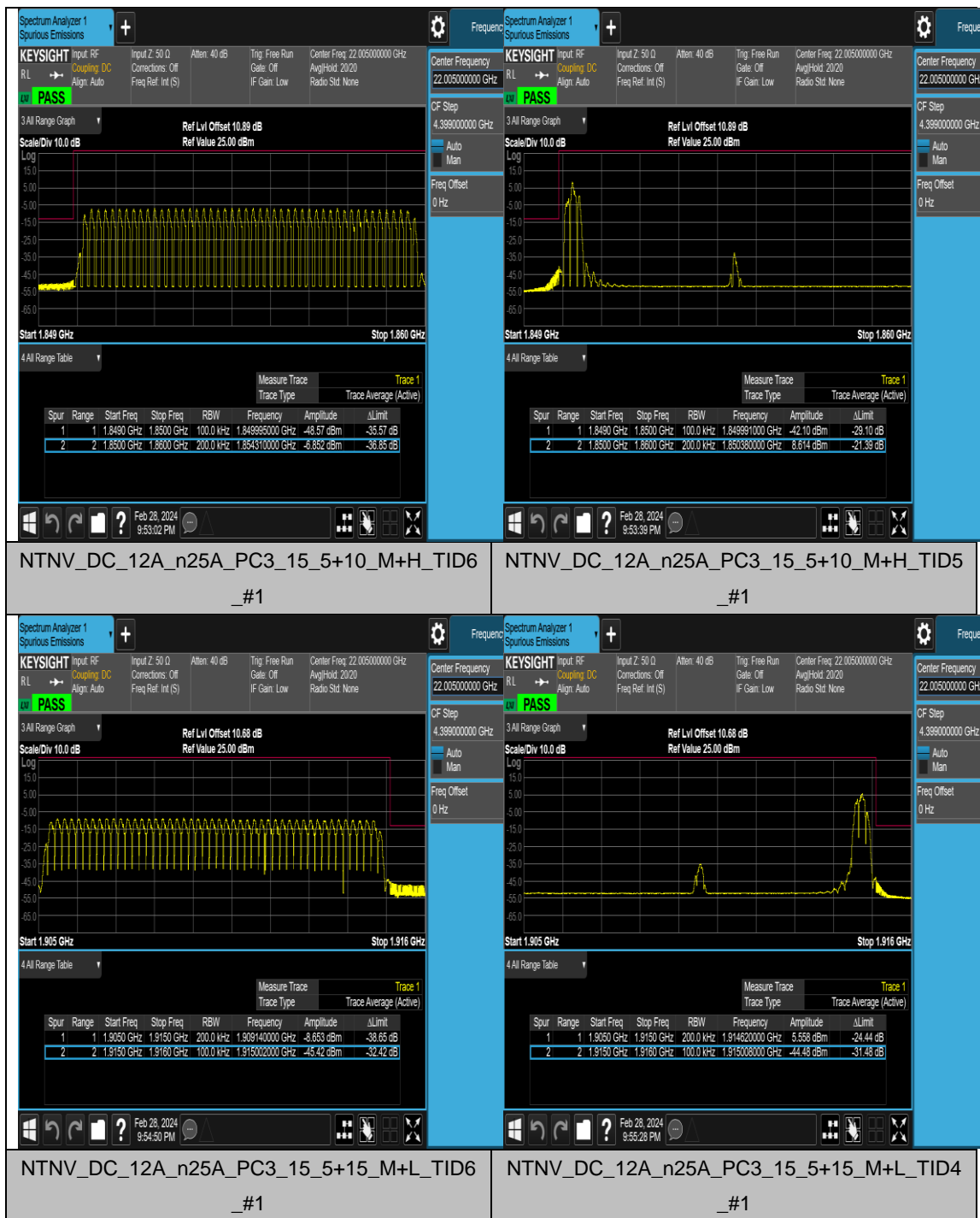
Appendix D: Band Edge for NSA

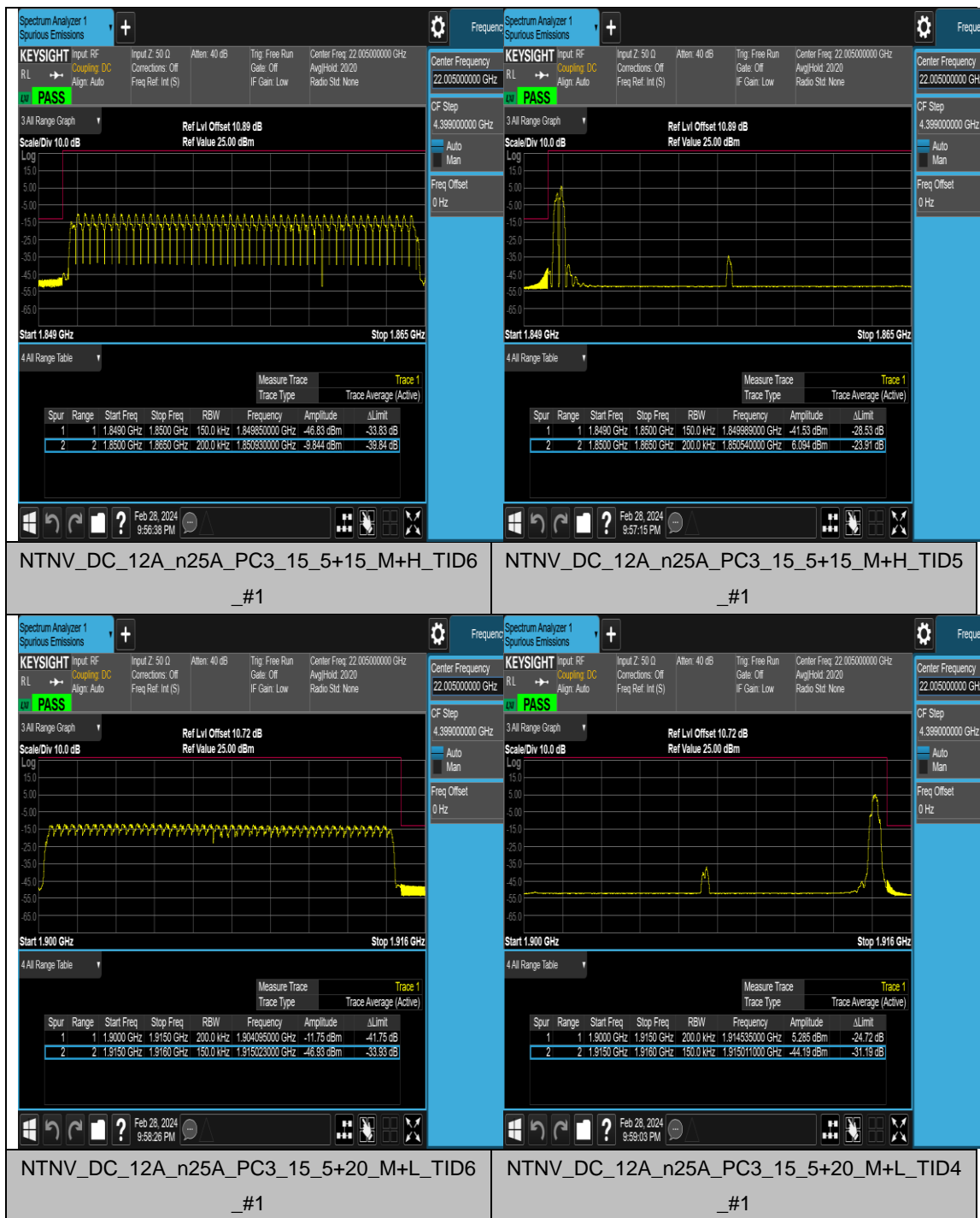
Test Result

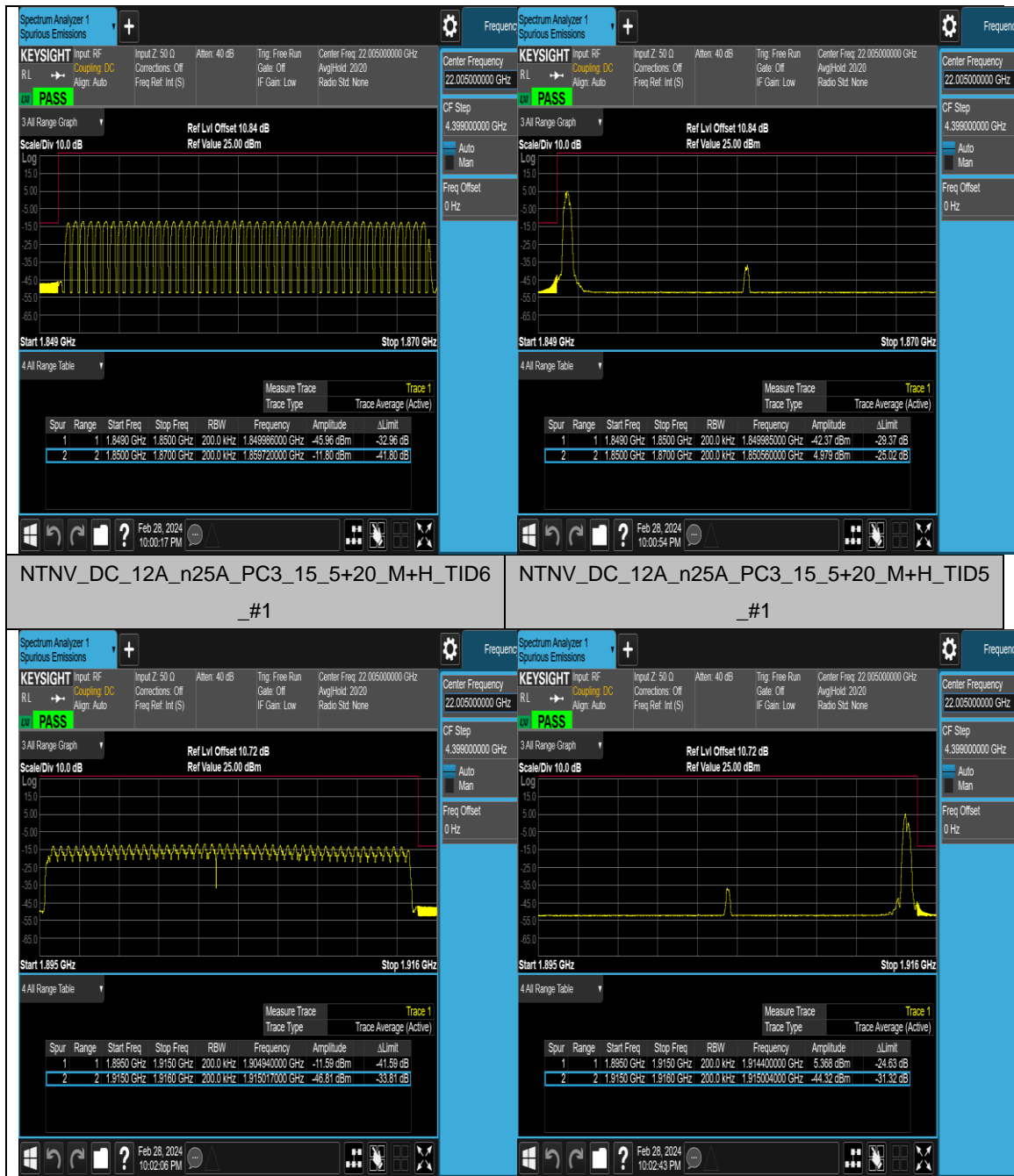
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS