

Appendix for 66A_n12A

Product Name: 5G CPE

Model No: C150

Appendix A: Average Power Output for SA

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	22.26	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Inner_Full	23.86	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	22.32	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	22.23	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	22.35	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Inner_Full	23.39	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	22.28	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	22.32	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	22.08	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Inner_Full	23.71	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	22.28	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	22.17	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	22.07	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Inner_Full	23.20	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	22.29	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	22.24	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	21.93	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Inner_Full	23.43	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	22.09	PC3	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.91	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	21.99	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Inner_Full	22.95	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	22.11	PC3	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.56	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	22.26	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Inner_Full	23.70	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	22.35	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	22.14	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	22.24	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Inner_Full	23.31	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	22.24	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	22.20	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	22.24	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Inner_Full	23.74	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	22.27	PC3	PASS

DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	22.07	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	22.24	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Inner_Full	23.26	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	22.16	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	22.04	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	22.21	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Inner_Full	23.55	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	22.04	PC3	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.88	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	22.05	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Inner_Full	23.05	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.80	PC3	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.47	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	22.30	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Inner_Full	23.87	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	22.58	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	22.03	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	22.41	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Inner_Full	23.27	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	22.47	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.90	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	22.37	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Inner_Full	23.78	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	22.37	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	22.11	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	22.31	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Inner_Full	23.31	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	22.74	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.68	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	22.25	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Inner_Full	23.71	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	22.50	PC3	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.83	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	22.25	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Inner_Full	23.16	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	22.55	PC3	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.71	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

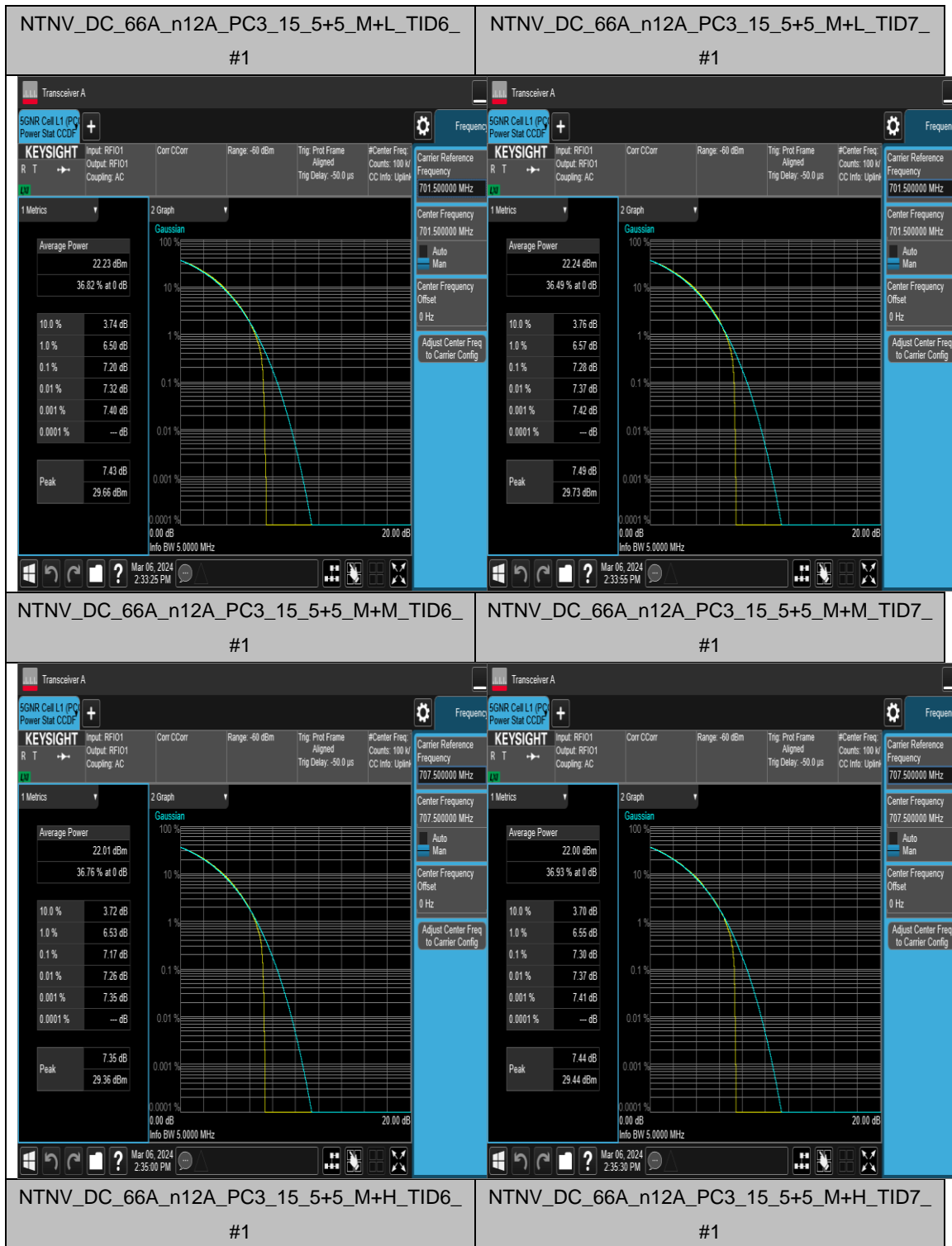
Peak-to-Average Ratio(CCDF)

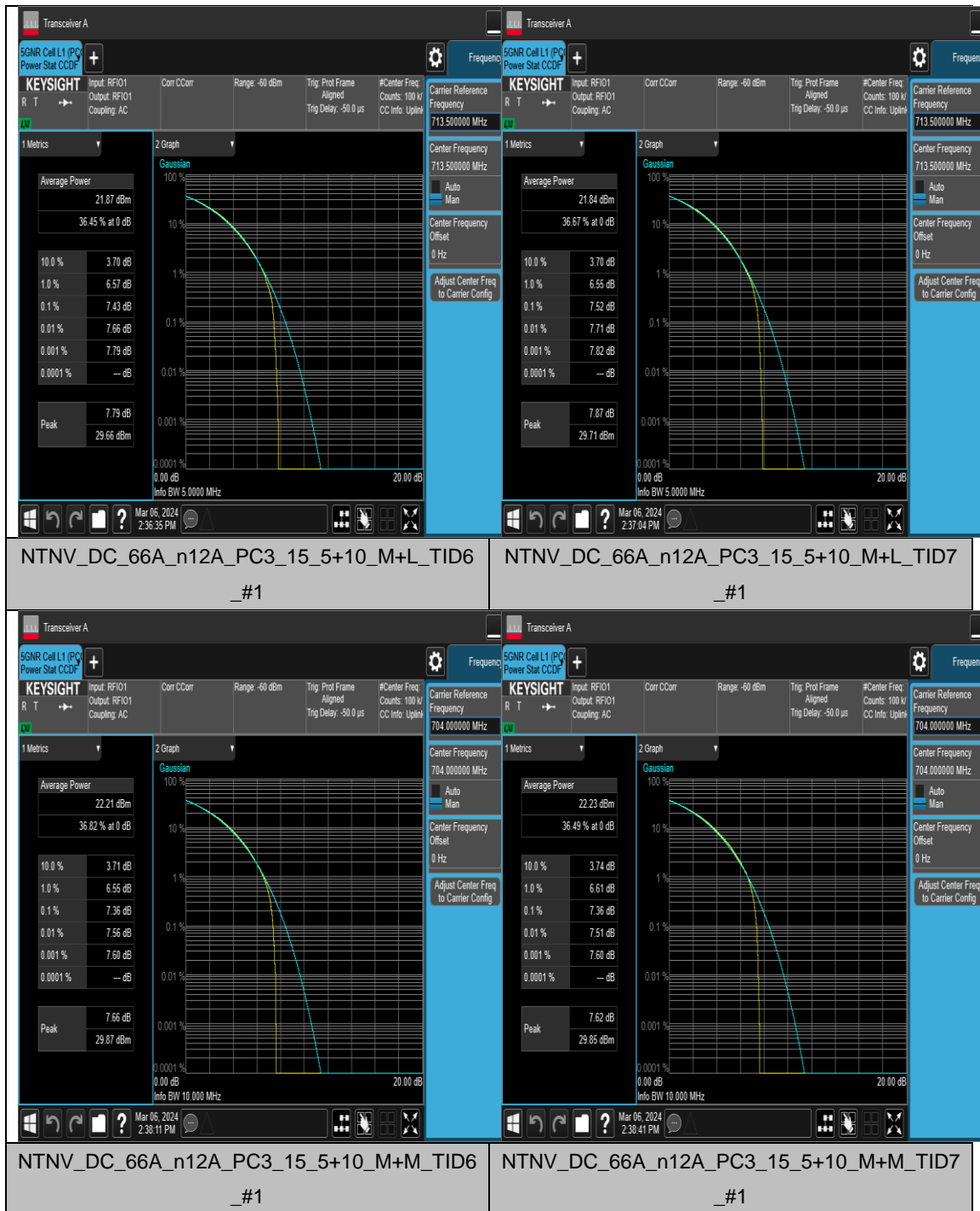
Test Result

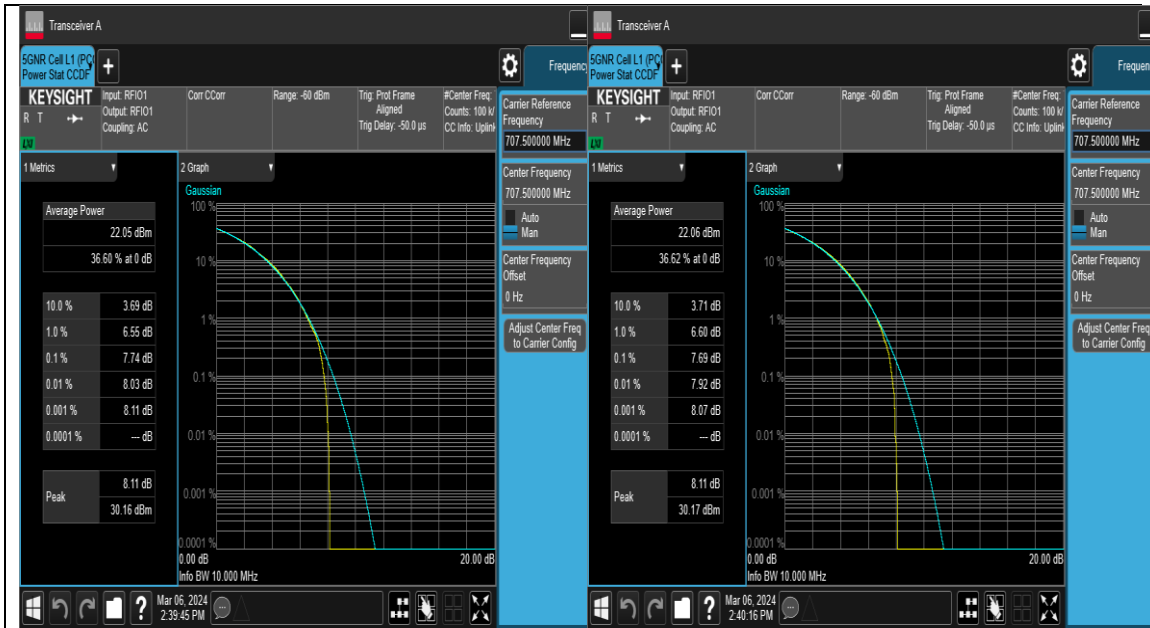
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_66A_n 12A	15	5+5	CP-QPSK	M+L	Outer_Full	7.20	≤13	PASS
DC_66A_n 12A	15	5+5	CP-16QAM	M+L	Outer_Full	7.28	≤13	PASS
DC_66A_n 12A	15	5+5	CP-QPSK	M+M	Outer_Full	7.17	≤13	PASS
DC_66A_n 12A	15	5+5	CP-16QAM	M+M	Outer_Full	7.30	≤13	PASS
DC_66A_n 12A	15	5+5	CP-QPSK	M+H	Outer_Full	7.43	≤13	PASS
DC_66A_n 12A	15	5+5	CP-16QAM	M+H	Outer_Full	7.52	≤13	PASS
DC_66A_n 12A	15	5+10	CP-QPSK	M+L	Outer_Full	7.36	≤13	PASS
DC_66A_n 12A	15	5+10	CP-16QAM	M+L	Outer_Full	7.36	≤13	PASS
DC_66A_n 12A	15	5+10	CP-QPSK	M+M	Outer_Full	7.74	≤13	PASS
DC_66A_n 12A	15	5+10	CP-16QAM	M+M	Outer_Full	7.69	≤13	PASS
DC_66A_n 12A	15	5+10	CP-QPSK	M+H	Outer_Full	7.69	≤13	PASS
DC_66A_n 12A	15	5+10	CP-16QAM	M+H	Outer_Full	7.64	≤13	PASS
DC_66A_n 12A	15	5+15	CP-QPSK	M+L	Outer_Full	7.25	≤13	PASS
DC_66A_n 12A	15	5+15	CP-16QAM	M+L	Outer_Full	7.24	≤13	PASS
DC_66A_n 12A	15	5+15	CP-QPSK	M+M	Outer_Full	7.24	≤13	PASS
DC_66A_n 12A	15	5+15	CP-16QAM	M+M	Outer_Full	7.24	≤13	PASS
DC_66A_n 12A	15	5+15	CP-QPSK	M+H	Outer_Full	7.24	≤13	PASS

DC_66A_n 12A	15	5+15	CP-16QAM	M+H	Outer_Full	7.24	≤13	PASS
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Test Graphs







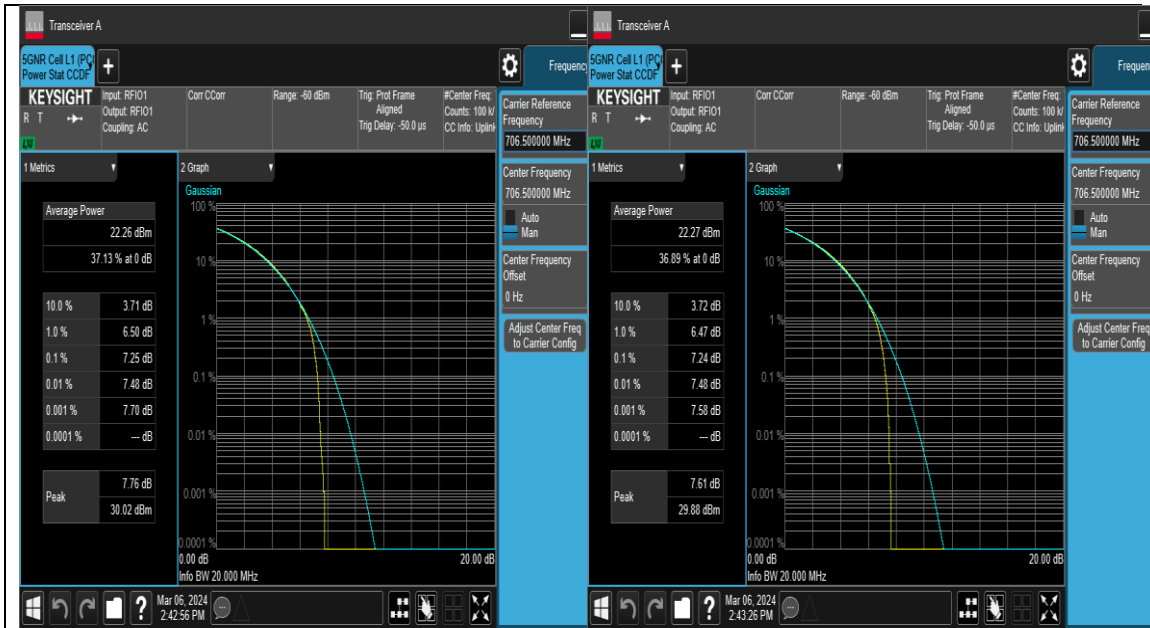
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_#1

NTNV_DC_66A_n12A_PC3_15_5+10_M+H_TID7
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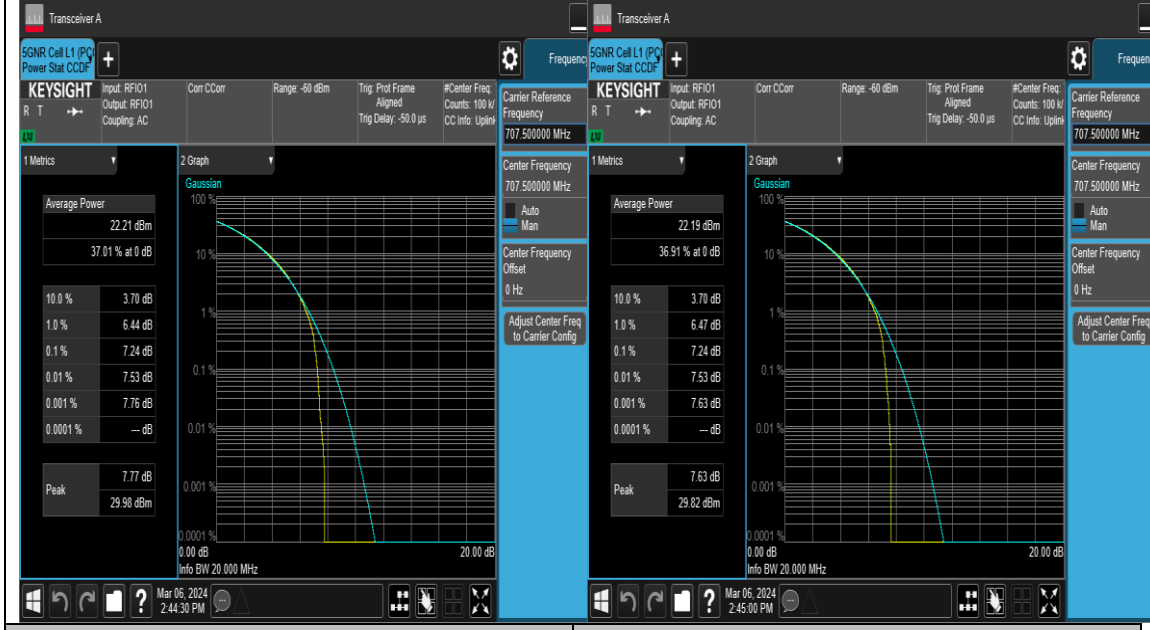
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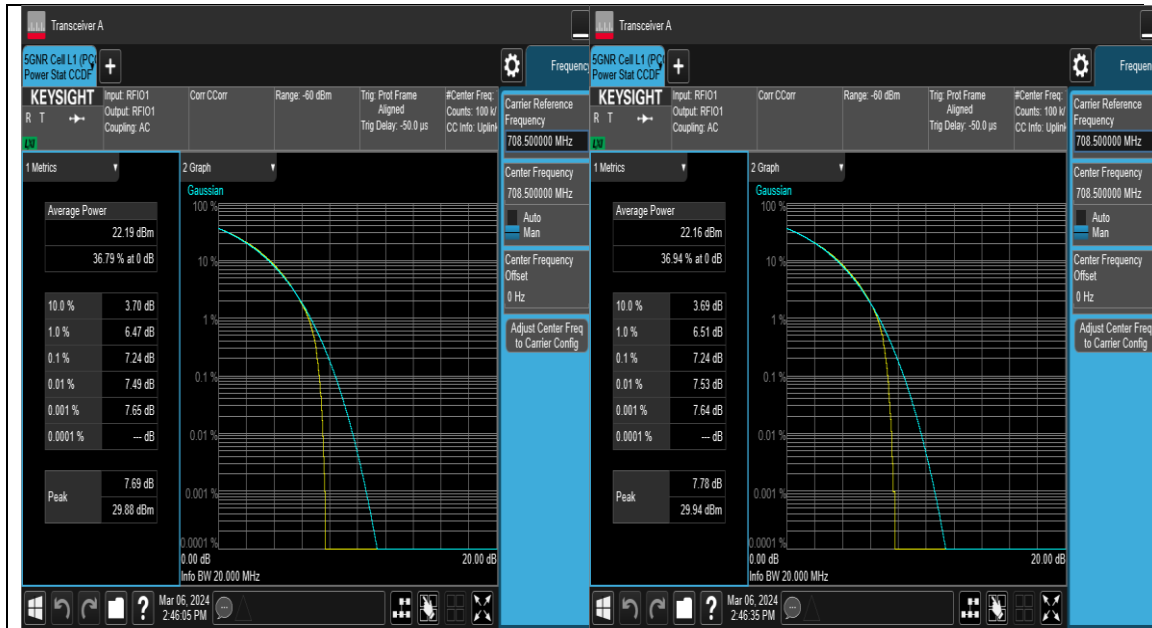
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NTNV_DC_66A_n12A_PC3_15_5+15_M+H_TID7

_#1



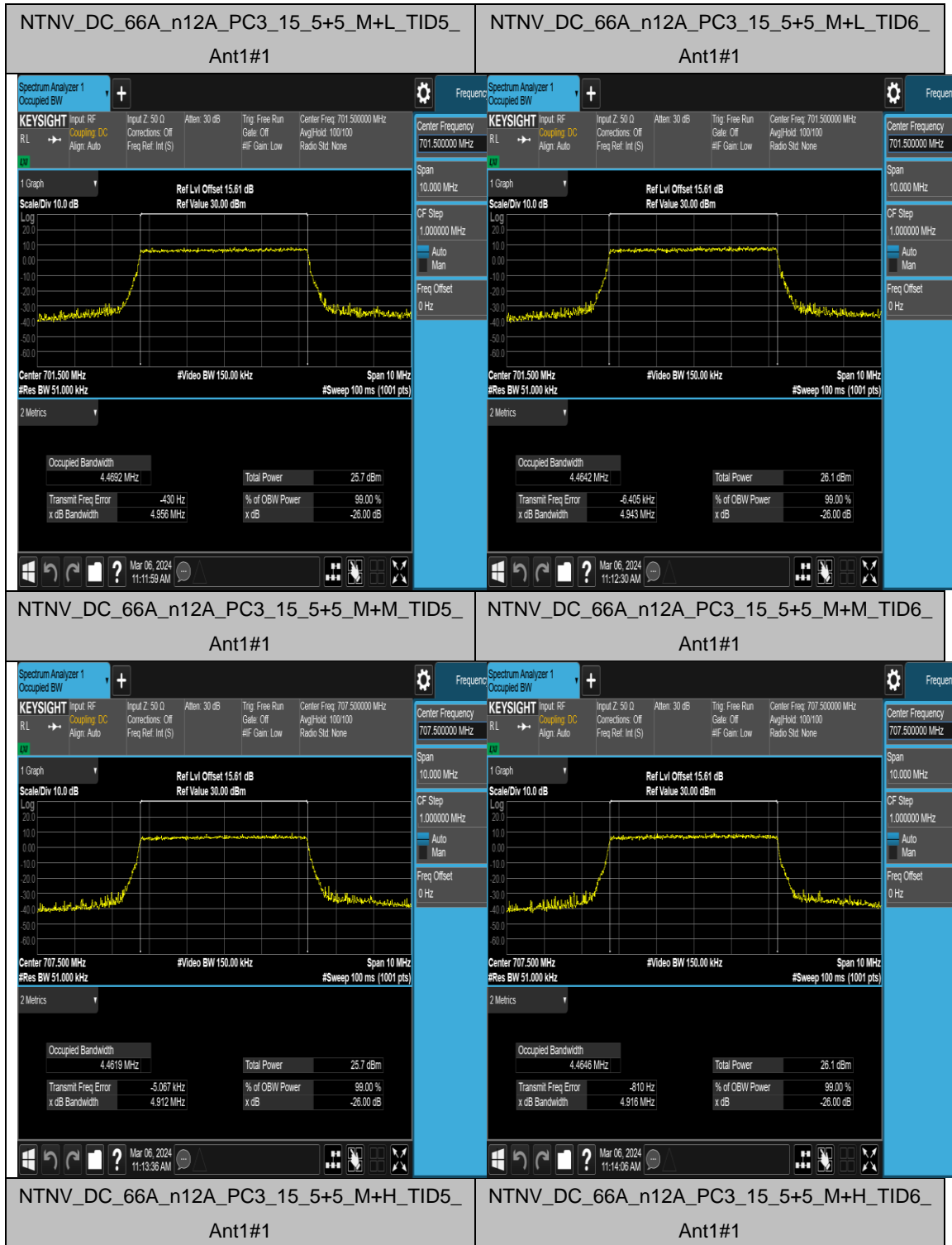
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

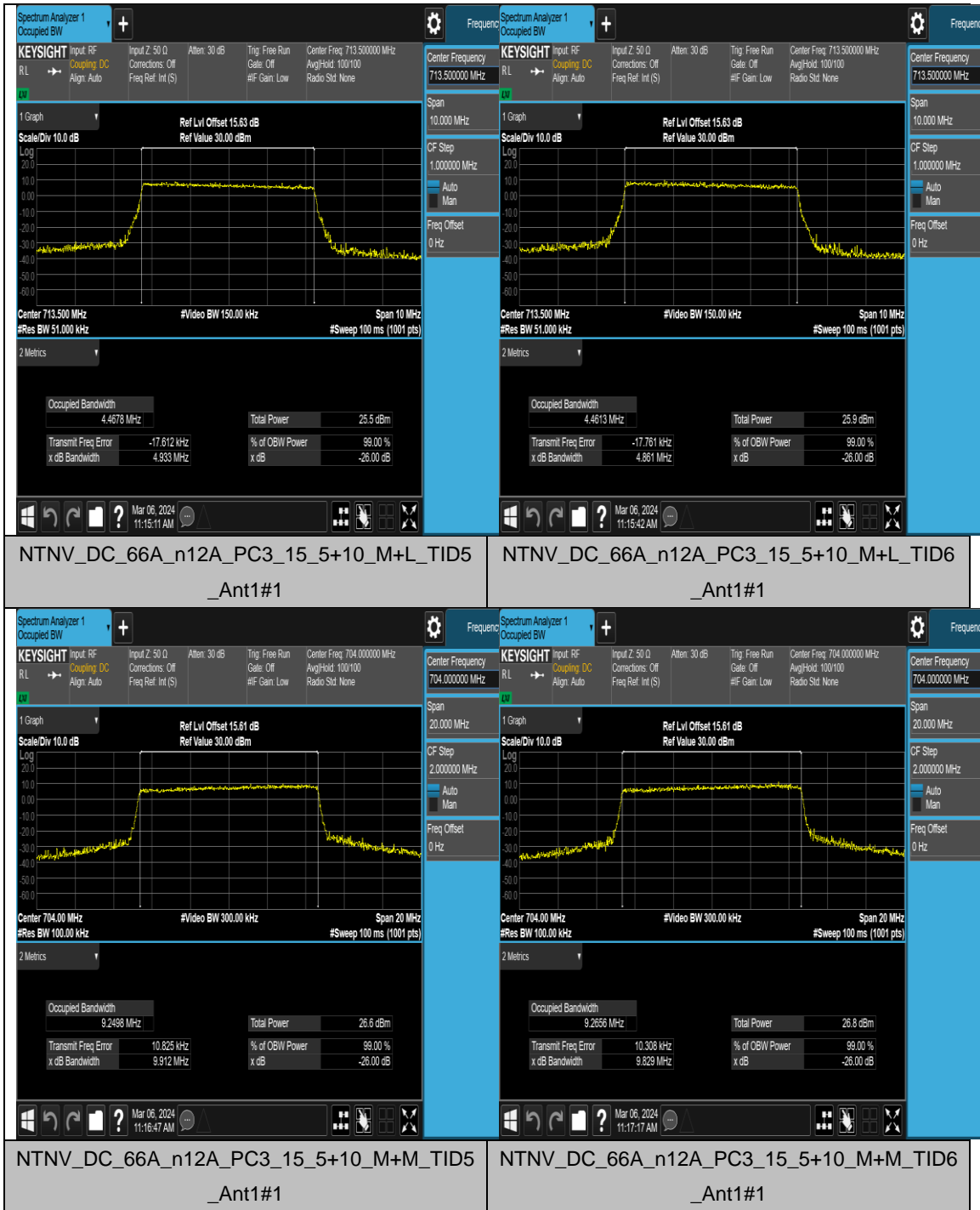
NSA

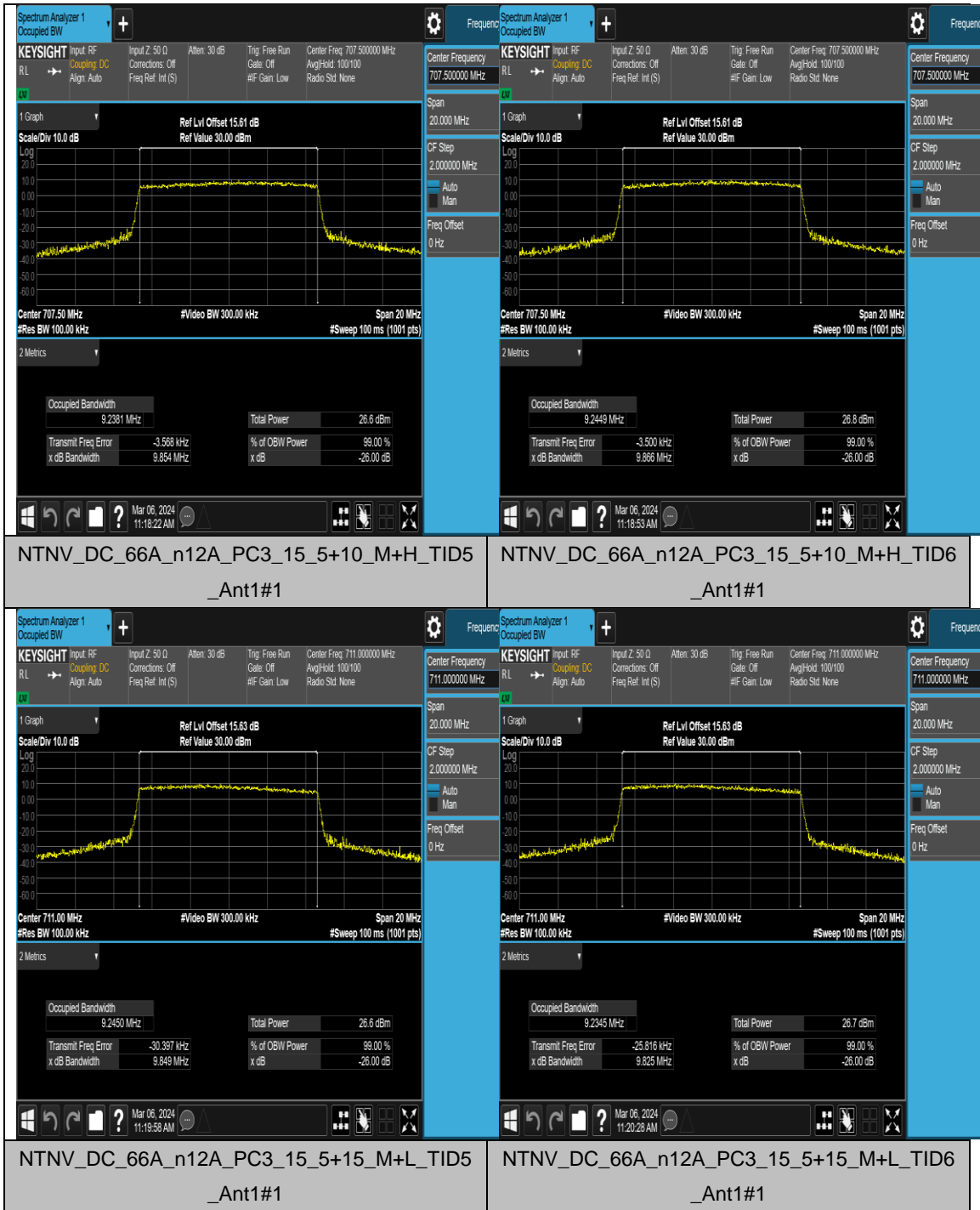
Test Result

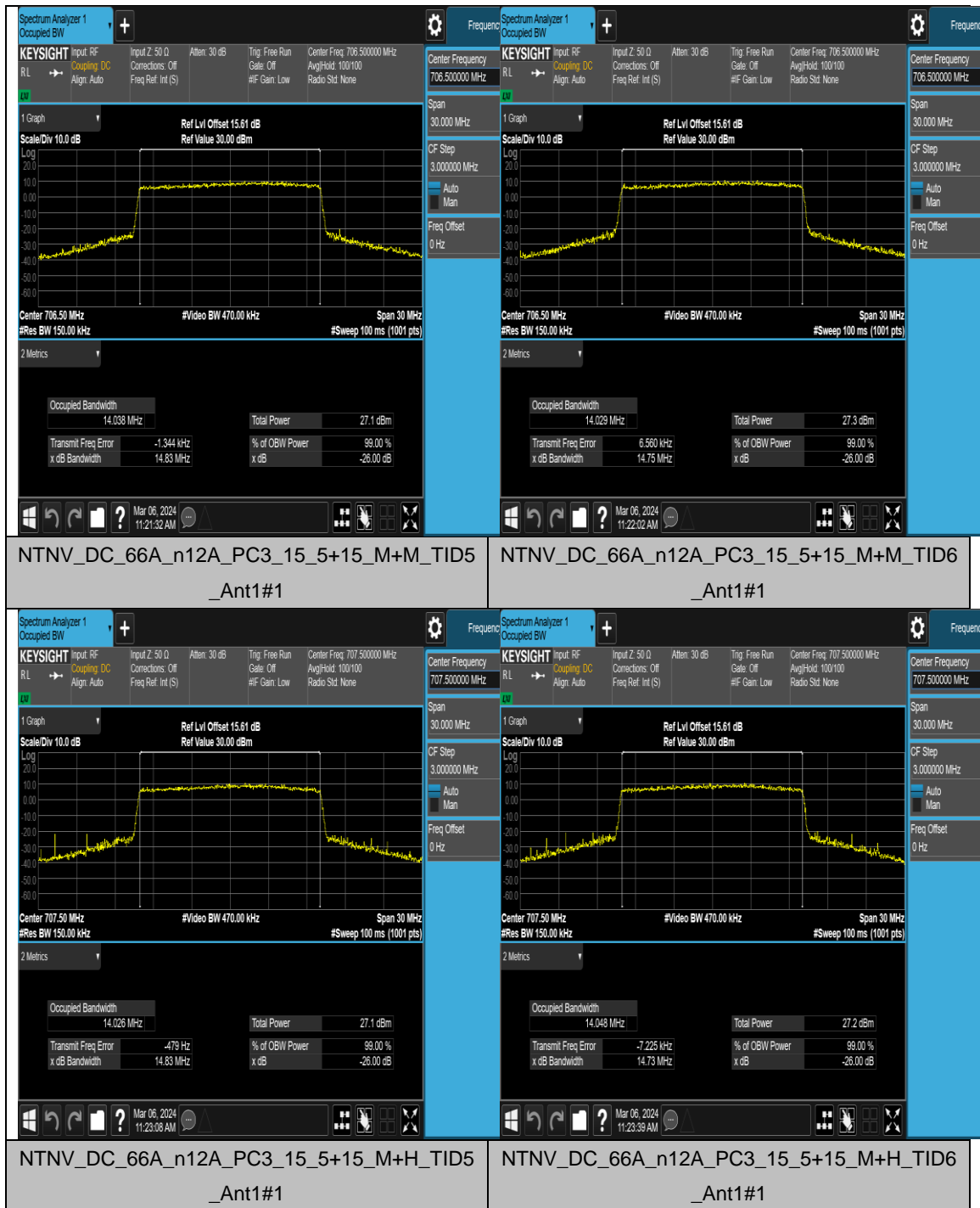
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4692	4.956	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4642	4.943	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4619	4.912	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4646	4.916	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4678	4.933	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4613	4.861	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2498	9.912	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2656	9.829	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2381	9.854	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2449	9.866	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2450	9.849	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2345	9.825	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	14.038	14.83	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	14.029	14.75	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	14.026	14.83	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	14.048	14.73	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	14.003	14.78	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	14.034	14.76	PASS

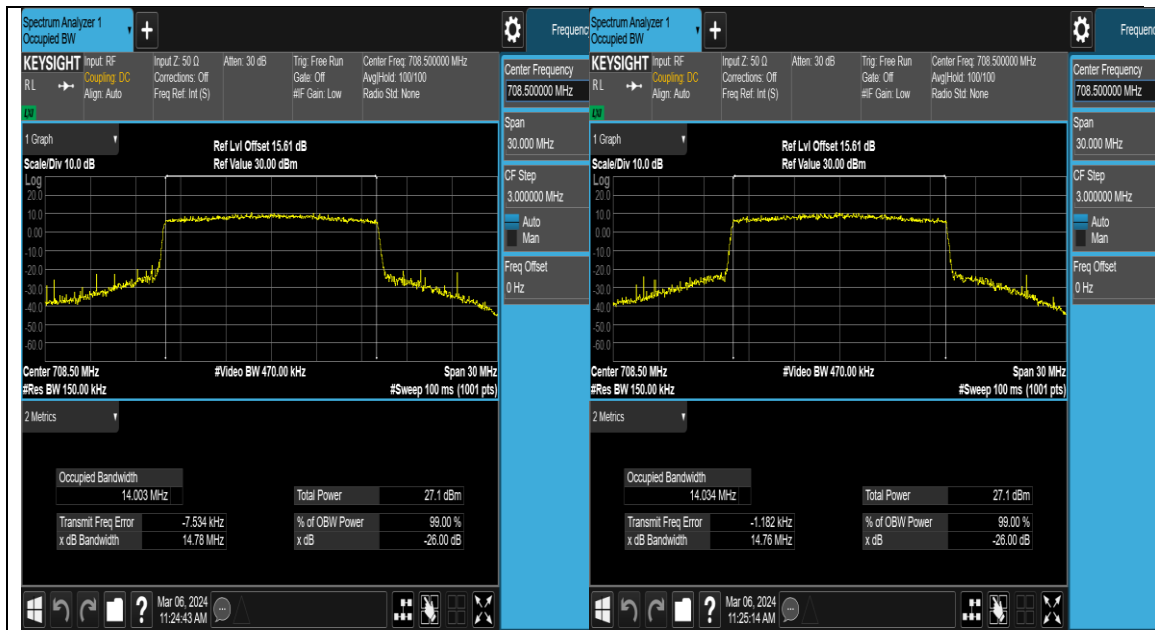
Test Graphs











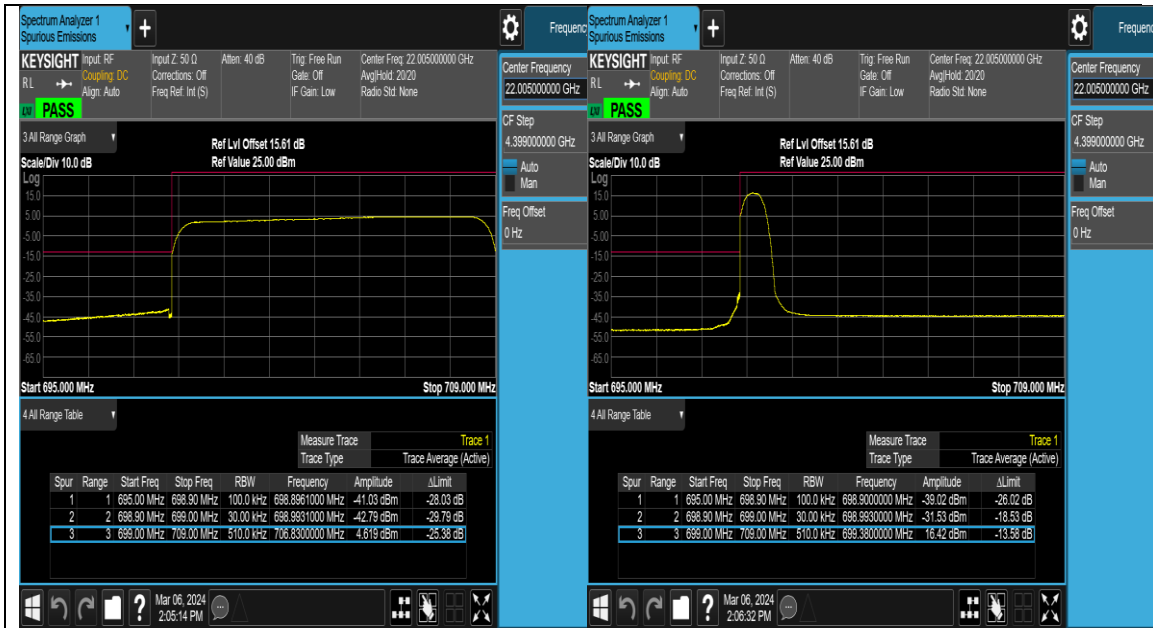
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs





NTNV_DC_66A_n12A_PC3_15_5+10_M+H_TID6
_#1

NTNV_DC_66A_n12A_PC3_15_5+10_M+H_TID5
_#1



NTNV_DC_66A_n12A_PC3_15_5+15_M+L_TID6
_#1

NTNV_DC_66A_n12A_PC3_15_5+15_M+L_TID4
_#1



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_66A_n12A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS

Test Graphs

