

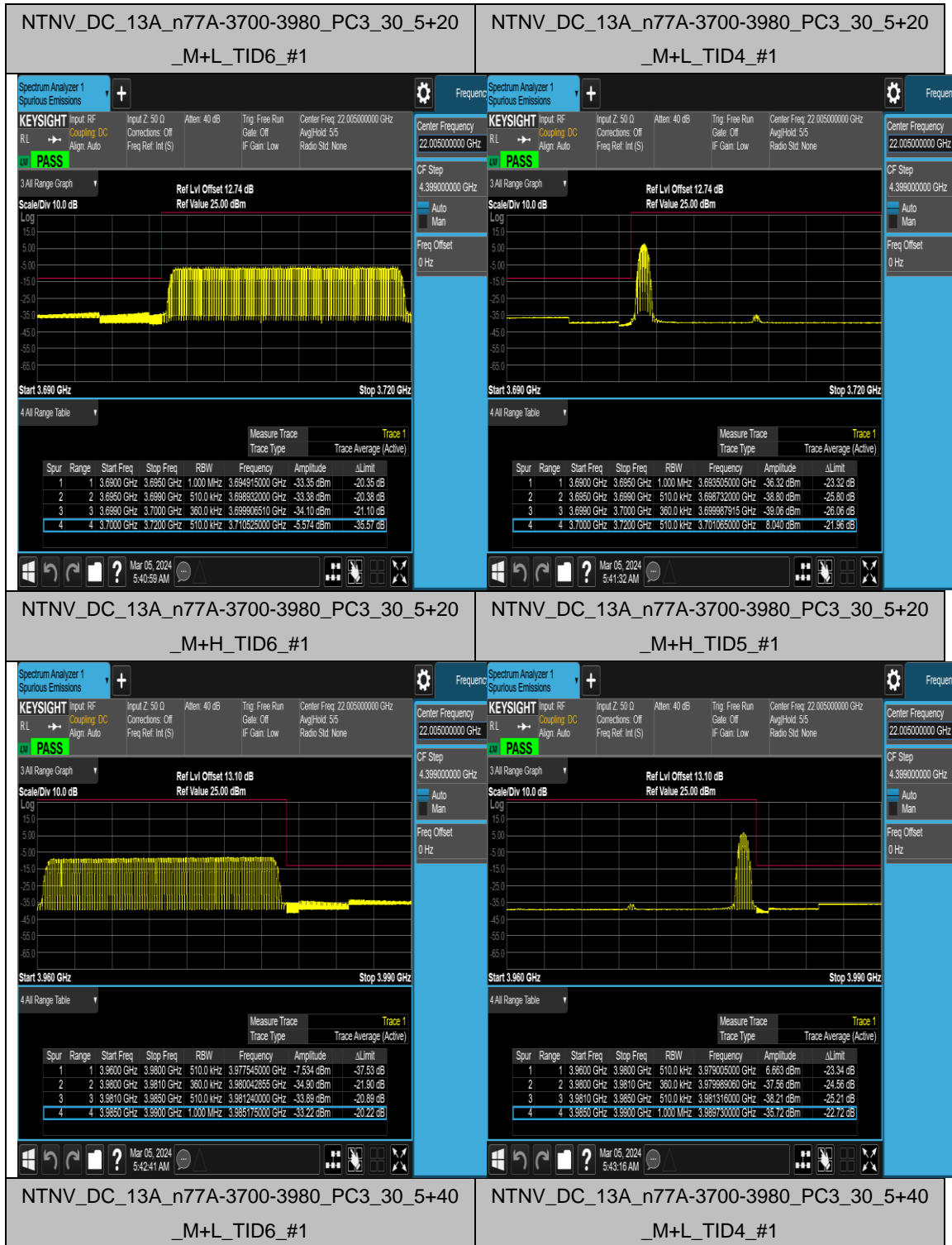
## Appendix D: Band Edge for NSA

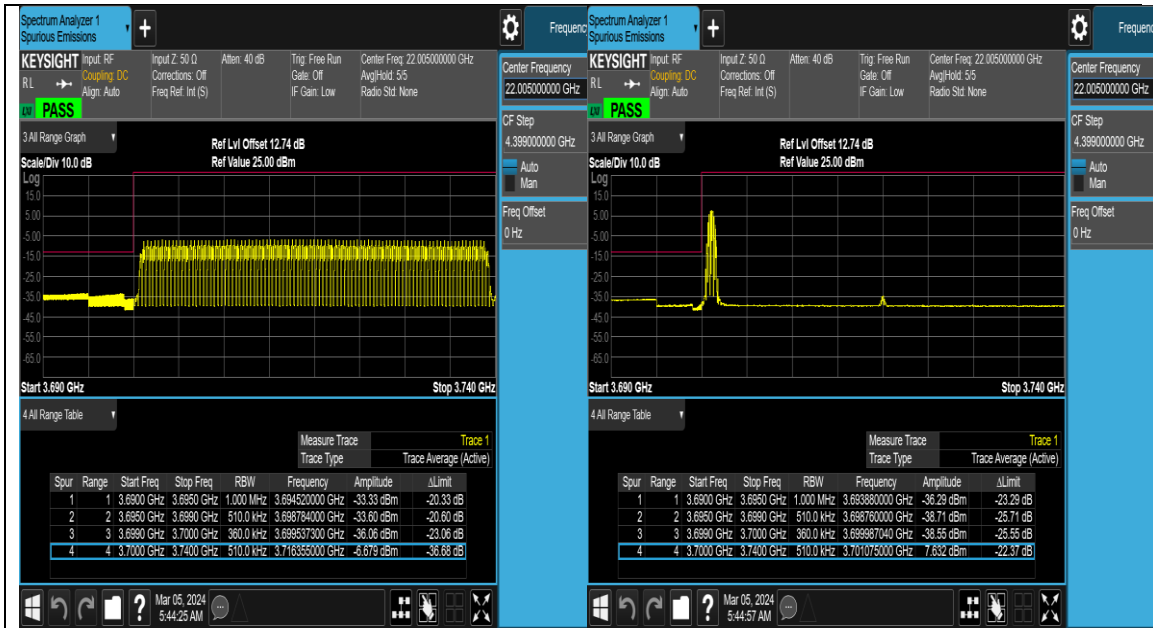
### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+H	Outer_Full	PASS

DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	PASS

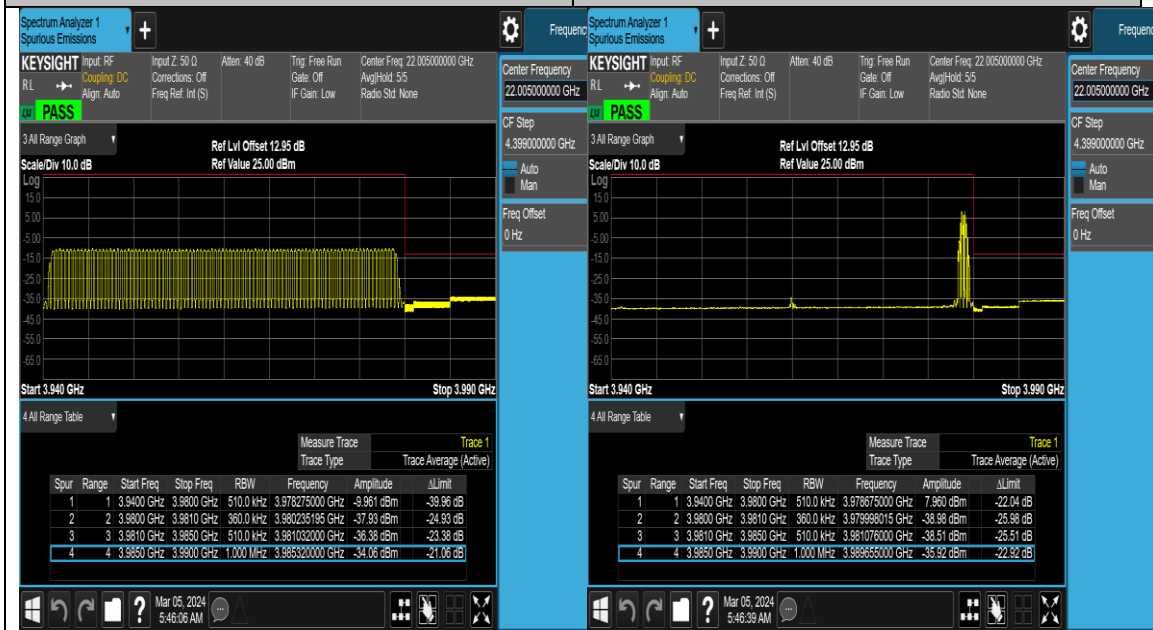
### Test Graphs





NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+40  
\_M+H\_TID6\_#1

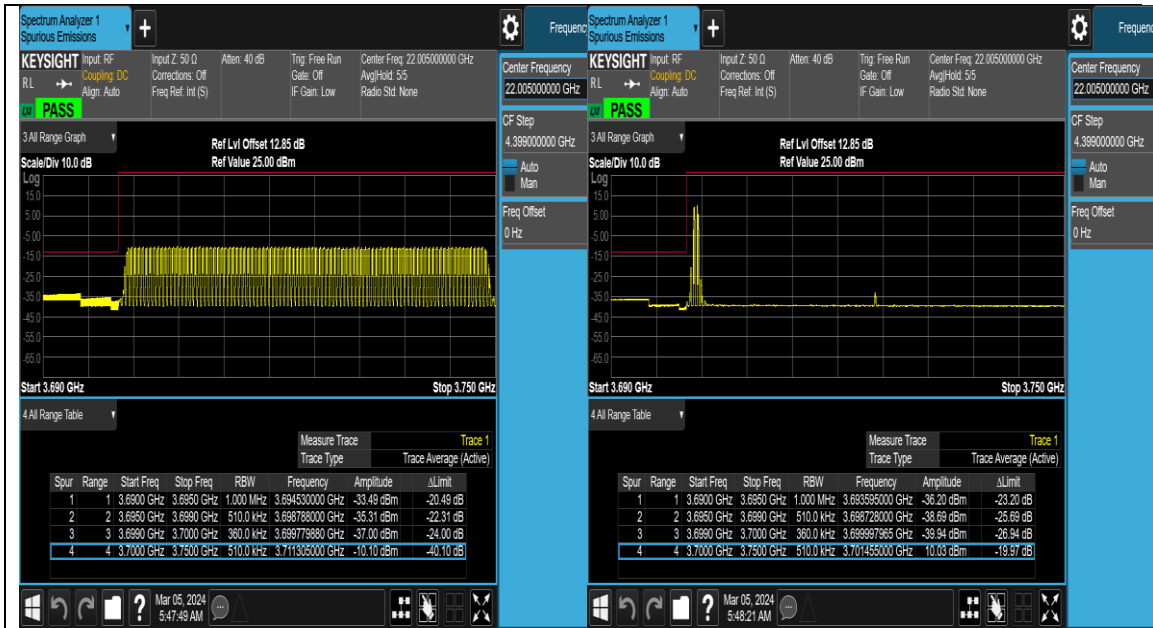
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+40  
\_M+H\_TID5\_#1



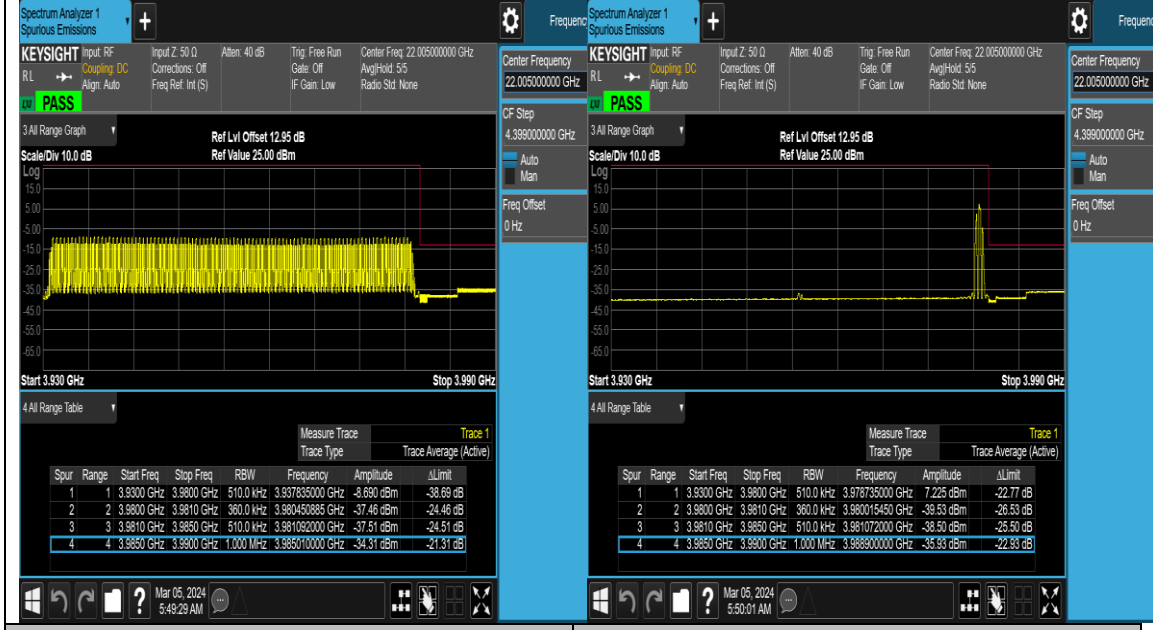
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+50  
\_M+L\_TID6\_#1

NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+50  
\_M+L\_TID4\_#1

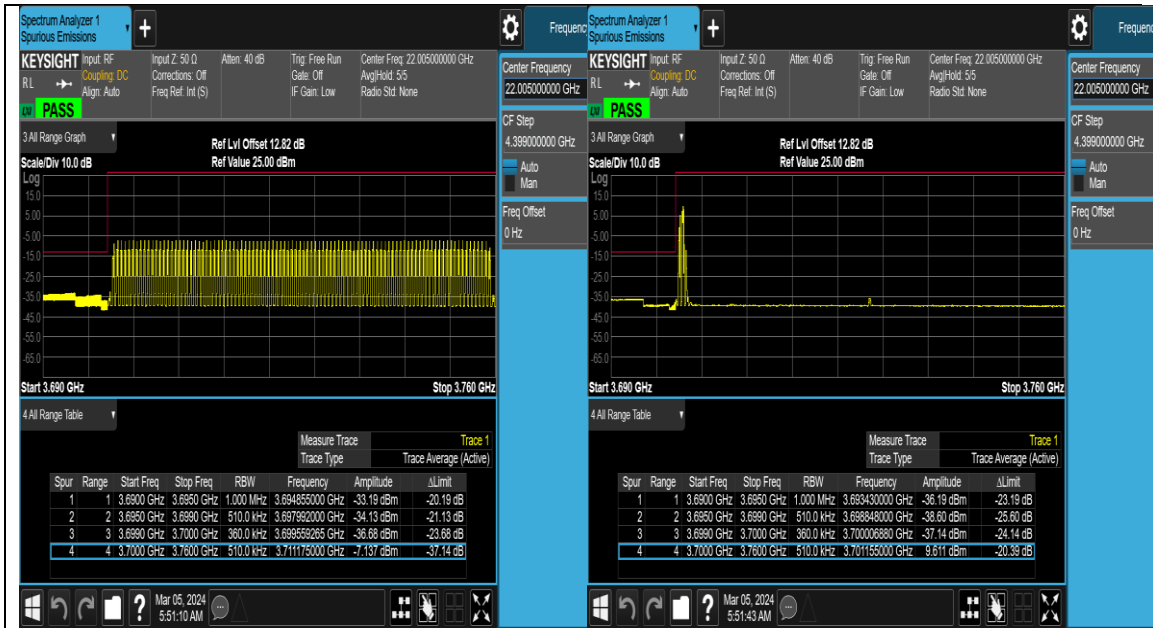




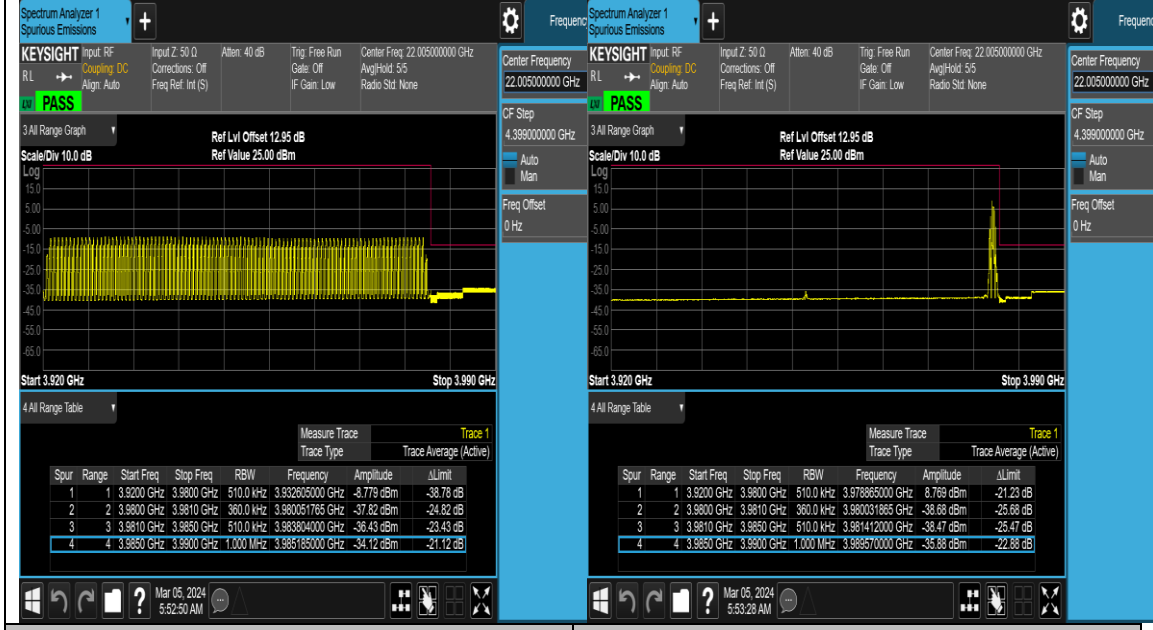
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+50  
\_M+H\_TID6\_#1



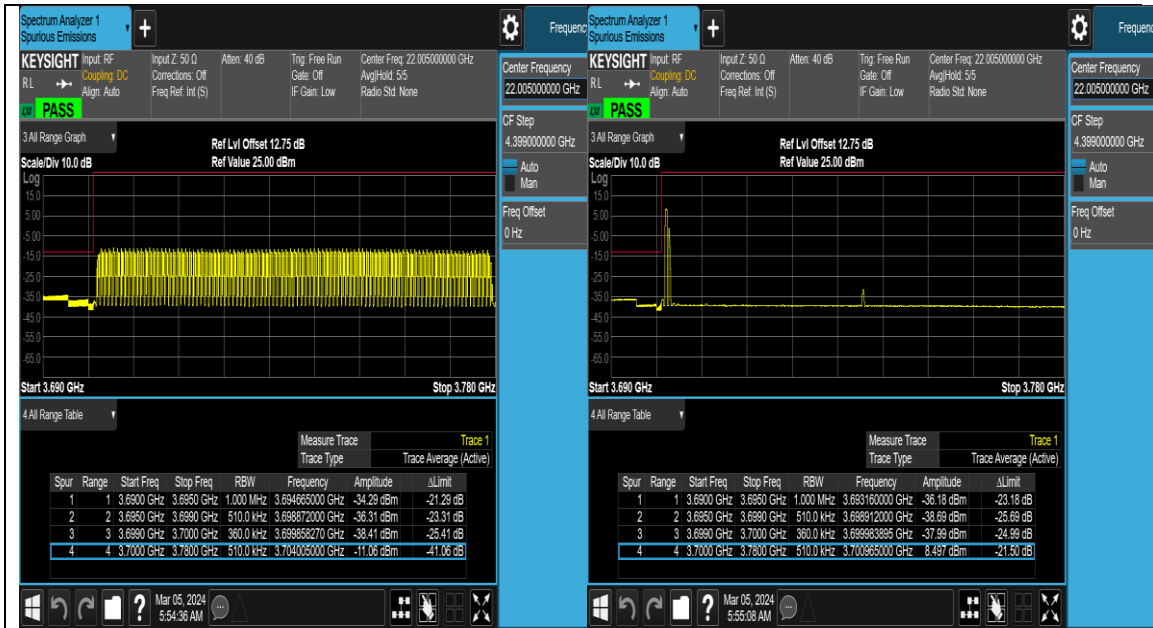
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+60  
\_M+L\_TID4\_#1



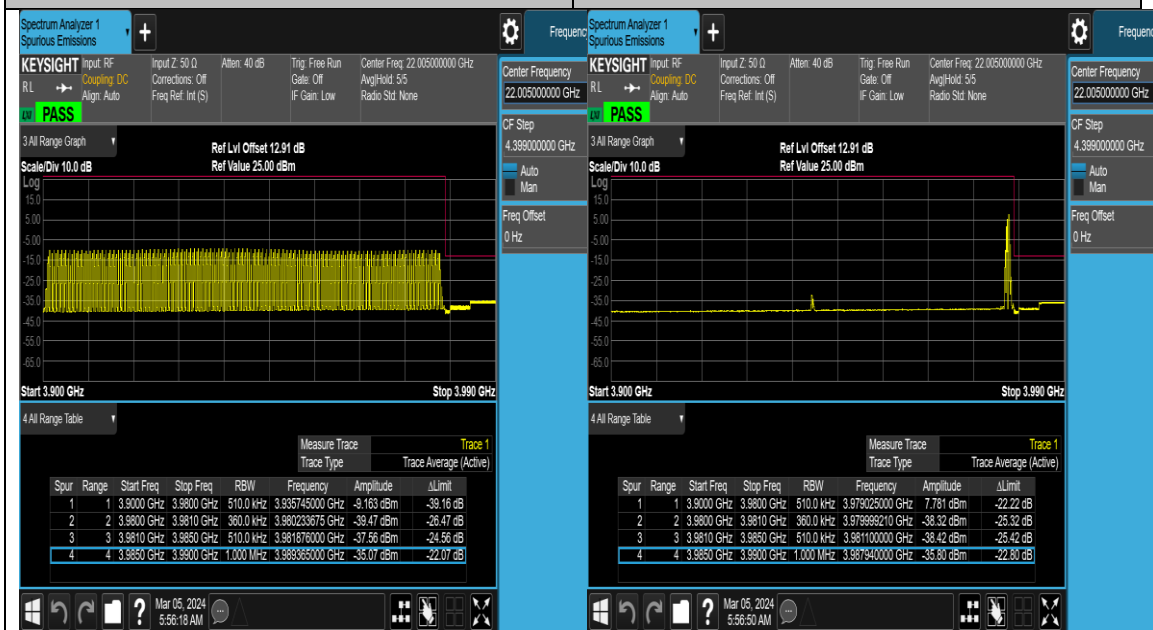
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+60  
 \_M+H\_TID6\_#1



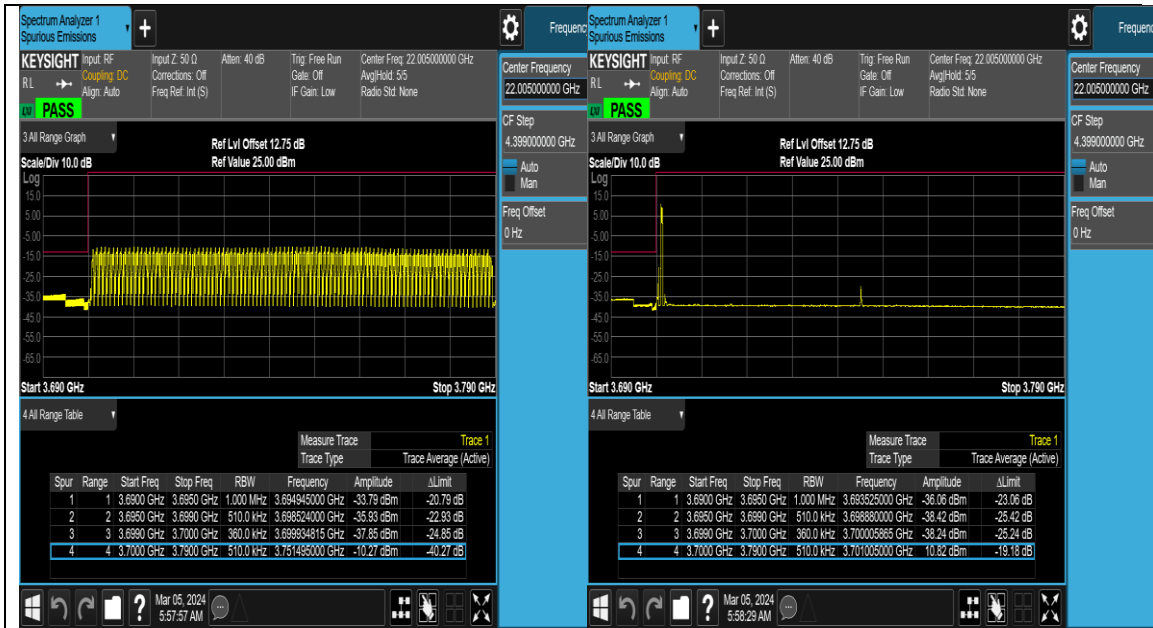
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+80  
 \_M+L\_TID6\_#1



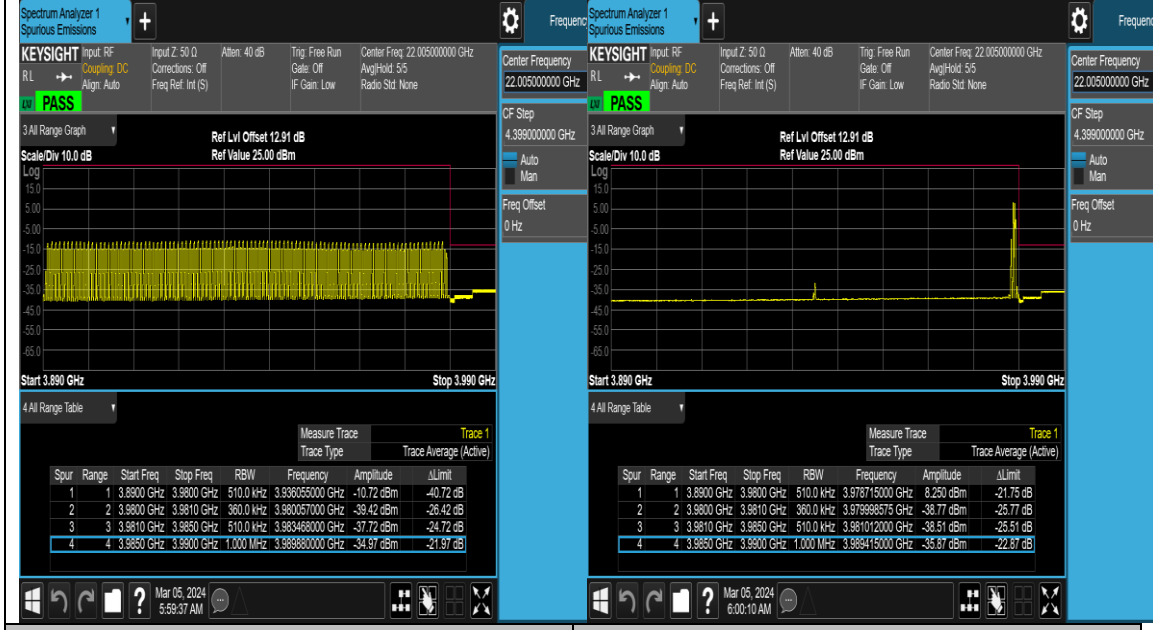
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+80  
 \_M+H\_TID6\_#1



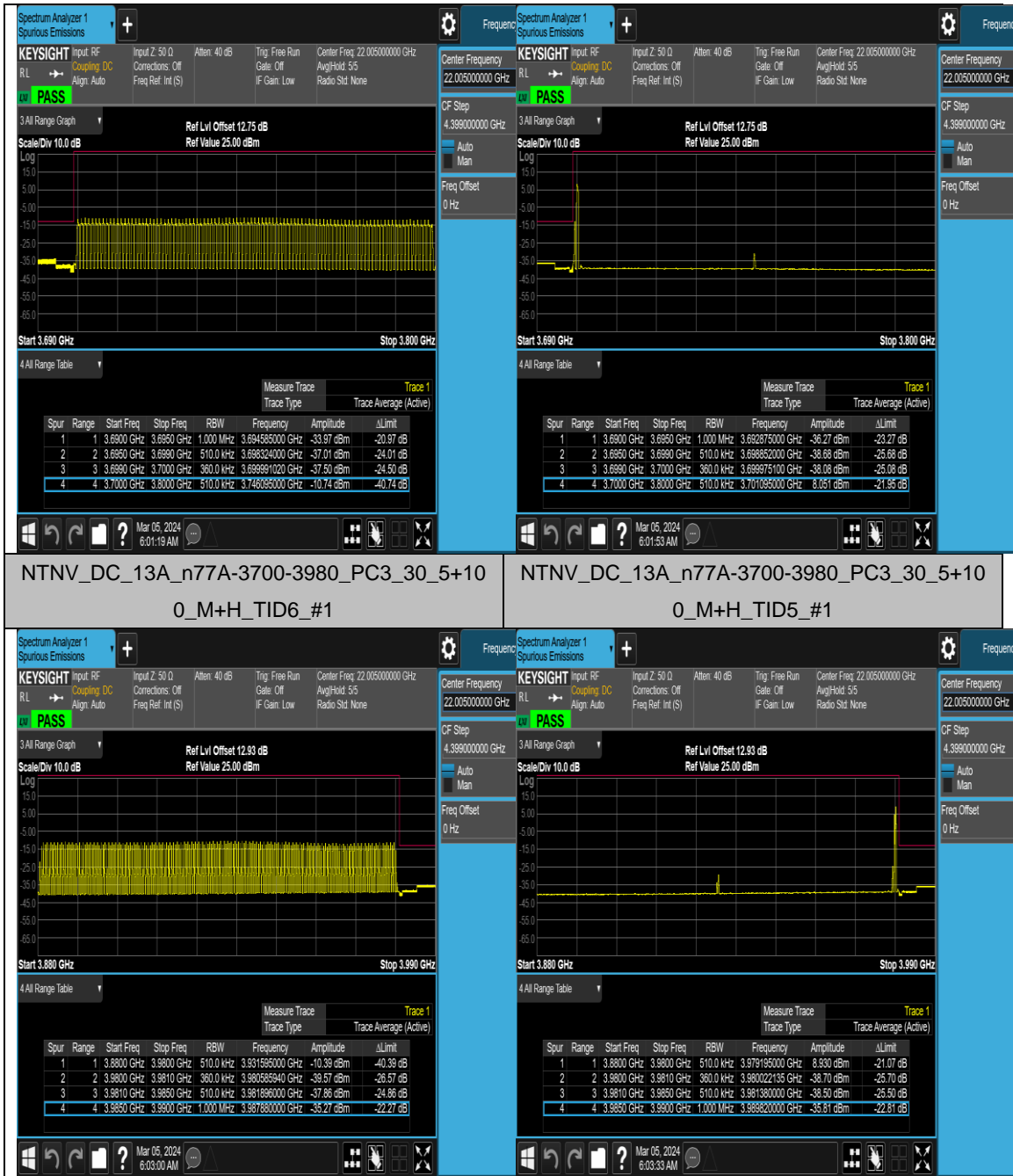
NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+90  
 \_M+L\_TID6\_#1



NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+90  
 \_M+H\_TID6\_#1



NTNV\_DC\_13A\_n77A-3700-3980\_PC3\_30\_5+10  
 0\_M+L\_TID6\_#1



## Appendix E: Conducted Spurious Emission for NSA

### Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS



DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS

DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS



DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS



DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS



DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

-3700-3980									
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS

DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

-3700-3980		0							
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	1000	6000	-13	PASS

DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

-3700-3980		0							
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS



DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS

### Test Graphs

