

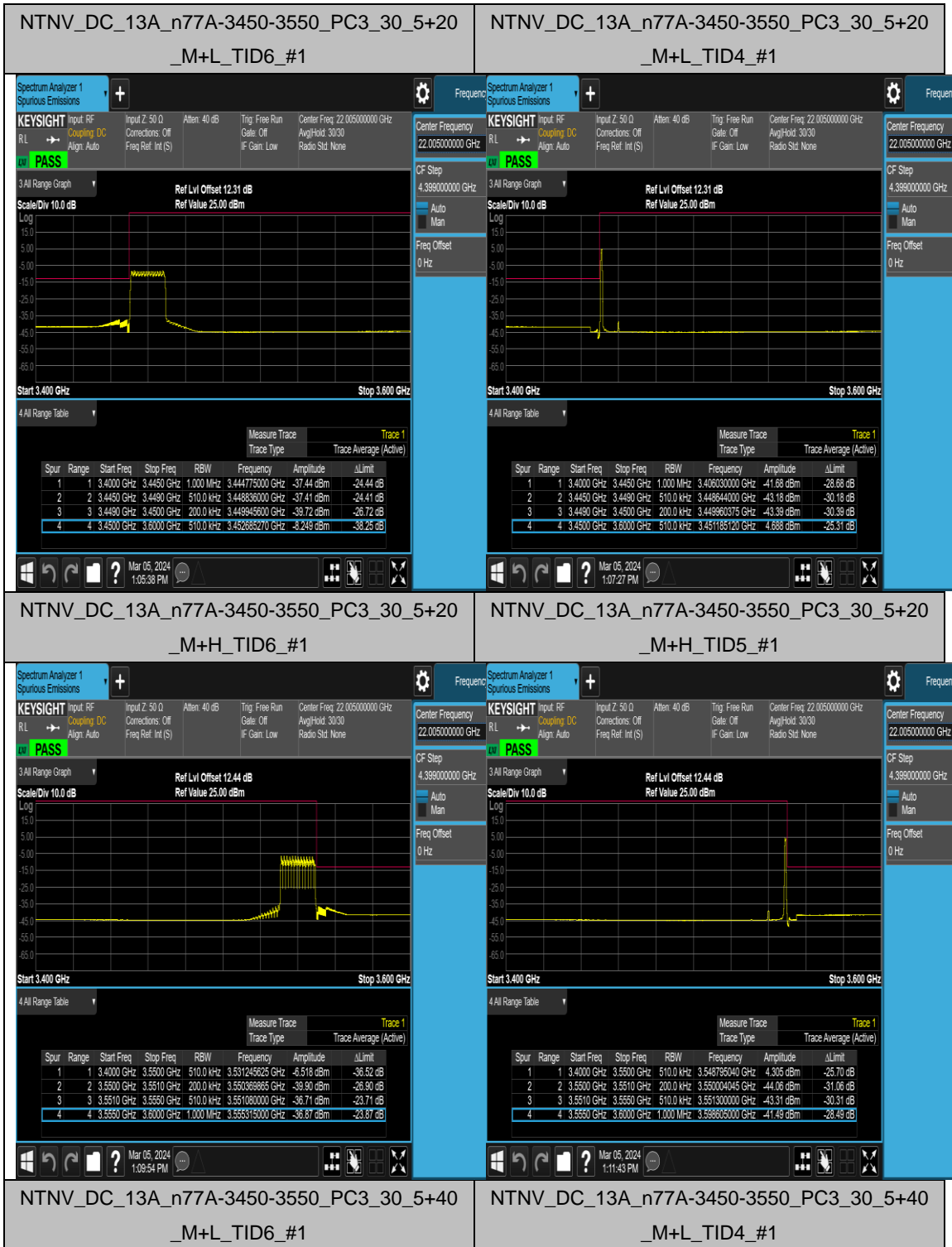
Appendix D: Band Edge for NSA

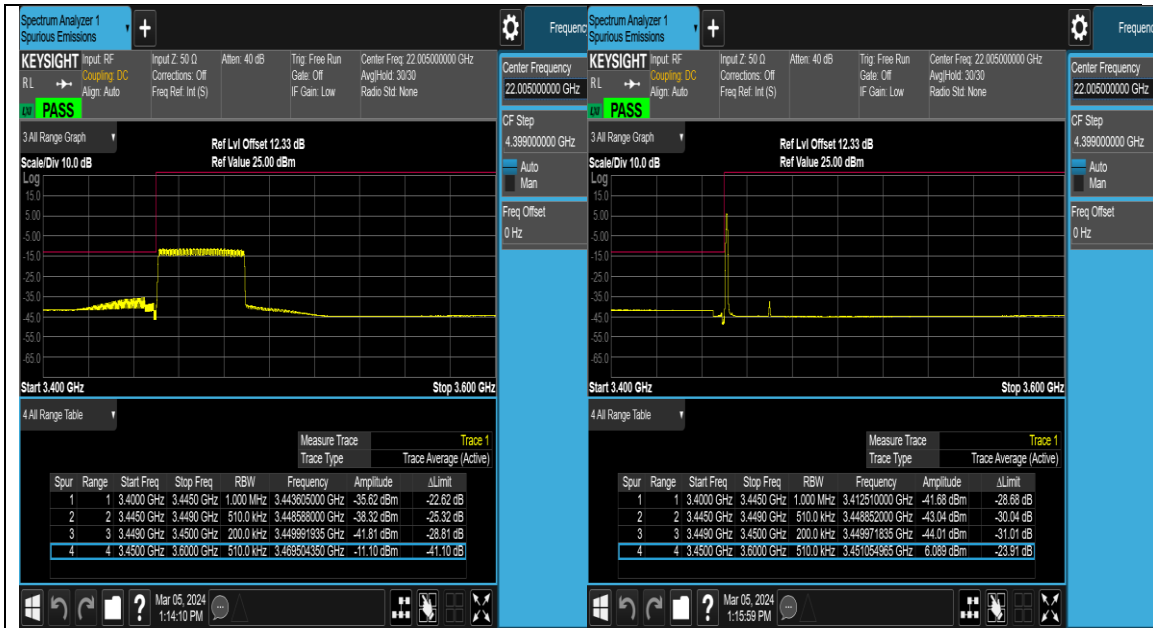
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_13A_n77A-3450-3 550	30	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3450-3 550	30	5+40	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+40	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3450-3 550	30	5+50	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+50	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3450-3 550	30	5+60	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+60	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3450-3 550	30	5+80	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+80	CP-QPSK	M+H	Outer_Full	PASS

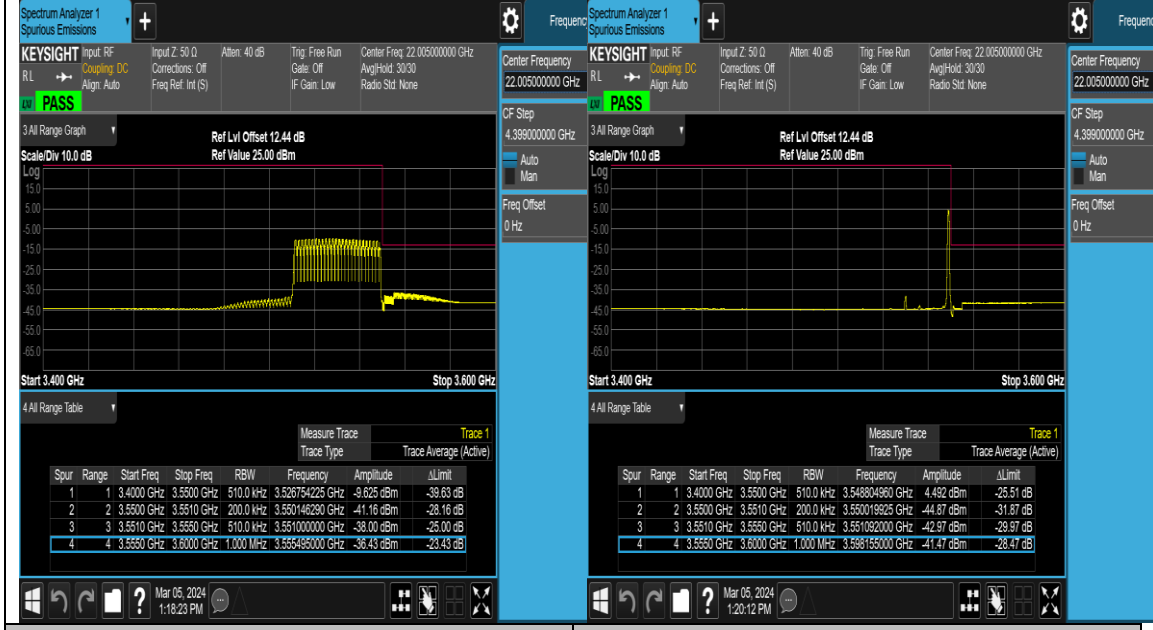
DC_13A_n77A-3450-3 550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3450-3 550	30	5+90	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3450-3 550	30	5+90	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3450-3 550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

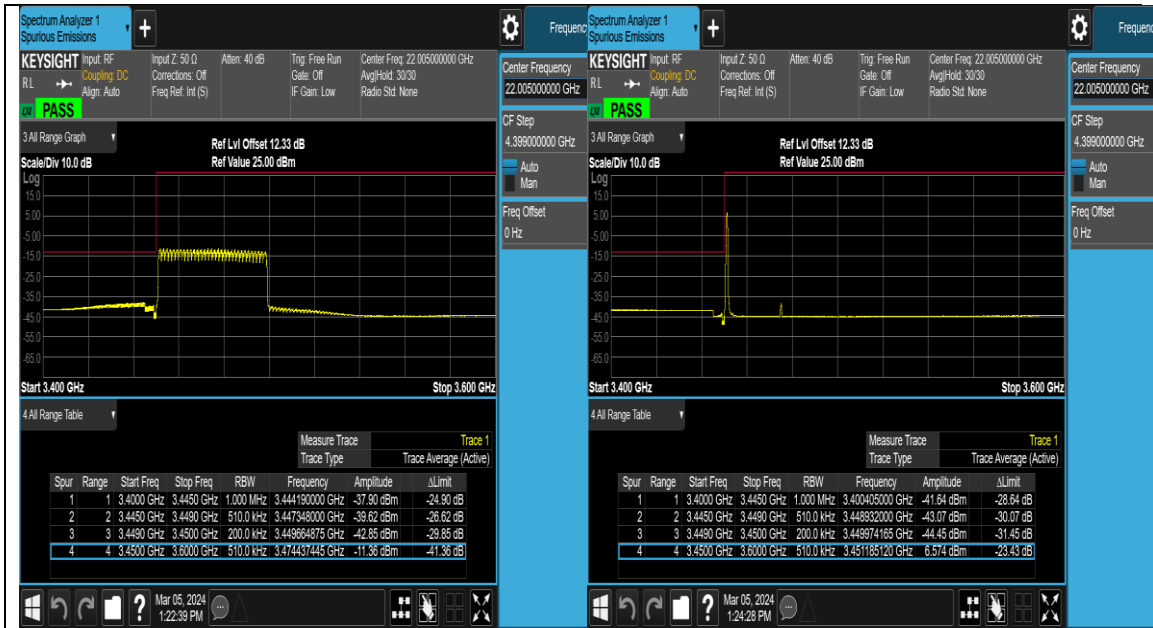




NTNV_DC_13A_n77A-3450-3550_PC3_30_5+40
 _M+H_TID6_#1



NTNV_DC_13A_n77A-3450-3550_PC3_30_5+50
 _M+L_TID6_#1



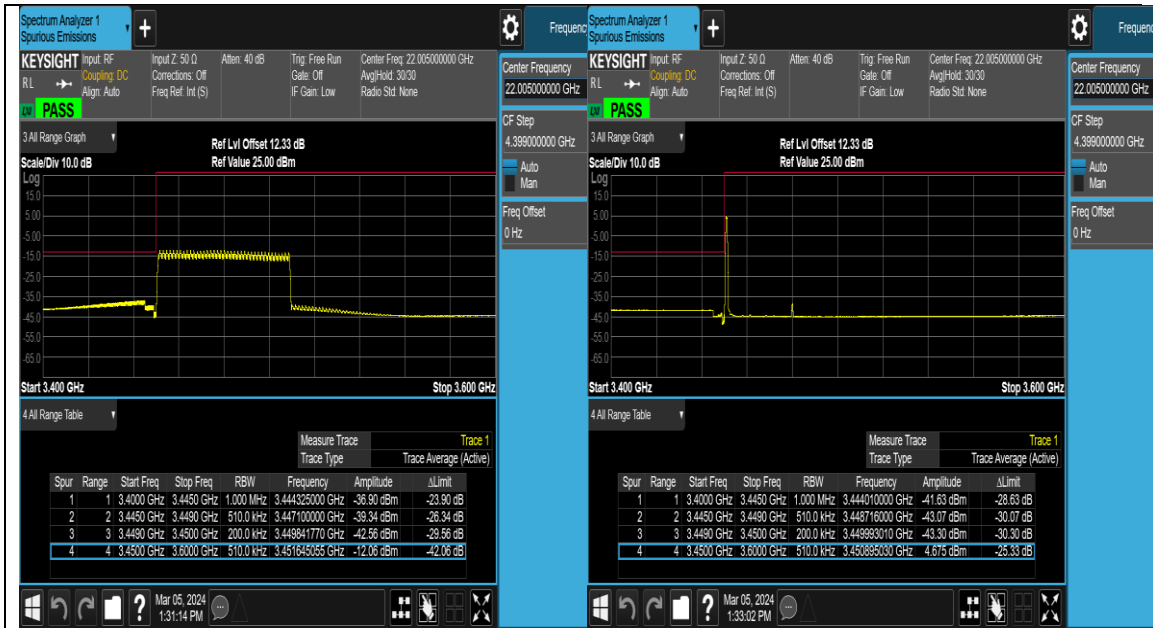
NTNV_DC_13A_n77A-3450-3550_PC3_30_5+50
 _M+H_TID6_#1

NTNV_DC_13A_n77A-3450-3550_PC3_30_5+50
 _M+H_TID5_#1



NTNV_DC_13A_n77A-3450-3550_PC3_30_5+60
 _M+L_TID6_#1

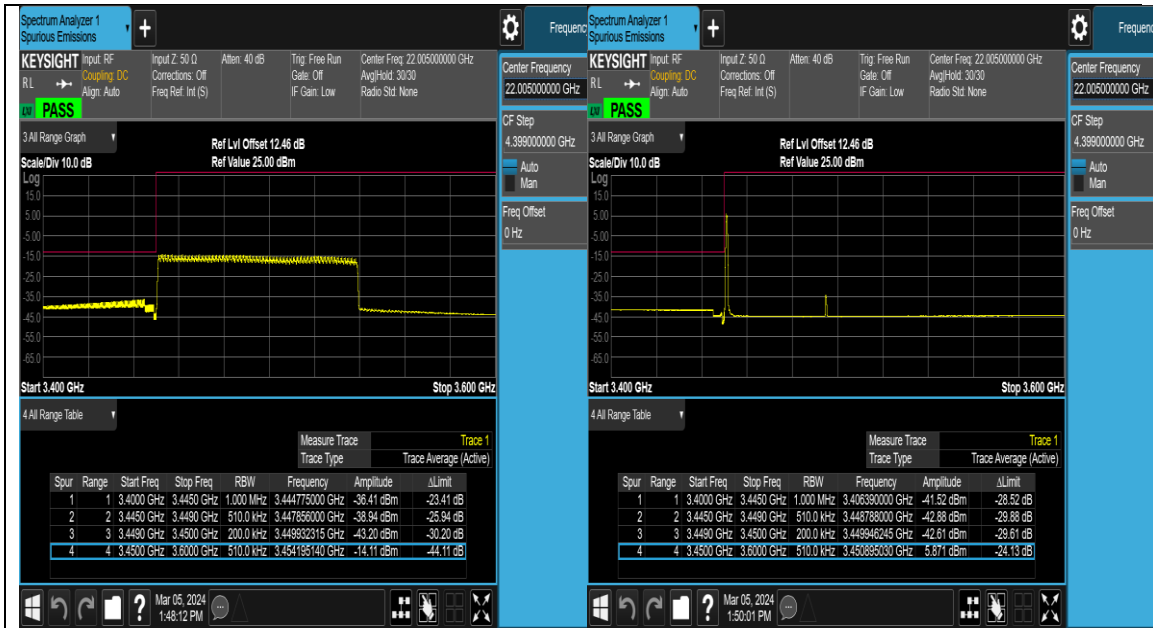
NTNV_DC_13A_n77A-3450-3550_PC3_30_5+60
 _M+L_TID4_#1



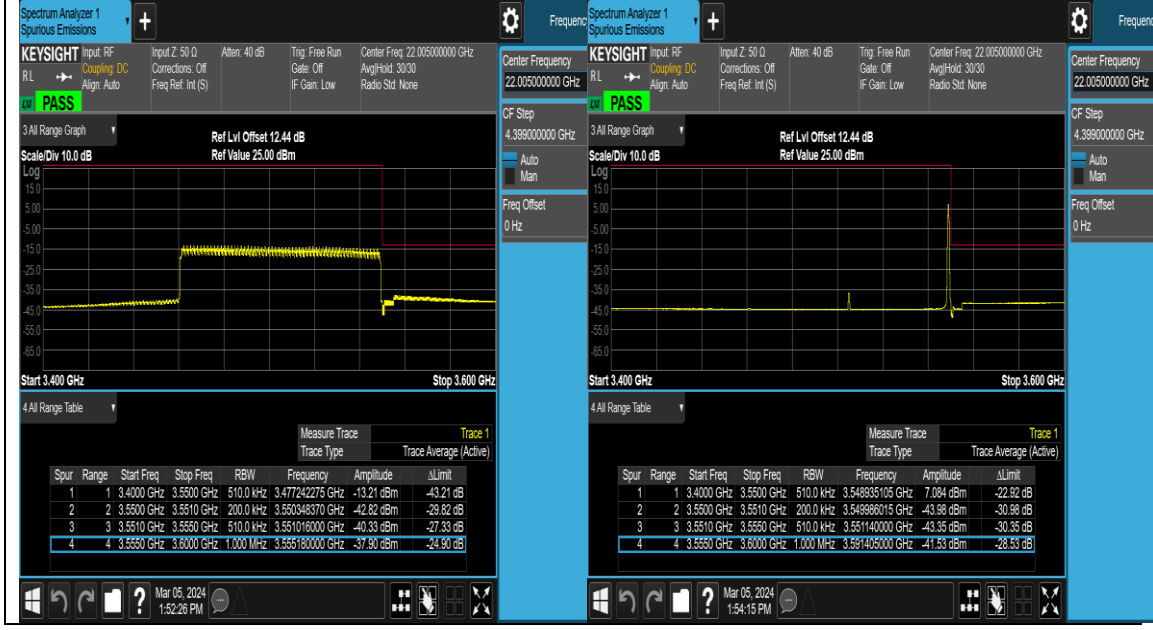
NTNV_DC_13A_n77A-3450-3550_PC3_30_5+60
 _M+H_TID6_#1



NTNV_DC_13A_n77A-3450-3550_PC3_30_5+80
 _M+L_TID6_#1



NTNV_DC_13A_n77A-3450-3550_PC3_30_5+90
 _M+H_TID6_#1



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS

DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS

DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS

DC_13A_n77A -3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS

DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS

DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS

DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS

DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A	30	5+50	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS

DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS

DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS

DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS

DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS

DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS

DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS

DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS

-3450-3550									
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-13	PASS

-3450-3550		0							
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	3000	6000	-13	PASS

DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS

-3450-3550		0							
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS

DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS

-3450-3550		0							
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS

DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	3000	6000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_13A_n77A -3450-3550	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS

Test Graphs

