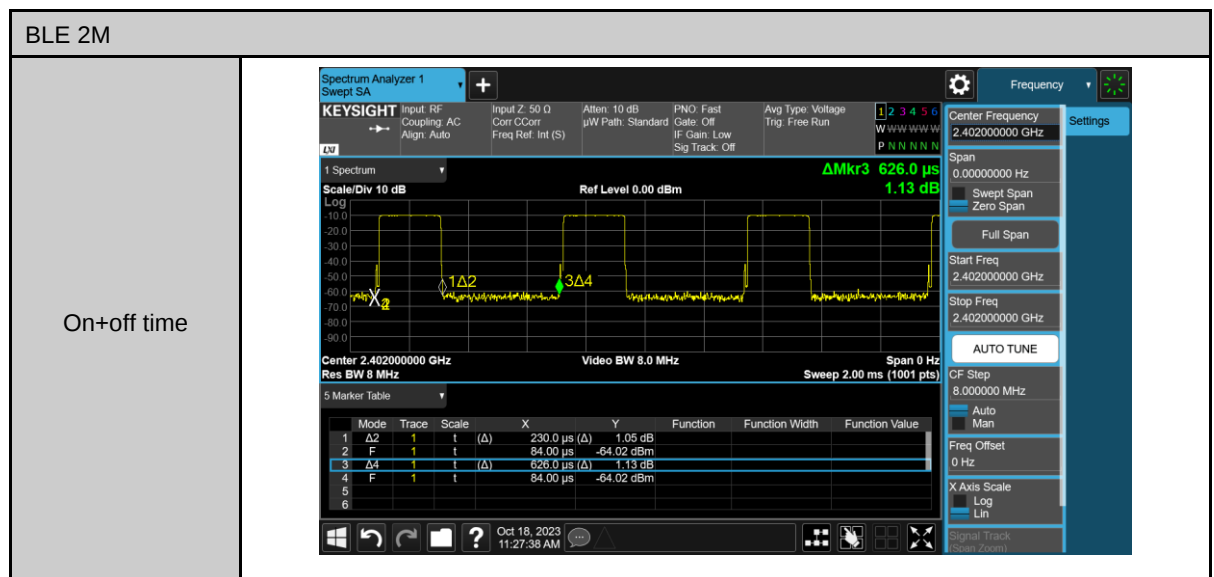
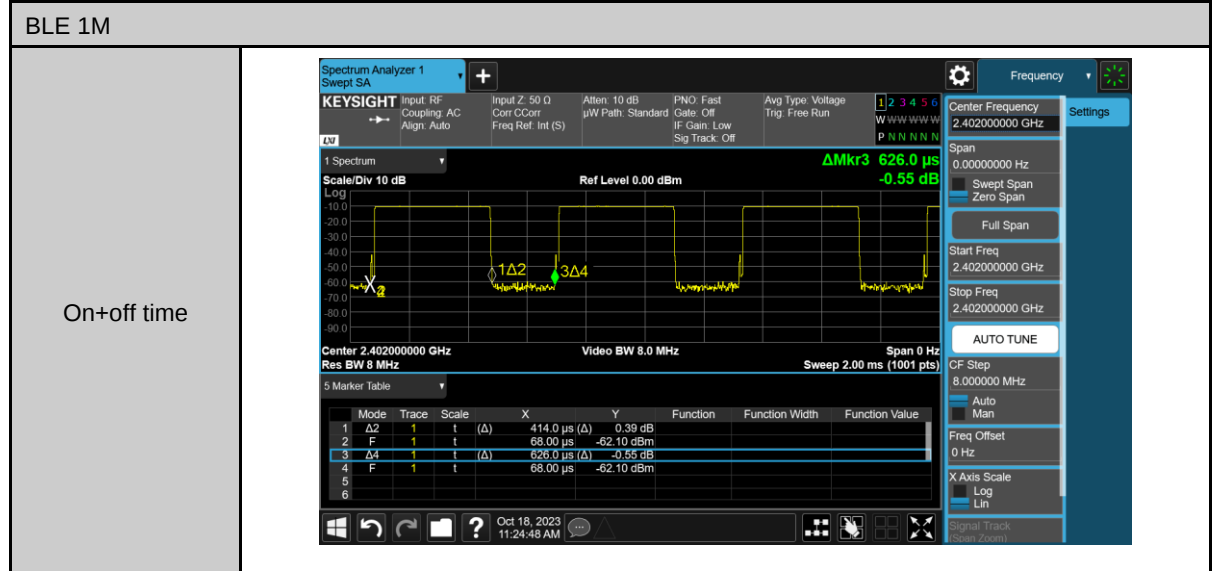


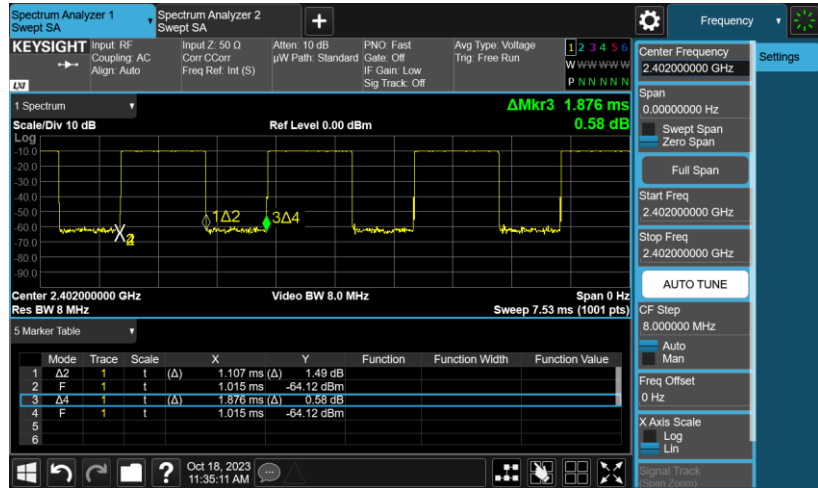
Appendix B. Test Plots

Duty cycle



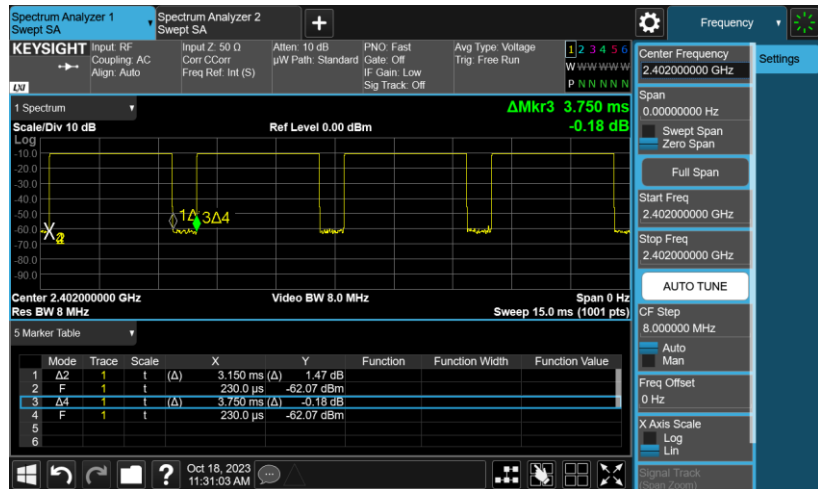
BLR C2

On+off time



BLR C8

On+off time



Out of Band Conducted Spurious Emission

BLE 2M

Low ch



Mid ch

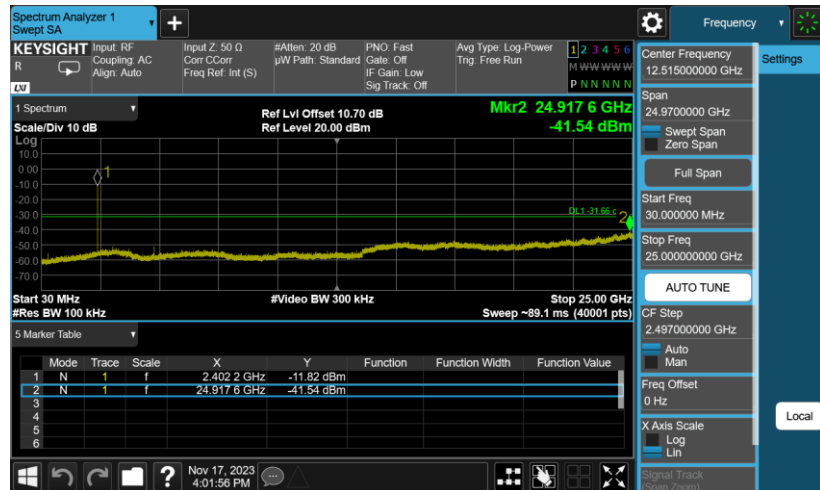


High ch



BLR C8

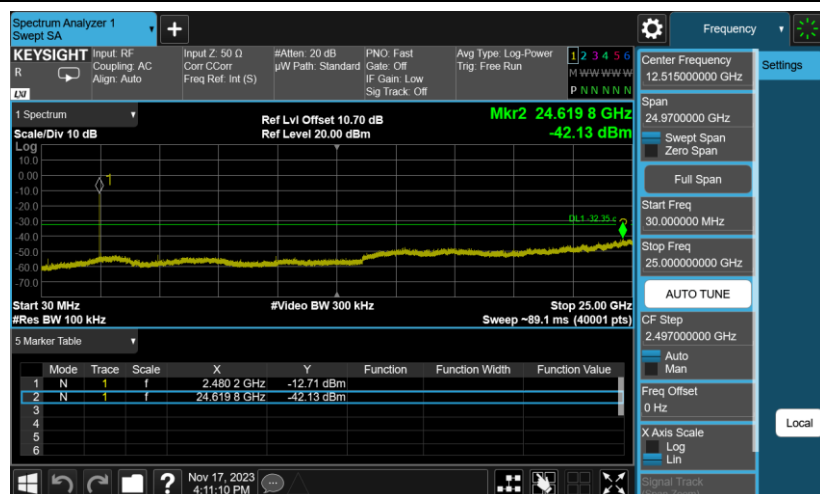
Low ch



Mid ch



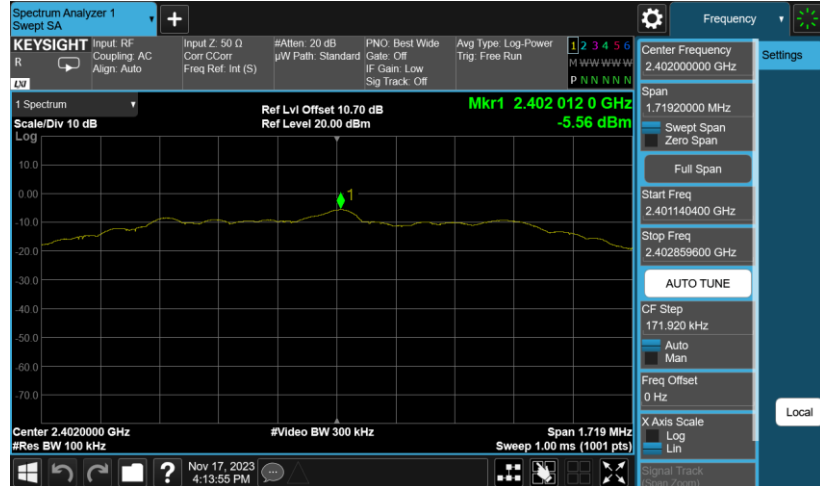
High ch



Reference level

BLE 2M

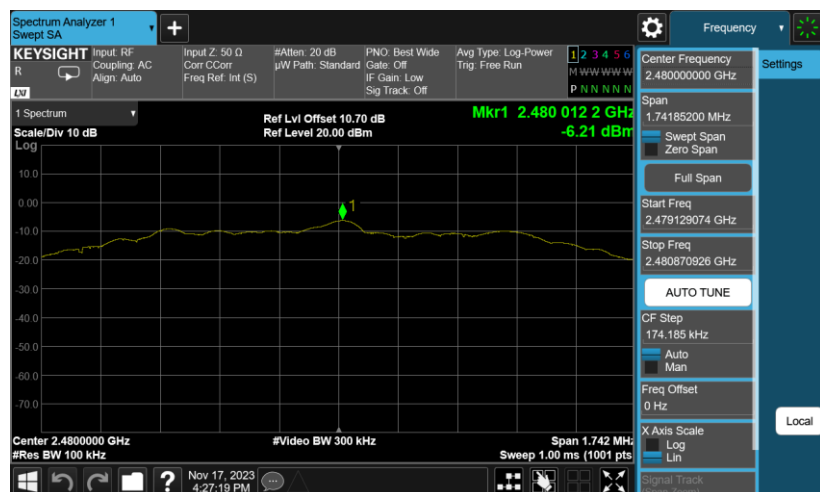
Low ch



Mid ch

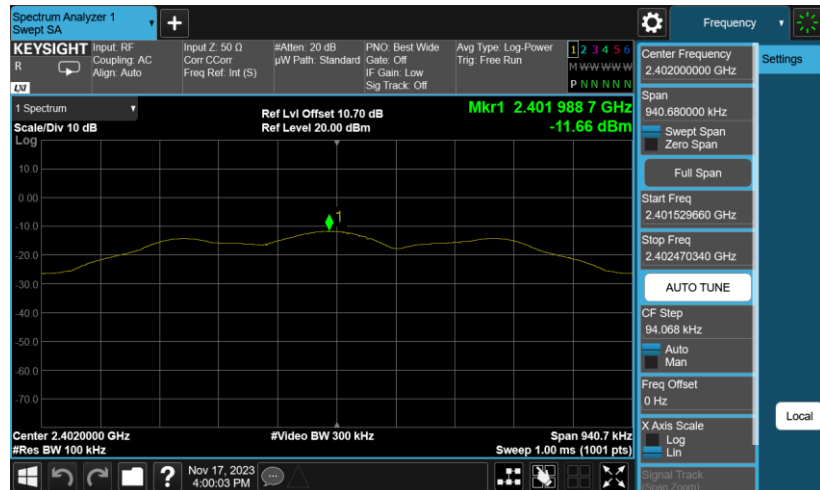


High ch

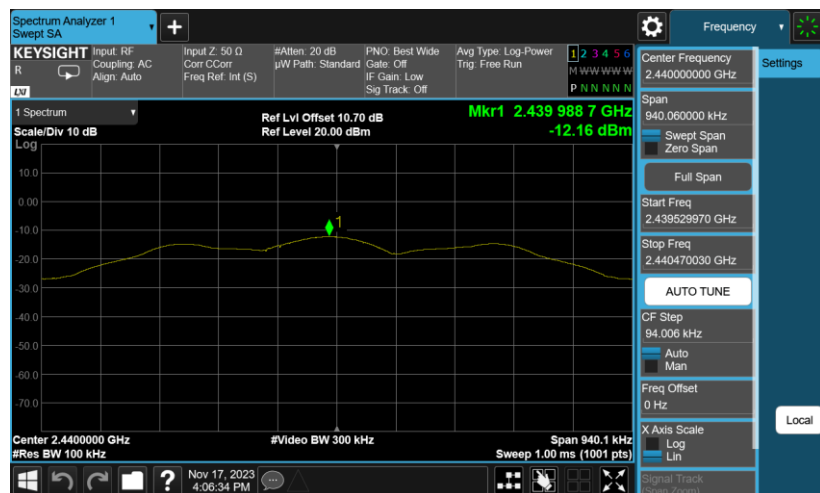


BLR C8

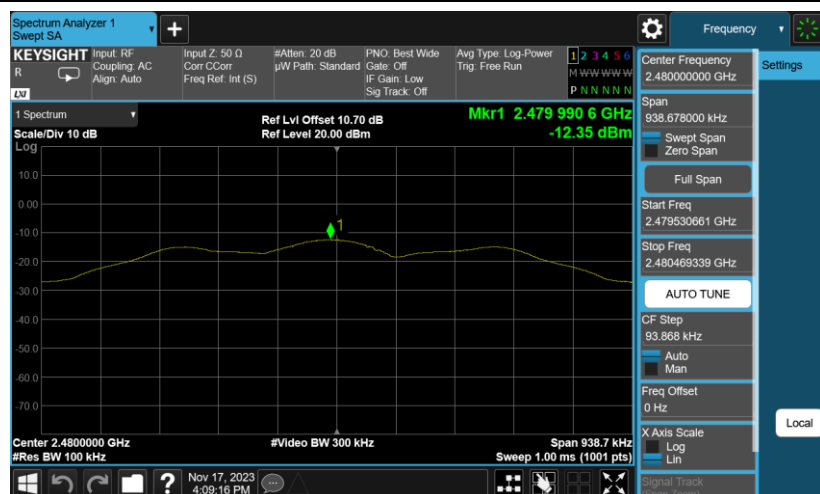
Low ch



Mid ch



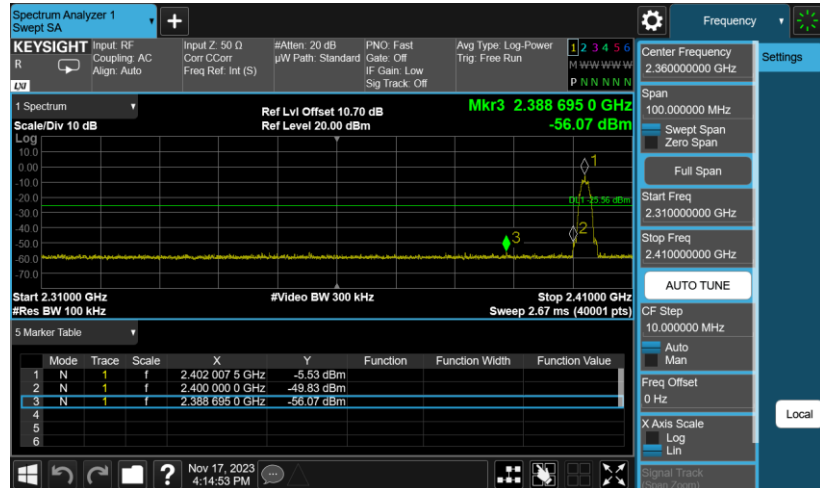
High ch



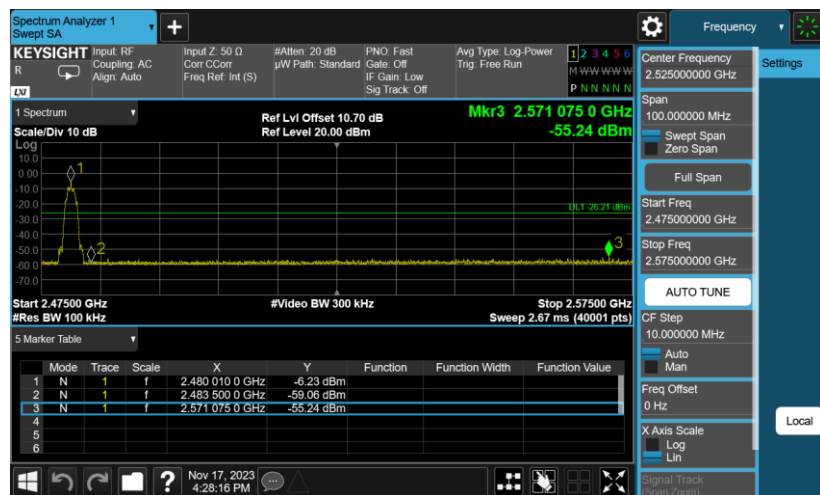
Conducted Band Edge

BLE 2M

Low ch

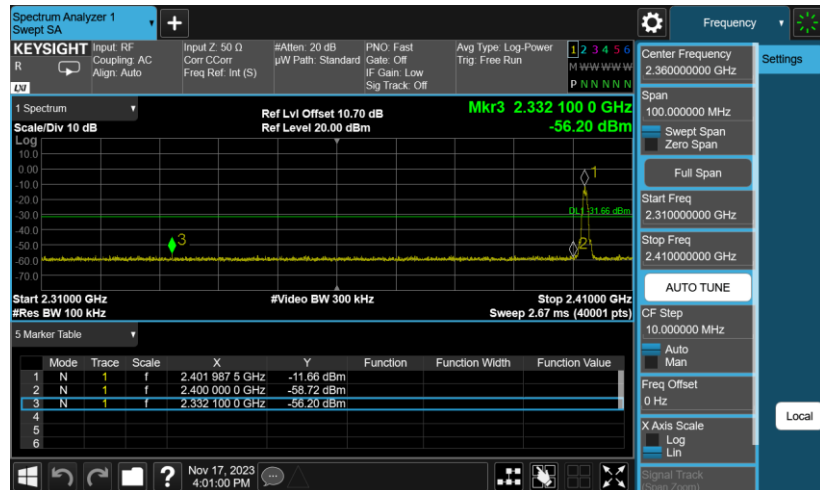


High ch

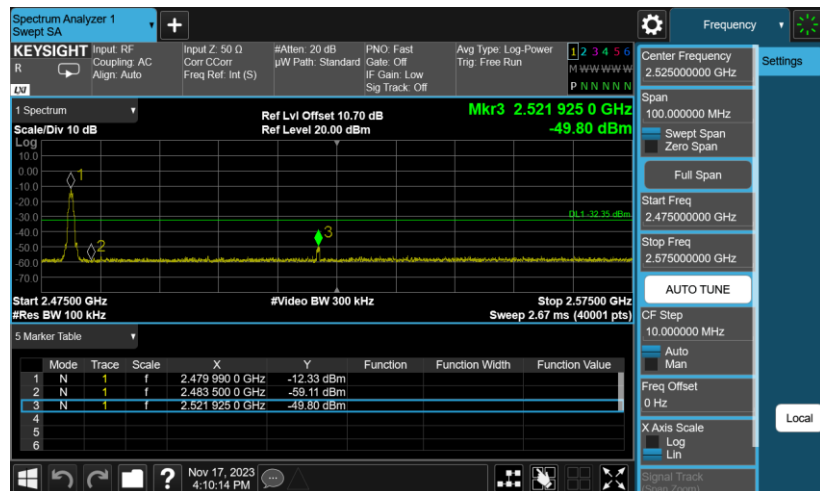


BLR C8

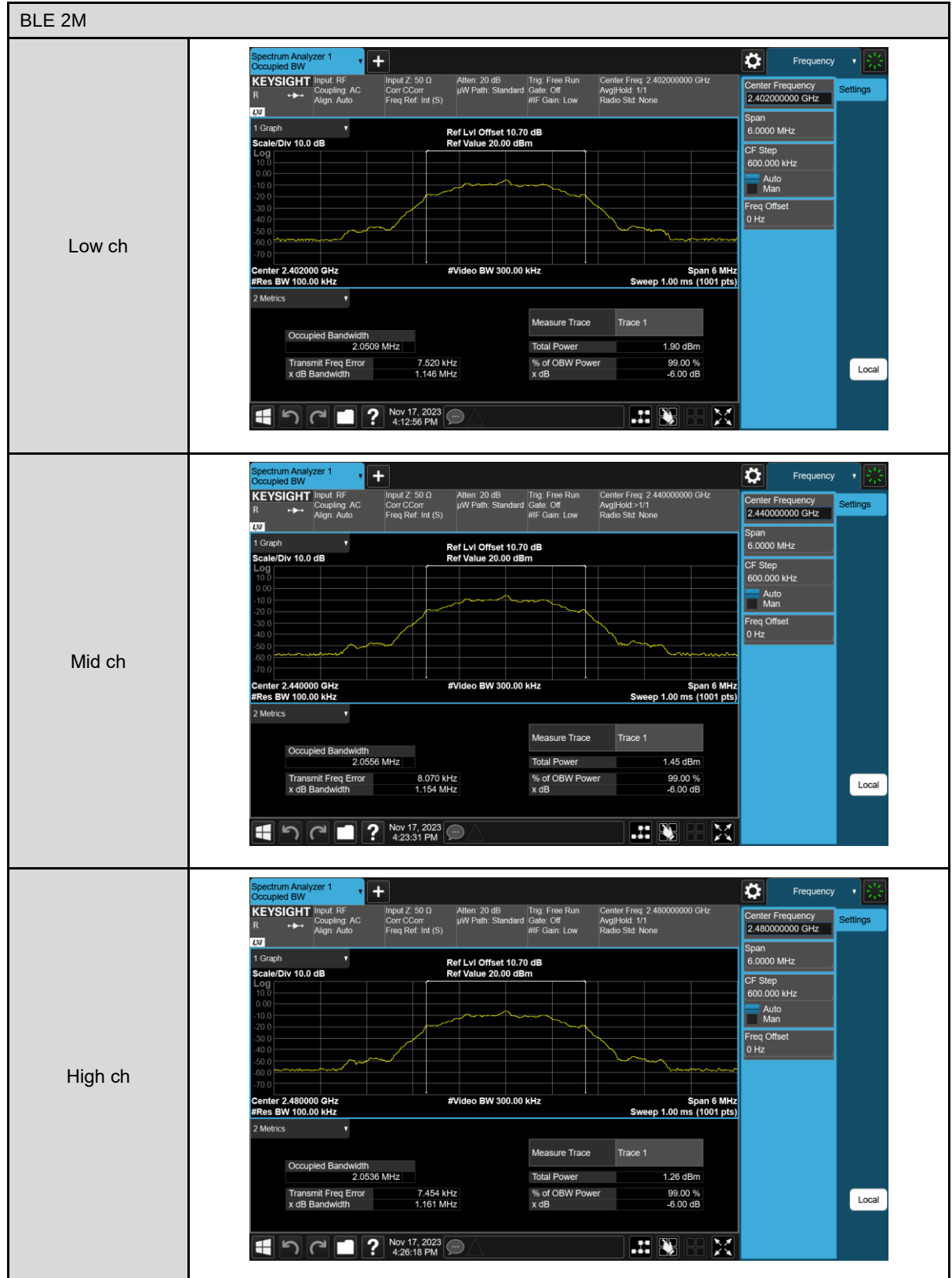
Low ch



High ch

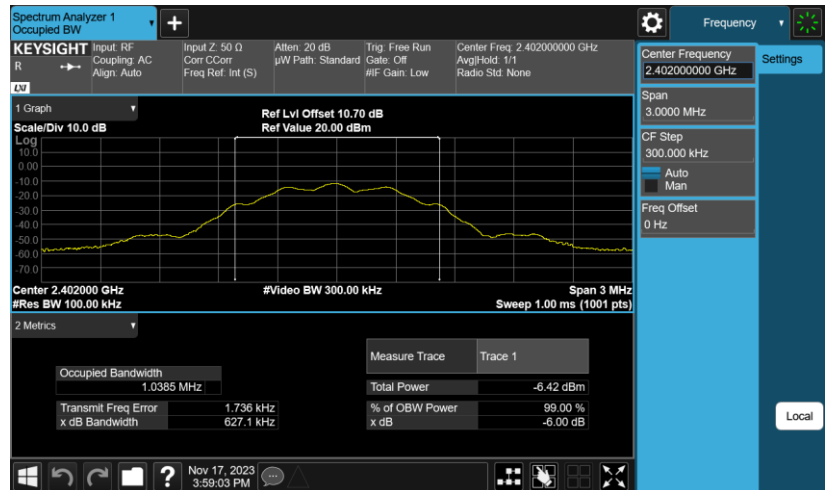


6 dB Bandwidth



BLR C8

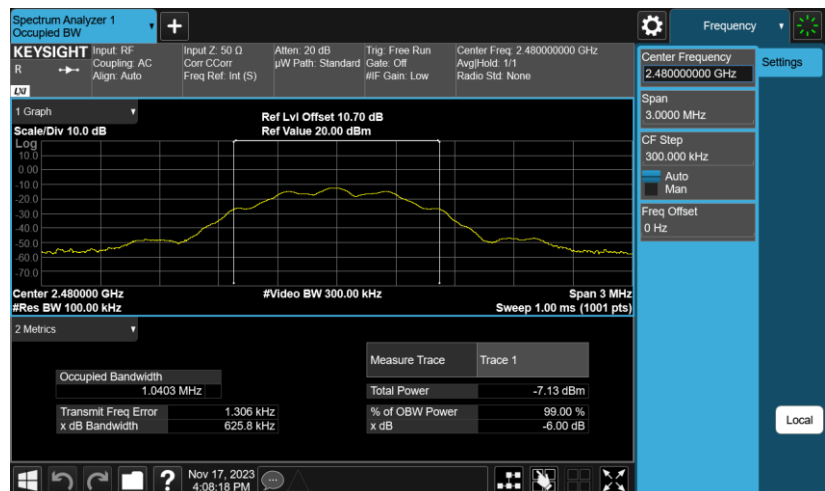
Low ch



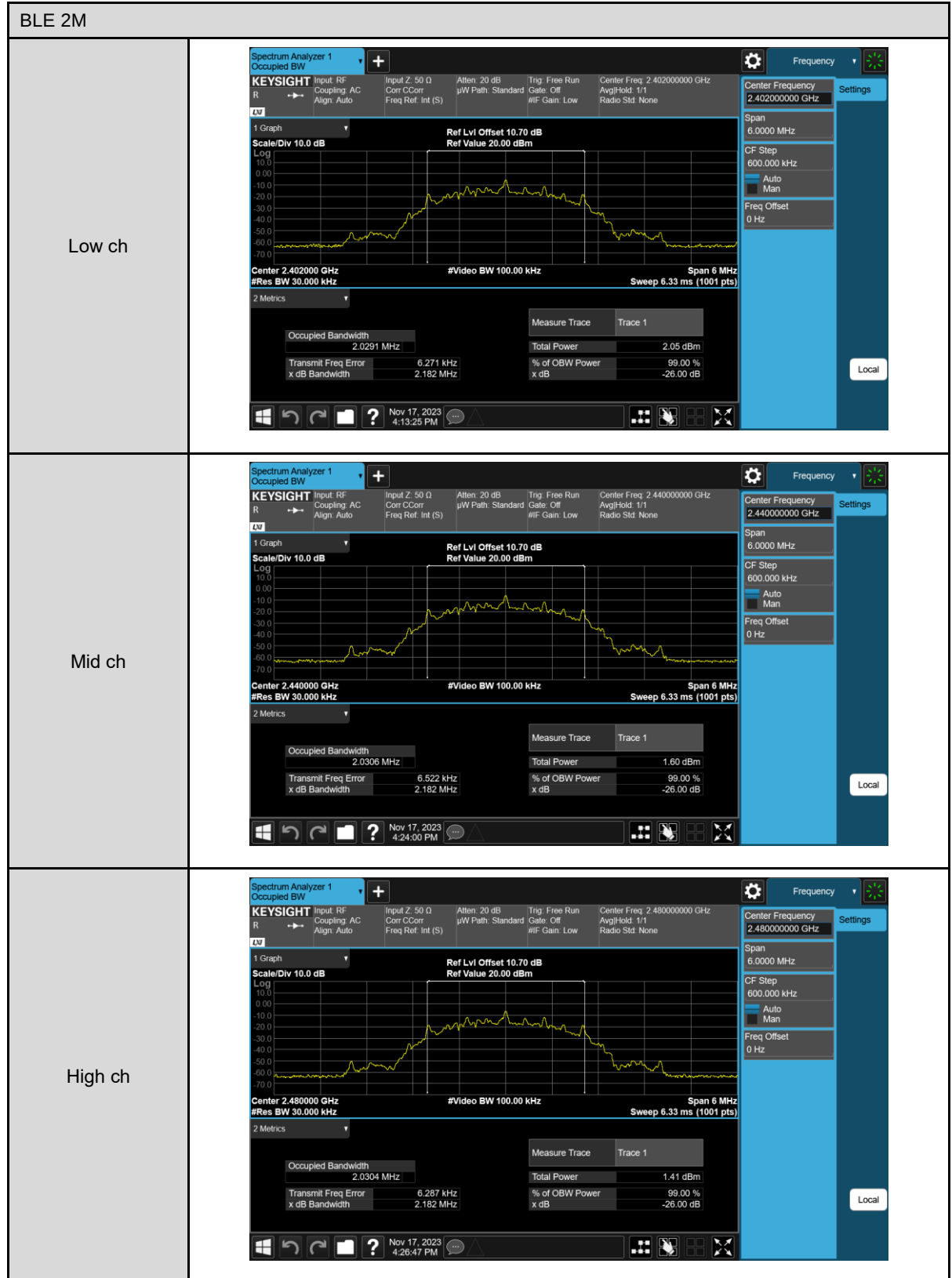
Mid ch



High ch

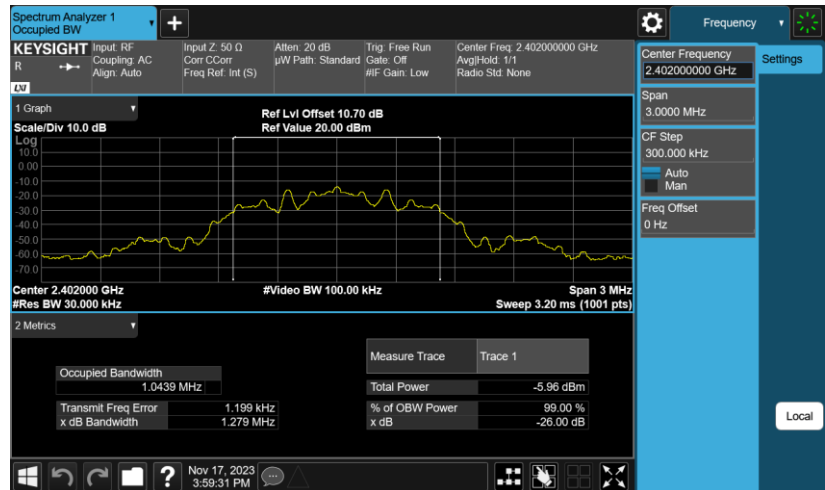


99 % Occupied Bandwidth

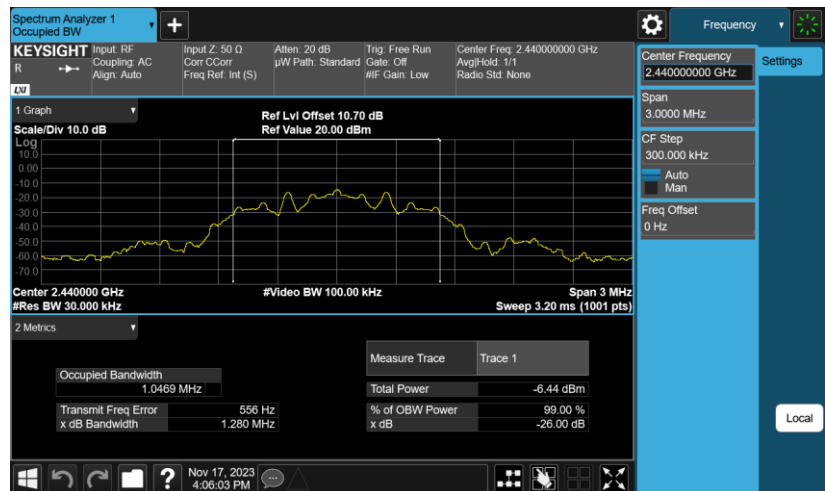


BLR C8

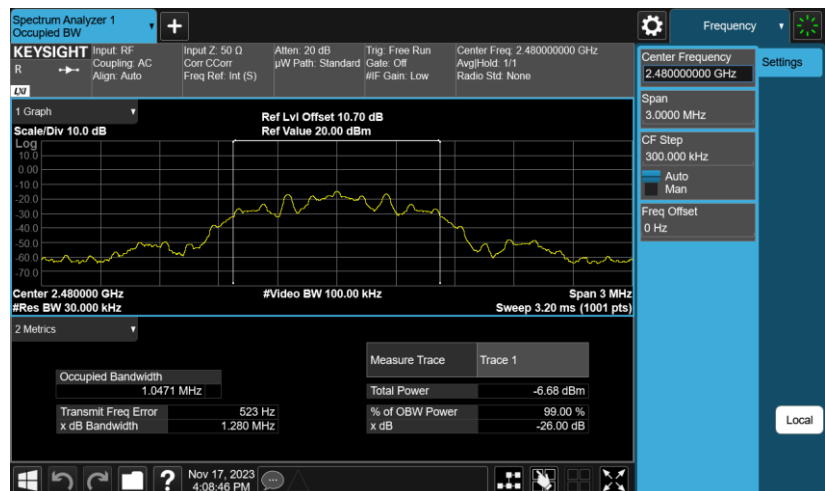
Low ch



Mid ch



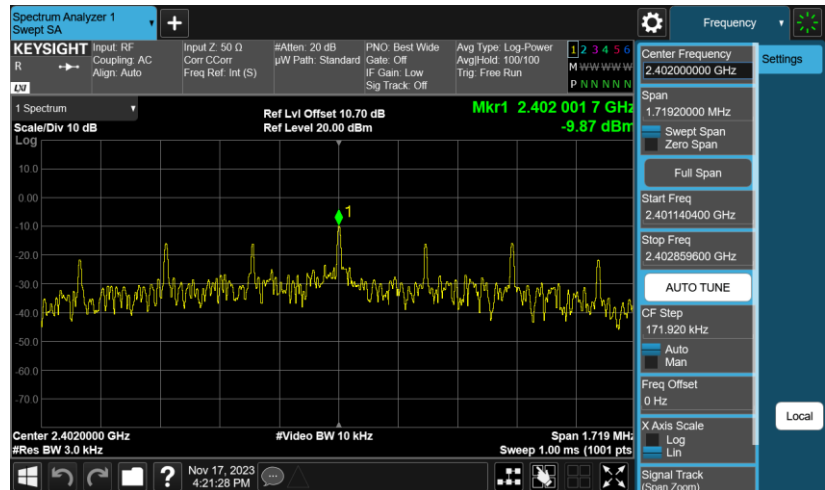
High ch



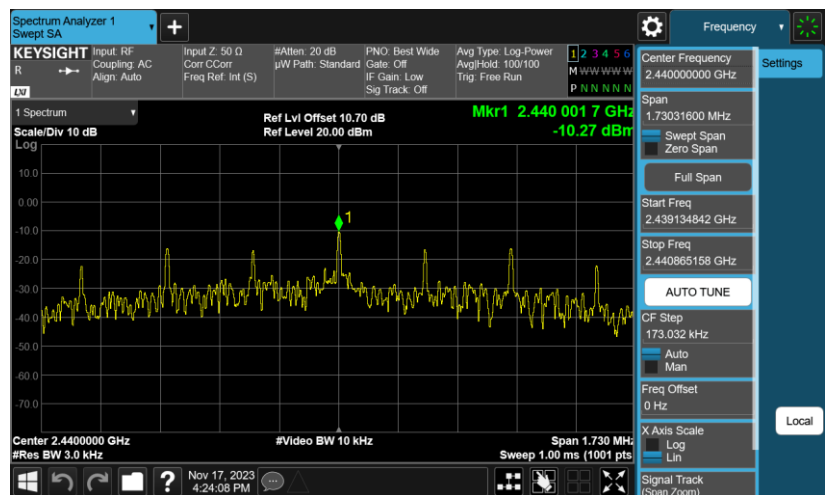
Power Density

BLE 2M

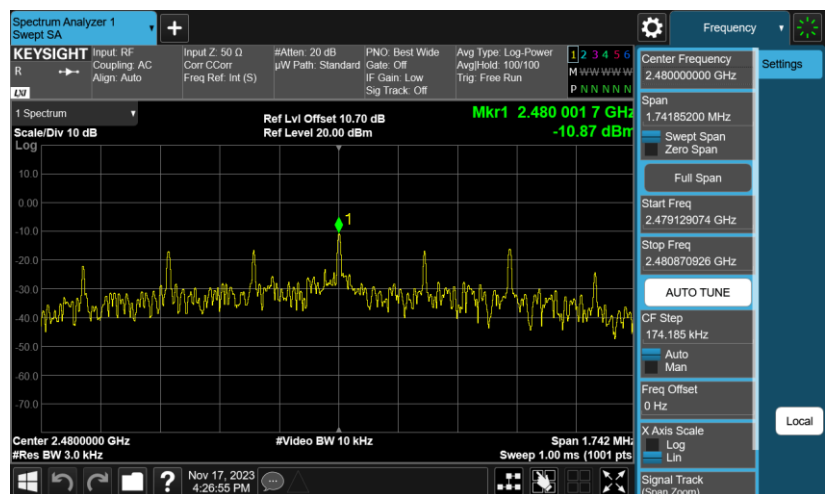
Low ch



Mid ch

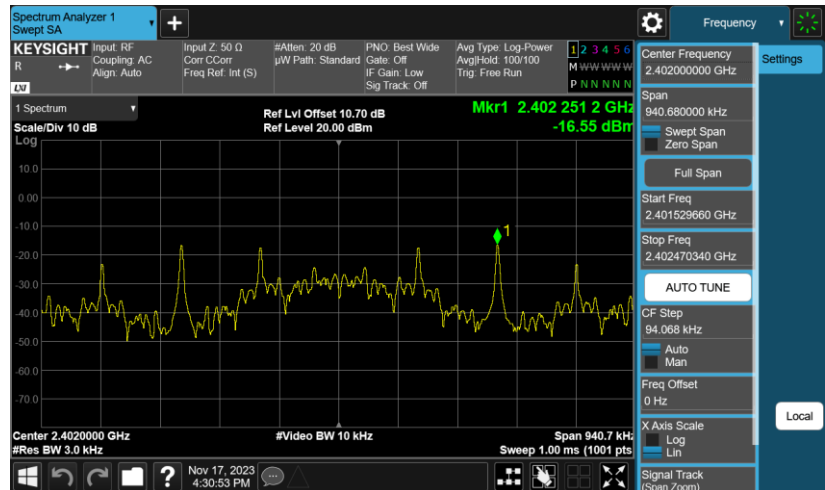


High ch

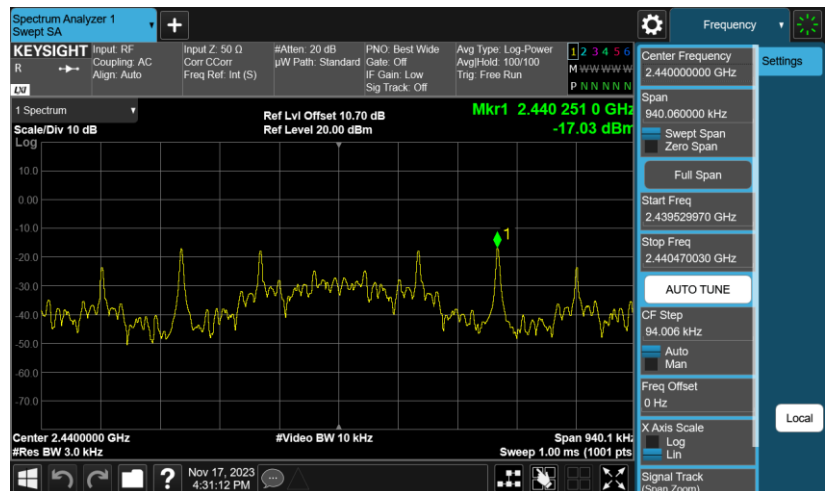


BLR C8

Low ch



Mid ch



High ch

