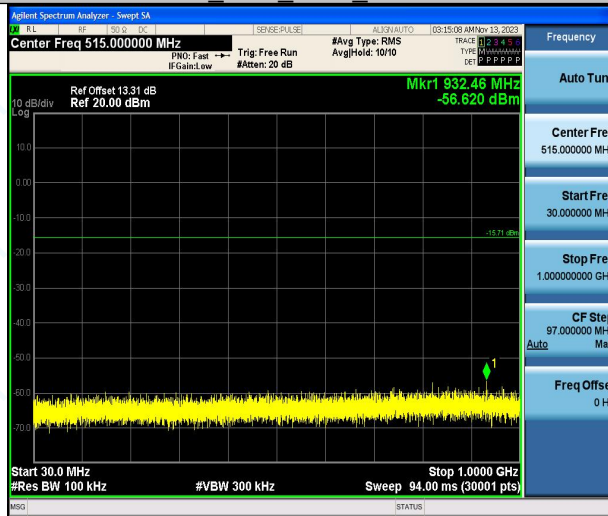




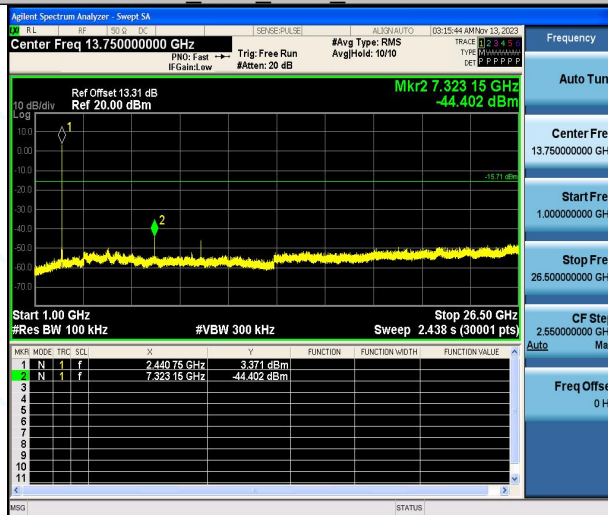
DH5 Ant1 2441 0~Reference



DH5 Ant1 2441 30~1000



DH5 Ant1 2441 1000~26500





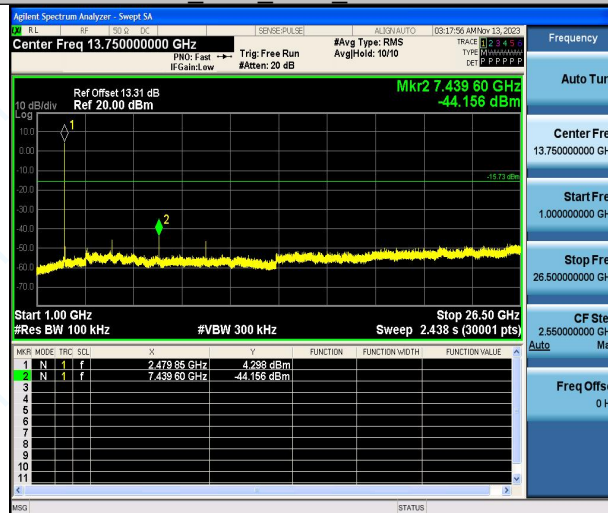
DH5 Ant1 2480 0~Reference



DH5 Ant1 2480 30~1000



DH5 Ant1 2480 1000~26500

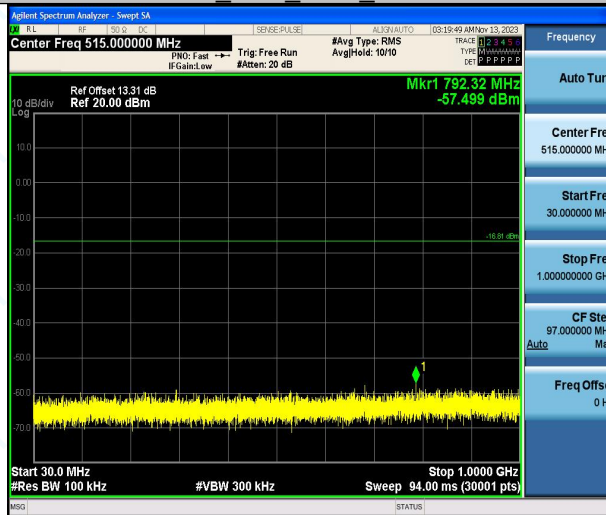




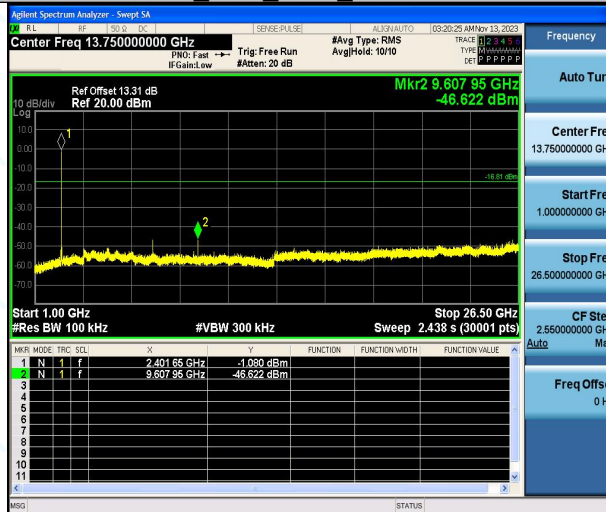
2DH5 Ant1 2402 0~Reference



2DH5 Ant1 2402 30~1000



2DH5 Ant1 2402 1000~26500

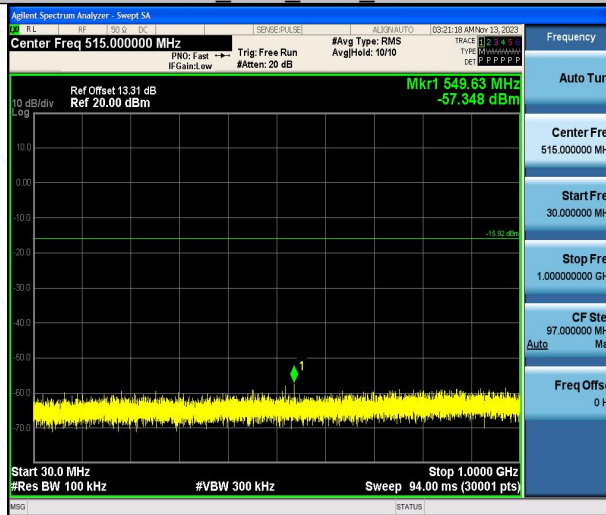




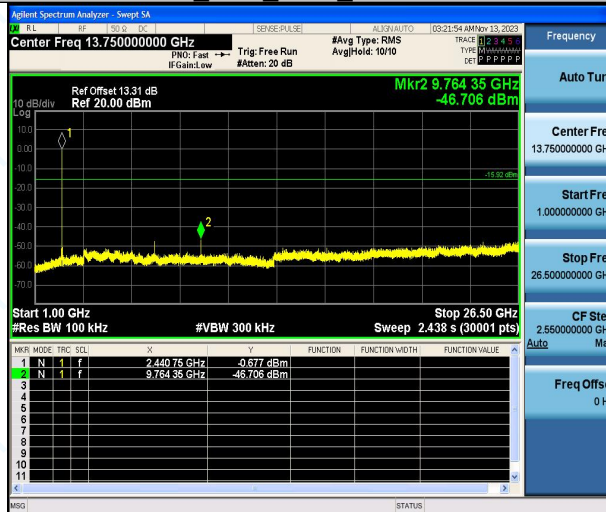
2DH5 Ant1 2441 0~Reference



2DH5 Ant1 2441 30~1000



2DH5 Ant1 2441 1000~26500

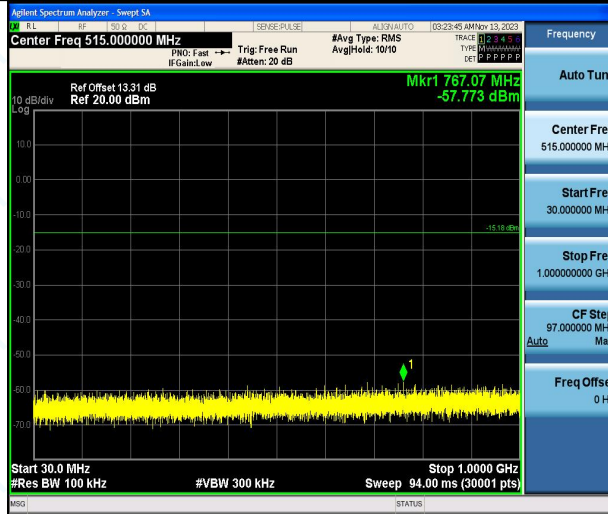




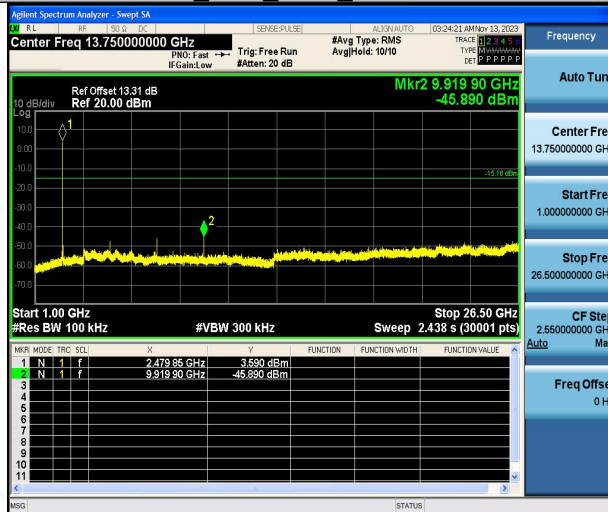
2DH5 Ant1 2480 0~Reference



2DH5 Ant1 2480 30~1000



2DH5 Ant1 2480 1000~26500

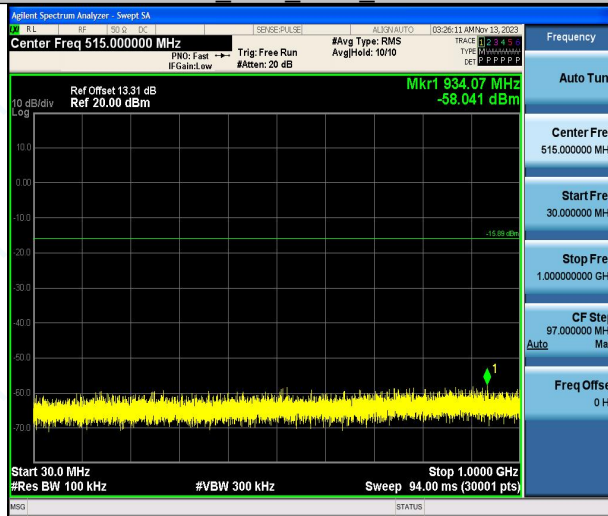




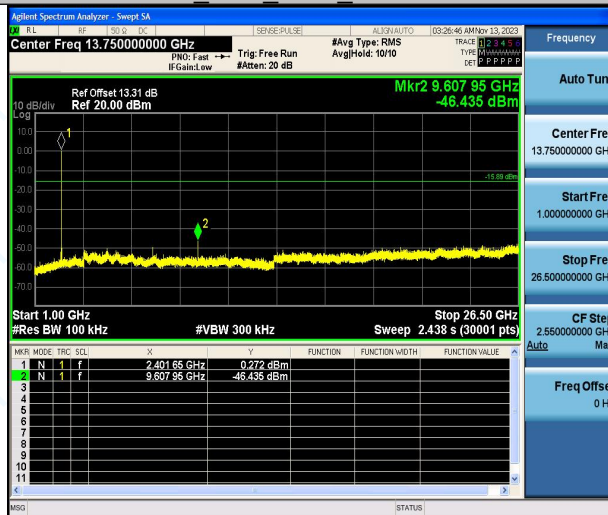
3DH5 Ant1_2402_0~Reference



3DH5 Ant1_2402_30~1000



3DH5 Ant1_2402_1000~26500

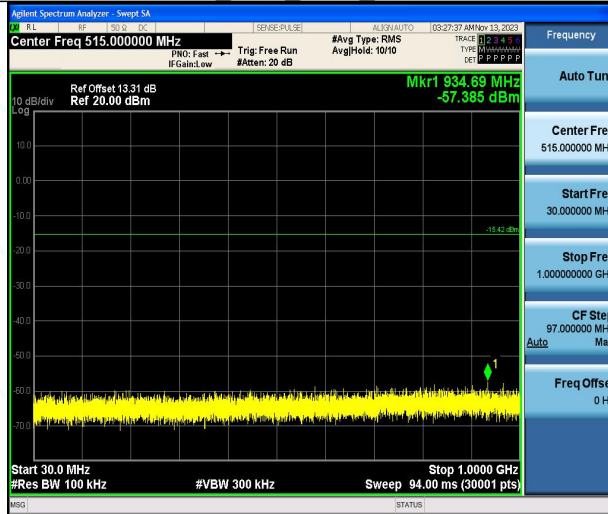




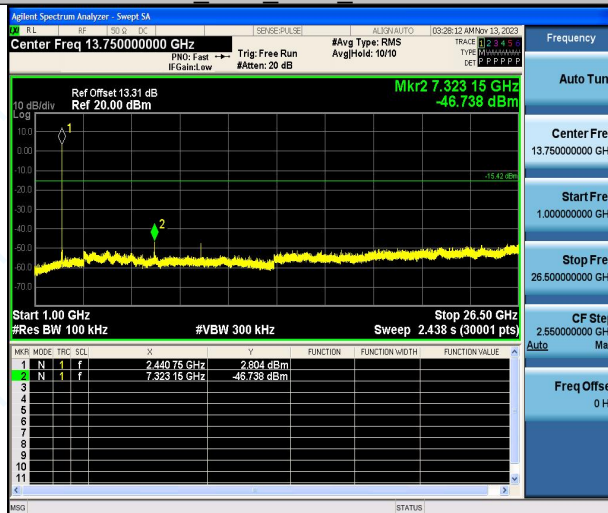
3DH5 Ant1 2441 0~Reference



3DH5 Ant1 2441 30~1000



3DH5 Ant1 2441 1000~26500

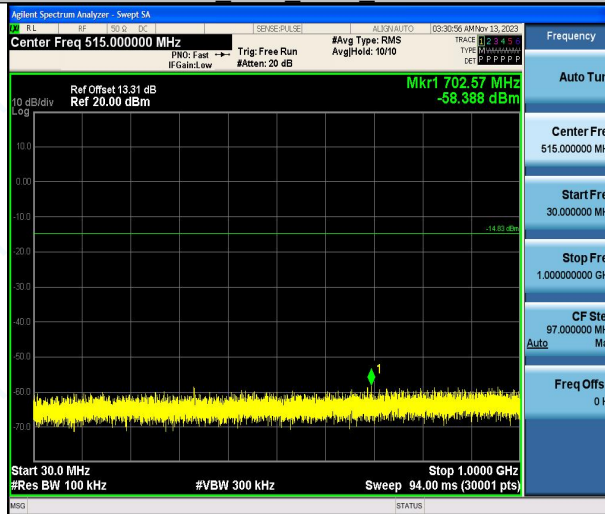




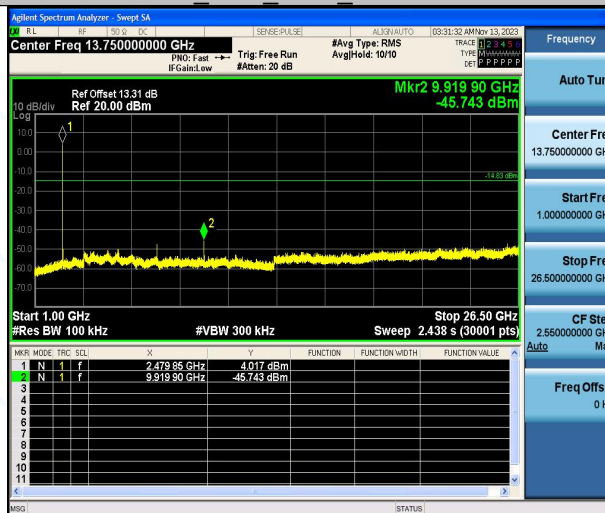
3DH5 Ant1 2480 0~Reference



3DH5 Ant1 2480 30~1000



3DH5 Ant1 2480 1000~26500



The End Repor