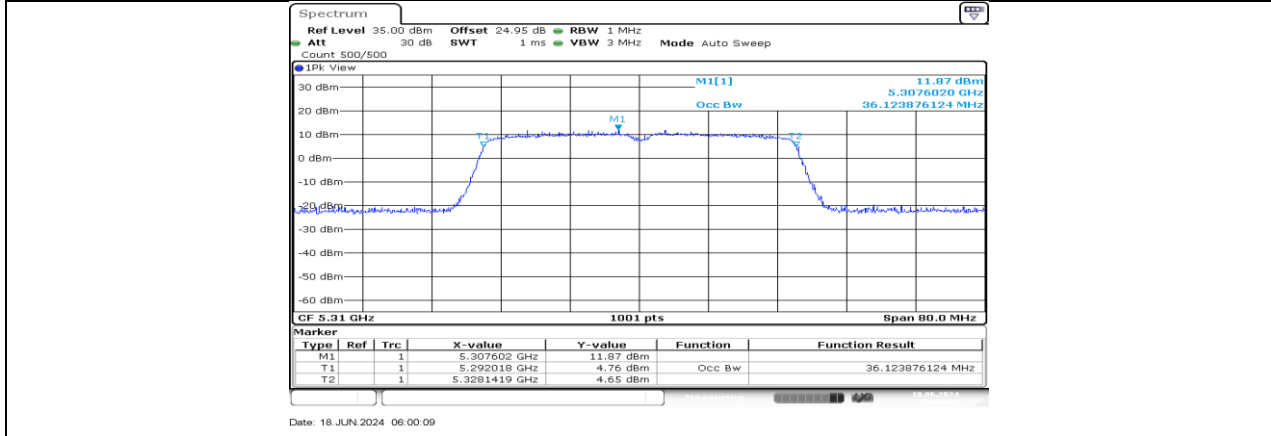
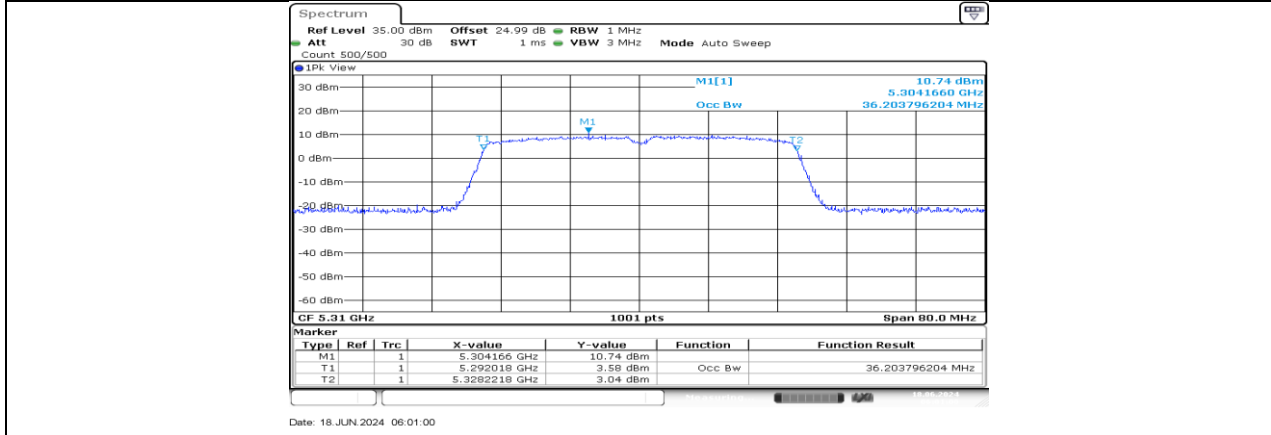


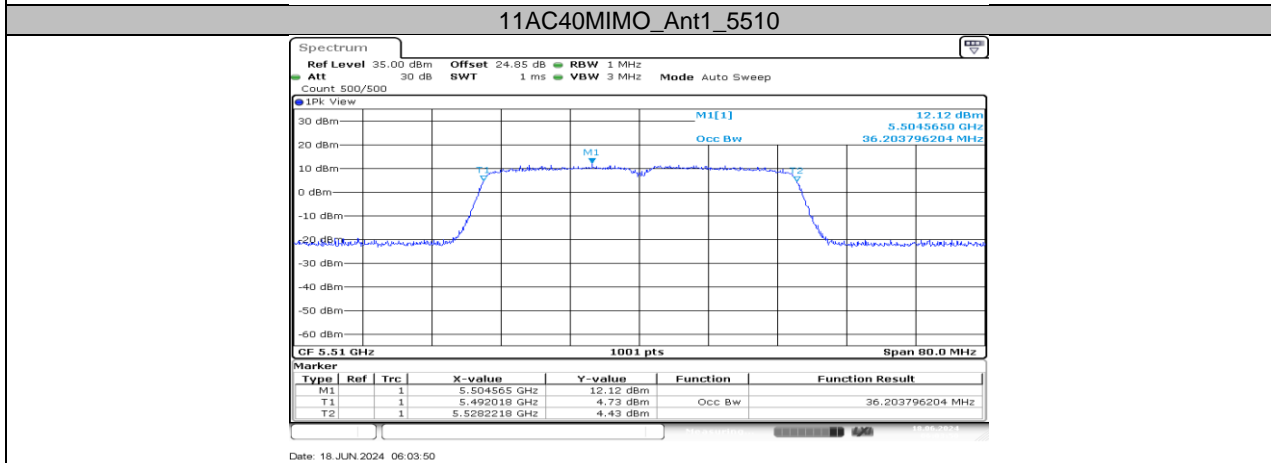
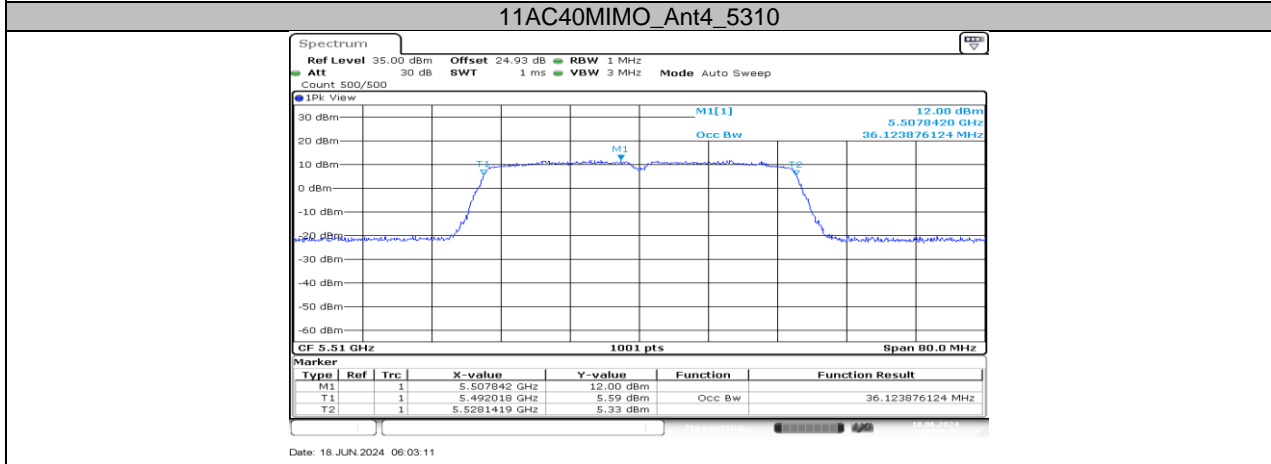
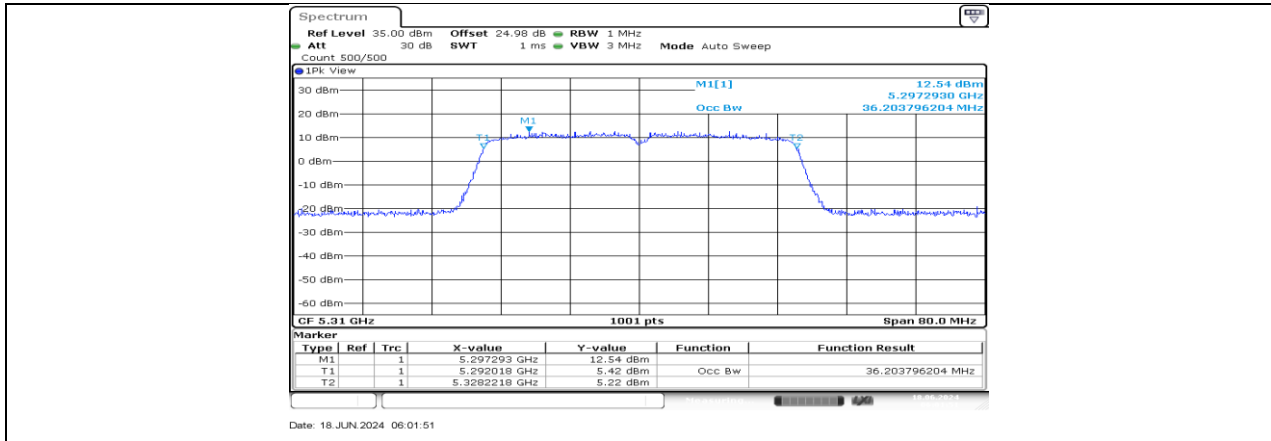
11AC40MIMO\_Ant1\_5310



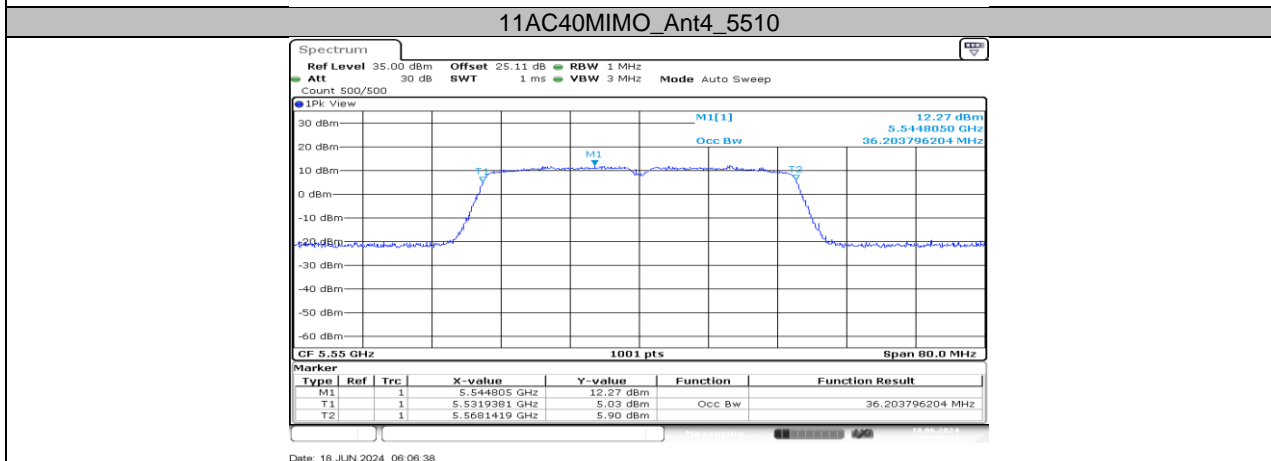
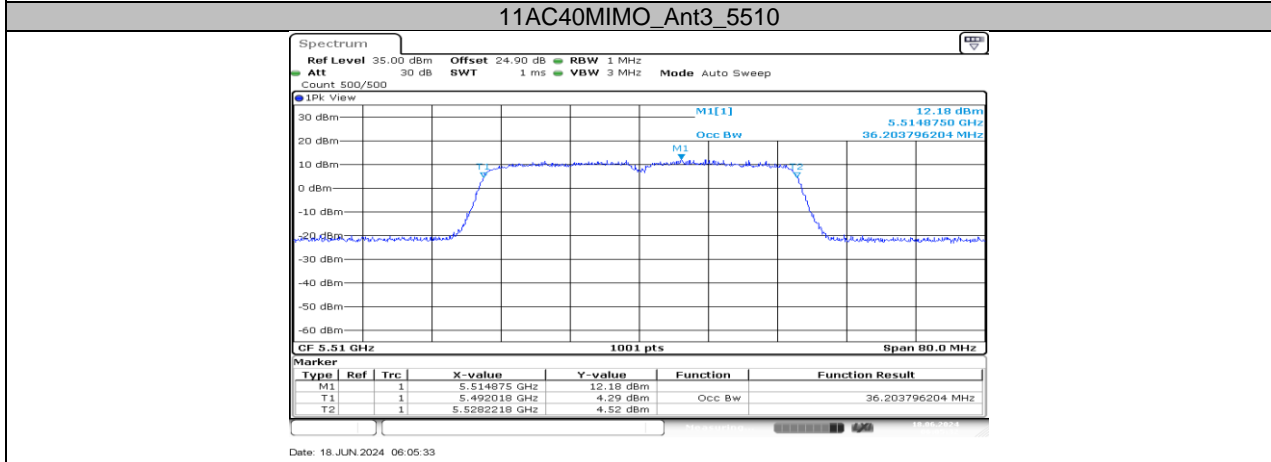
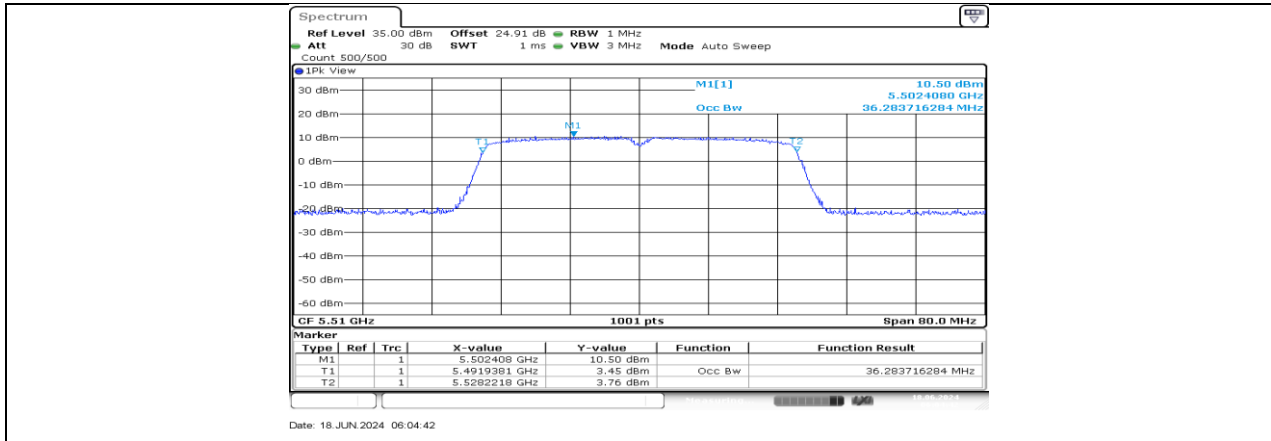
11AC40MIMO\_Ant2\_5310

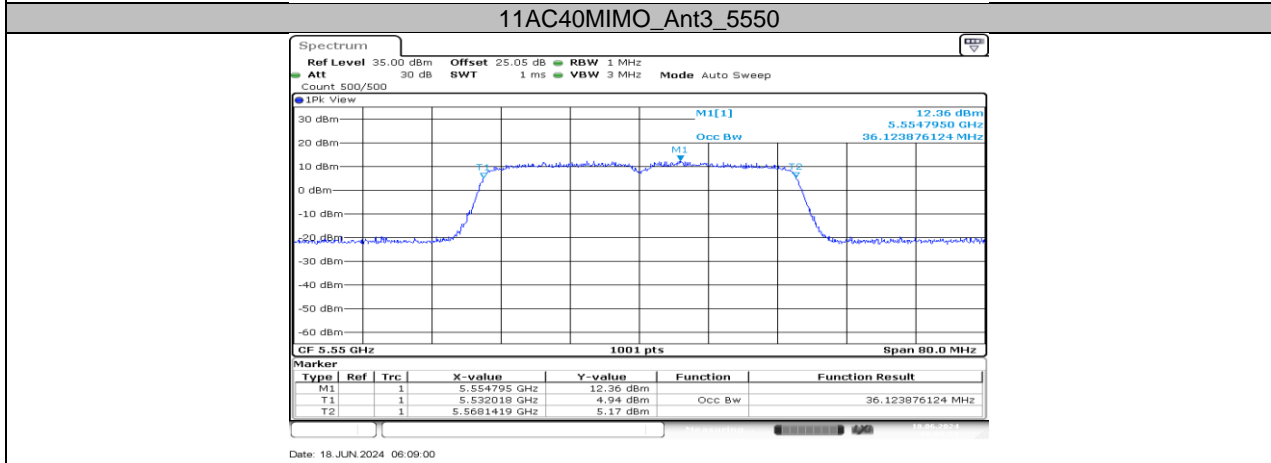
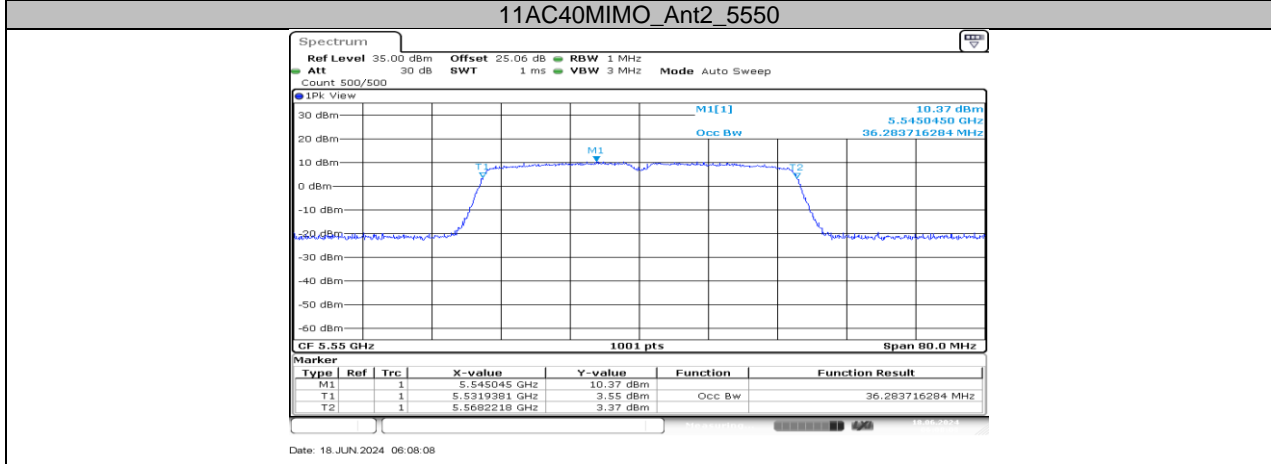
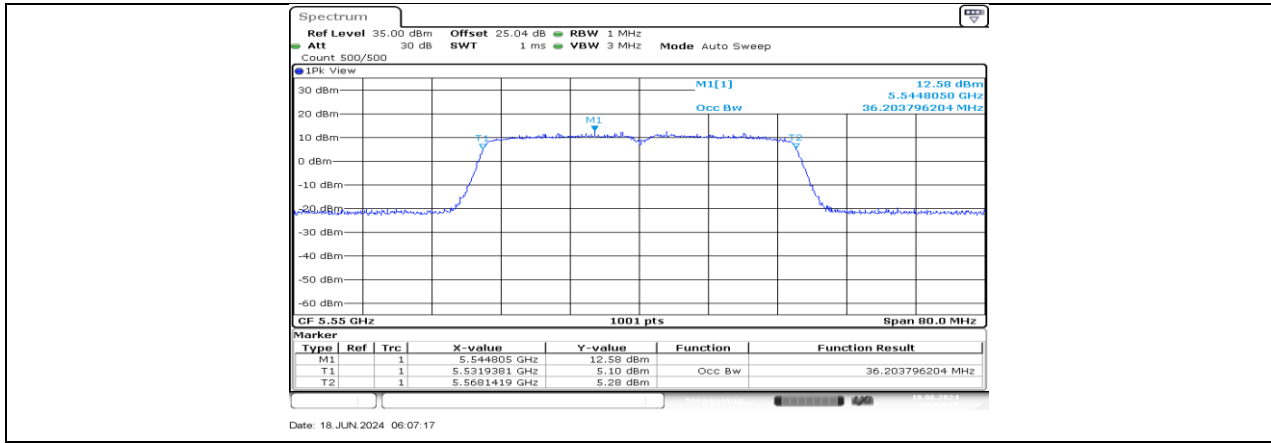


11AC40MIMO\_Ant3\_5310

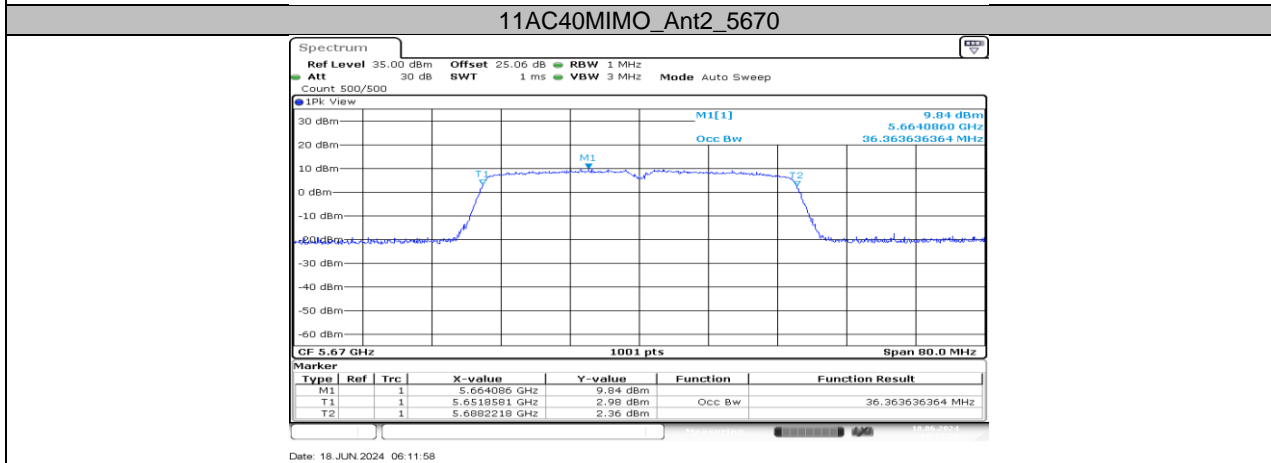
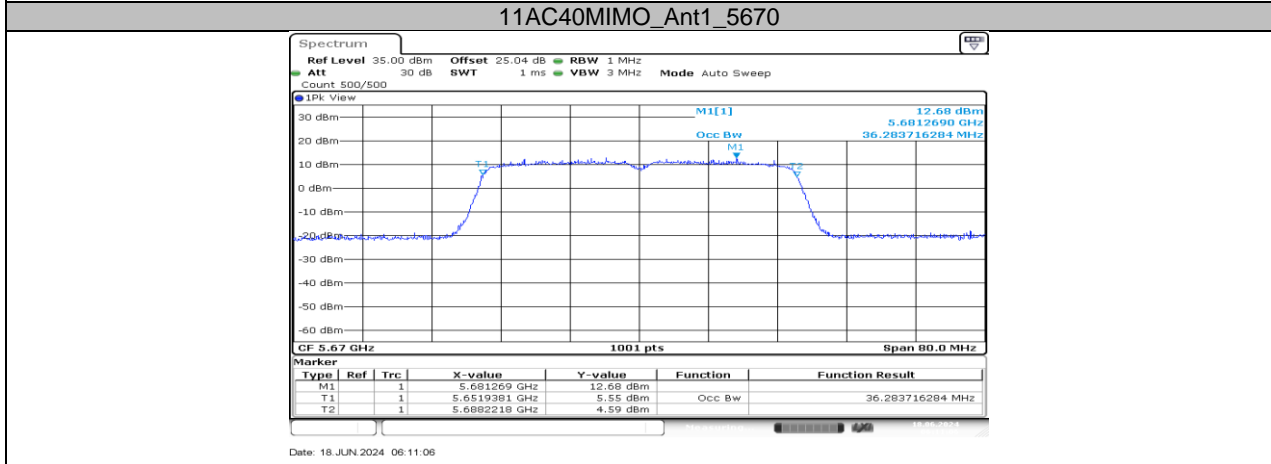
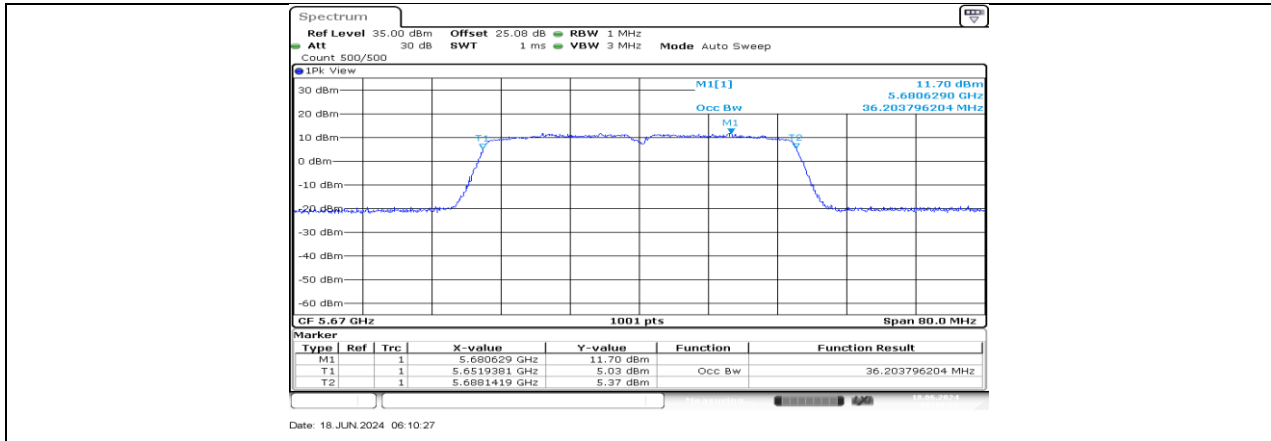


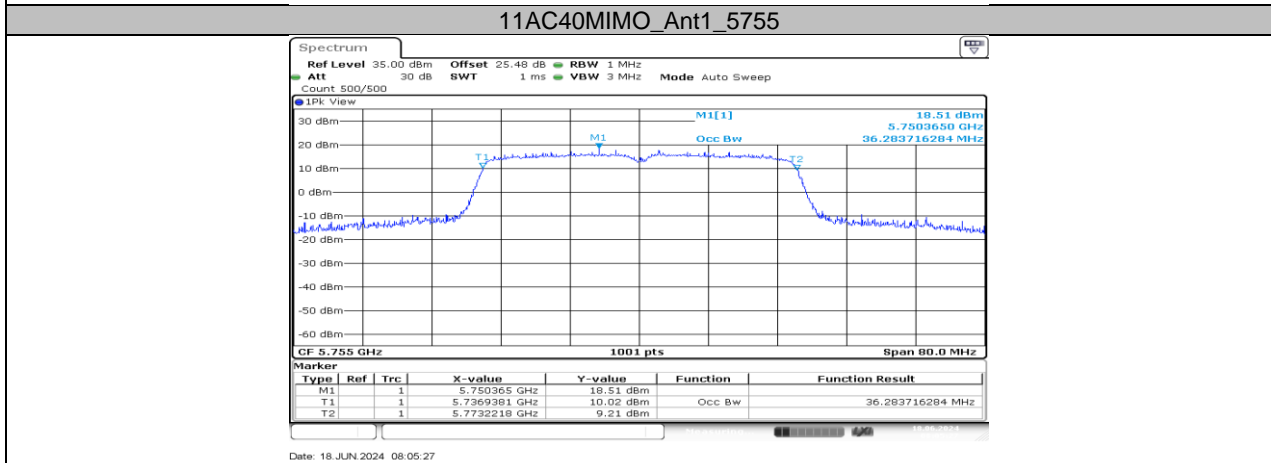
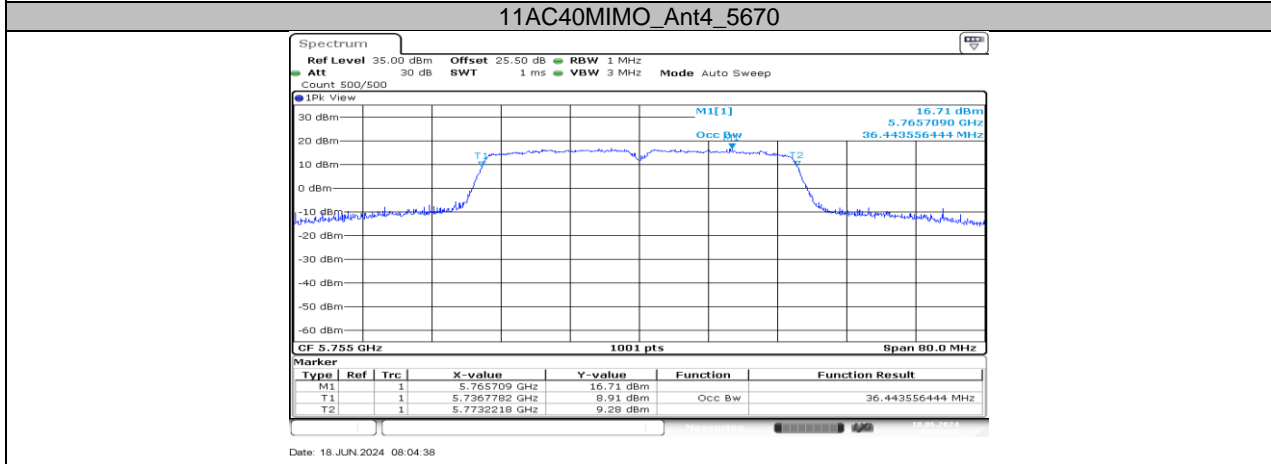
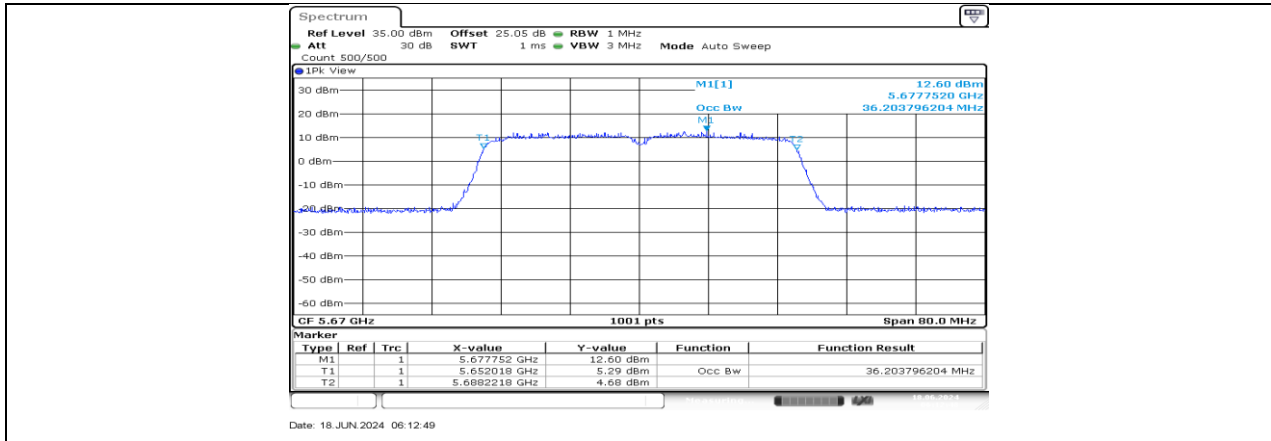
**11AC40MIMO\_Ant2\_5510**



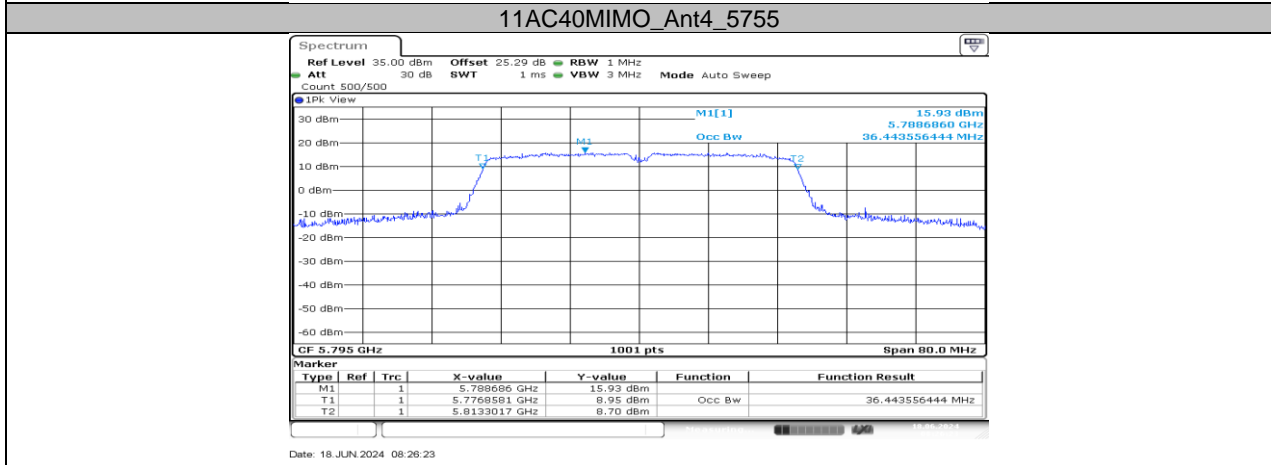
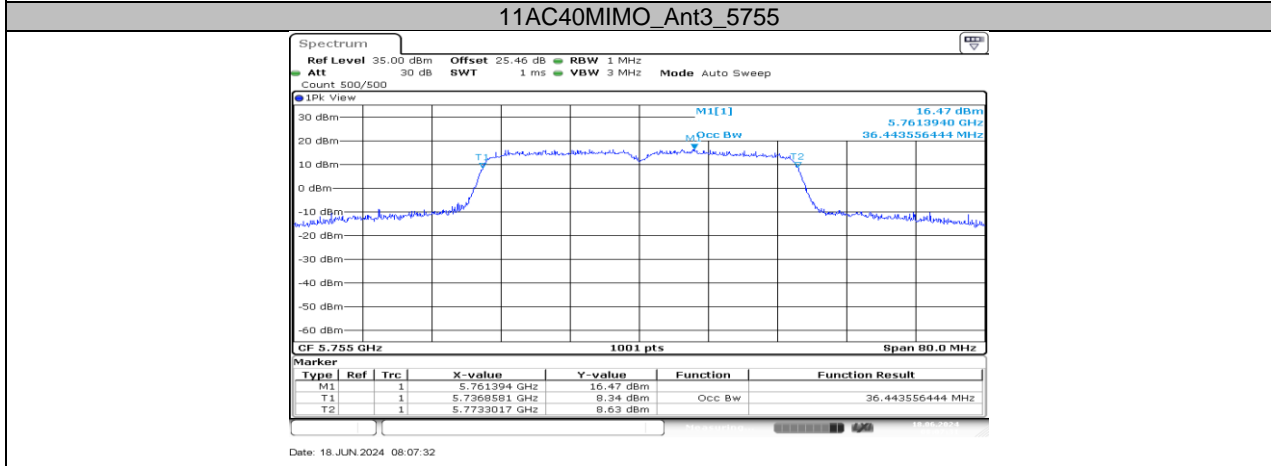
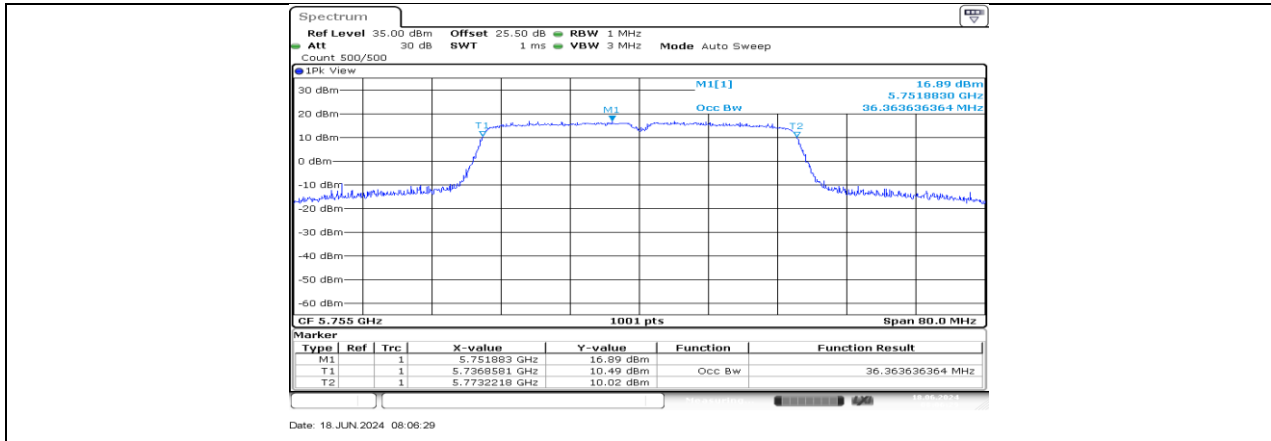


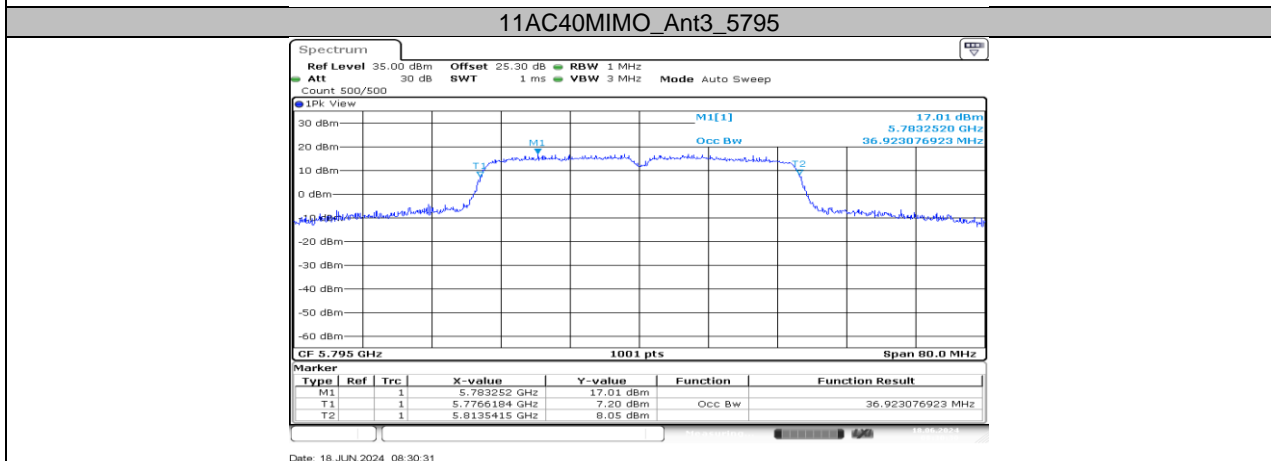
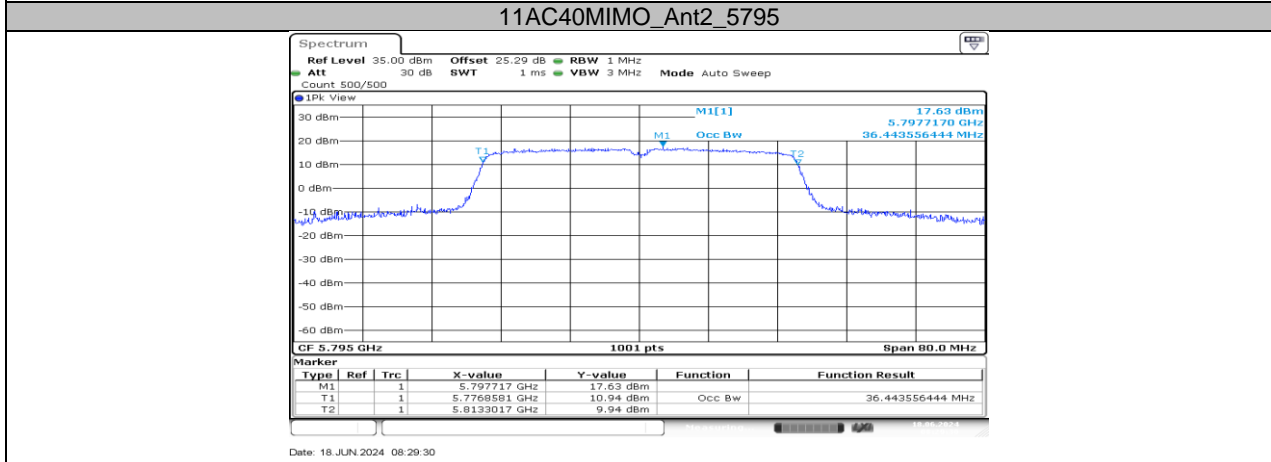
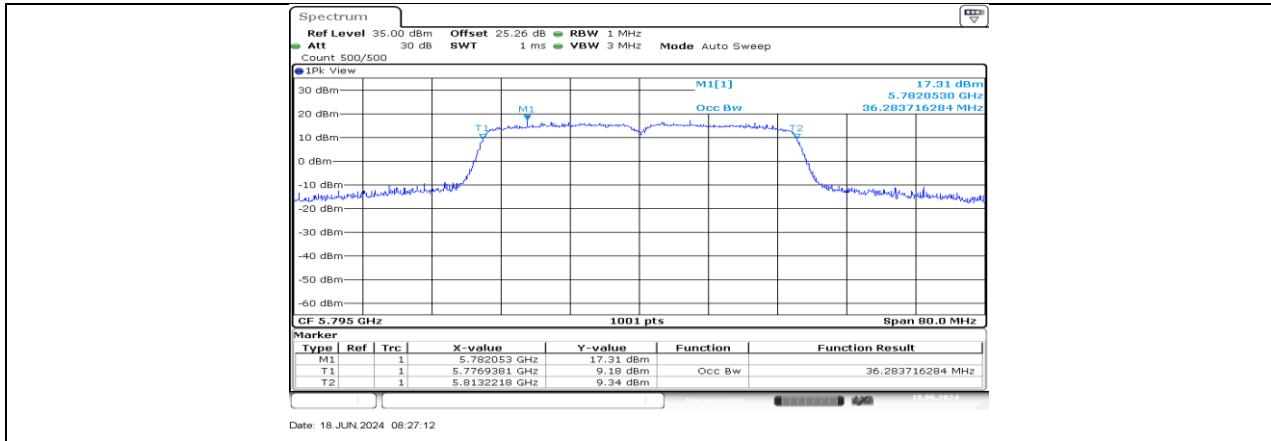
**11AC40MIMO\_Ant4\_5550**



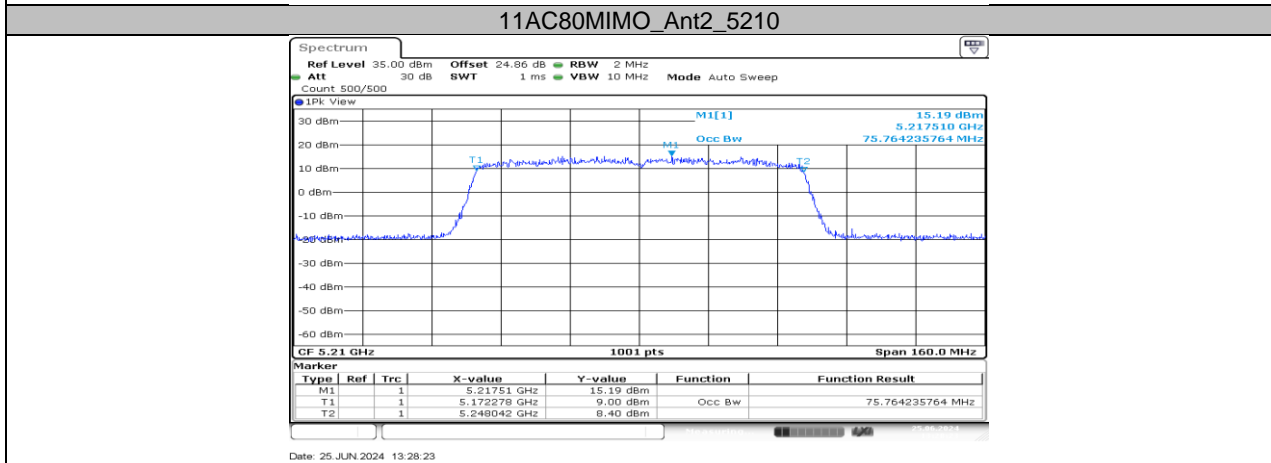
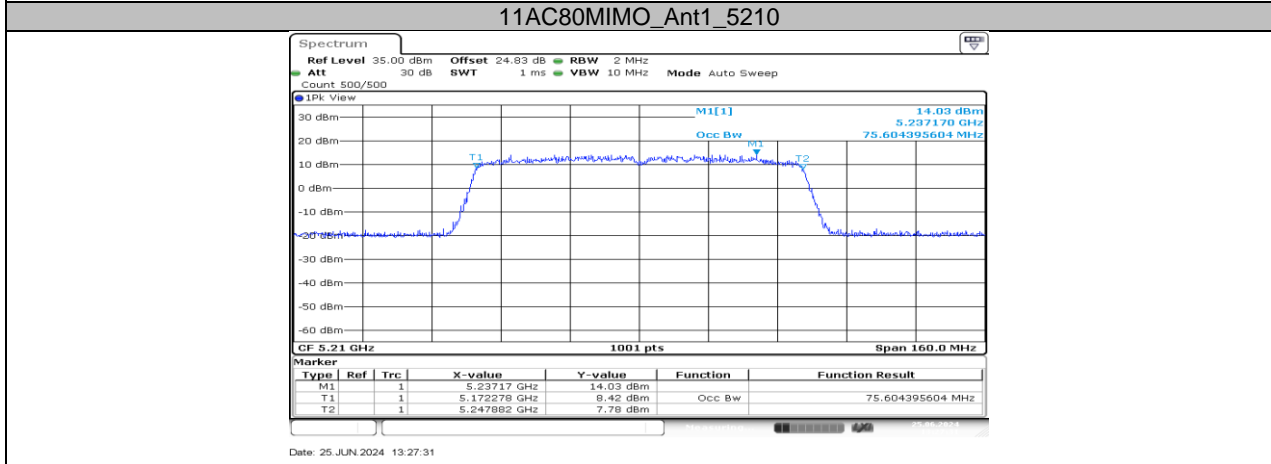
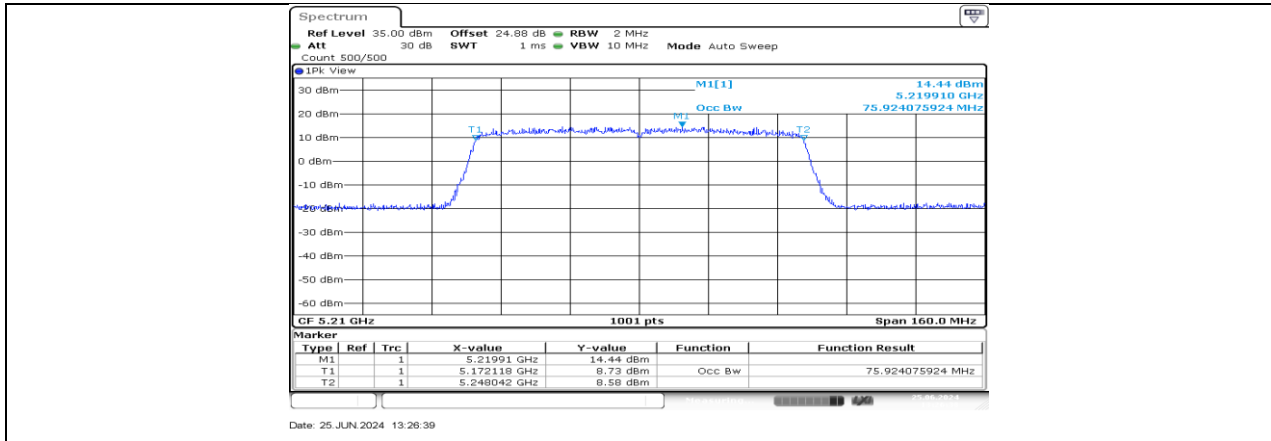


**11AC40MIMO\_Ant2\_5755**

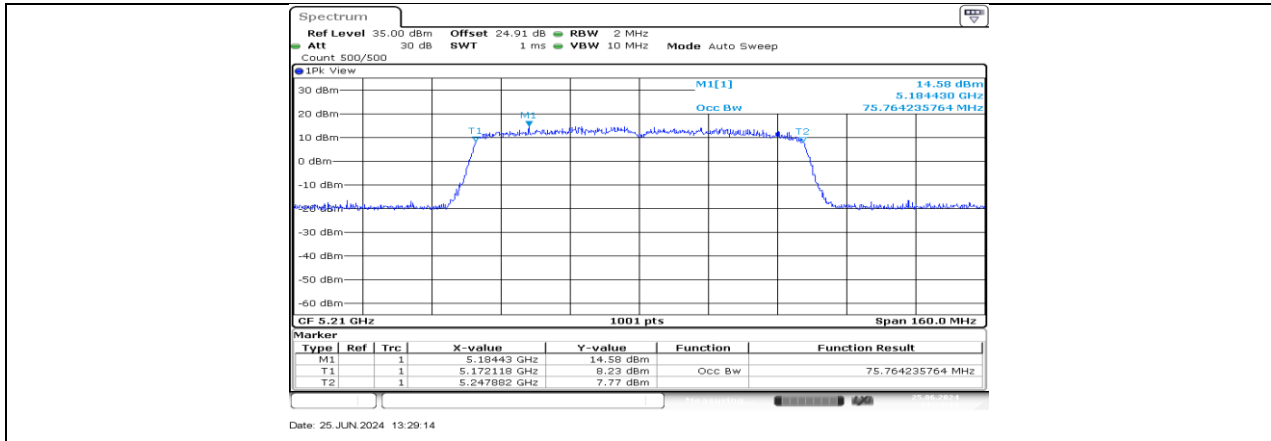




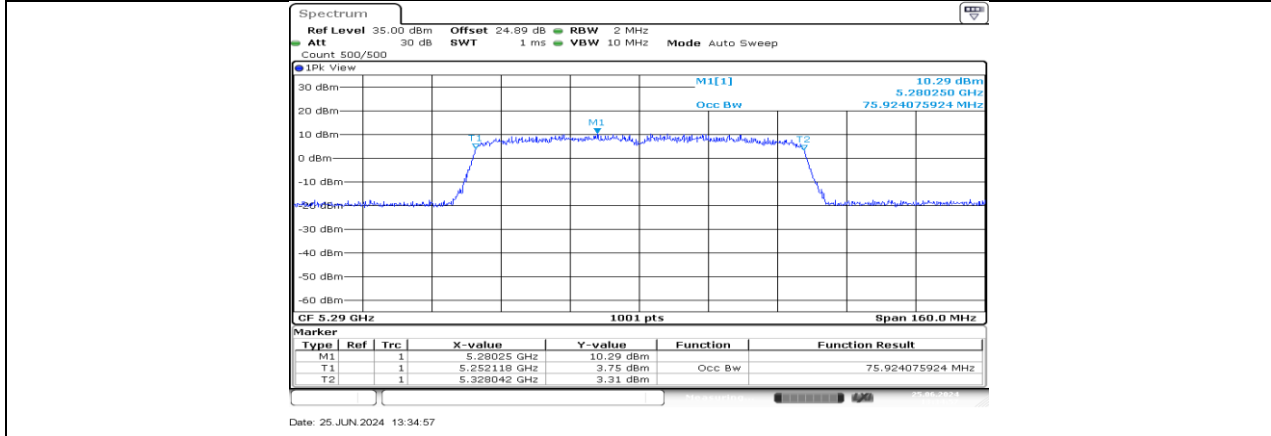




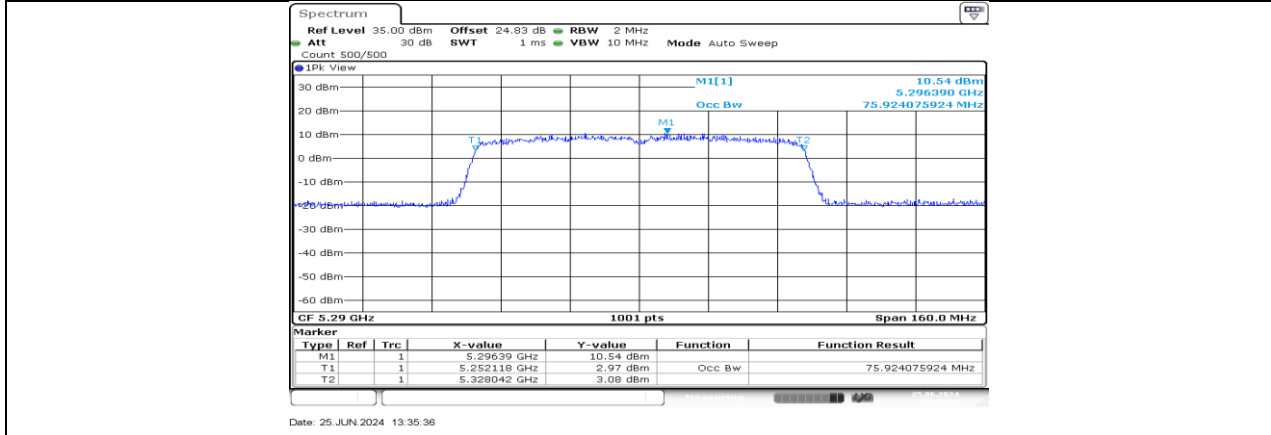
**11AC80MIMO\_Ant3\_5210**



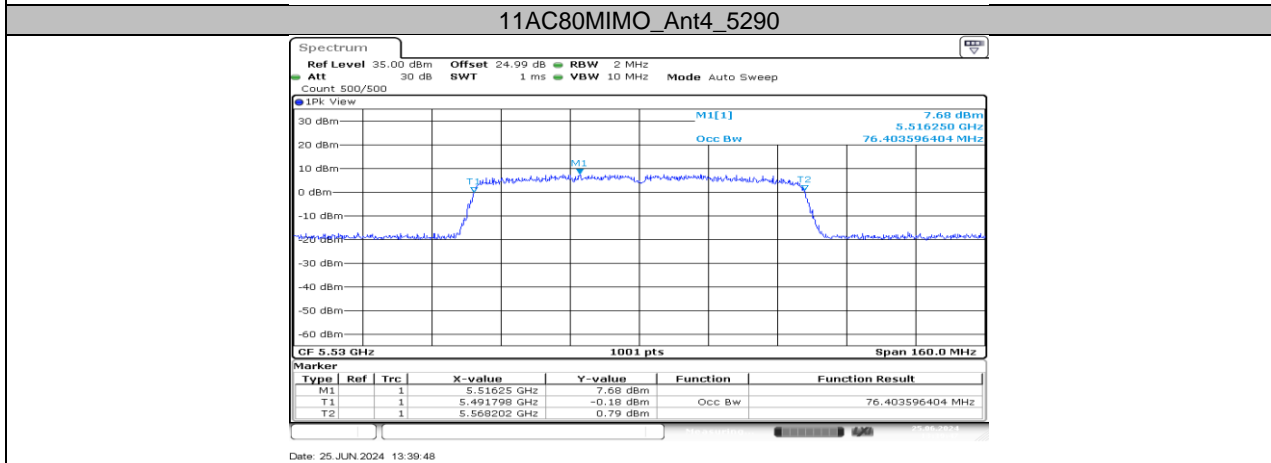
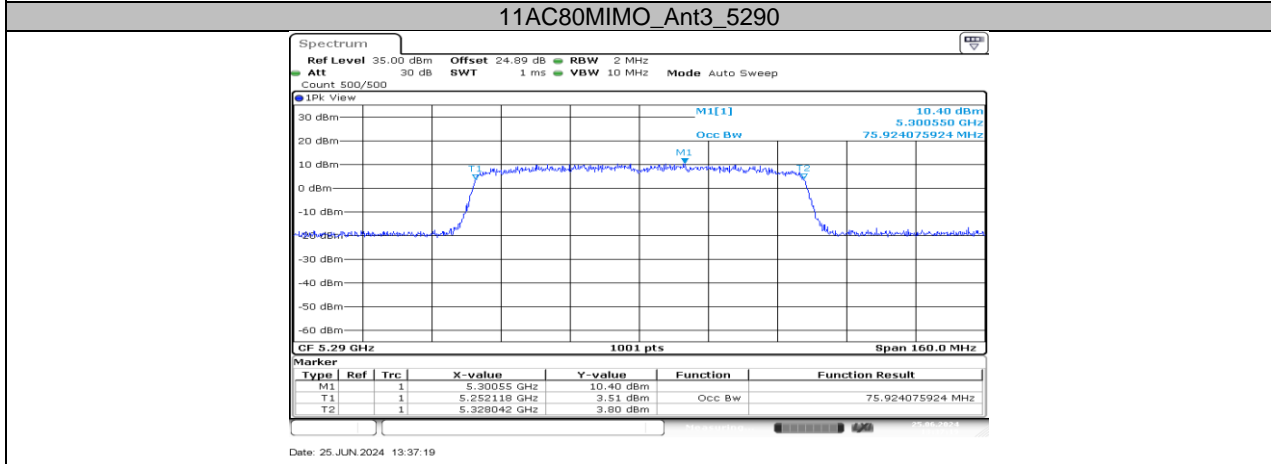
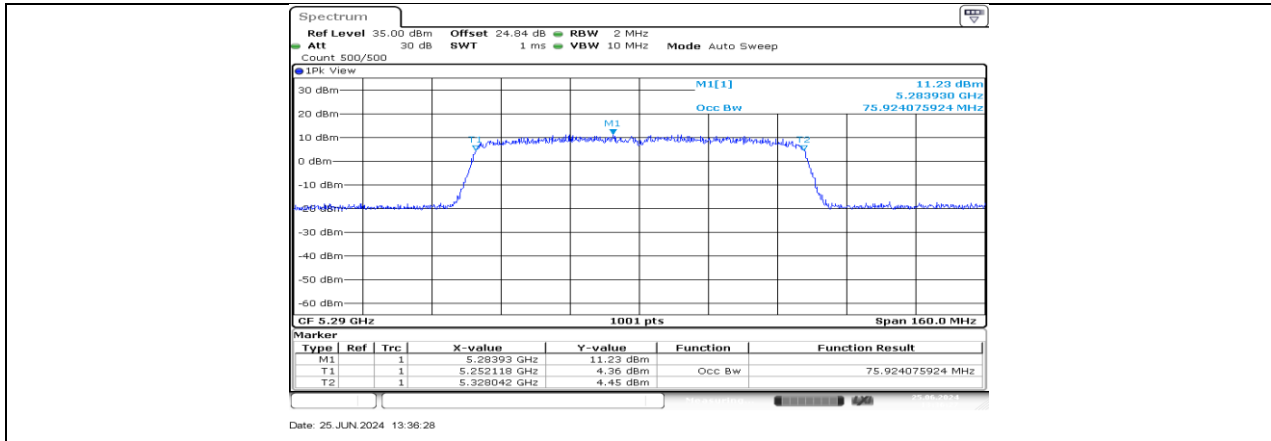
11AC80MIMO\_Ant4\_5210

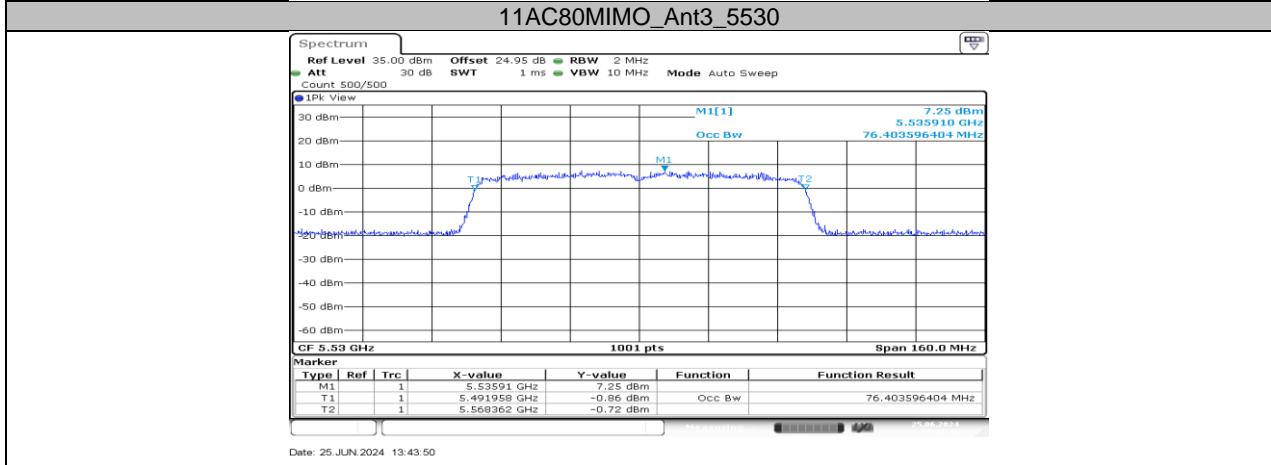
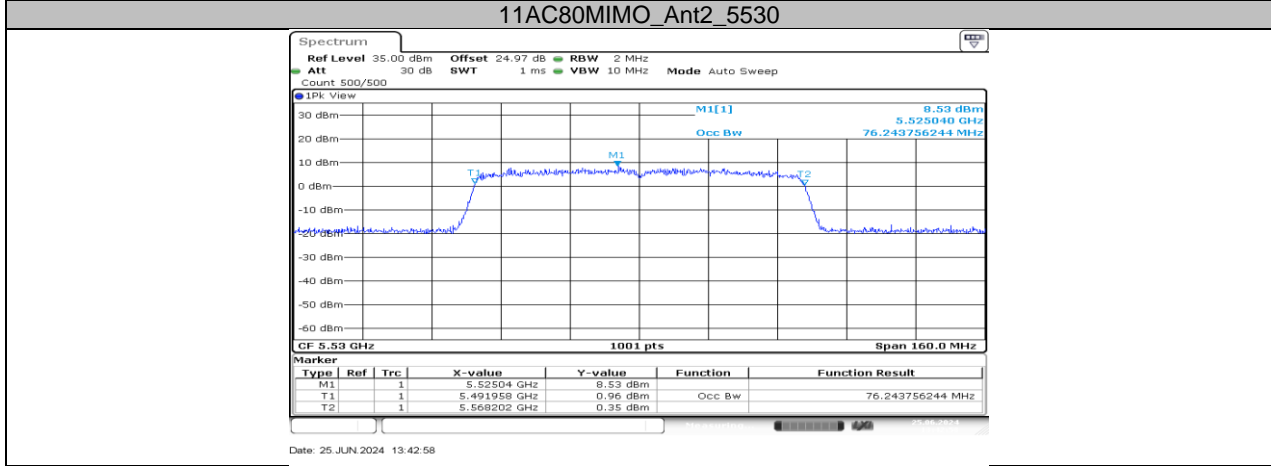
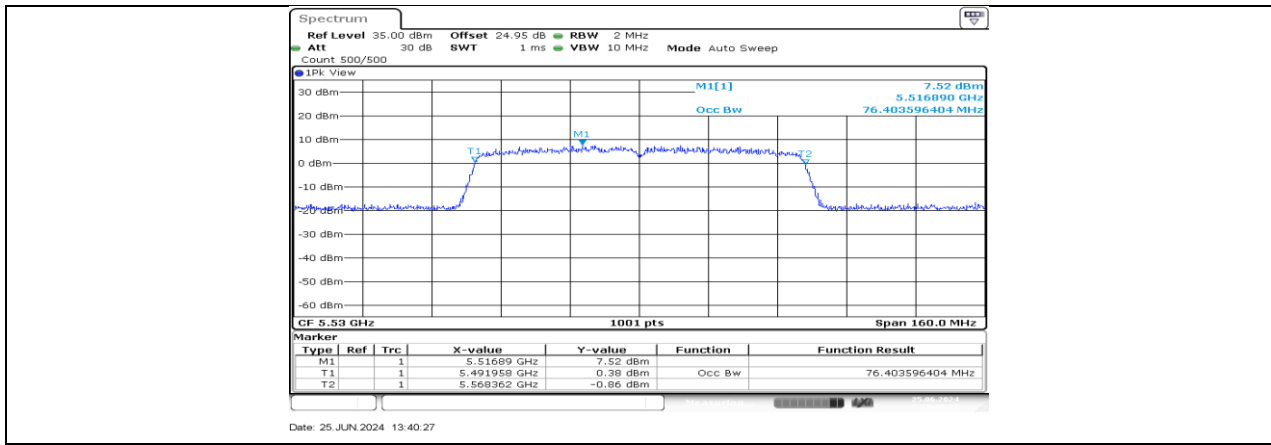


11AC80MIMO\_Ant1\_5290

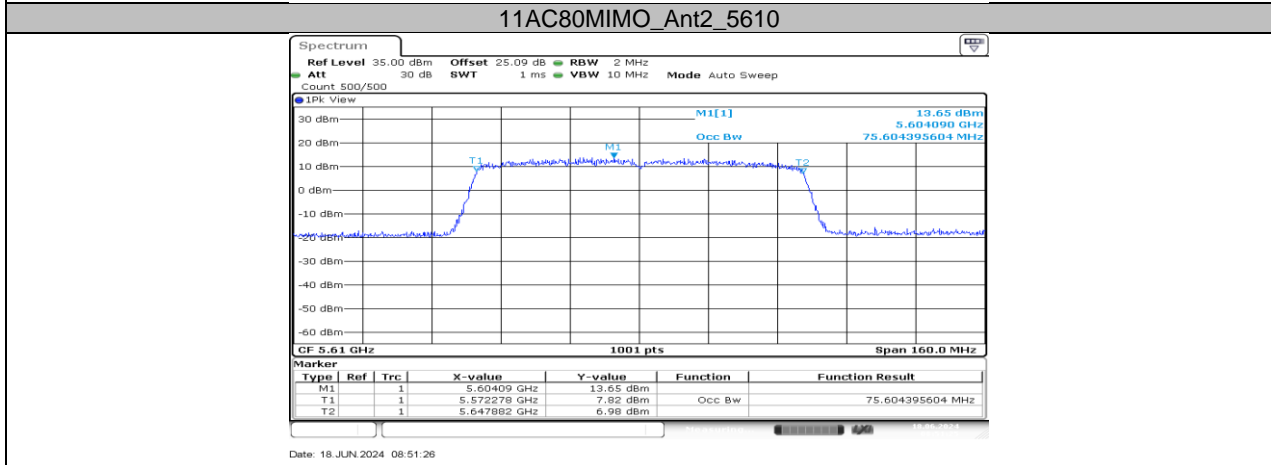
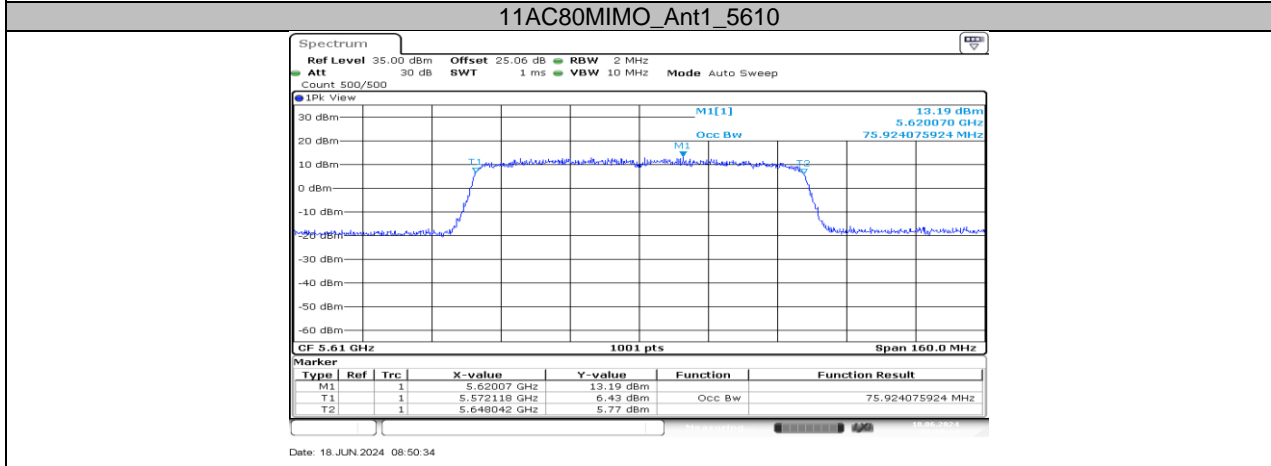
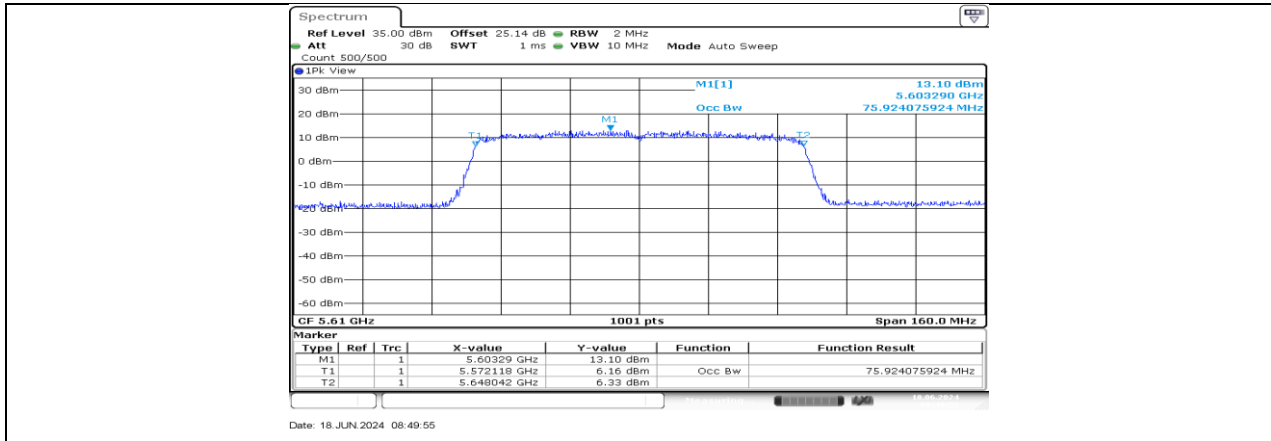


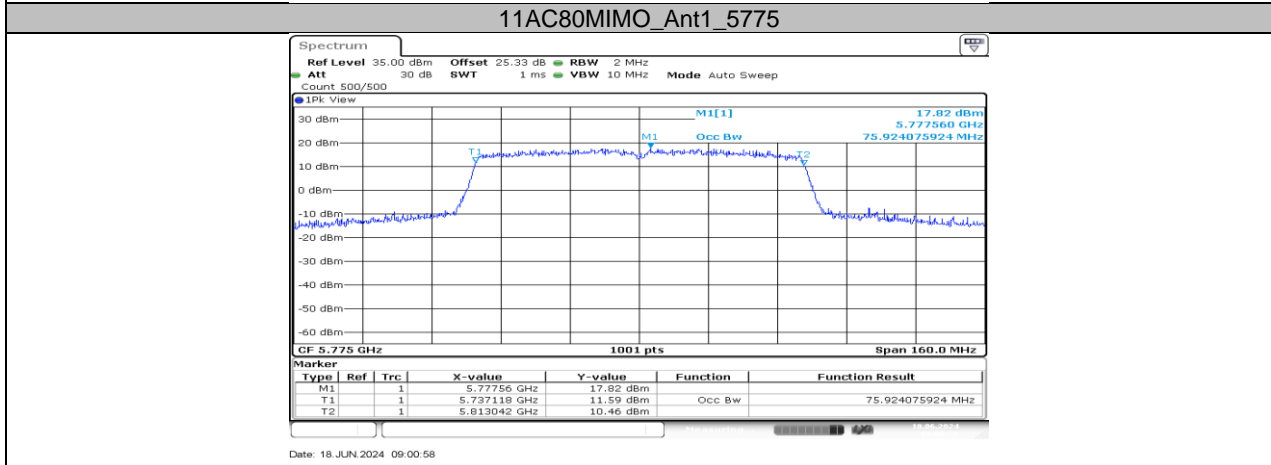
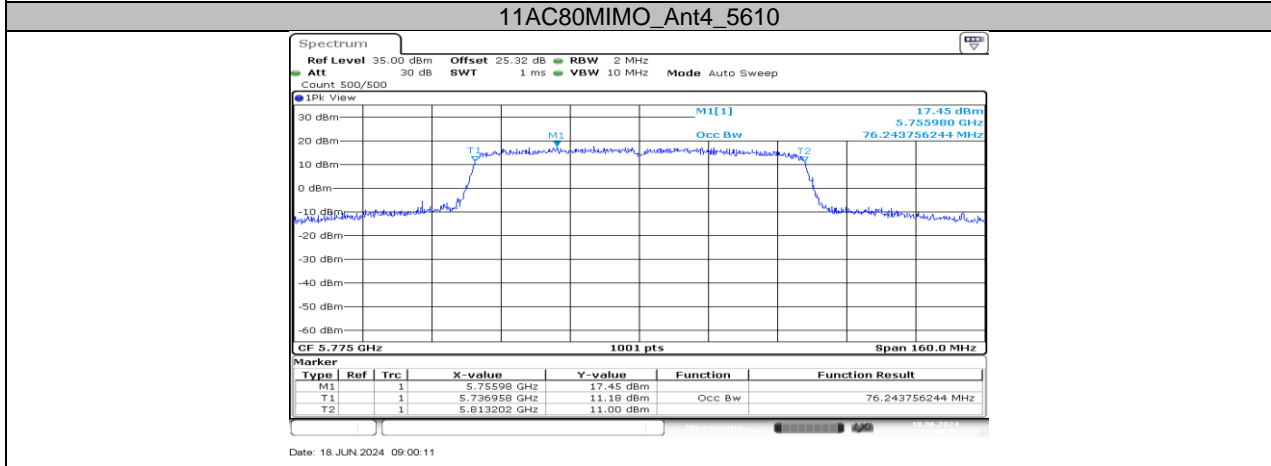
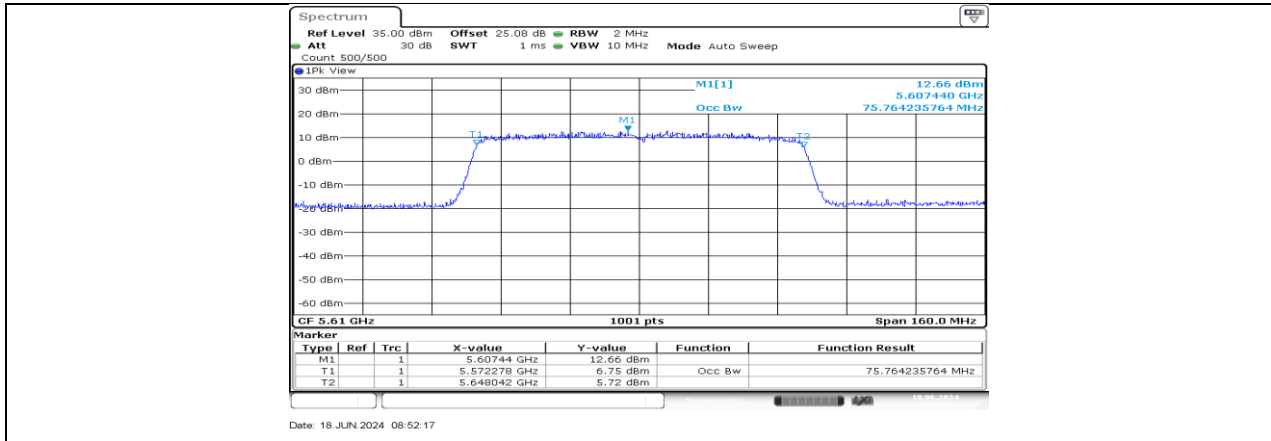
11AC80MIMO\_Ant2\_5290



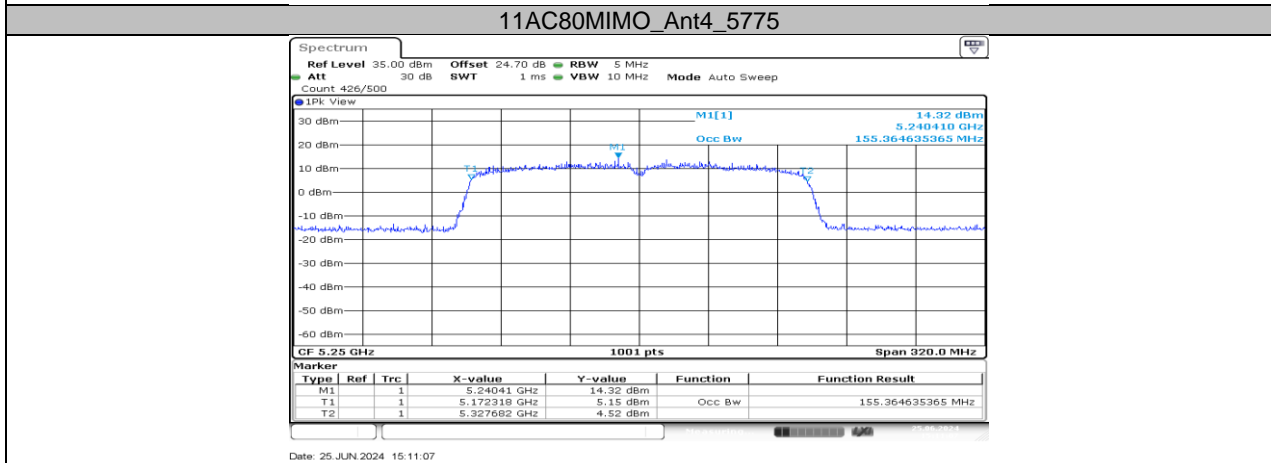
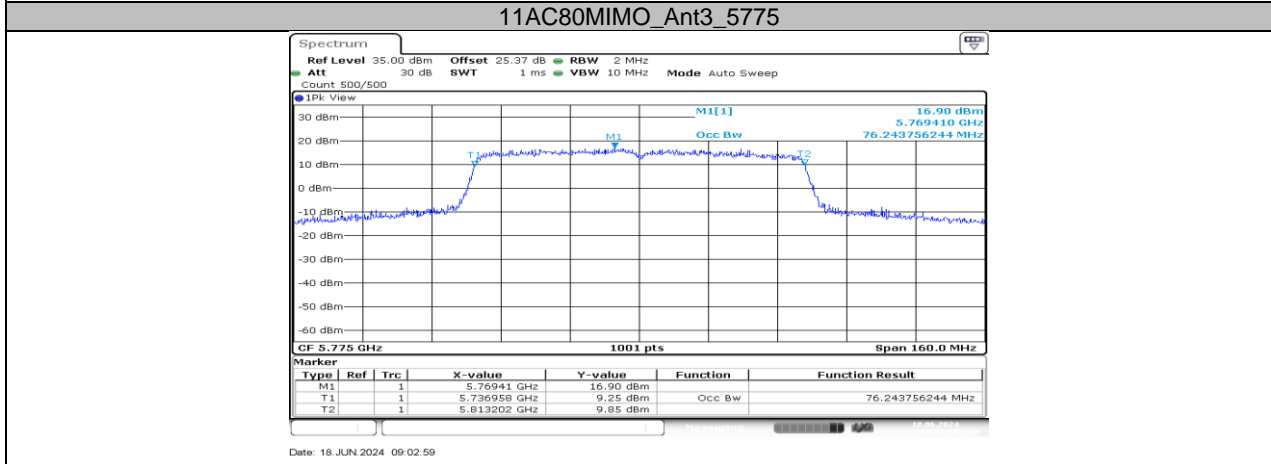
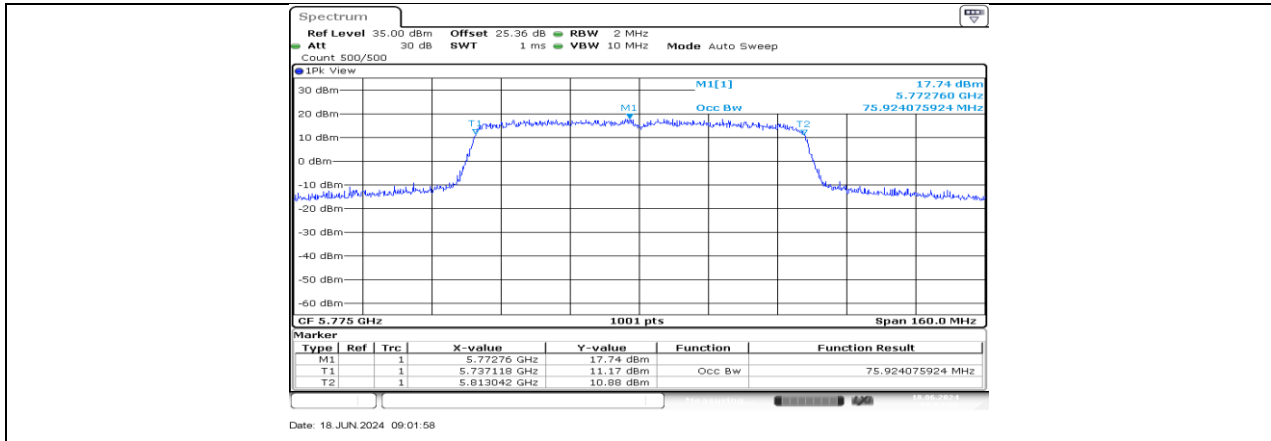


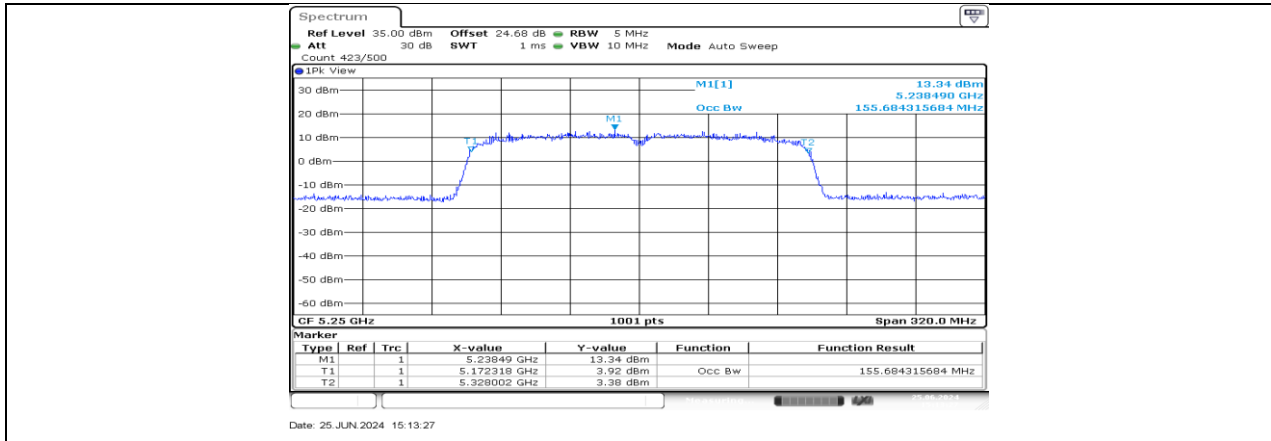
**11AC80MIMO\_Ant4\_5530**



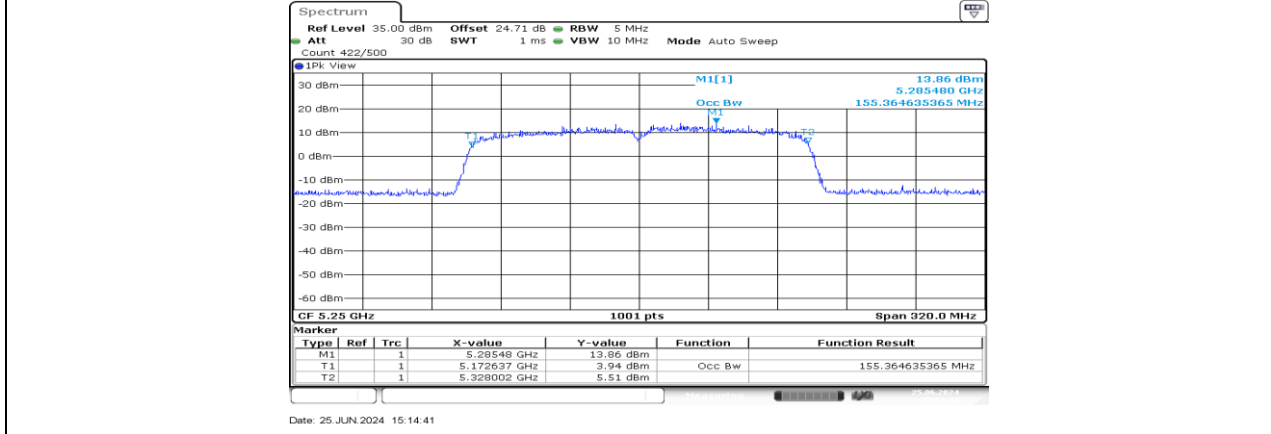


**11AC80MIMO\_Ant2\_5775**

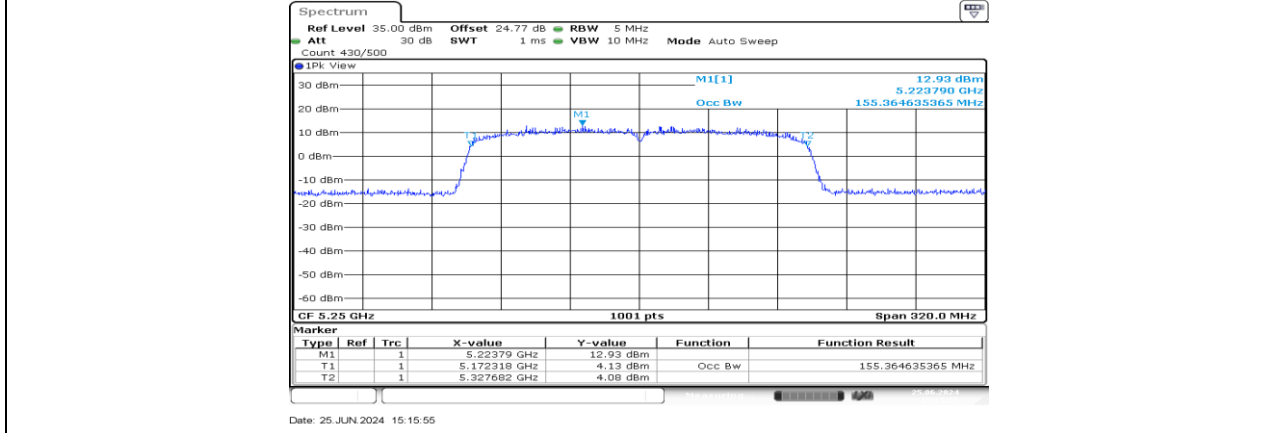




11AC160MIMO\_Ant2\_5250

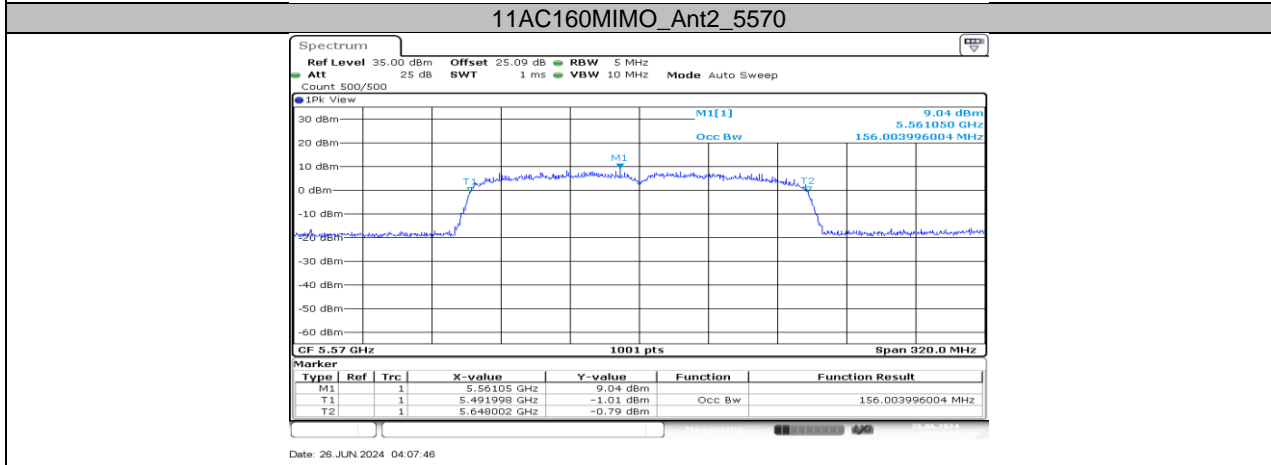
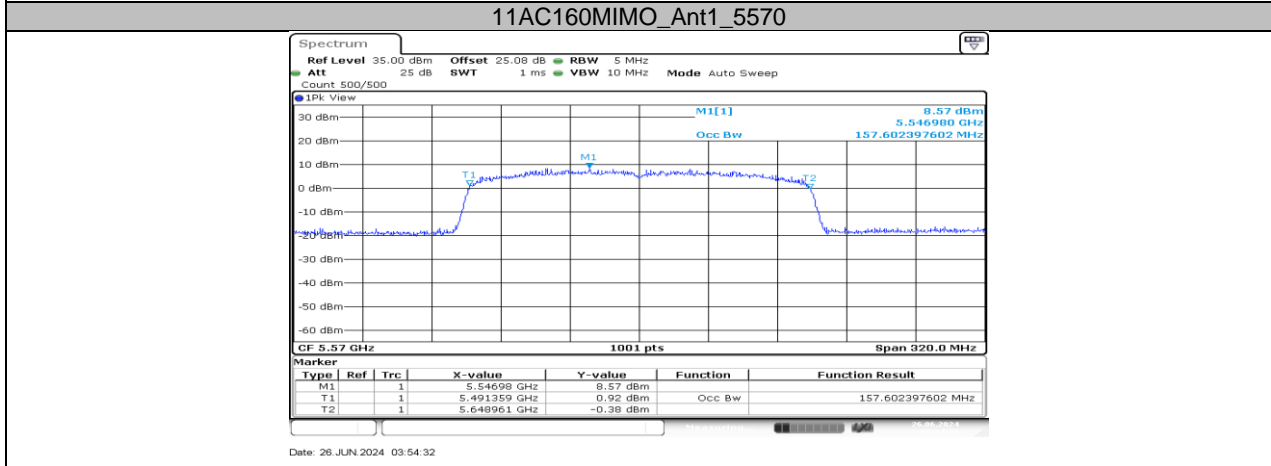
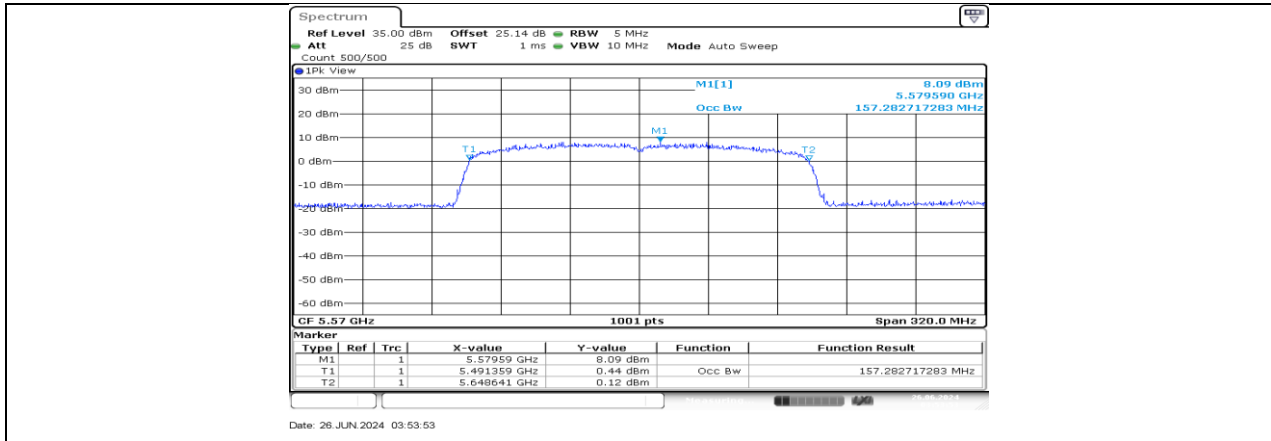


11AC160MIMO\_Ant3\_5250

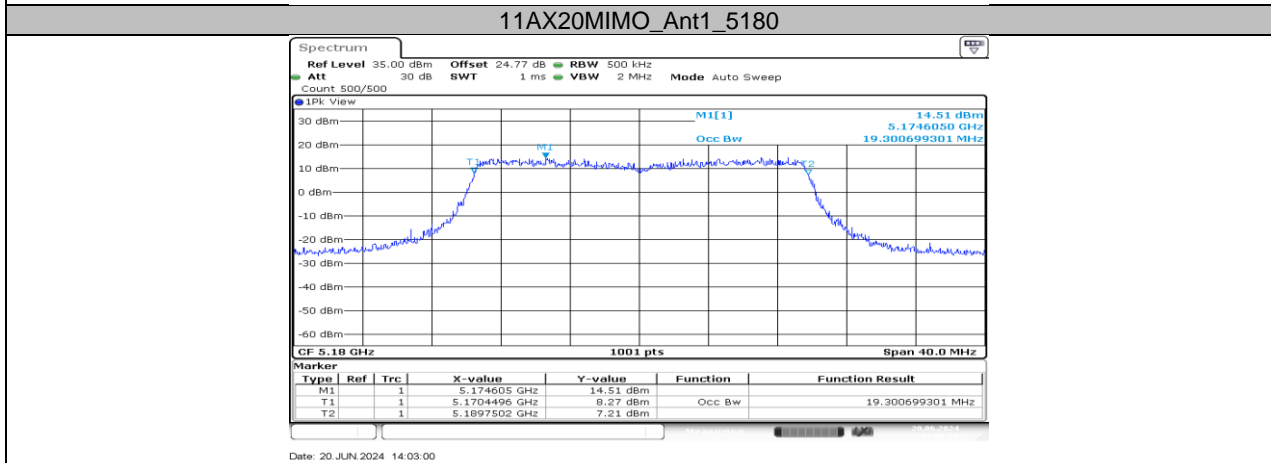
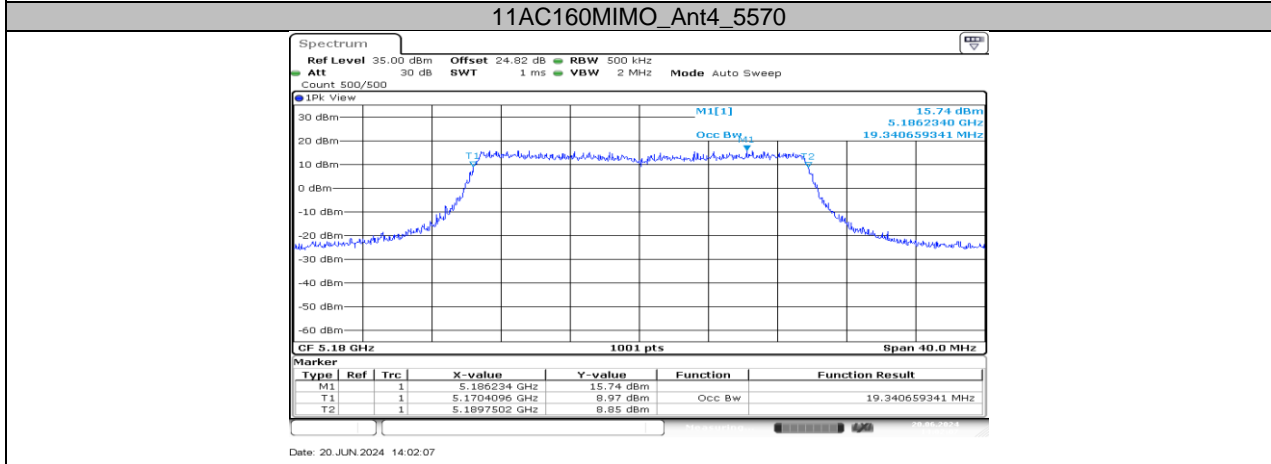
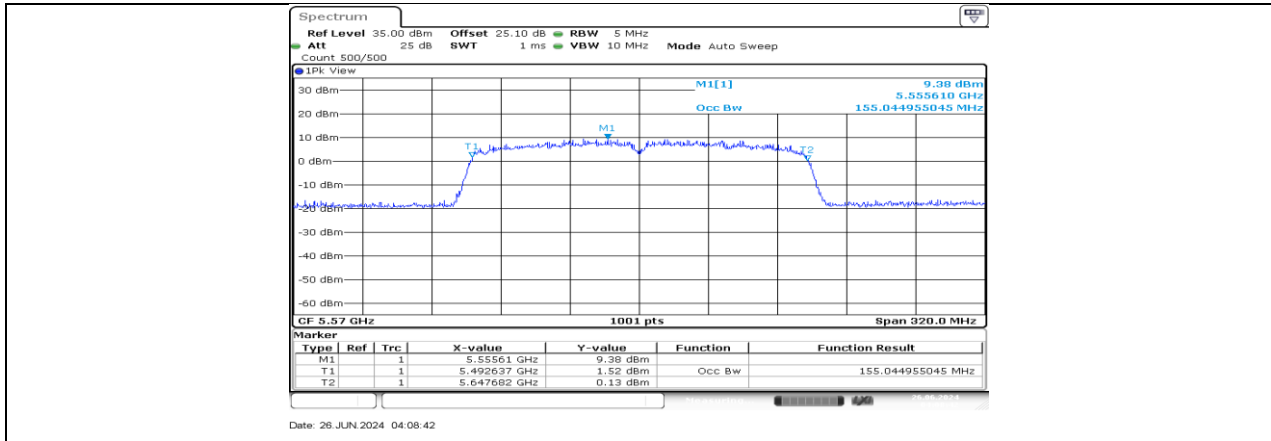


11AC160MIMO\_Ant4\_5250

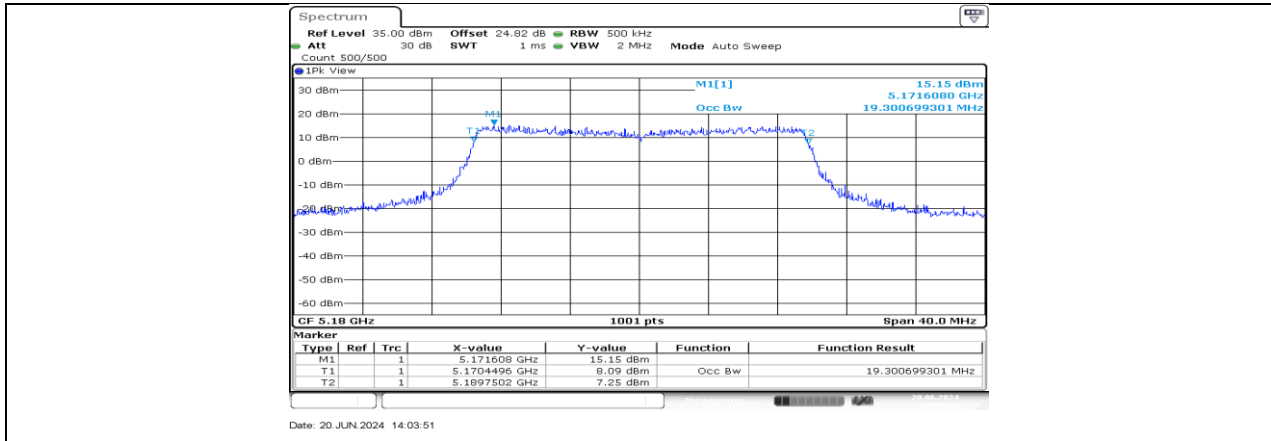




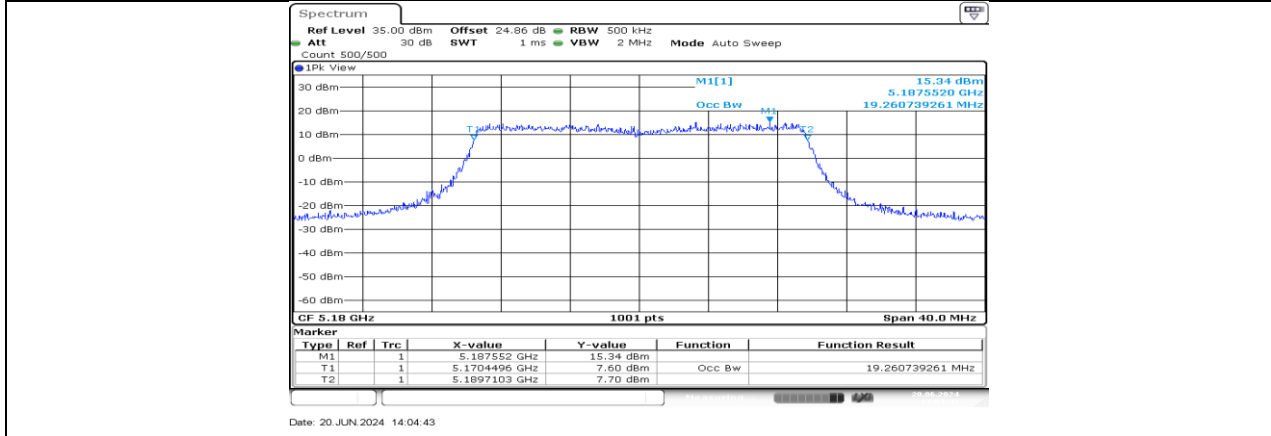
**11AC160MIMO\_Ant3\_5570**



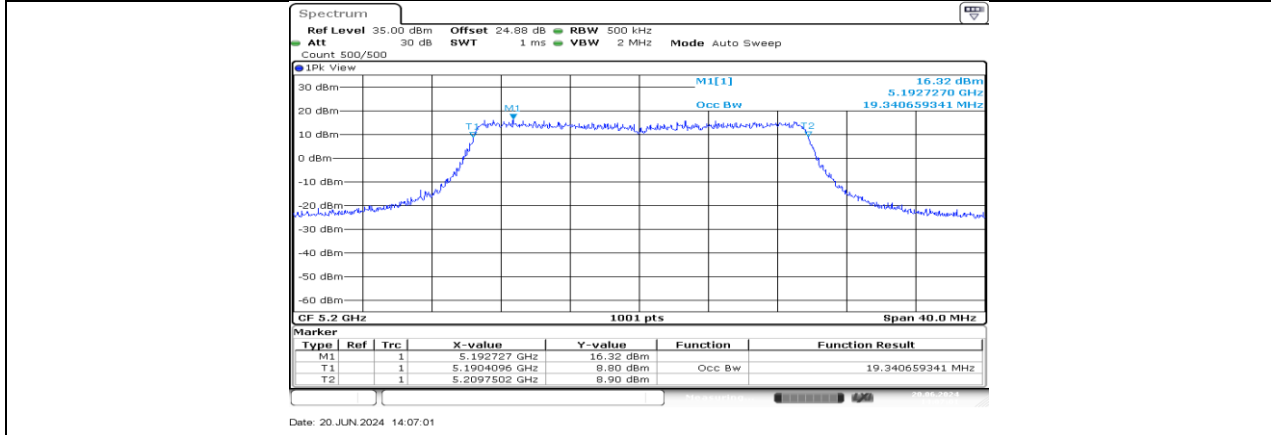
**11AX20MIMO\_Ant2\_5180**



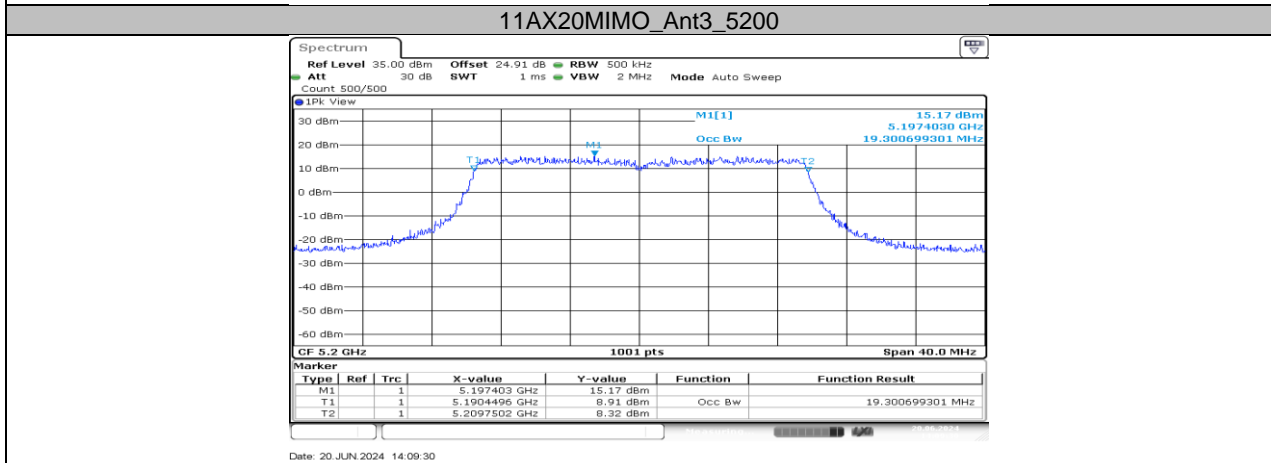
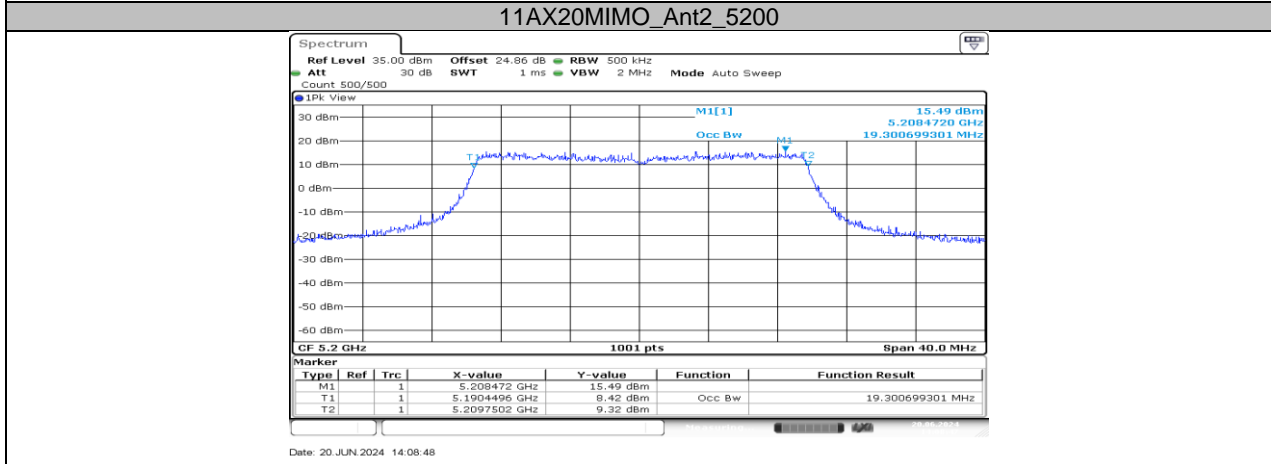
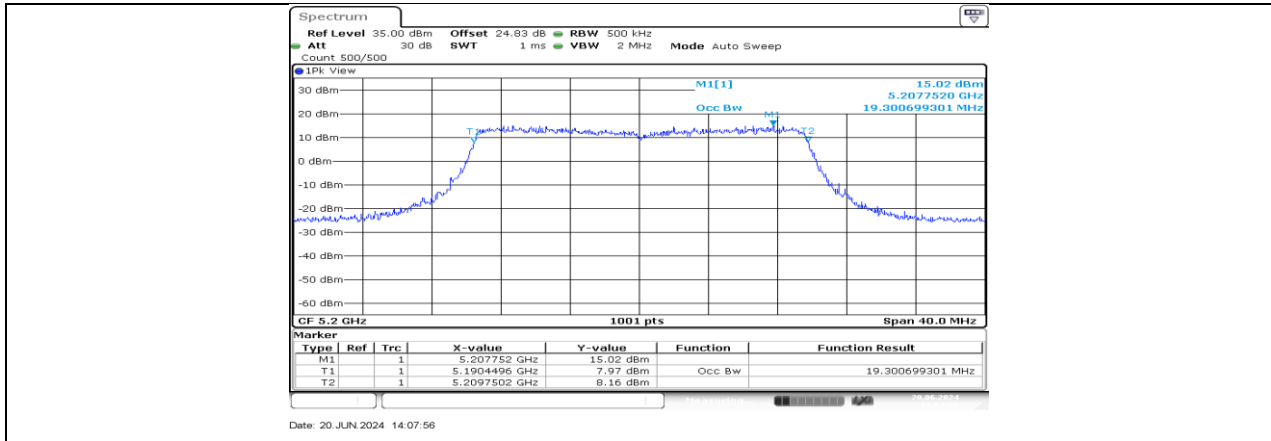
11AX20MIMO\_Ant3\_5180



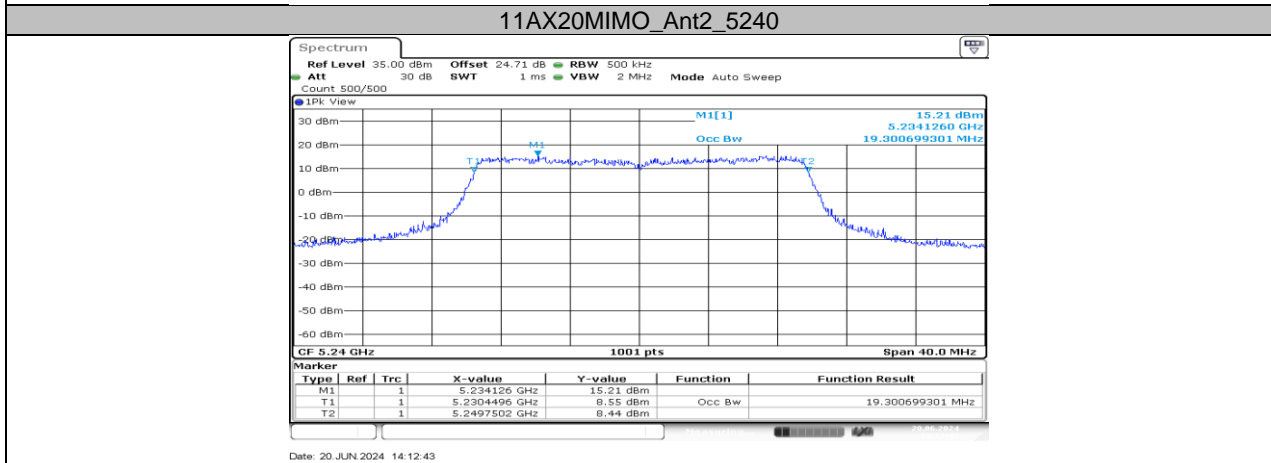
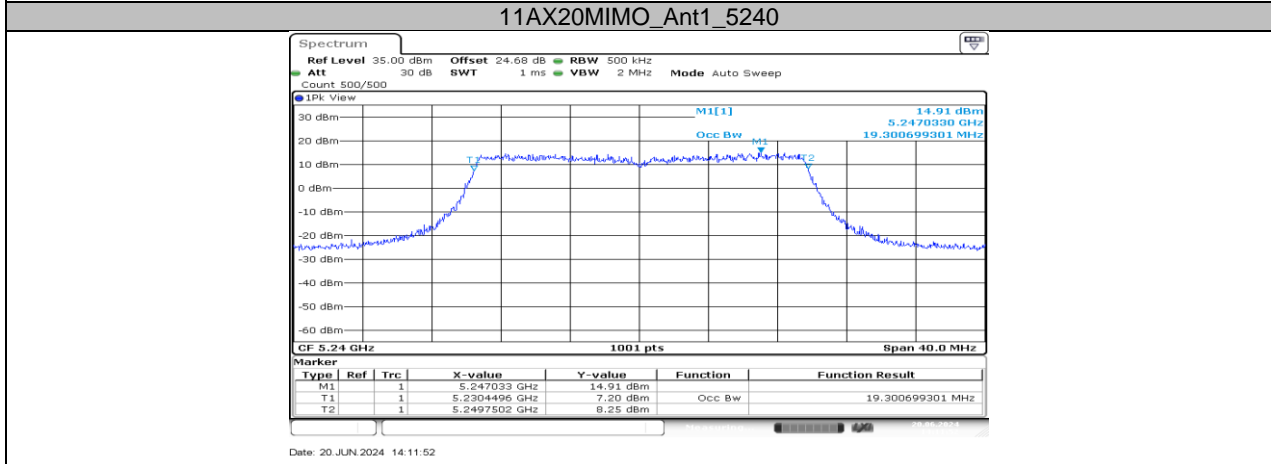
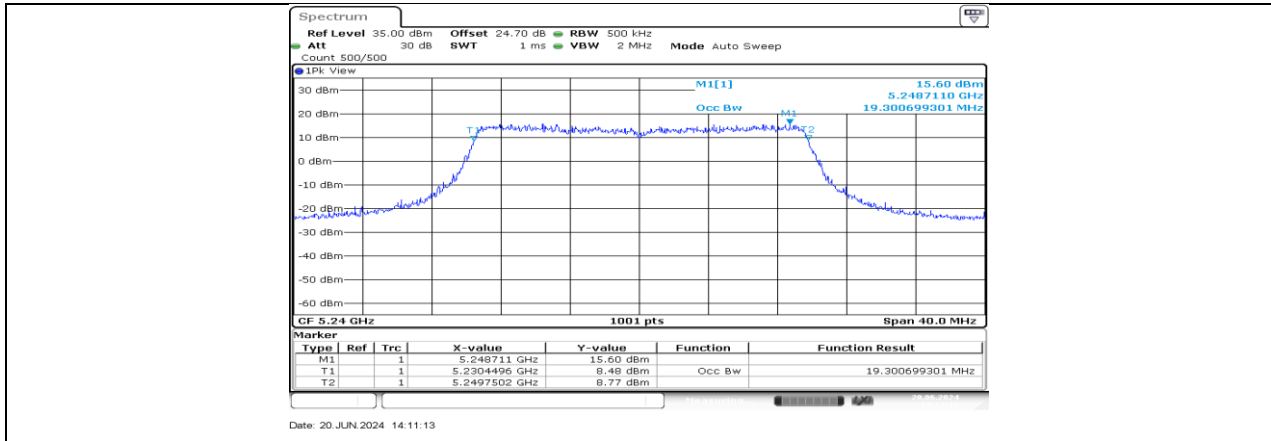
11AX20MIMO\_Ant4\_5180



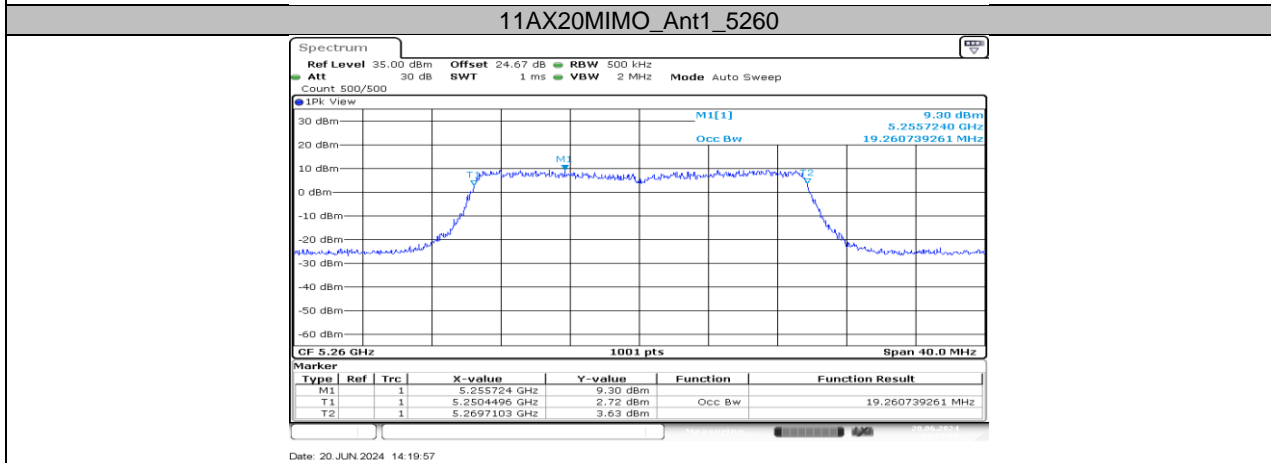
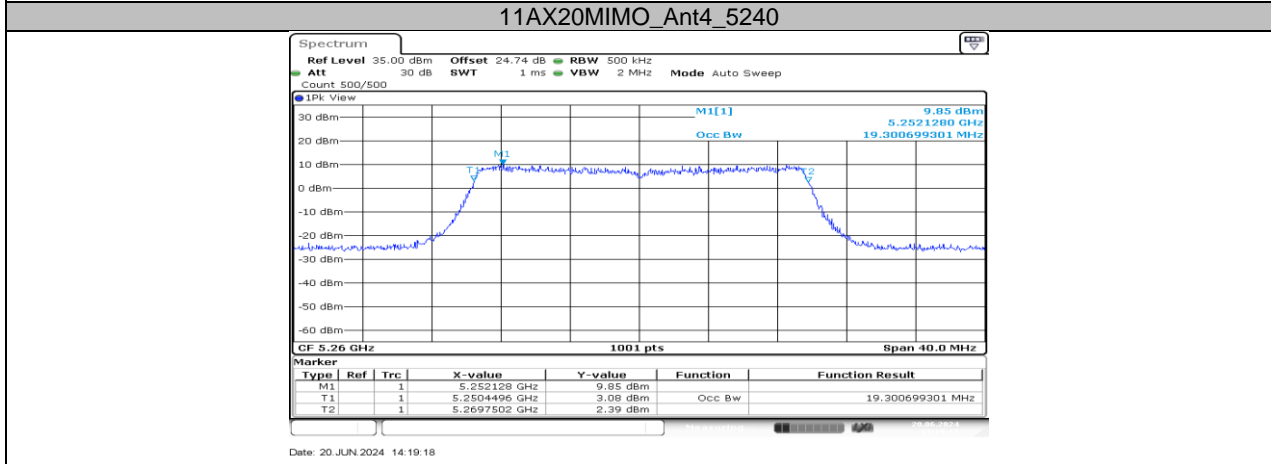
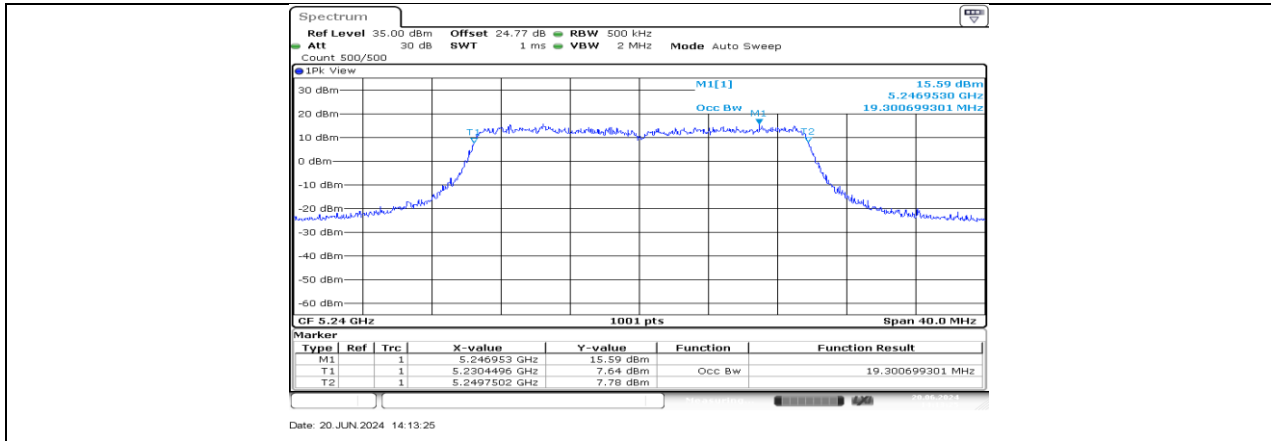
11AX20MIMO\_Ant1\_5200



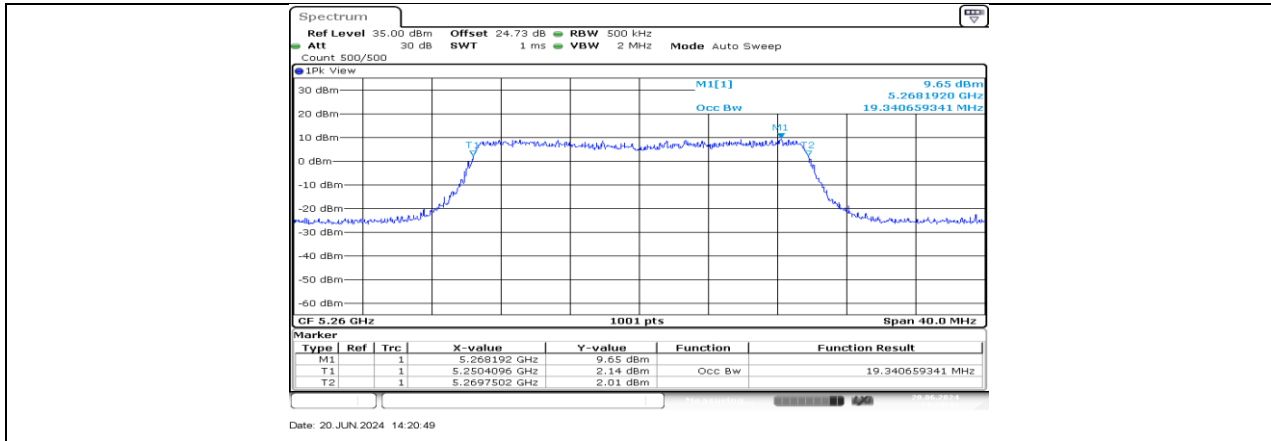
**11AX20MIMO\_Ant4\_5200**



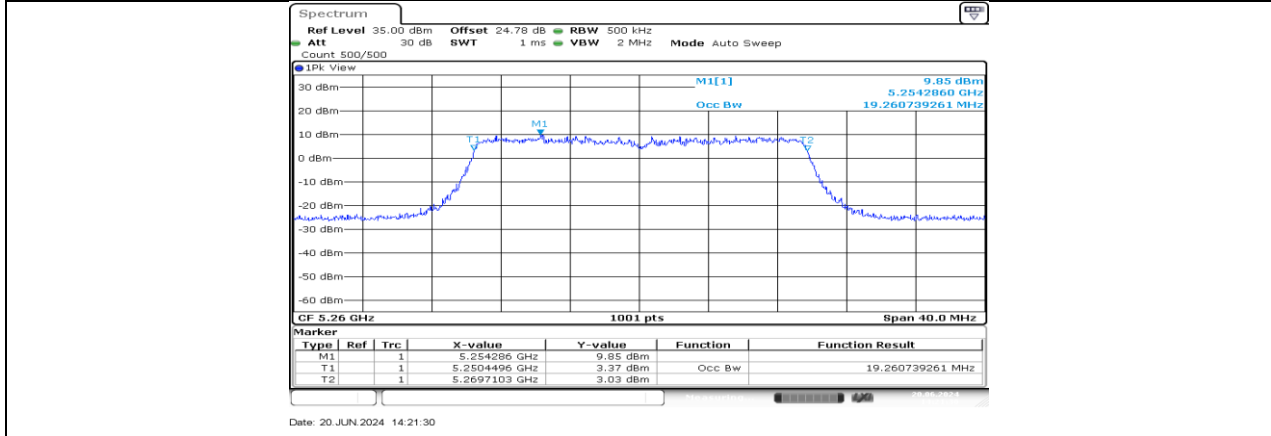
**11AX20MIMO\_Ant3\_5240**



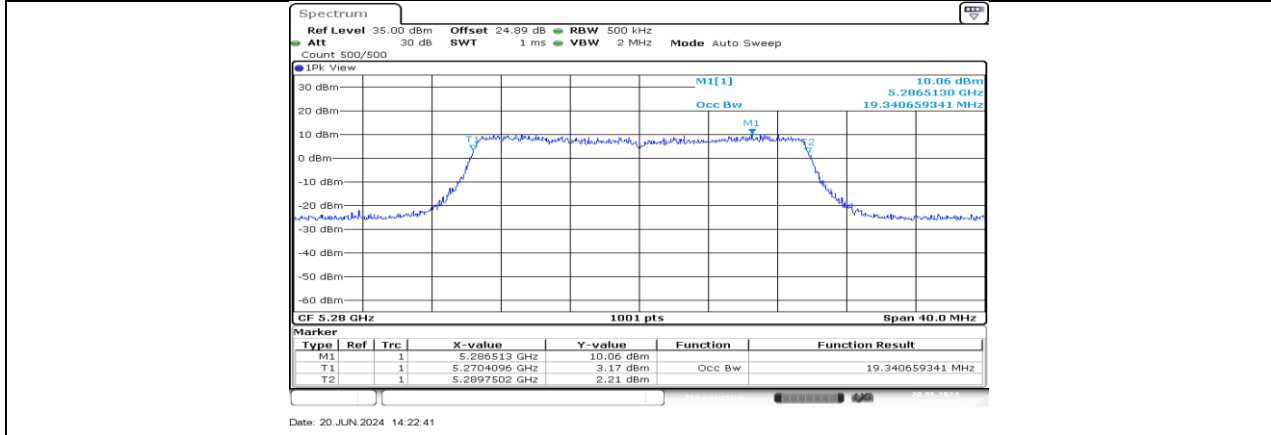
**11AX20MIMO\_Ant2\_5260**



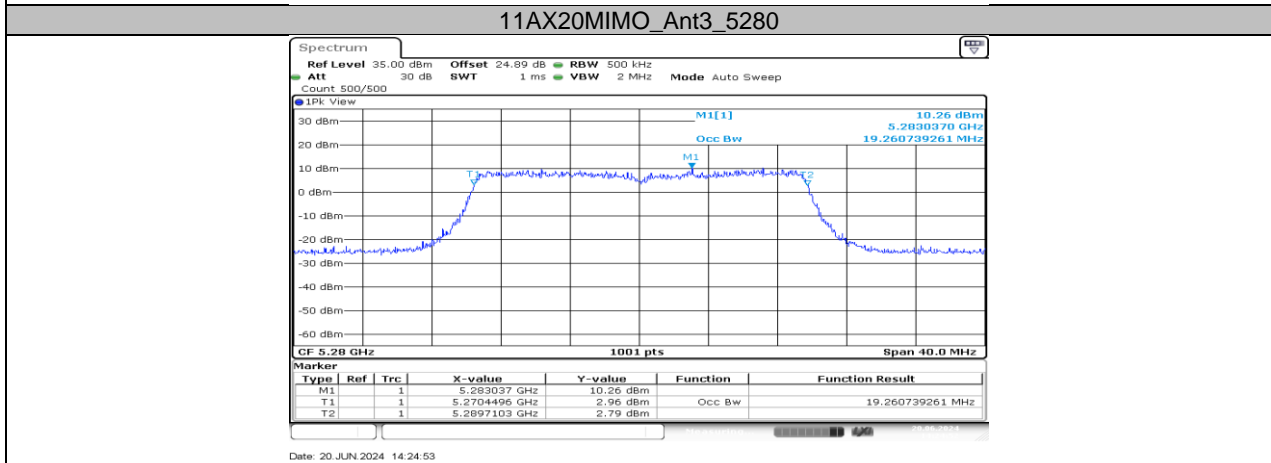
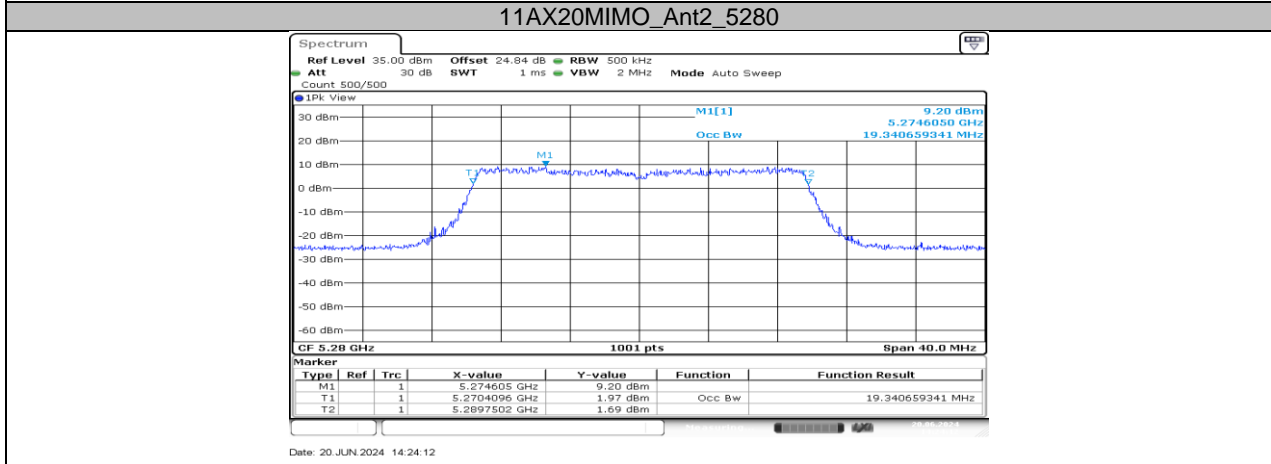
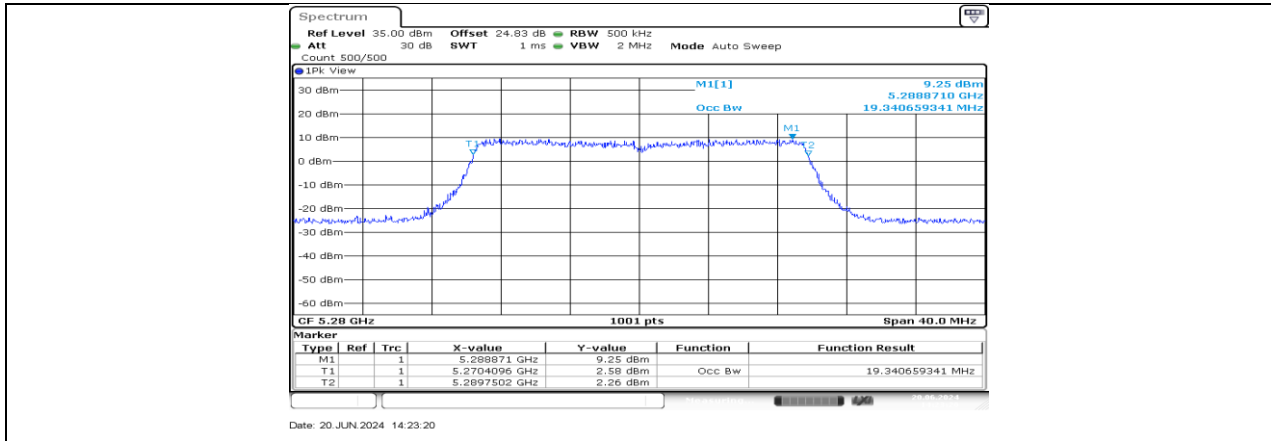
11AX20MIMO\_Ant3\_5260



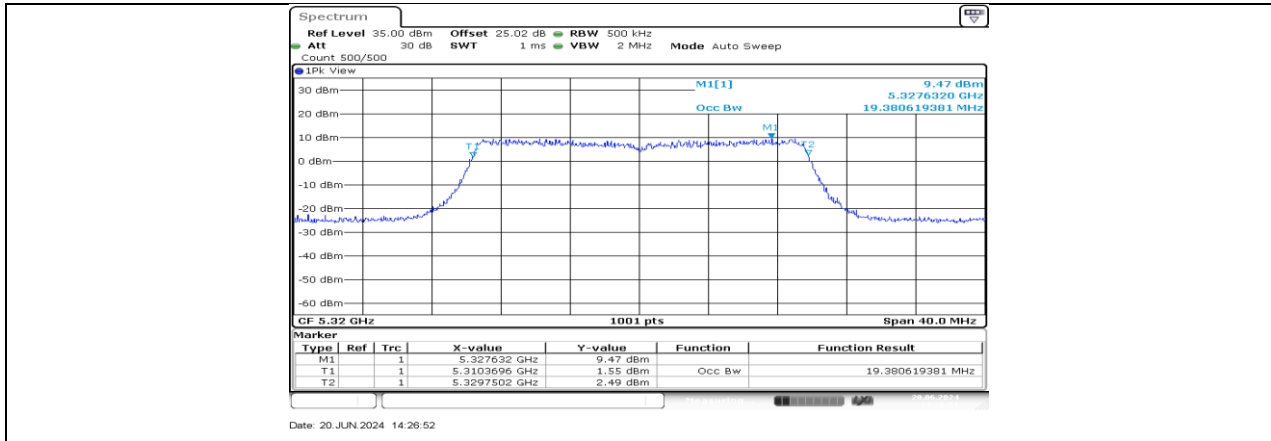
11AX20MIMO\_Ant4\_5260



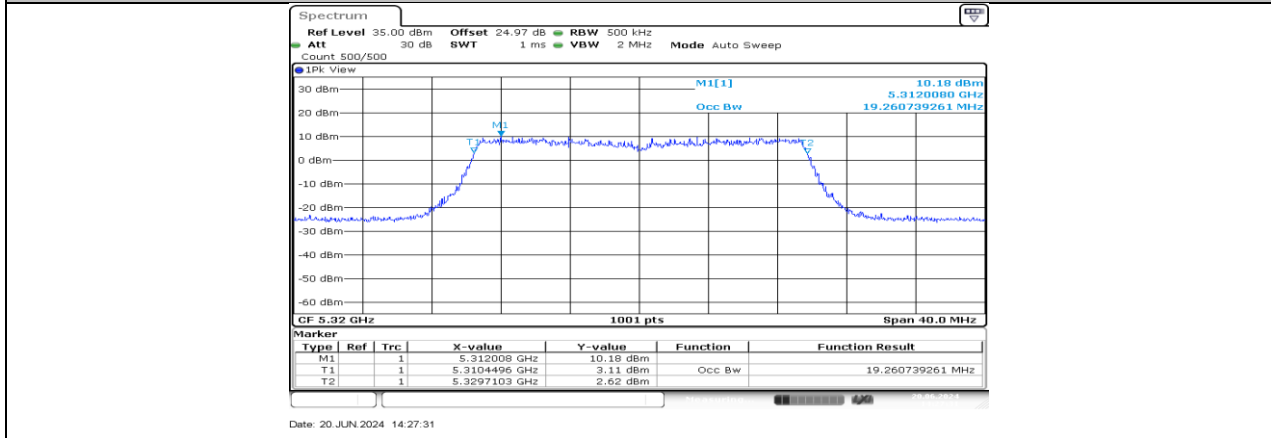
11AX20MIMO\_Ant1\_5280



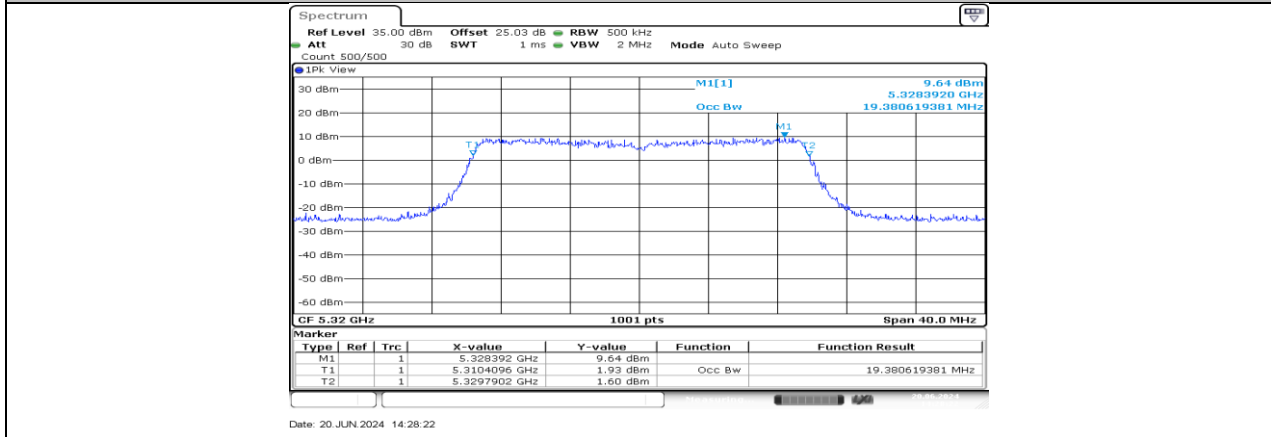




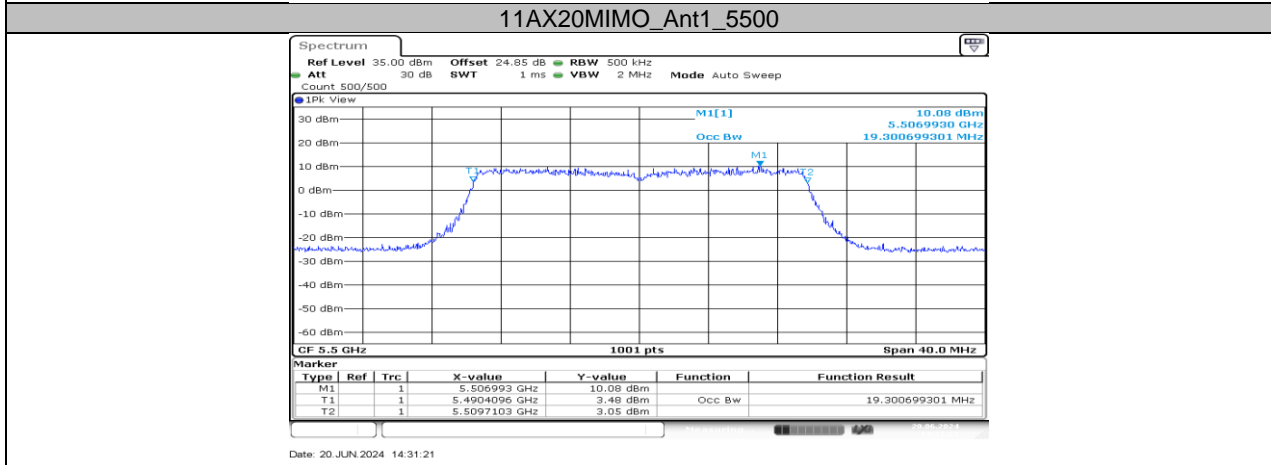
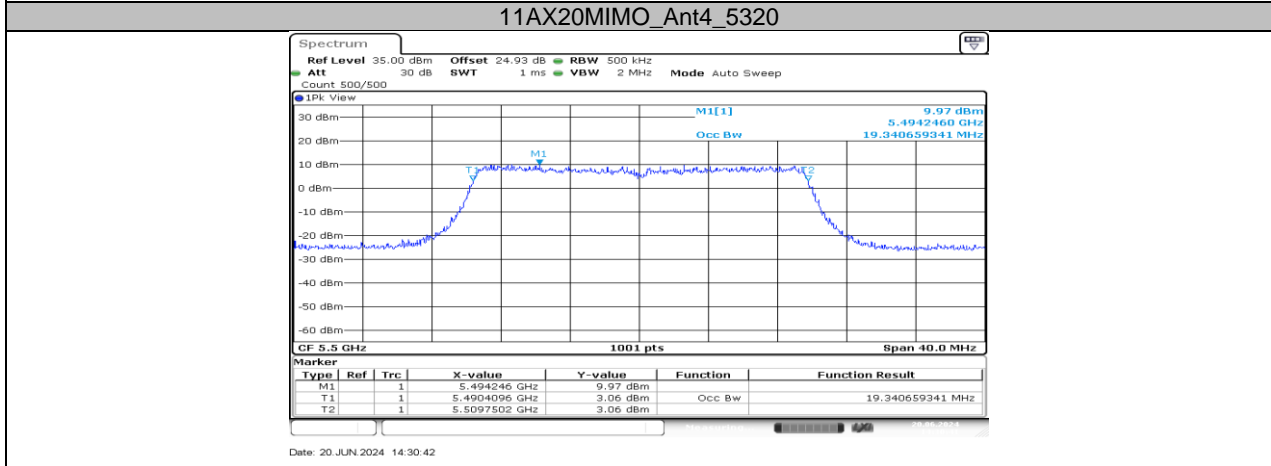
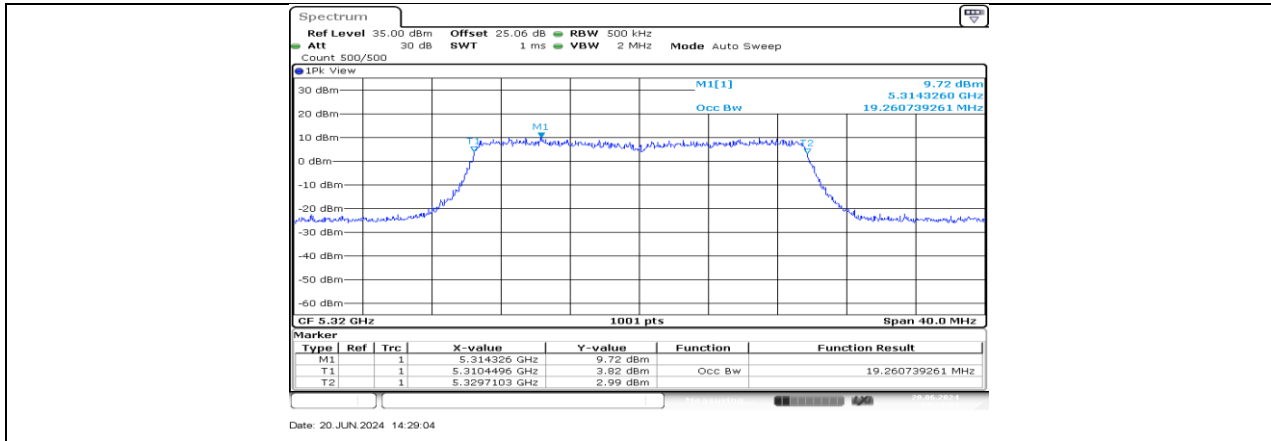
11AX20MIMO\_Ant1\_5320

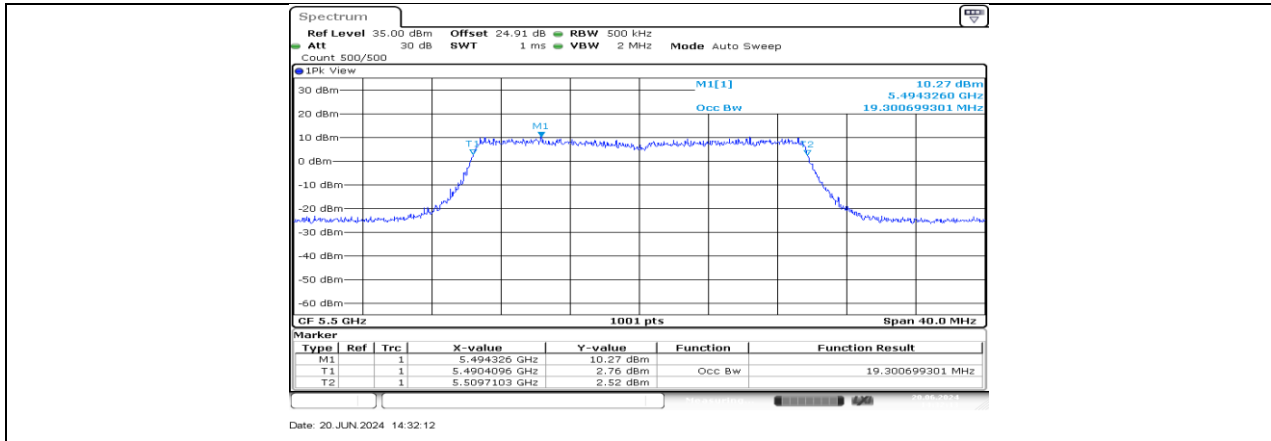


11AX20MIMO\_Ant2\_5320

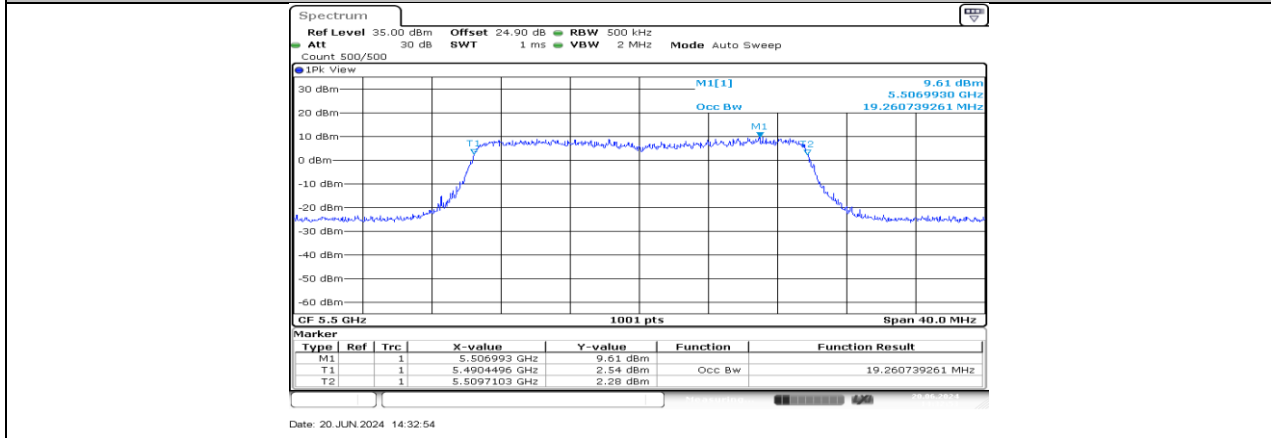


11AX20MIMO\_Ant3\_5320

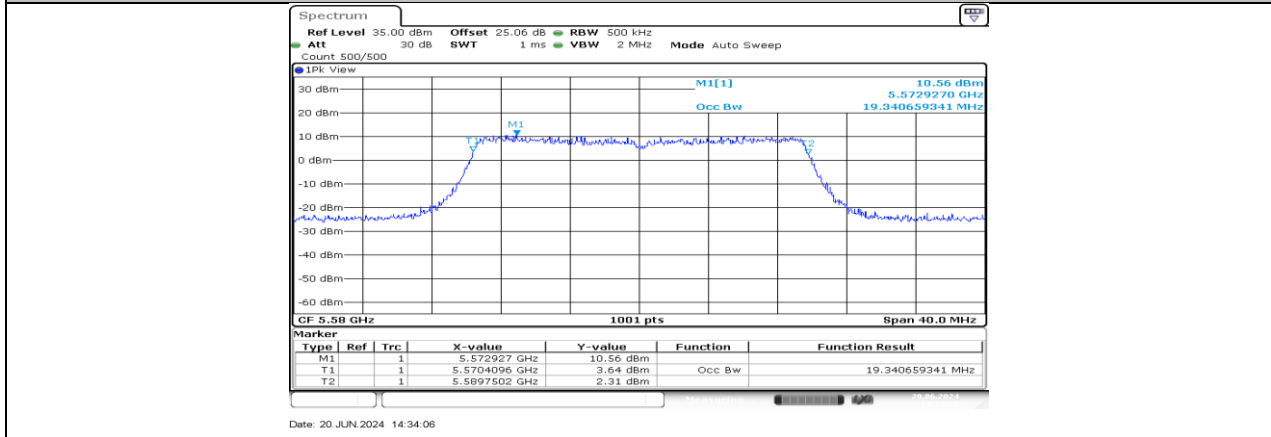




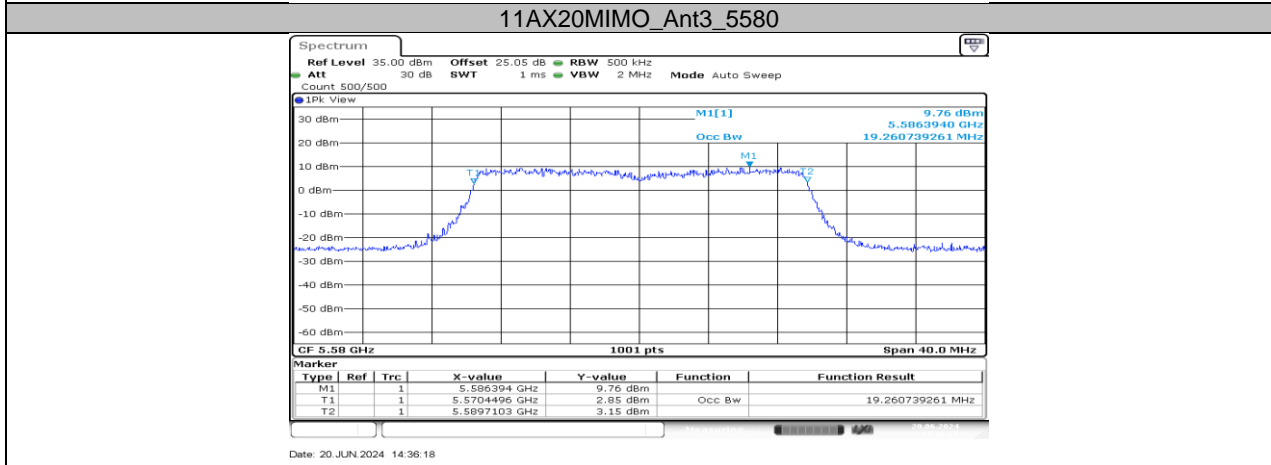
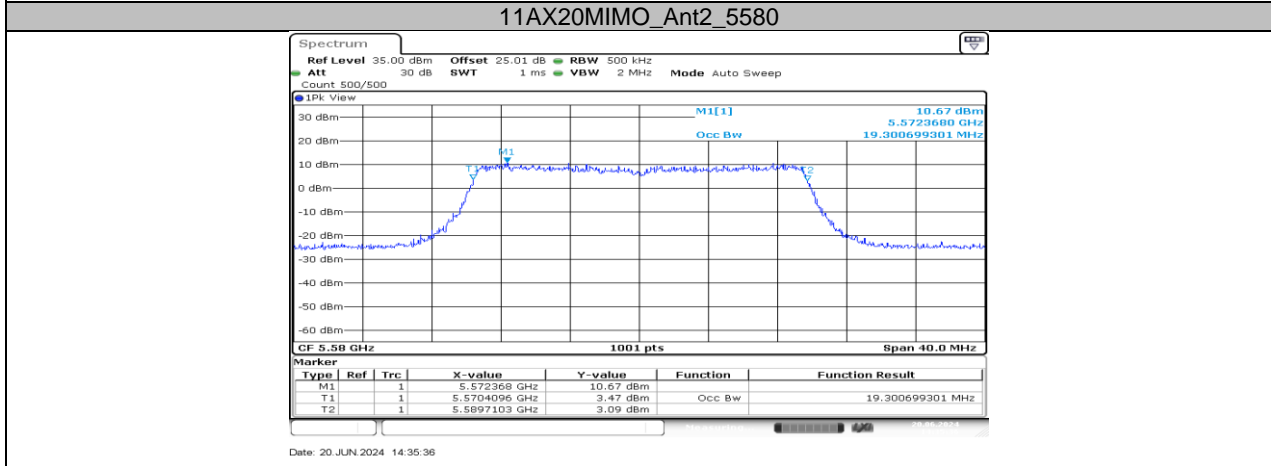
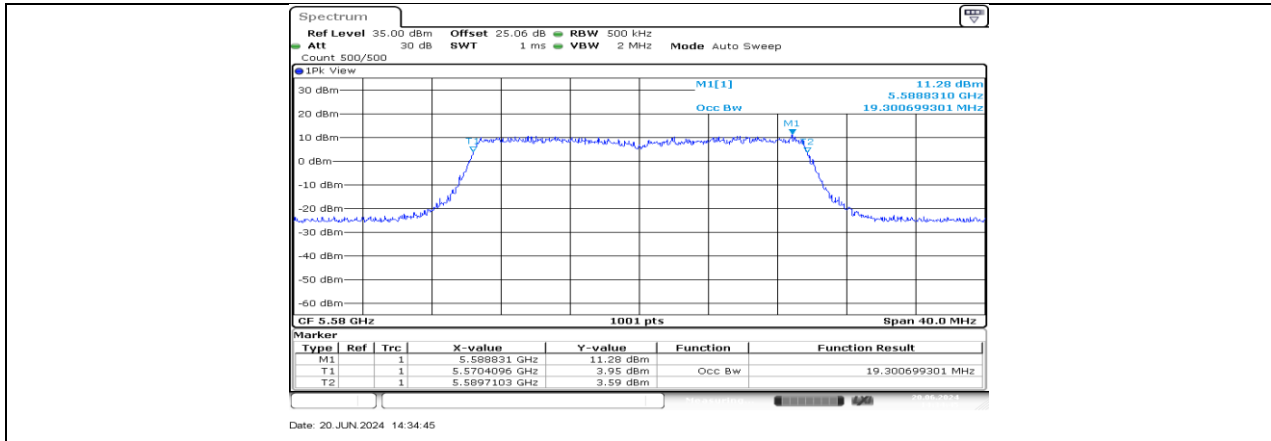
11AX20MIMO\_Ant3\_5500



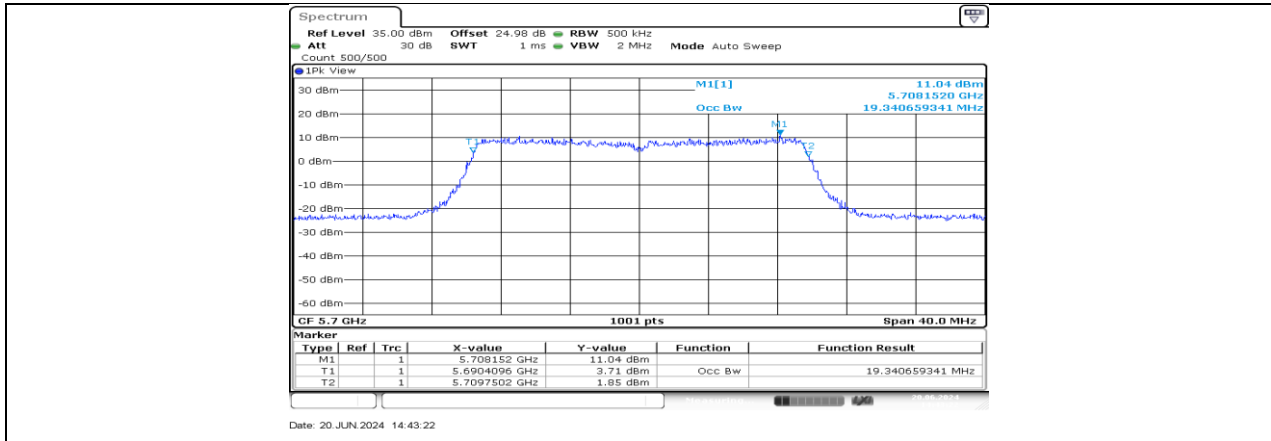
11AX20MIMO\_Ant4\_5500



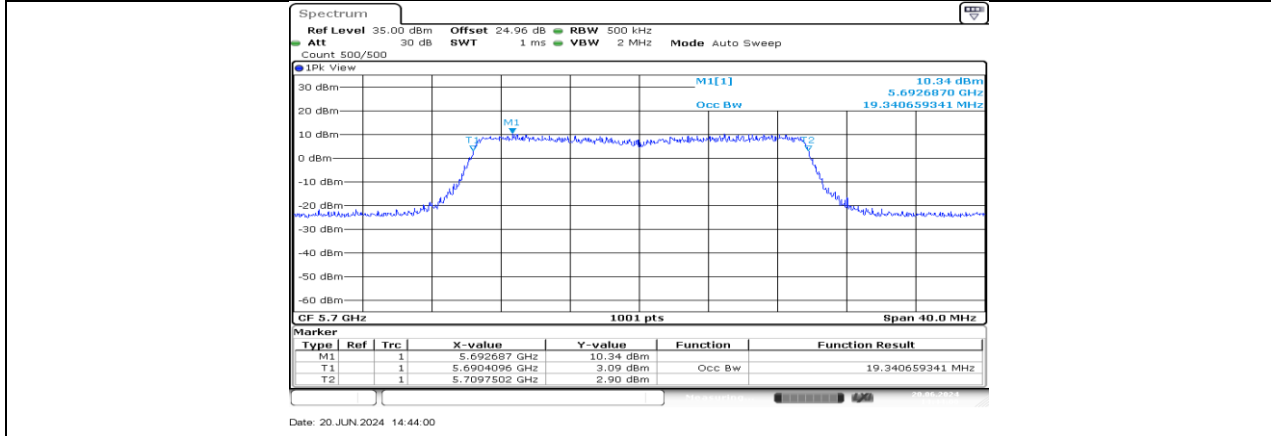
11AX20MIMO\_Ant1\_5580



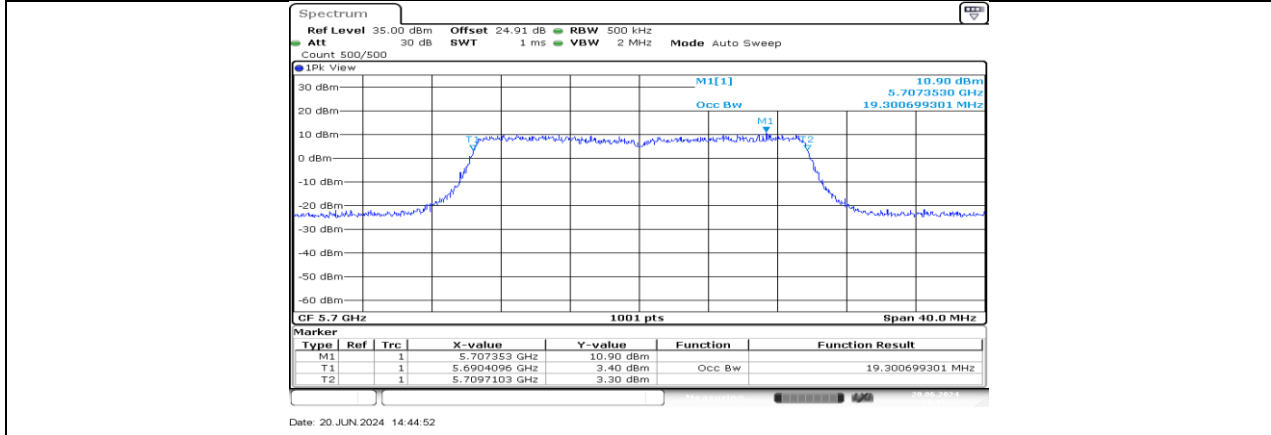
**11AX20MIMO\_Ant4\_5580**



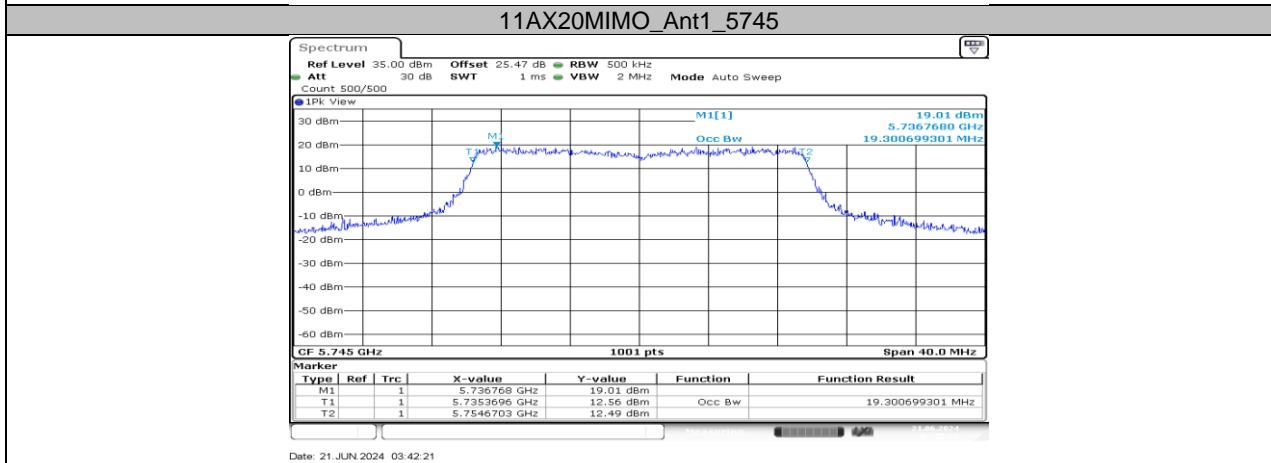
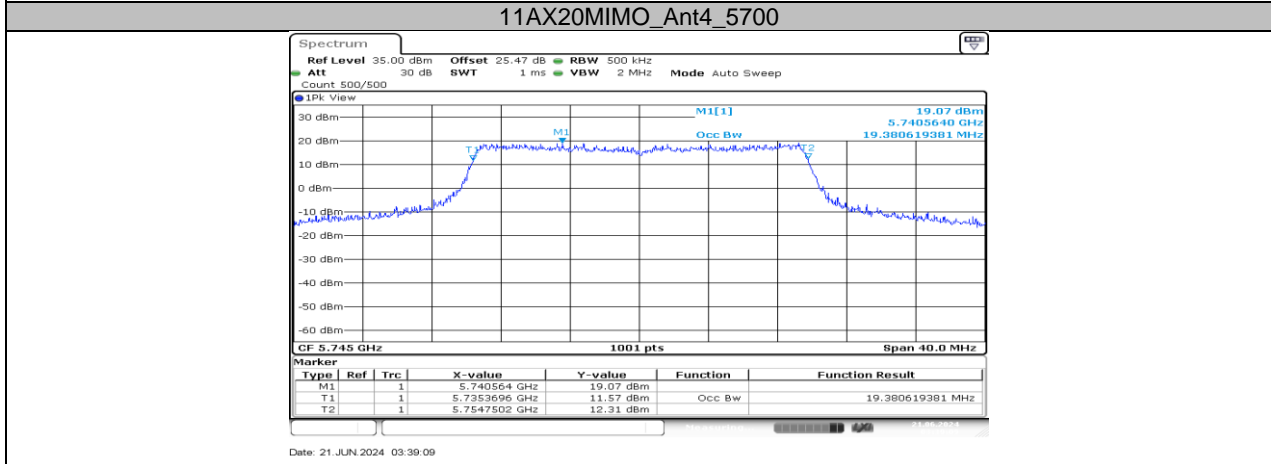
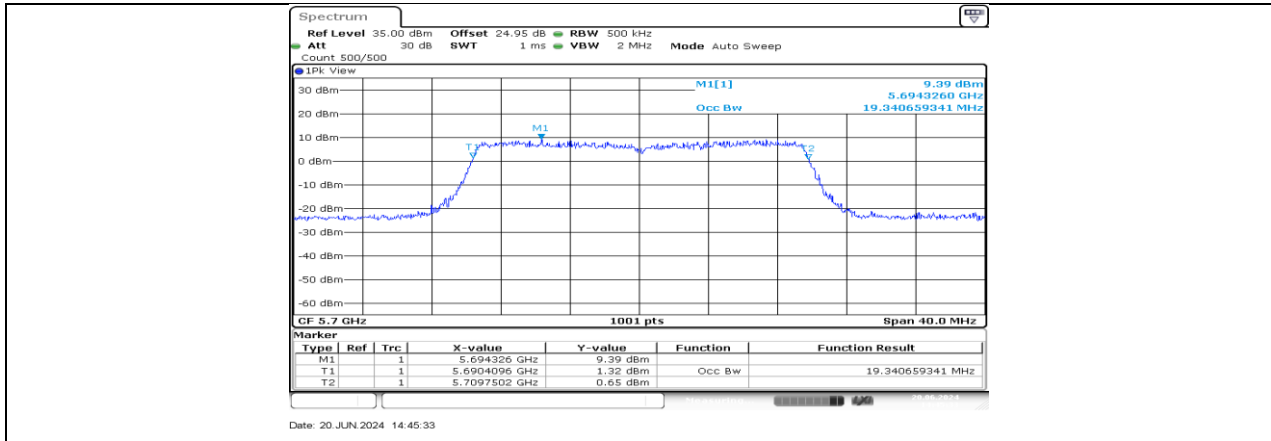
11AX20MIMO\_Ant1\_5700



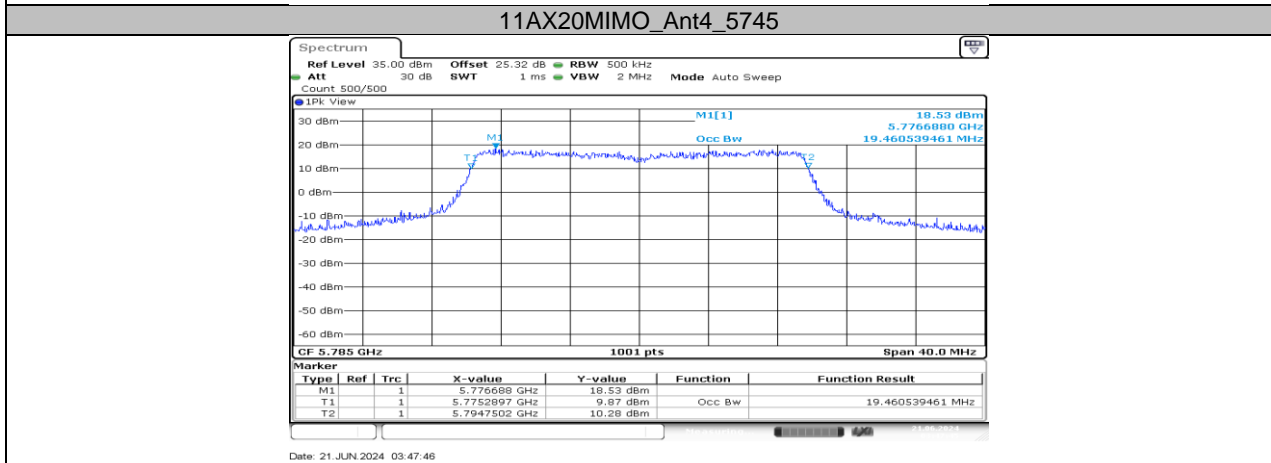
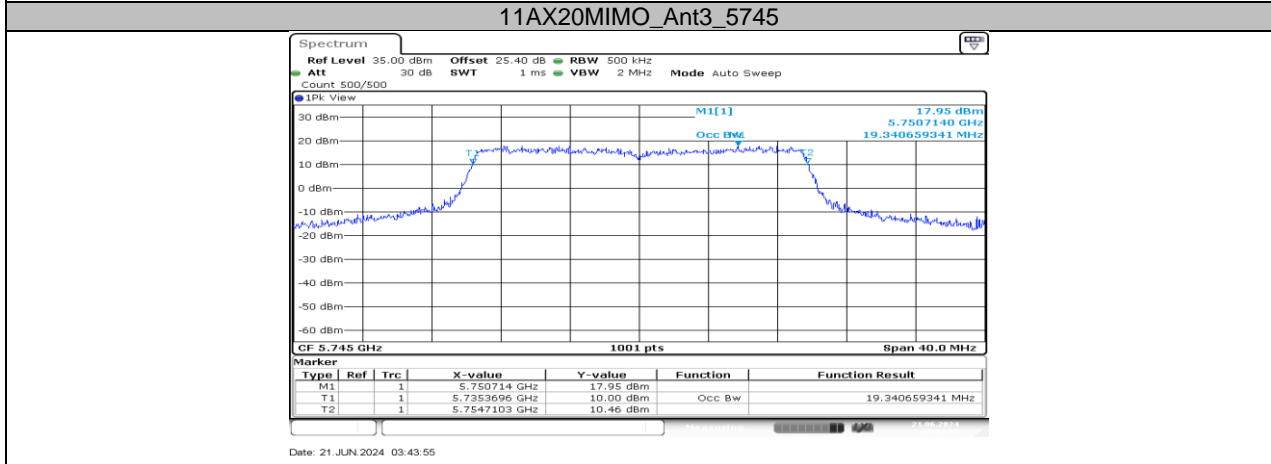
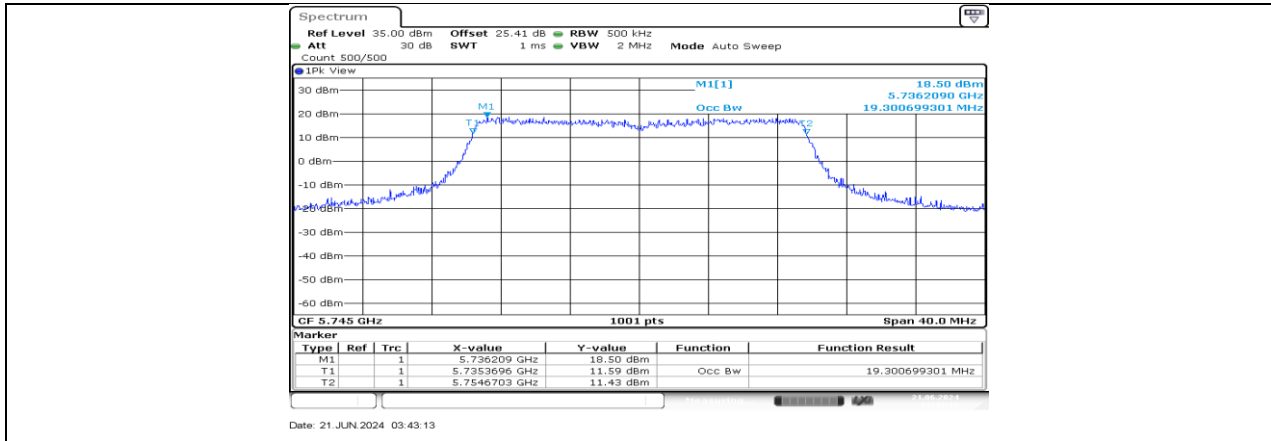
11AX20MIMO\_Ant2\_5700

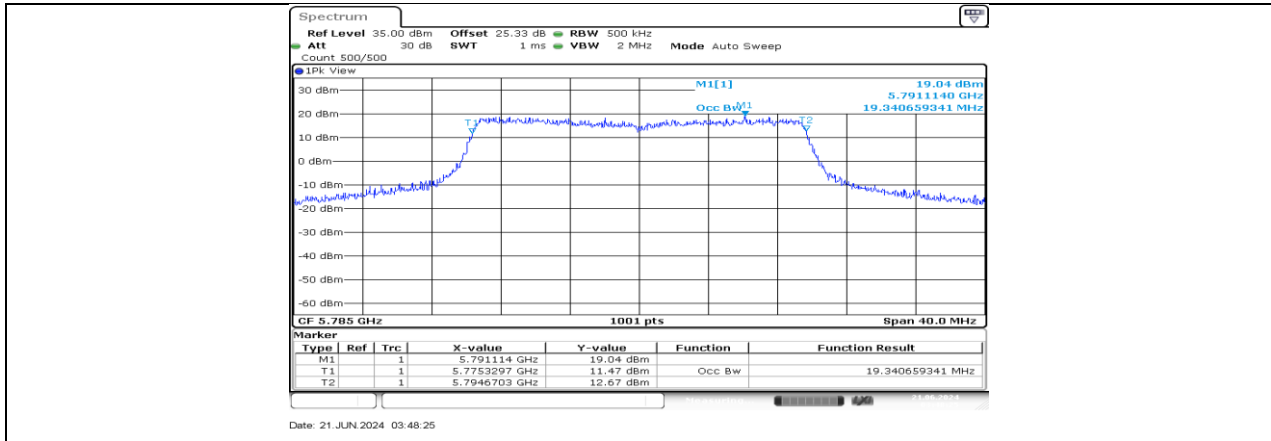


11AX20MIMO\_Ant3\_5700

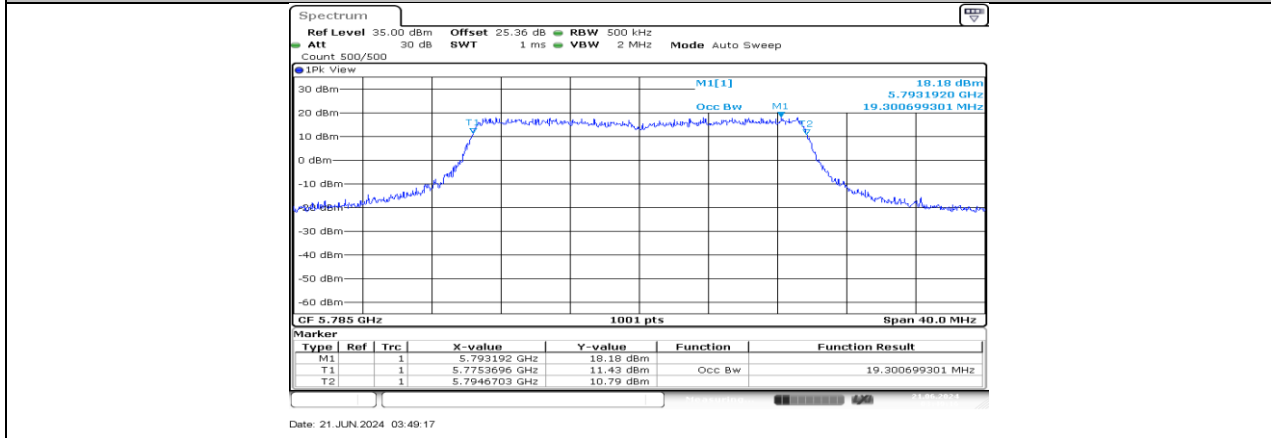


**11AX20MIMO\_Ant2\_5745**

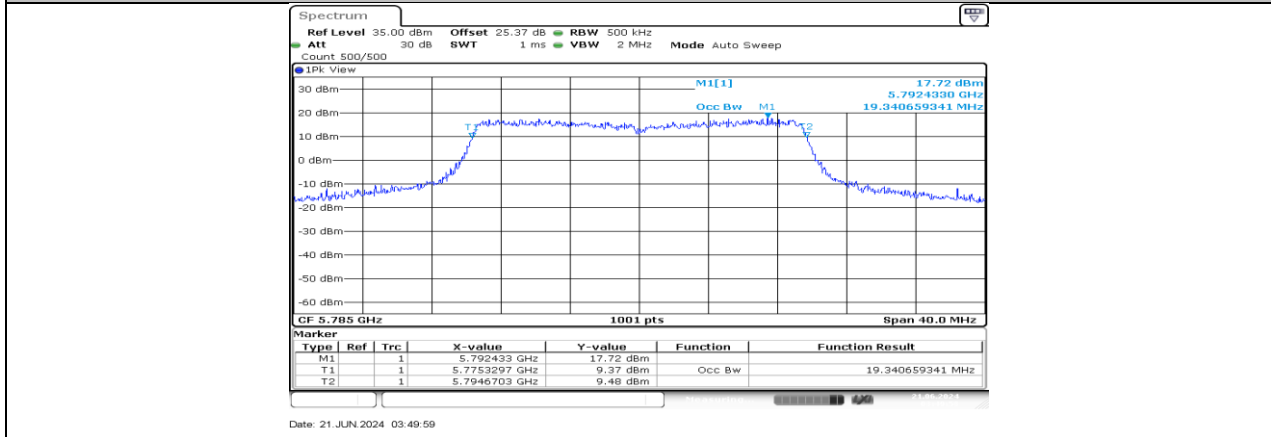




11AX20MIMO\_Ant2\_5785



11AX20MIMO\_Ant3\_5785



11AX20MIMO\_Ant4\_5785



