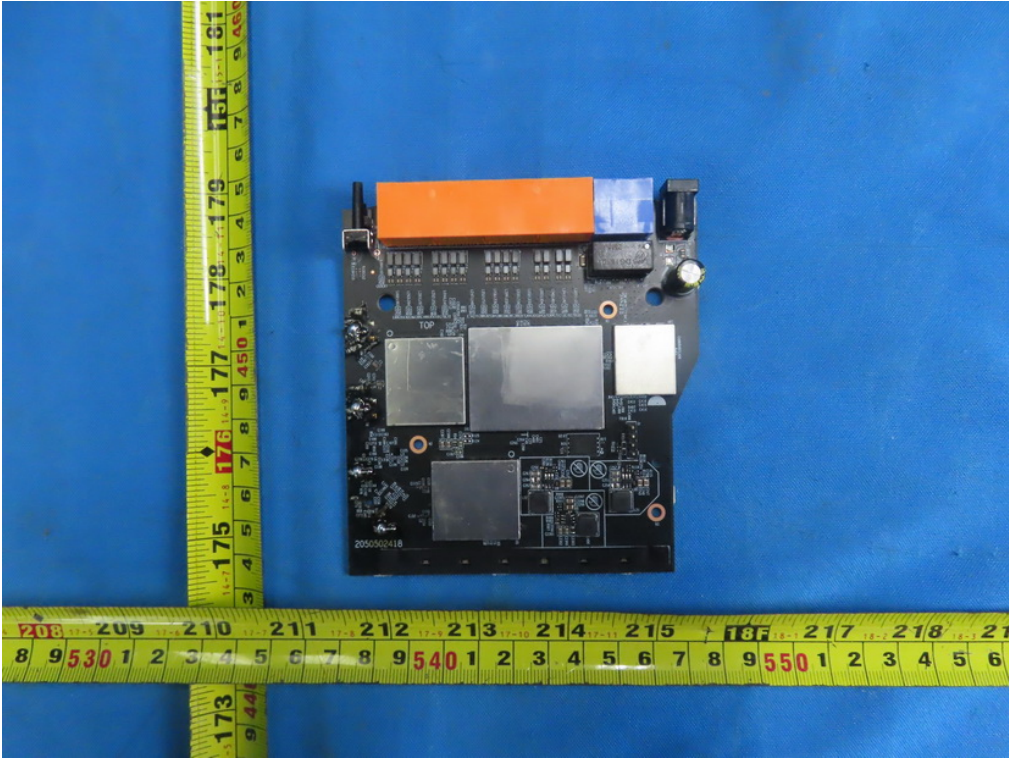


Chip with shielded cover



Chip without shielded cover

