Smarty 8 inch

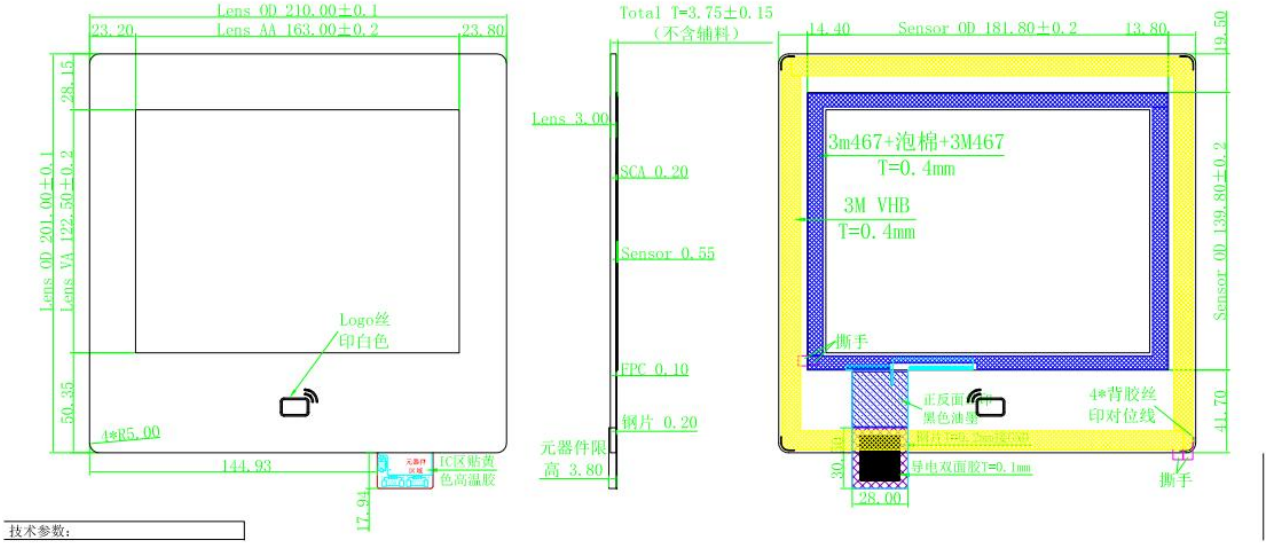
- Plug can be used
- PIN+RFID
- Administrator login function
- Off-line mode
- Complete scenario possibilities

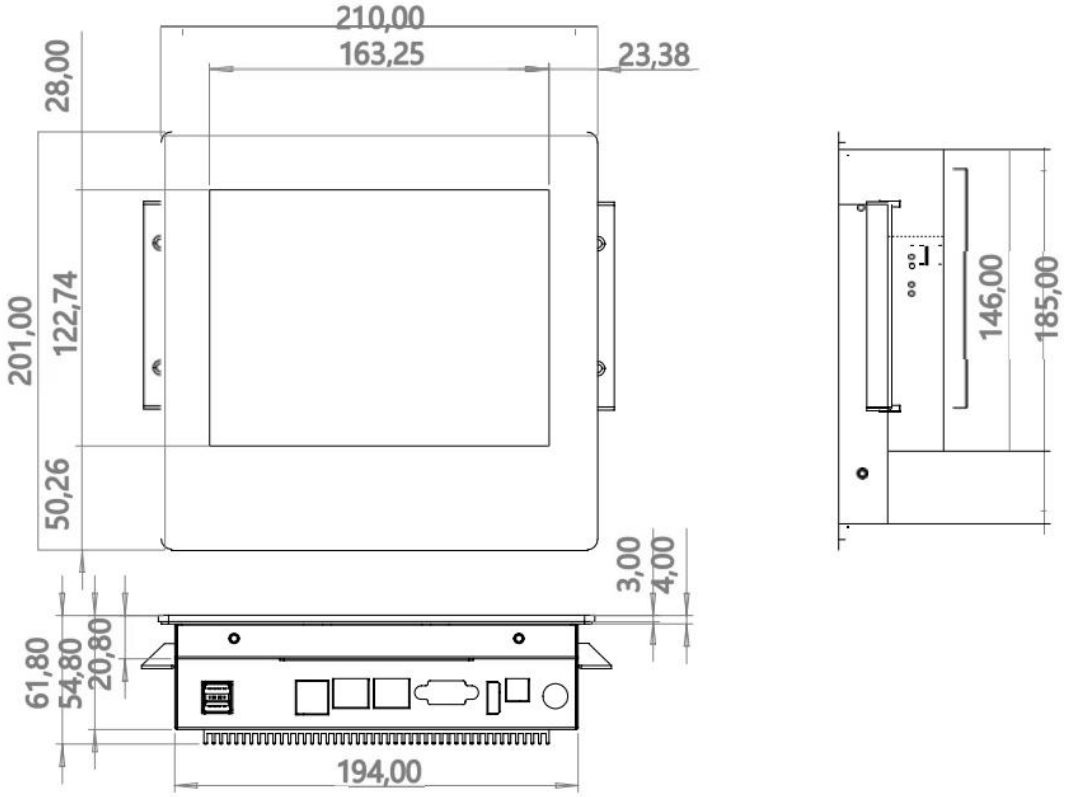
type	Parameter information
Size(mm)	(Length x width x height) 2010 x 1605 x 467
Weight(kg)	1.6
Shell material	steel
Operating temperature	-10℃ to +60℃
Humidity range	10%-90%
Power source	Input end: 100-240V to 2.5A
	Power supply: Switch: 12V DC output
	Working voltage: 12V
	Power:14W
	Reserve consumption: < 1
Touch control panel	8-inch touch screen 1024 x 768 (4:3)
	10-finger multi-touch capacitive touch screen
	3mm hardened glass 7H
	Surface treatment: anti-glare (optional)
Connection option	Wifi
	2x RJ45 LAN Ethernet
Technology and storage	Intel Celeron quad-core processor
	4GB RAM
	64 GB SSD
	Local cache
	The terminal supports DHC
Card reader	MiFare Reader: 13.56Mhz (optional)
	HID Reader: 13.56Mhz (optional)

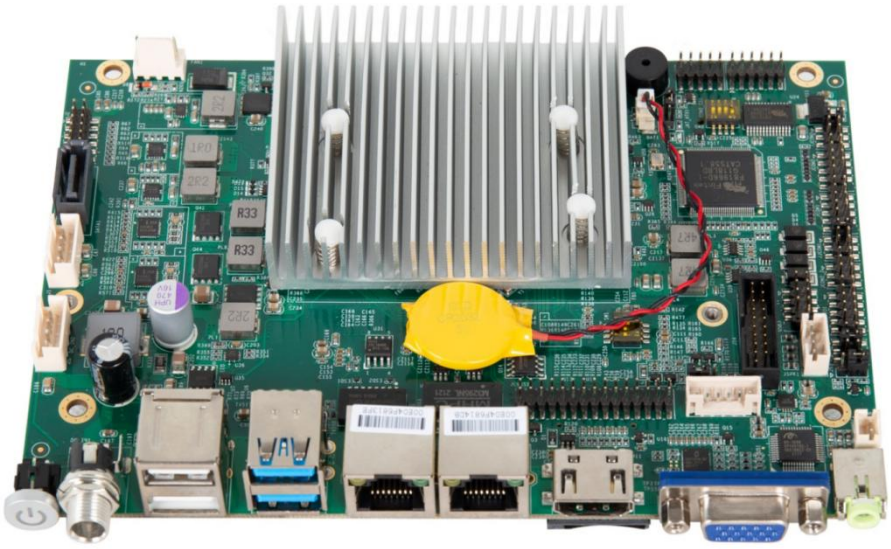
Technical Drawing

Front view　　Side view　　Backside attempt

正视图　　侧视图　　背视图







Main board matching description:

1. CX-J4125 is standard with double Gigabit
2. CX-J4125 has two motherboards: LVDS interface and EDP interface
3. The WIFI module and 4G module can be selected
4. LCD screen, touch screen and supporting wire material can be selected
5. I / O interface wire materials are optional

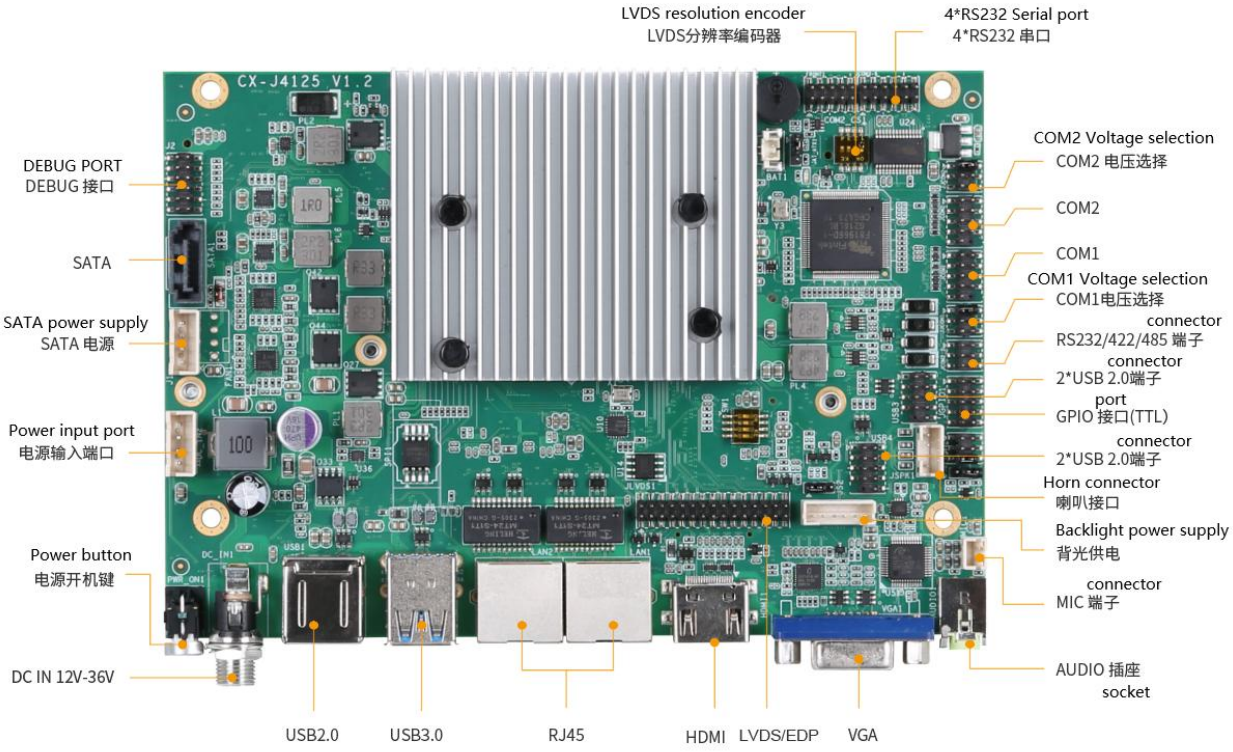
catalogue

Chapter 1, Product specifications

type		Specification parameters
CPU		Intel® Celeron® J4125, clock speed 2.GHZ, Quad-core, TDP 10W
chip set		Using the Intel Bay trail SOC, the chipset
internal storage		Support DDR4-2666 MHz, 1 * SO-DIMM Slot, Up to 8GB 。
caliche		1 * SATA3.0 + 1 * mSATA
electric fan		No (silent)
operating system		Win10 Professional Edition / Win10 Enterprise Edition / Win10 Home Edition / Win11 Professional Edition
		Ubuntu16.04.7/Ubuntu18.04.6
		Centos7.8/Centos8.4
Displays the interface	LVDS output	support
	EDP output	Support (for different motherboards)
	VGA output	Support for the highest resolution of 1920 * 1200 @ 60 HZ
	HDMI output	Support for 1080P@120Hz, 4kx2k@60Hz output
I/O joggle	USB2.0	6:2 * USB2.0 seats; 4 * USB pins
	USB3.0	2 shoreline seats
	GPIO	8 Road
	232	6 (1 of them is RS232 / RS422 / RS485 interchange)
	422	1 (RS232 / RS422 / RS485 interchange)
	485	1 (RS232 / RS422 / RS485 interchange)
network	WIFI、BT	1 * Mini-PCIE interface, with optional WiFi module
	3G/4G	1 * Mini-PCIE, interface, 4G optional 4 G module
	Ethernet	2 * Realtek PCI-E NIC RTL8111H for 1000Mbps (Optional Double Gigabit)
audio frequency		1 * The MIC supports the microphone input
		1 * SPK power amplifier supports 8 euro 5 W horn dual channel output
		1 * AUDIO supports audio output
Timed switch machine		Support (power on)
supply electricity		5525 Thread width pressure (12V~36V)
static electricity		Contact 4KV and air 8KV
size		160mm*110mm*27mm

Java runtime environment Java	Operating temperature: -20℃ ~60℃; operating humidity: 5%~95% relative humidity, no condensation

1.1 Schematic diagram of the motherboard interface



Chapter 2, Definition and description of the mainboard pin

2.1 Definition description of the interface terminals

2.1.1 MIC

MIC: PH2.0 /2PIN	
stitch	definition
1	MIC-
2	MIC+

2.1.2 Horn horn

SPK PH2.0/4PIN	
stitch	definition
1	RP
2	RN
3	LN
4	LP

2.1.3 GPIO port and configuration instructions

GPIO DuPont 2.0 / 2 * 10 PIN			
stitch	definition	stitch	definition
1	GPIO77	2	GPIO76
3	GPIO75	4	GPIO74
5	GPIO73	6	GPIO72
7	GPIO71	8	GPIO70
9	GPIO-VCC	10	GPIO-GND

2.1.4 GPIO, power supply selection

GPIO Du Pont 2.0 / 3 PIN			
stitch	definition	stitch	definition
1	gpio-5V	2	GND
3	gpio-3.3v		

2.1.5 AT / ATX selection

AT / ATX duPont 2.0 / 3 PIN			
stitch	definition	stitch	definition
1	ATX	2	GND
3	ATX		

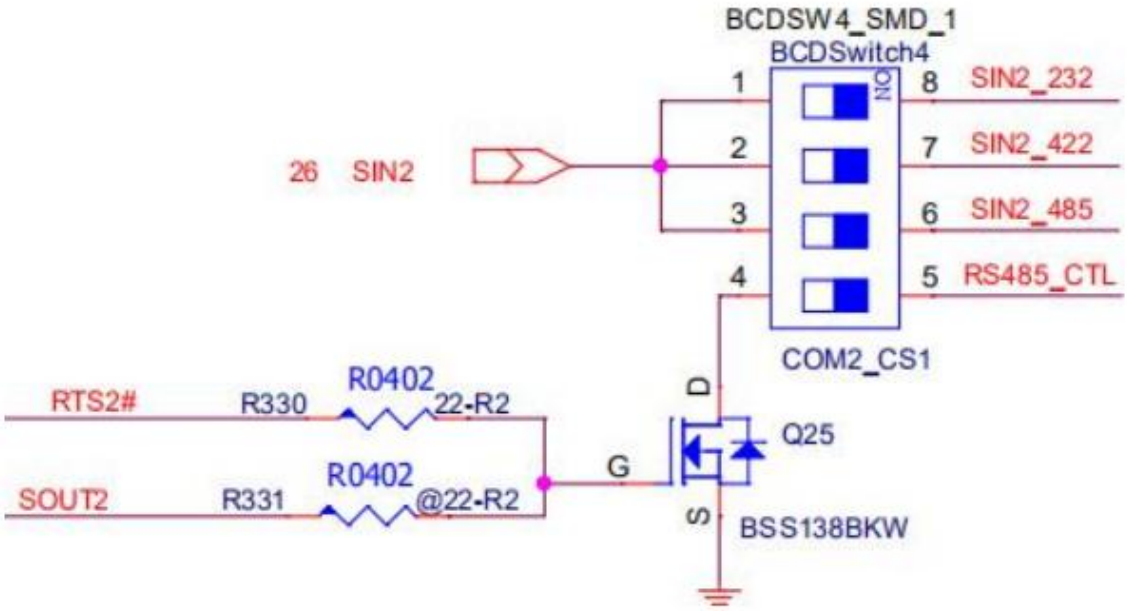
2.1.6 Definition and configuration description of the RS 232 / RS422 / RS485 interchange interface

JJCOM2 DuPont 2.0 / 2 * 3 PIN			
stitch	definition	stitch	definition
1	COM2 TX+	2	COM2 TX-
3	COM2 RX+	4	COM2 RX-
5	GND	6	GND

RS422 COM 2 _ TX +, COM 2 _ TX-, COM 2 _ RX +, COM 2 _ RX when in mode

RS485 In the mode, RS485 + corresponds to COM 2 _ TX +, and RS485-corresponds to COM 2 _ TX-

The dial switch is defined as follows:



A. When 1 / 2 ON, 3 / 4 OFF, COM2 in BIOS choose RS422, motherboard JJCOM2 For RS422 function, at this time the motherboard JCOM 2, no function

B. When 1 / 2 OFF, 3 / 4 ON, COM selected RS485 in BIOS, the motherboard JJCOM2

For RS485 function, at this time the motherboard JCOM 2, no function C. When 1 ON, 2 / 3 / 4 OFF, and the COM selects RS232 in BIOS, then the motherboard JCOM 2

For the full signal RS232 function, the main board default dial code status

2.1.7 JCOM (full signal RS232) interface definition

JCOM1 DuPont 2.0 / 2 * 5 PIN			
stitch	definition	stitch	definition
1	COM1-DCD	2	COM1-RXD
3	COM1-TXD	4	COM1-DTR
5	GND	6	COM1-DSR
7	COM1-RTS	8	COM1-CTS
9	COM1-R1	10	

2.1.8 JCOM3 RS232 interface definition

JCOM 3-1 Du Pont 2.0 / 2 * 6 PIN			
stitch	definition	stitch	definition
1	COM3-RX	2	COM4-RX
3	COM3-TX	4	COM4-TX
5	GND	6	GND
7	COM5-RX	8	COM6-RX
9	COM5-TX	10	COM6-TX
11	GND	12	GND

2.1.8 FRONT Jumper interface definition

FRONT1 DuPont 2.0 / 2 * 5 PIN			
stitch	definition	stitch	definition
1	HDD-LED+	2	PWR-LED+
3	HDD-LED-	4	PWR-LED-
5	GND	6	PWRSW
7	SYSRST	8	GND

9	BLUP	10	BLDN

2.1.9 USB terminal interface definition

USB 3 / 4 duPont 2.0 / 2 * 5 PIN			
stitch	definition	stitch	definition
1	VCC	2	VCC
3	-	4	-
5	+	6	+
7	GND	8	GND
9	GND	10	

2.1.10, definition of fan interface

Fan seat							
stitch	definition	stitch	definition	stitch	definition	stitch	definition
1	GND	2	12V	3	FAN-TACH	4	FAN-PWM

2.1.11 LVDS, power supply selection

JS 1 DuPont 2.0 / 2 * 3 PIN			
stitch	definition	stitch	definition
1	12V	2	GND
3	5V	4	GND
5	3.3V	6	GND

2.3.12 LVDS, interface definition

JLVDS1 DuPont 2.0 / 2 * 15 PIN			
stitch	definition	stitch	definition
1	VCC	2	VCC
3	VCC	4	LVDS-DEDECT
5	GND	6	GND
7	D0N	8	D0P
9	D1N	10	D1P
11	D2N	12	D2P
13	GND	14	GND
15	CLK0N	16	CLK1N
17	D3N	18	D3P
19	D4N	20	D4P

21	D5N	22	D5P
23	D6N	24	D6P
25	GND	26	GND
27	CLK1N	28	CLK1P
29	D7N	30	D7P

LVDS resolution dial code definition 26

0000→1024x600 1ch/18bit

0001→1024x768 1ch/18bit

0010→800x600 1ch/18bit

0011→1280x800 1ch/18bit

01001920x1080 2ch / 24bit ad hoc mode

0101→1680x1050 2ch/24bit

0110→800x600 1ch/24bit

0111→1024x768 1ch/24bit

1000→1024x600 1ch/24bit

1001→1280x800 1ch/24bit

1010→1920x1080 2ch/18bit

1011→1366x768 1ch/24bit

1100→1920x1200 2ch/24bit

1101→1280x1024 2ch/24bit

1110→1440x900 2ch/24bit

1111→1920x1080 2ch/24bit

2.1.13 EDP interface definition

JEDP 1 DuPont 2.0 / 2 * 10 PIN			
stitch	definition	stitch	definition

1	VCC	2	VCC
3	GND	4	GND
5	TX0N	6	TX0P
7	TX1N	8	TX1P
9	TX2N	10	TX2P
11	NC	12	NC
13	GND	14	GND
15	AUXN	16	AUXP
17	GND	18	GND
19	GND	20	HPD

2.1.14 SATA POWER interface definition

J3 XH2.54/4PIN	
stitch	definition
1	5V
2	GND
3	GND
4	12V

2.1.15, backlight interface definition

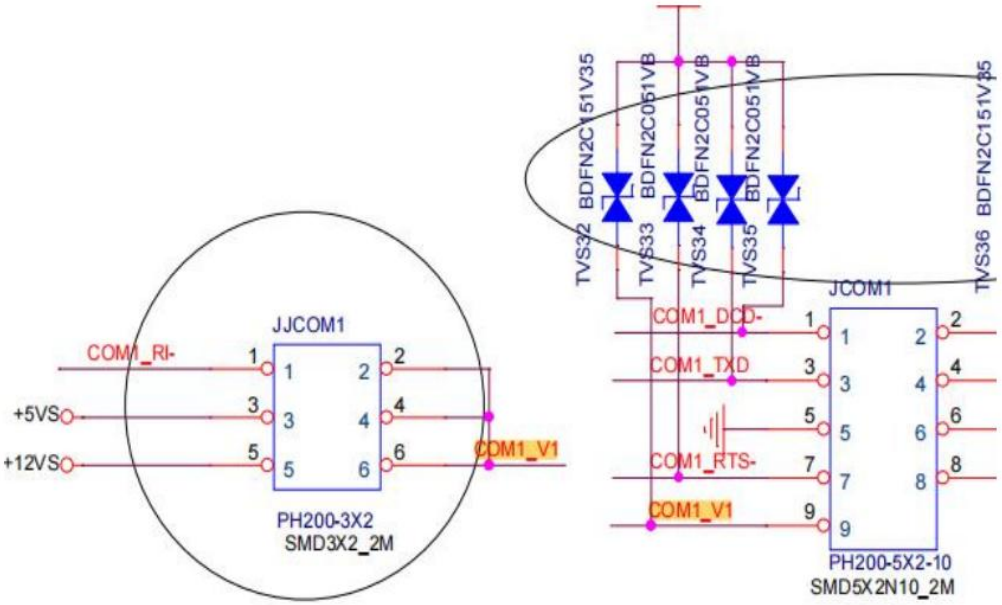
J1NV1 PH2.0/6PIN	
stitch	definition
1	GND
2	GND
3	ADJ
4	EN
5	12V
6	12V

JS 2 Du Pont 2.0 / 3 PIN	
stitch	definition
1	PWM
2	GND
3	DC

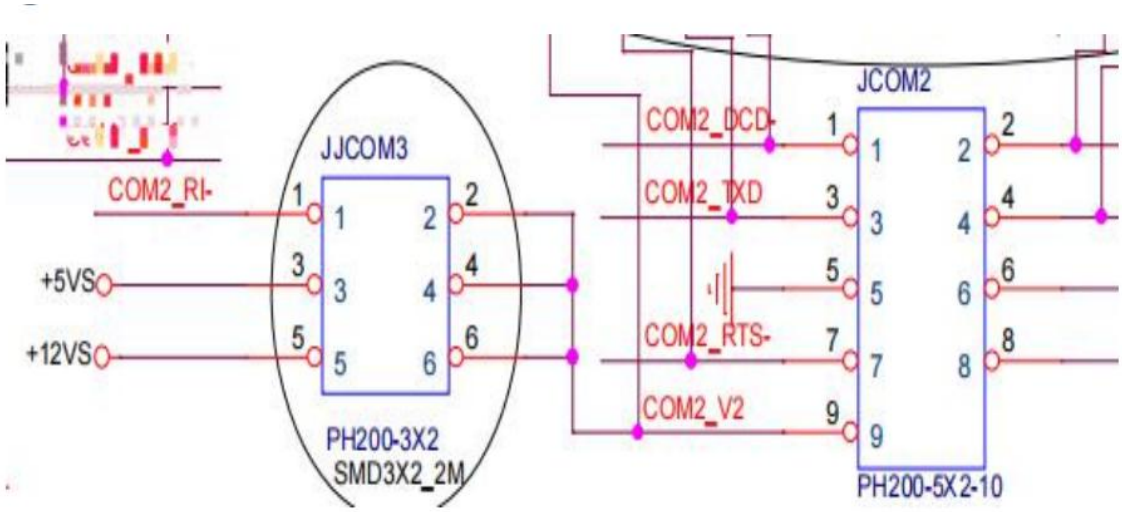
2.1.16 Definition of the power supply terminal interface

DC IN2 XH2.54/4PIN	
stitch	definition
1	GND
2	GND
3	VCC (12V-36V)
4	VCC(12V-36V)

2.1.17 JJCOM1 Interface definition (select by corresponding function of hop cap JCOM1 pin9)



2.1.18 JJCOM3 Interface Definition (Change the JCOM2 pin9 function selection by jumping the cap)



FCC WARNING

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation.

If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

-- Reorient or relocate the receiving antenna.

-- Increase the separation between the equipment and receiver.

-- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

-- Consult the dealer or an experienced radio/TV technician for help.

To maintain compliance with FCC’s RF Exposure guidelines, This equipment should be installed and operated with minimum distance between 20cm the radiator your body: Use only the supplied antenna.