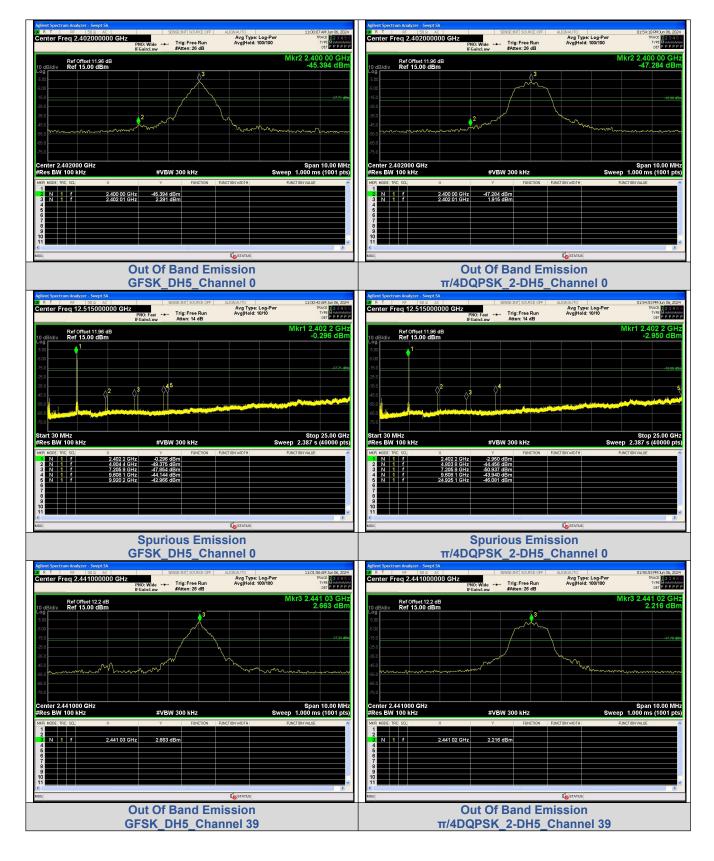
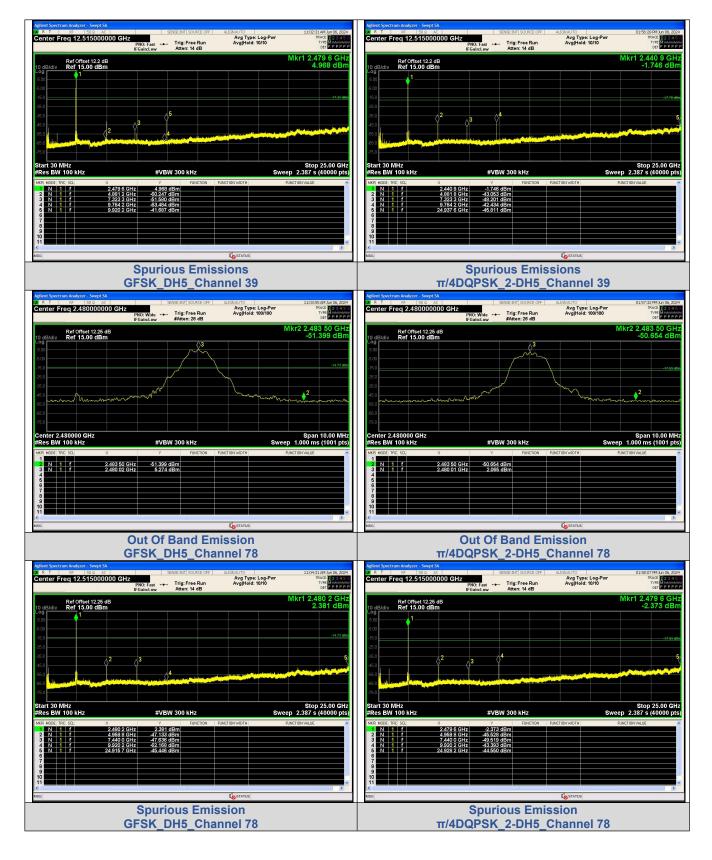
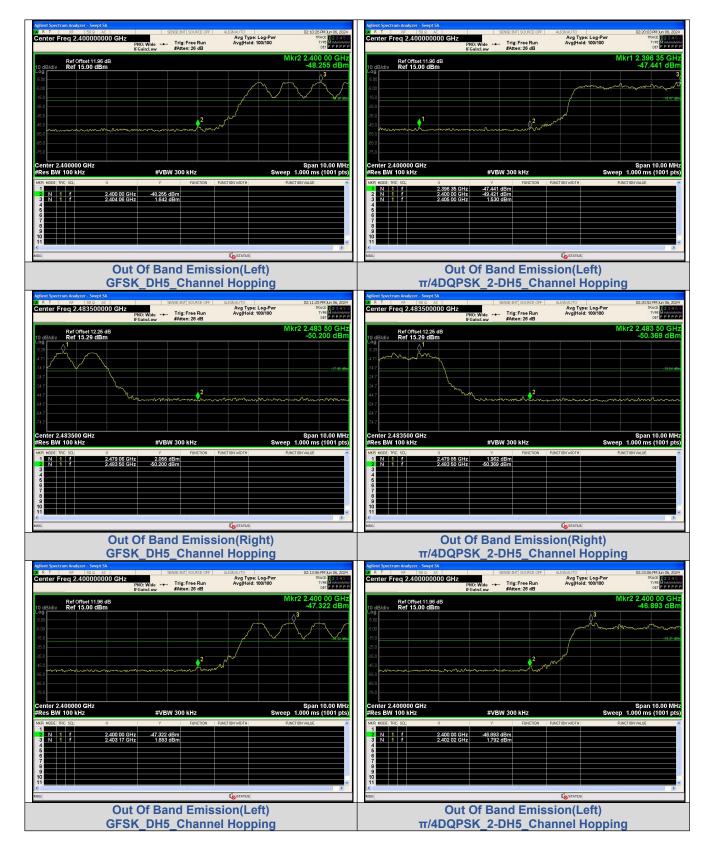
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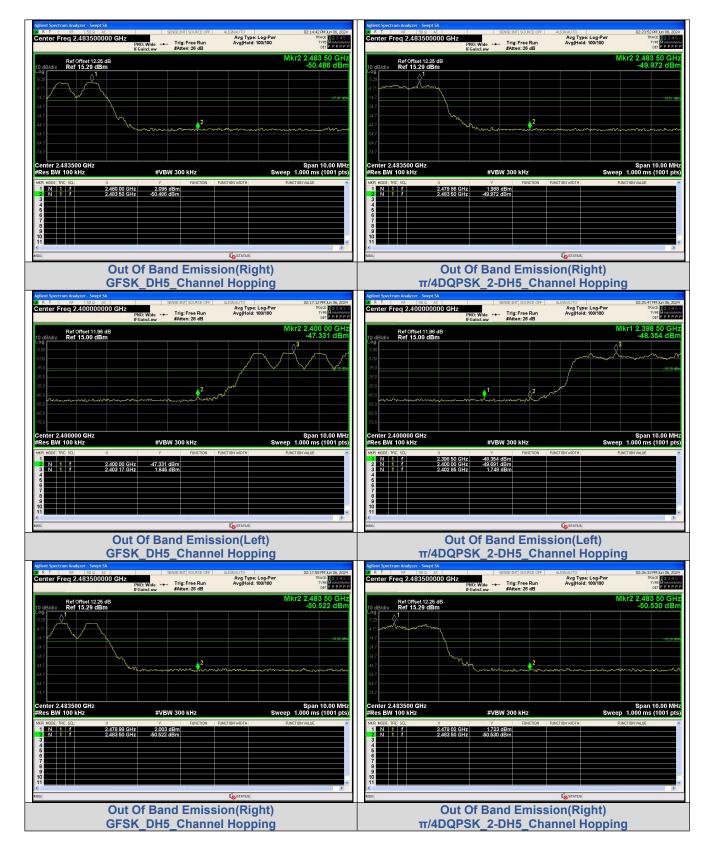
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APPENDIX VI.Duty Cycle

Test Result

Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.884	3.748	76.95	0.7695	1.1379
		39	2.886	3.748	77.00	0.7700	1.1351
		78	2.886	3.748	77.00	0.7700	1.1351
π/4DQPSK	2-DH5	0	2.890	3.748	77.11	0.7711	1.1289
		39	2.892	3.748	77.16	0.7716	1.1261
		78	2.890	3.748	77.11	0.7711	1.1289

Test Graphs

