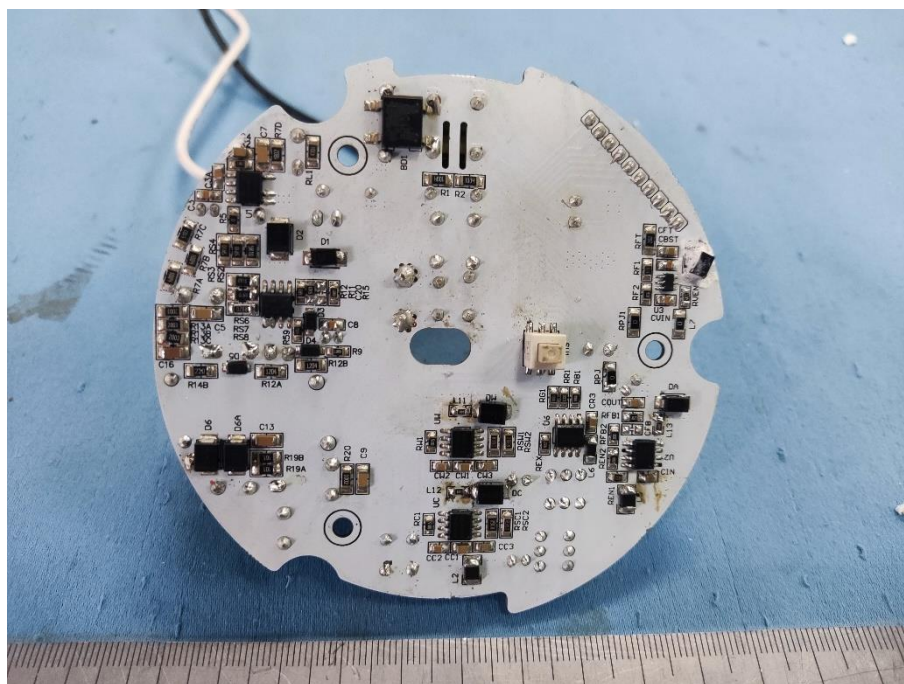
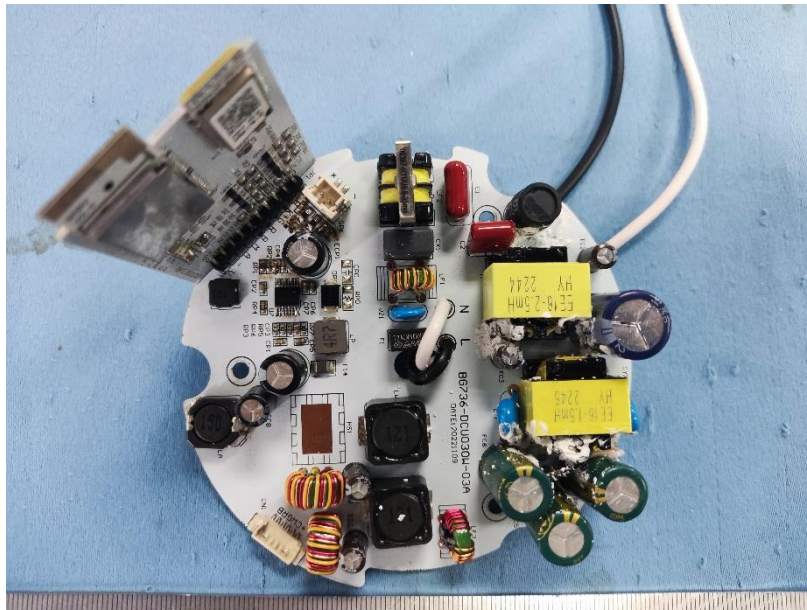
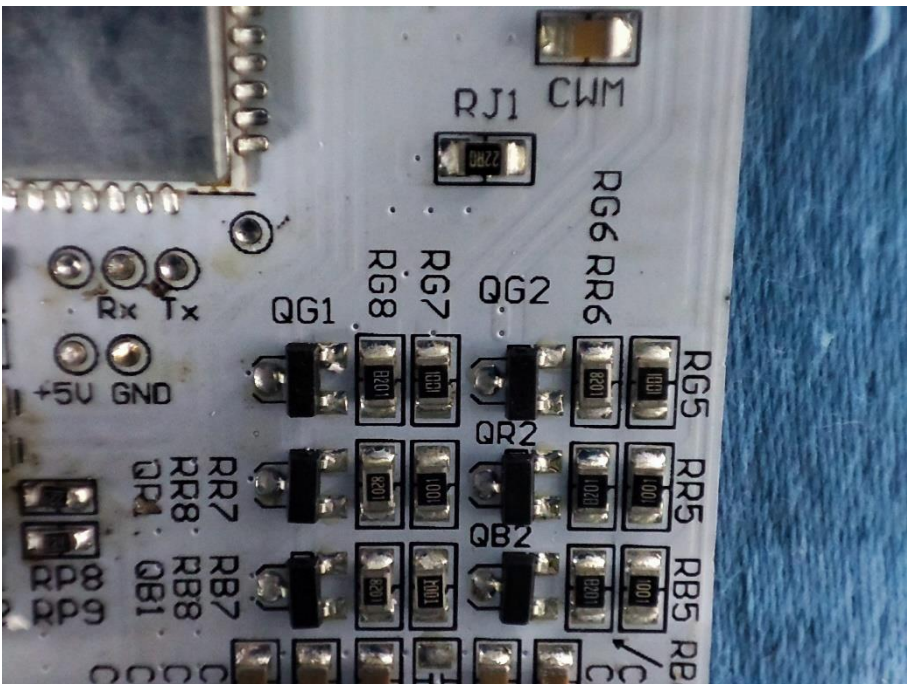
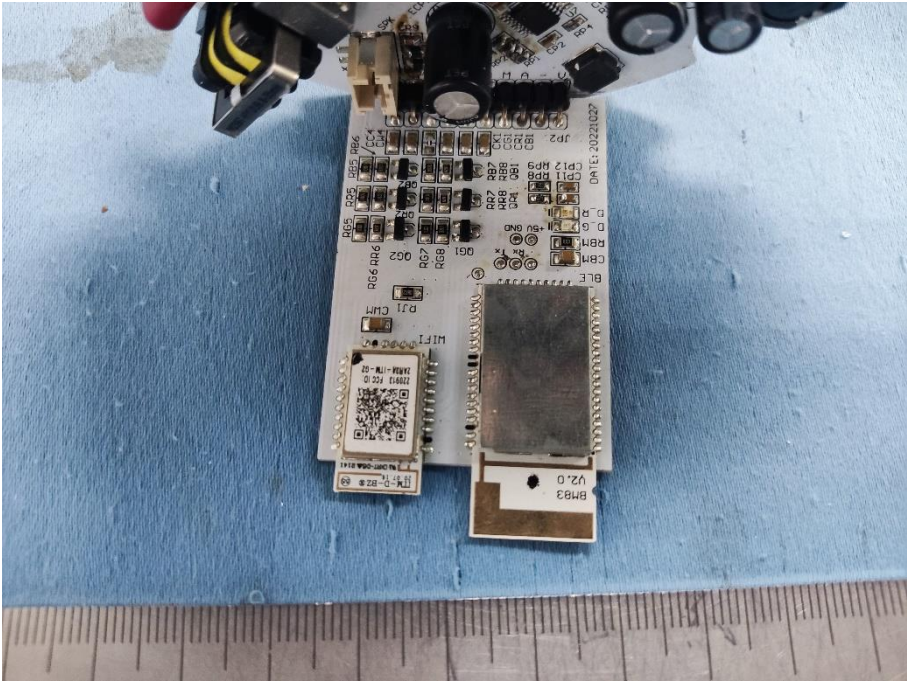




circuit design version 1

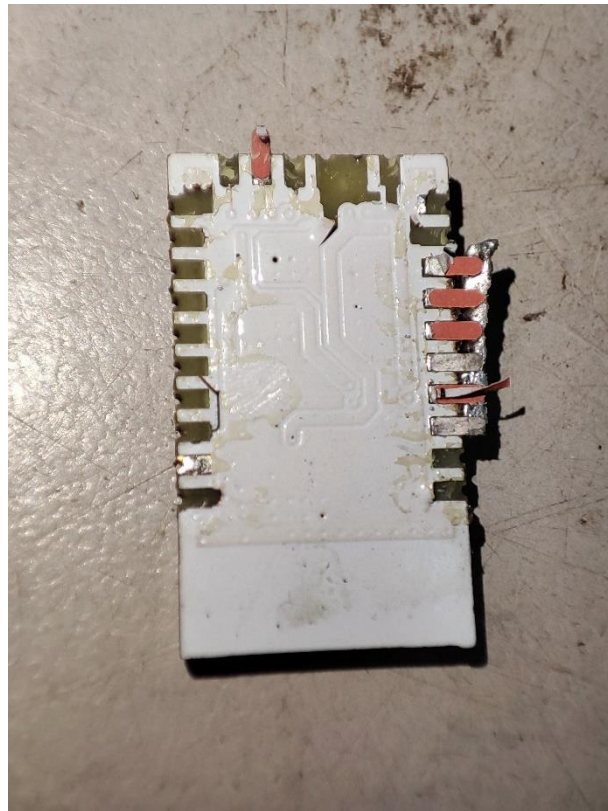
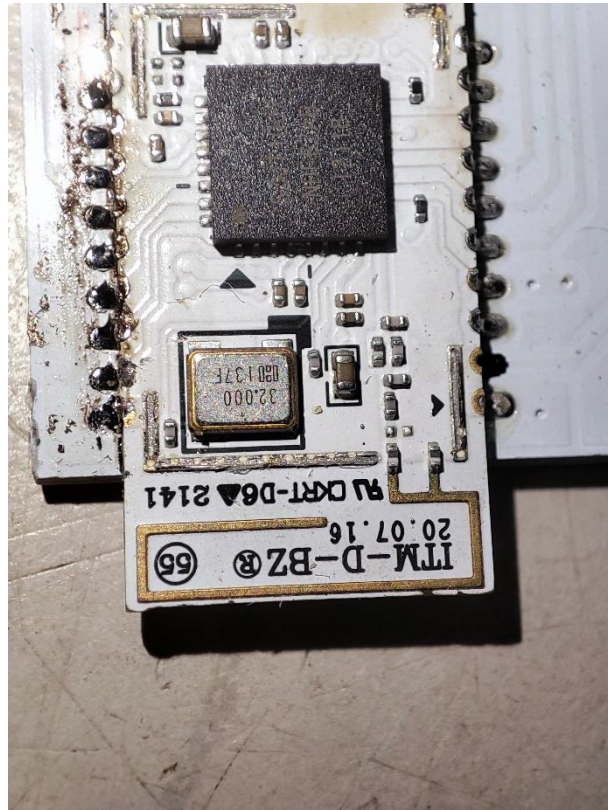


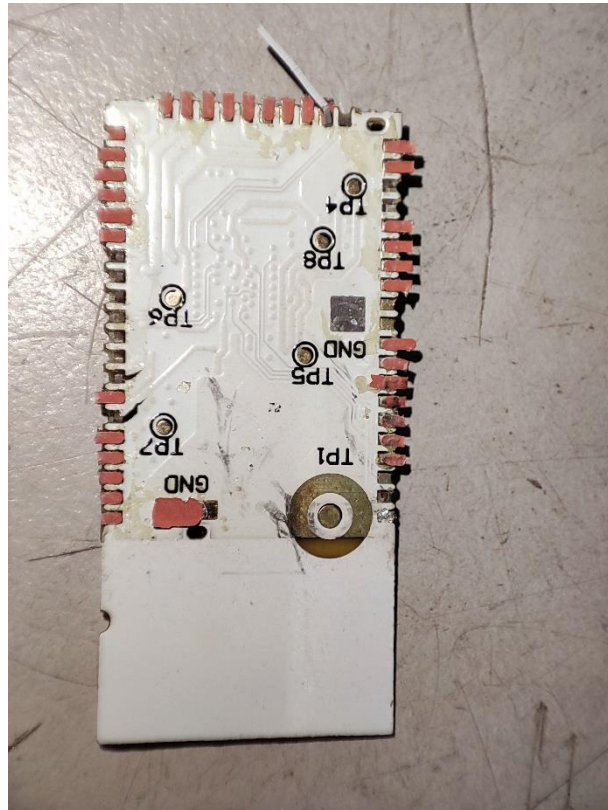












## circuit design version 2

