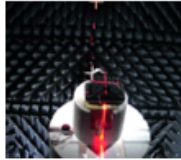

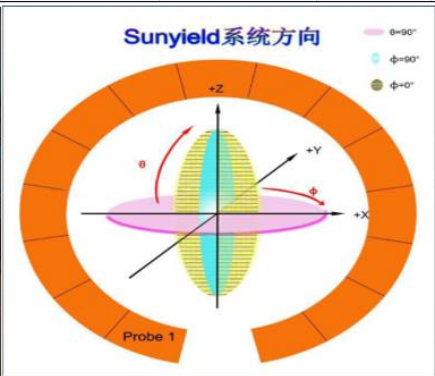

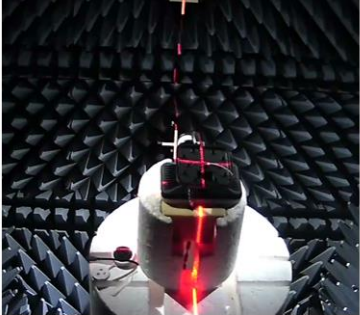


Product Information					
Client	LDS	Protocol	BT		
Product Description	蓝牙磁灯	Sample Quantity	1		
Model Number		RF PCB Version			
Chip Type		Software Version	正式模		
Sample Picutres					
Conclusions	Pass				
Author	李德云 (Davin)	Test Date	2022.07.05		
Verifier		Review Date			
Content History					
Date	Revise Content			Reviser	Comment
2023.07.05	Establish Report			Davin	

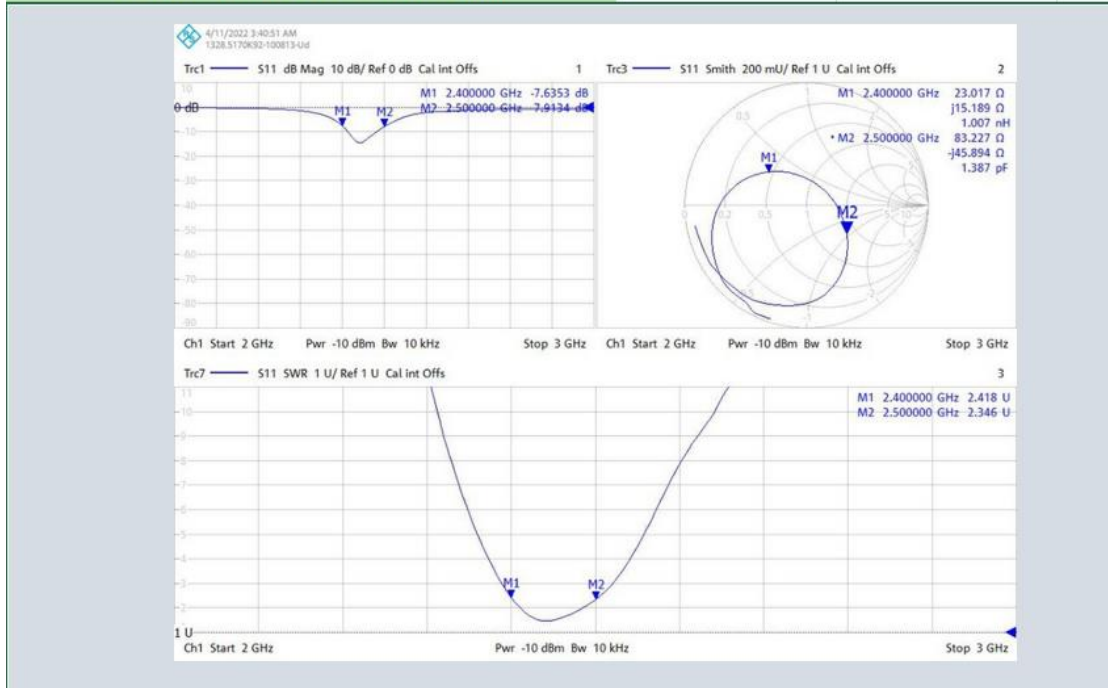
Conclusion		Pass		
Category	Item	Index	Test Result	Conclusion
Test Data	VSWR	≤ 2.5	2.4	Pass
	Average Efficiency (%)	$\geq 50\%$	BT: 36.4%	Pass
		Level Classification:		Level: B
		A ($\geq 45\%$) B (35%~44.99%) C (25%~34.99%)		
Distance communication	BT	40M	80M	Pass

Test Environment				
SY-16M Test System				
				

Picture				
				
Test Sample		Test Environment		

VNA Data

VSWR & Return Loss & Smith Chart

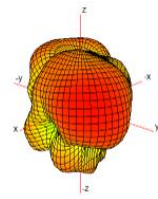


Chamber Data

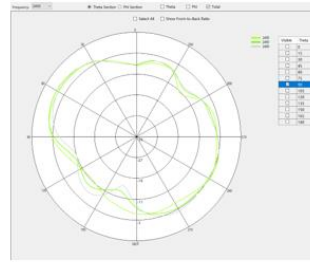
Gain & Efficiency

Frequency (MHz)	Gain (dBi)	Efficiency (dB)	Efficiency (%)
2400	1.60	-4.25	37.59
2410	1.32	-4.45	35.93
2420	1.12	-4.53	35.26
2440	1.06	-4.53	35.23
2450	1.15	-4.40	36.31
2460	1.27	-4.29	37.25
2470	1.26	-4.29	37.26
2480	1.15	-4.38	36.49
2490	1.19	-4.39	36.41
2500	1.19	-4.41	36.25
MIN	1.18	-4.38	36.46
Max	1.60	-4.25	37.59
AVG	1.23	-4.39	36.40

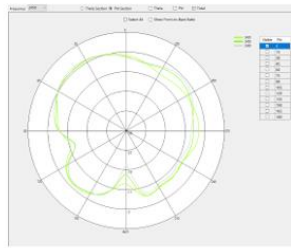
3D



$\theta = 90^\circ$ (XY Plane)



$\phi = 0^\circ$ (XZ Plane)



$\phi = 90^\circ$ (YZ Plane)

