

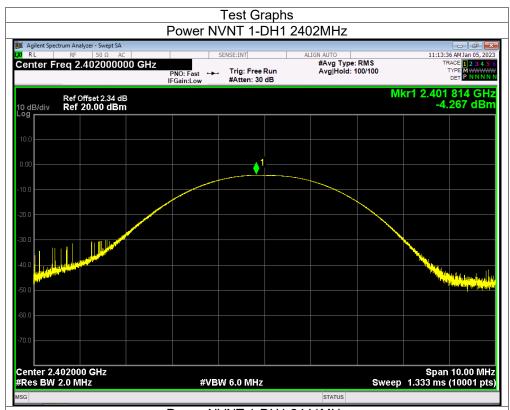
# 11.4 Test Result

Temperature :	26℃	Relative Humidity :	54%
Test Voltage :	DC 3.7V	Remark:	N/A

Modulation	Test Channel	Output Power (dBm)	Limit (dBm)	
GFSK	Low	-4.27	21	
GFSK	Middle	-3.57	21	
GFSK	High	-3.62	21	
π/4DQPSK	Low	-3.39	21	
π/4DQPSK	Middle	-2.70	21	
π/4DQPSK	High	-2.76	21	
8DPSK	Low	-2.74	21	
8DPSK	Middle	-2.10	21	
8DPSK	High	-2.14	21	

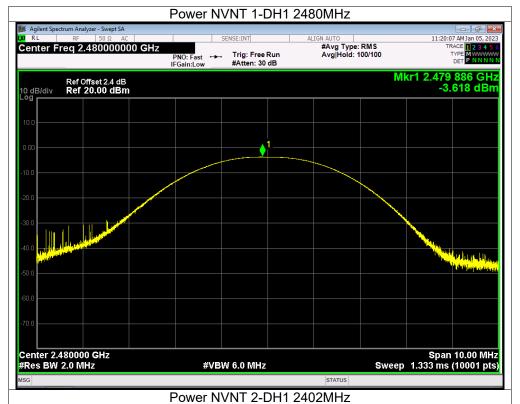
No.: BCTC/RF-EMC-005 Page 55 of 82 Edition: A.5

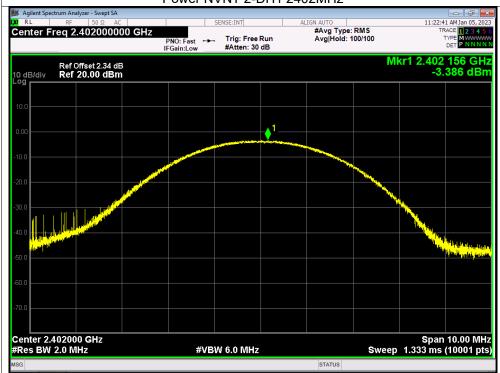










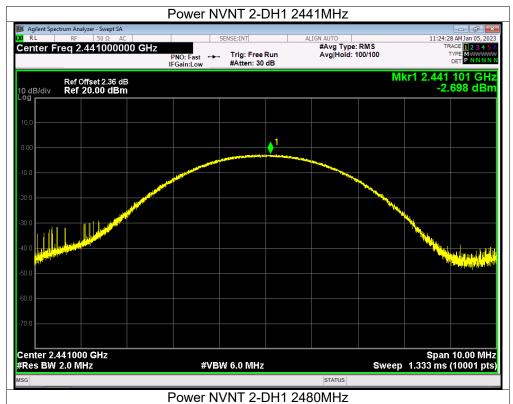


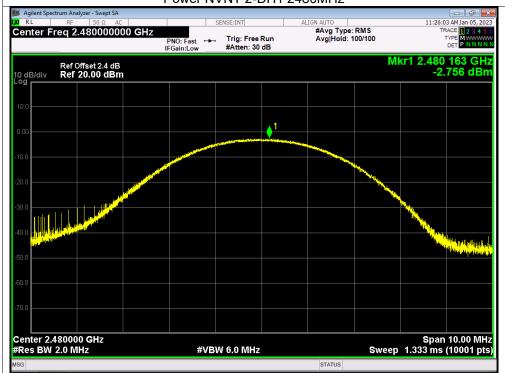
,TC

3C





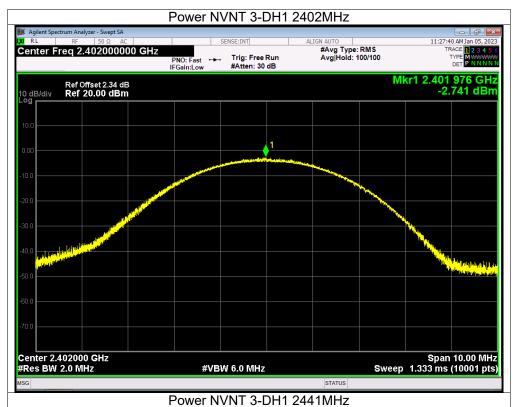


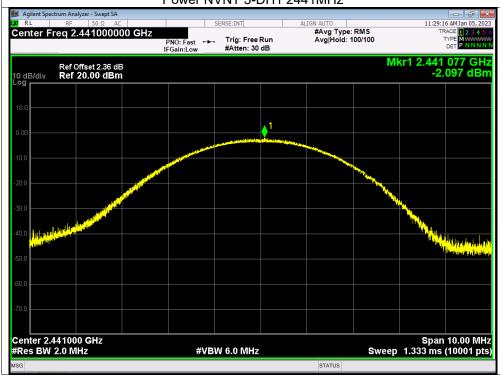




















# 12. Hopping Channel Separation

## 12.1 Block Diagram Of Test Setup



#### 12.2 Limit

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20 dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 0.125W.

### 12.3 Test Procedure

- 1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
- 2. Set the spectrum analyzer: RBW = 30kHz. VBW = 100kHz , Span = 2.0MHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
- 3. Allow the trace to stabilize. Use the marker-delta function to determine the separation between the peaks of the adjacent channels. The limit is specified in one of the subparagraphs of this Section Submit this plot.

No.: BCTC/RF-EMC-005 Page 61 of 82 Edition: A.5

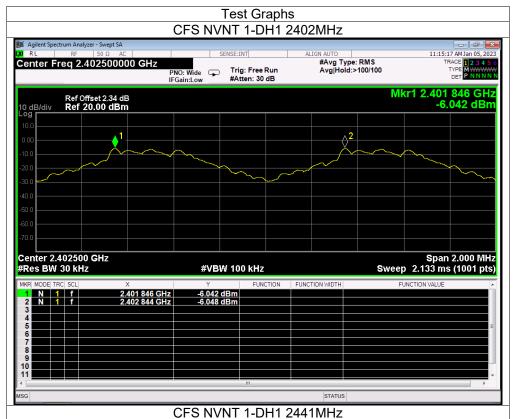


# 12.4 Test Result

Modulation	Test Channel	Separation (MHz)	Limit(MHz)	Result
GFSK	Low	0.998	0.848	PASS
GFSK	Middle	1	0.865	PASS
GFSK	High	1	0.868	PASS
π/4DQPSK	Low	1	0.832	PASS
π/4DQPSK	Middle	1	0.827	PASS
π/4DQPSK	High	1	0.833	PASS
8DPSK	Low	1	0.795	PASS
8DPSK	Middle	1	0.812	PASS
8DPSK	High	0.998	0.805	PASS







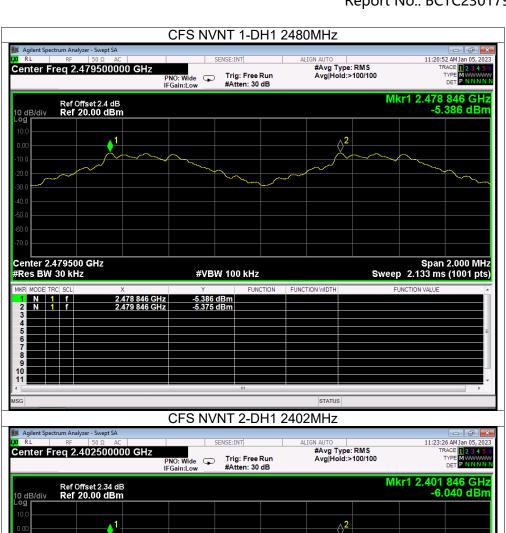


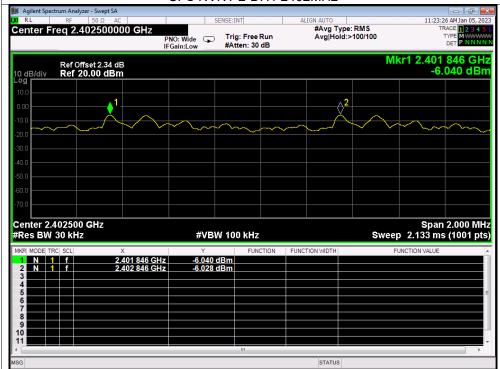
,TC

) C

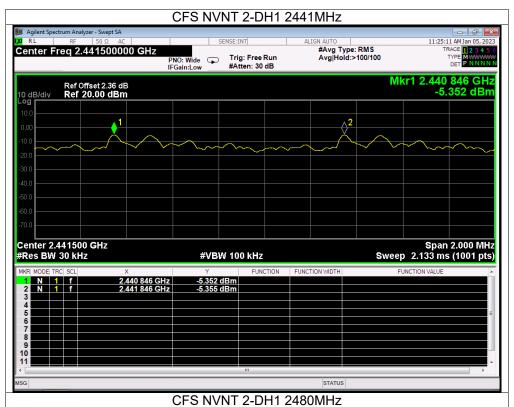








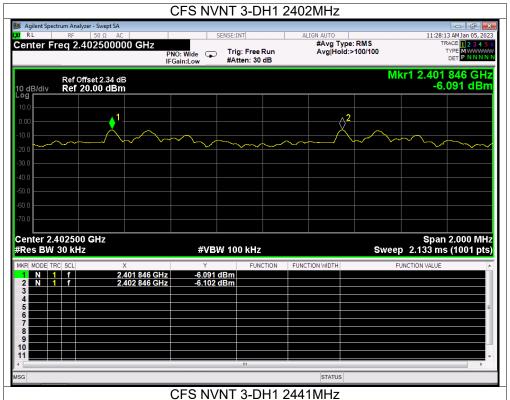






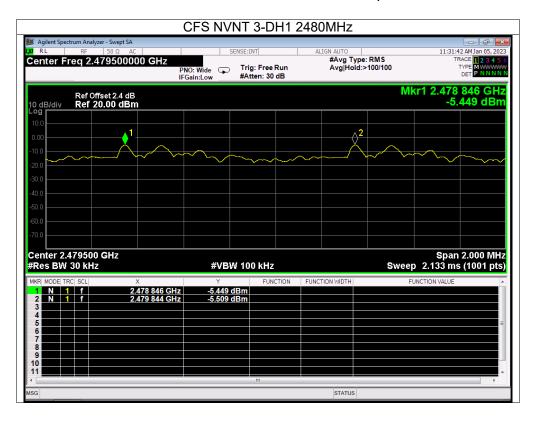














# 13. Number Of Hopping Frequency

## 13.1 Block Diagram Of Test Setup



#### 13.2 Limit

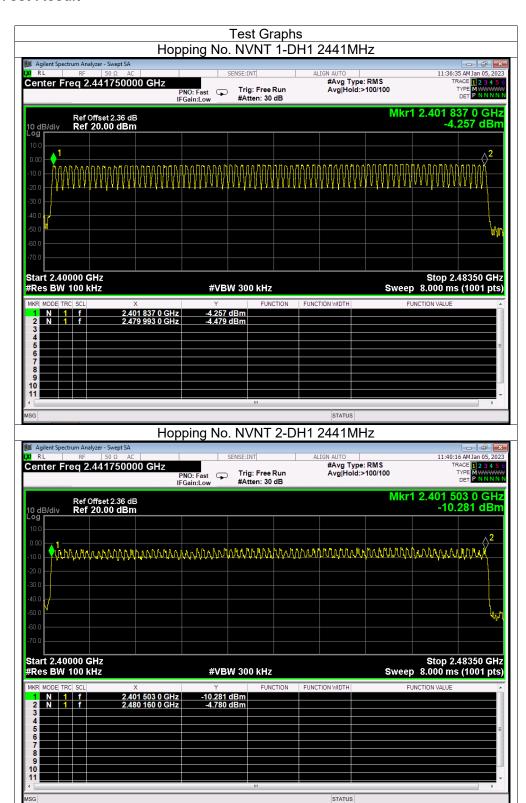
Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels.

#### 13.3 Test Procedure

- 1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
- 2. Set the spectrum analyzer: RBW = 100kHz. VBW = 300kHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
- 3. Allow the trace to stabilize. It may prove necessary to break the span up to sections. in order to clearly show all of the hopping frequencies. The limit is specified in one of the subparagraphs of this Section.
- 4. Set the spectrum analyzer: Start Frequency = 2.4GHz, Stop Frequency = 2.4835GHz. Sweep=auto;



### 13.4 Test Result

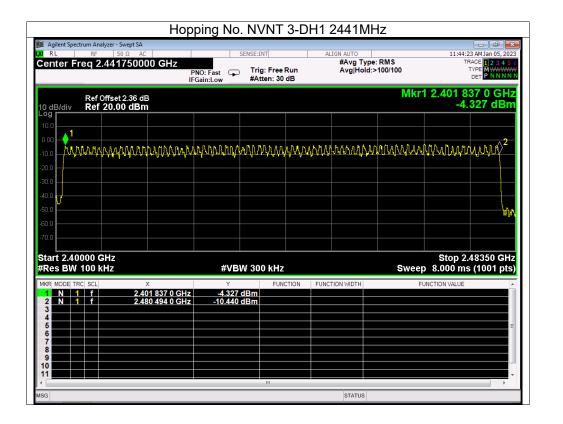


,TC

















#### 14. Dwell Time

# 14.1 Block Diagram Of Test Setup

EUT	SPECTRUM	
	ANALYZER	

### 14.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

#### 14.3 Test Procedure

- 1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
- 2. Set spectrum analyzer span = 0. Centred on a hopping channel;
- 3. Set RBW = 1MHz and VBW = 3MHz.Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
- 4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).



# 14.4 Test Result

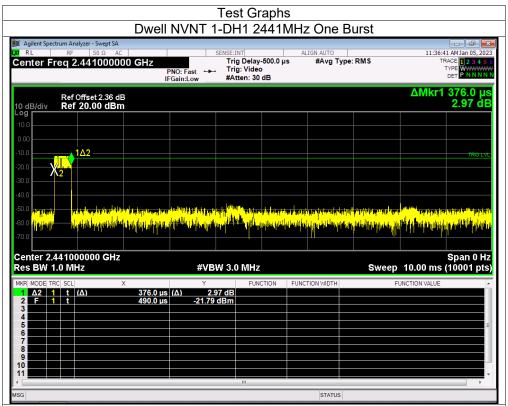
DH5 Packet permit maximum 1600 / 79 / 6 hops per second in each channel (5 time slots RX, 1 time slot TX).

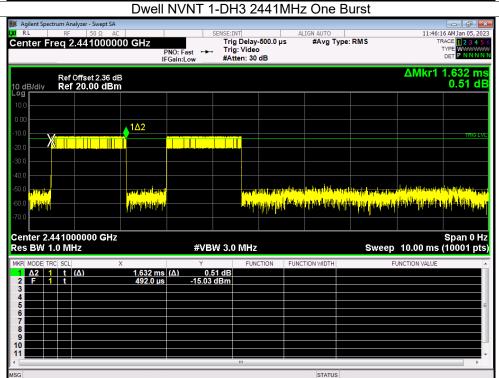
DH3 Packet permit maximum 1600 / 79 / 4 hops per second in each channel (3 time slots RX, 1 time slot TX).

DH1 Packet permit maximum 1600 / 79 /2 hops per second in each channel (1 time slot RX, 1 time slot TX). So, the Dwell Time can be calculated as follows:

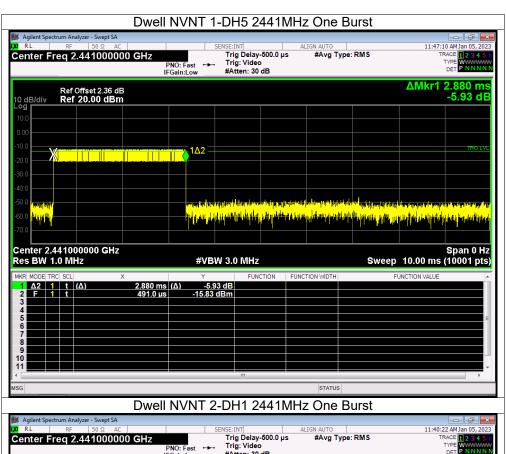
DH5:1600/79/6\*0.4\*79\*(MkrDelta)/1000 DH3:1600/79/4\*0.4\*79\*(MkrDelta)/1000 DH1:1600/79/2\*0.4\*79\*(MkrDelta)/1000 Remark: Mkr Delta is once pulse time.

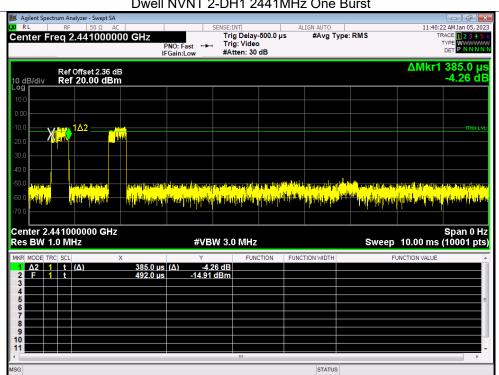
Modulation	Channel Data	Packet	pulse time(ms)	Dwell Time(s)	Limits(s)
GFSK	Middle	DH1	0.376	0.120	0.4
		DH3	1.632	0.261	0.4
		DH5	2.88	0.307	0.4
π/4DQPSK	Middle	2DH1	0.385	0.123	0.4
		2DH3	1.638	0.262	0.4
		2DH5	2.879	0.307	0.4
8DPSK	Middle	3DH1	0.387	0.124	0.4
		3DH3	1.637	0.262	0.4
		3DH5	2.887	0.308	0.4







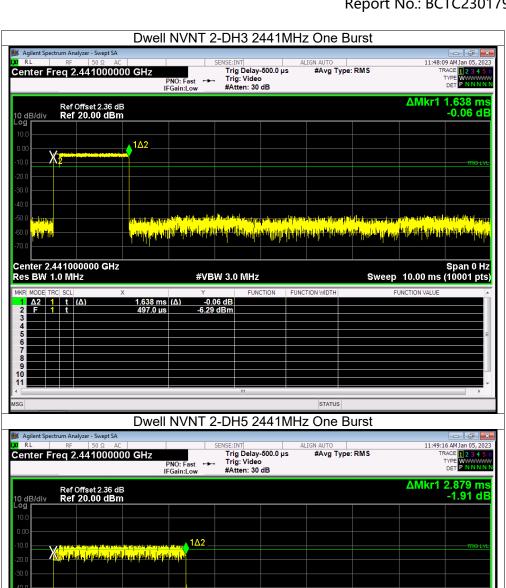






Center 2.441000000 GHz Res BW 1.0 MHz

## Report No.: BCTC2301793001E



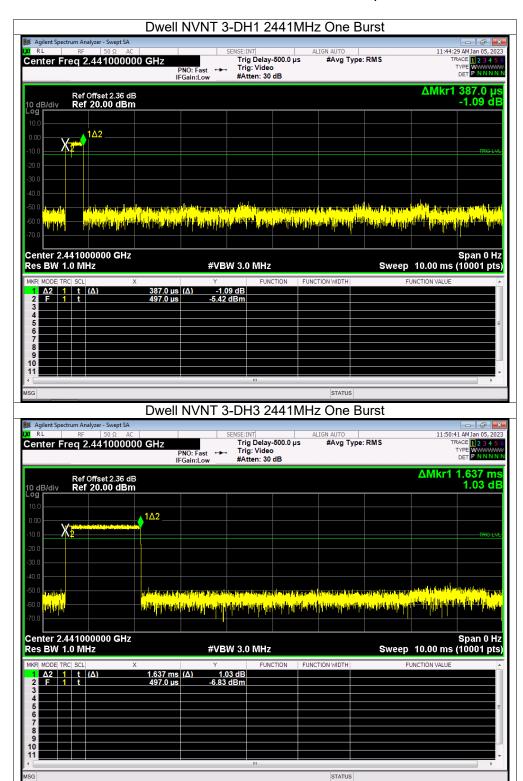
Span 0 Hz Sweep 10.00 ms (10001 pts)

STATUS

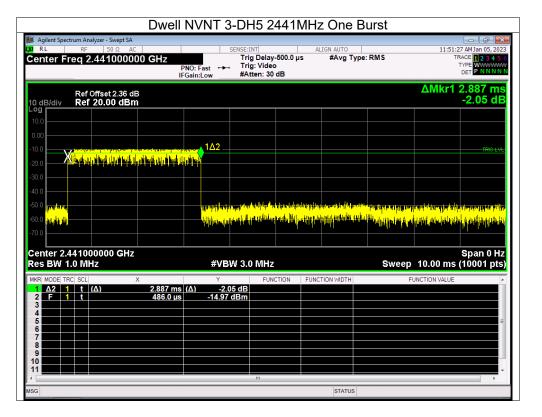
#VBW 3.0 MHz

-1.91 dB -14.99 dBm











# 15. Antenna Requirement

### 15.1 Limit

15.203 requirement: For intentional device, according to 15.203: an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device.

### 15.2 Test Result

The EUT antenna is PCB antenna, The antenna gain is -0.68 dBi, fulfill the requirement of this section.

6. 60.,LT/



### 16. EUT Photographs

### ET2240A, Triangle



#### ET2240B



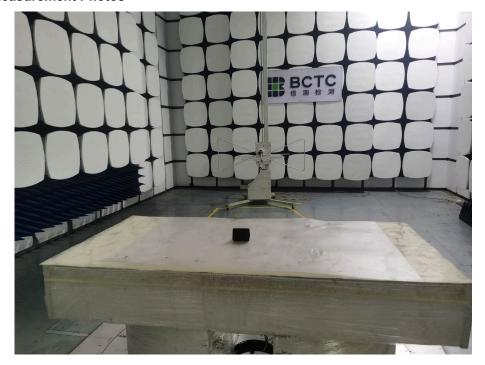
No.: BCTC/RF-EMC-005 Page 79 of 82 Edition: A.5

# 17. EUT Test Setup Photographs

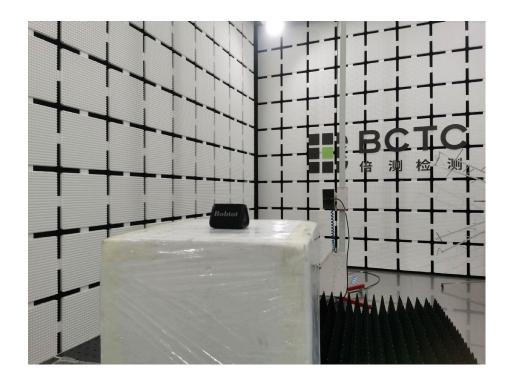
# **Conducted Measurement Photo**



## **Radiated Measurement Photos**









### **STATEMENT**

1. The equipment lists are traceable to the national reference standards.

2. The test report can not be partially copied unless prior written approval is issued from our

lab.

3. The test report is invalid without the "special seal for inspection and testing".

4. The test report is invalid without the signature of the approver.

5. The test process and test result is only related to the Unit Under Test.

6. Sample information is provided by the client and the laboratory is not responsible for its

authenticity.

7. The test report without CMA mark is only used for scientific research, teaching, enterprise

product development and internal quality control purposes.

8. The quality system of our laboratory is in accordance with ISO/IEC17025.

9. If there is any objection to this test report, the client should inform issuing laboratory

within 15 days from the date of receiving test report.

Address:

1-2/F., Building B, Pengzhou Industrial Park, No.158, Fuyuan 1st Road, Zhancheng, Fuhai

Subdistrict, Bao'an District, Shenzhen, Guangdong, China

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E-Mail: bctc@bctc-lab.com.cn

\*\*\*\* END \*\*\*\*

10 CO.,LTO