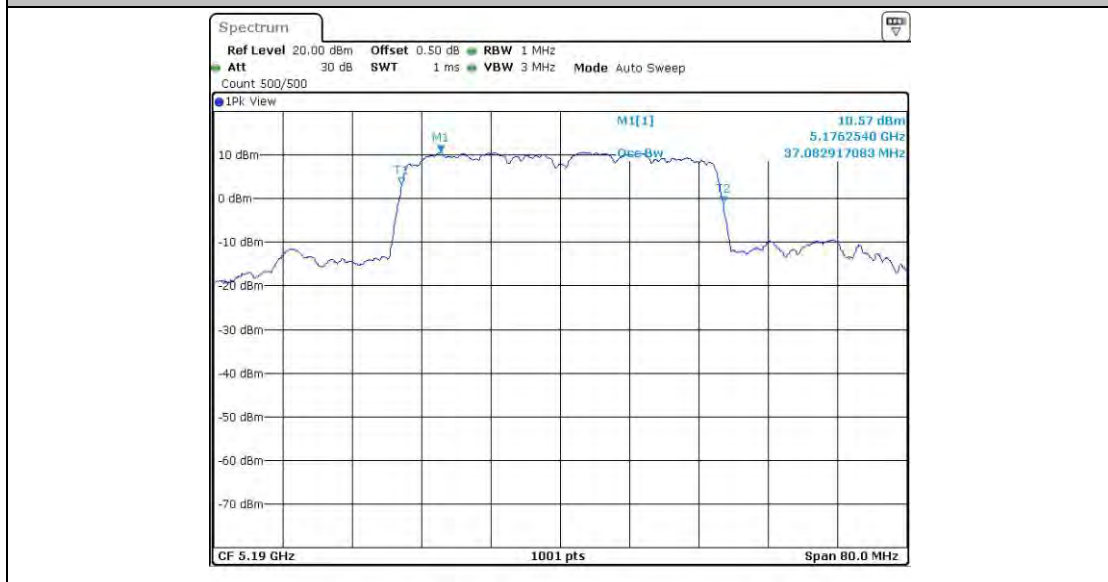
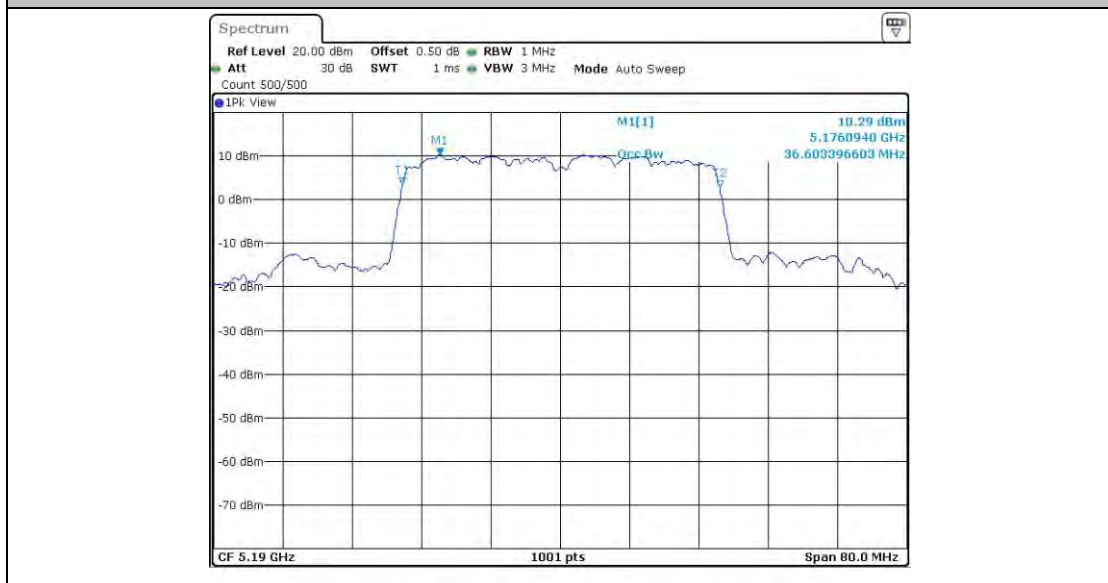


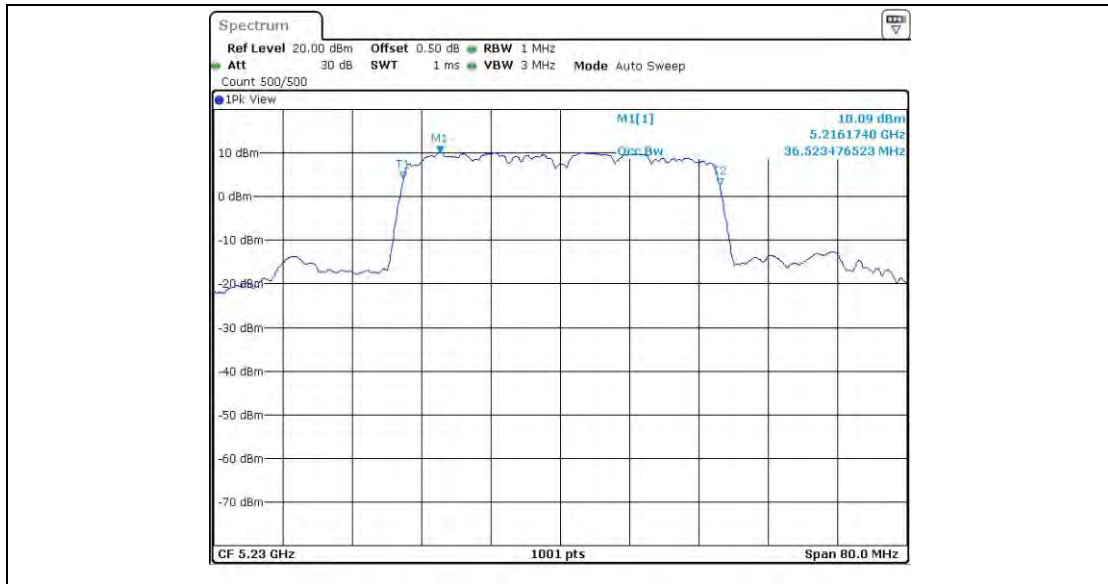
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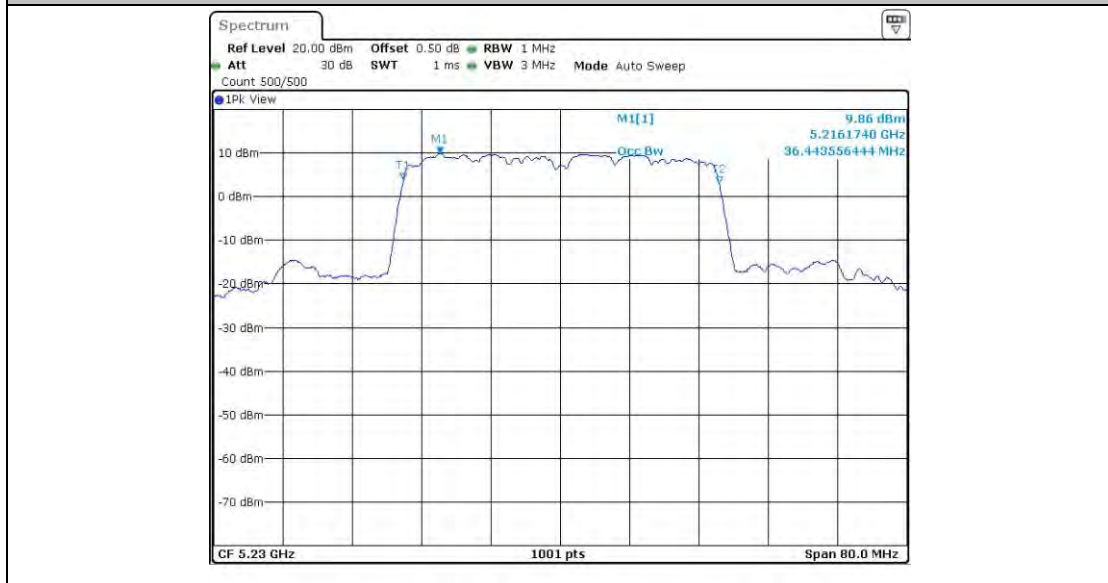
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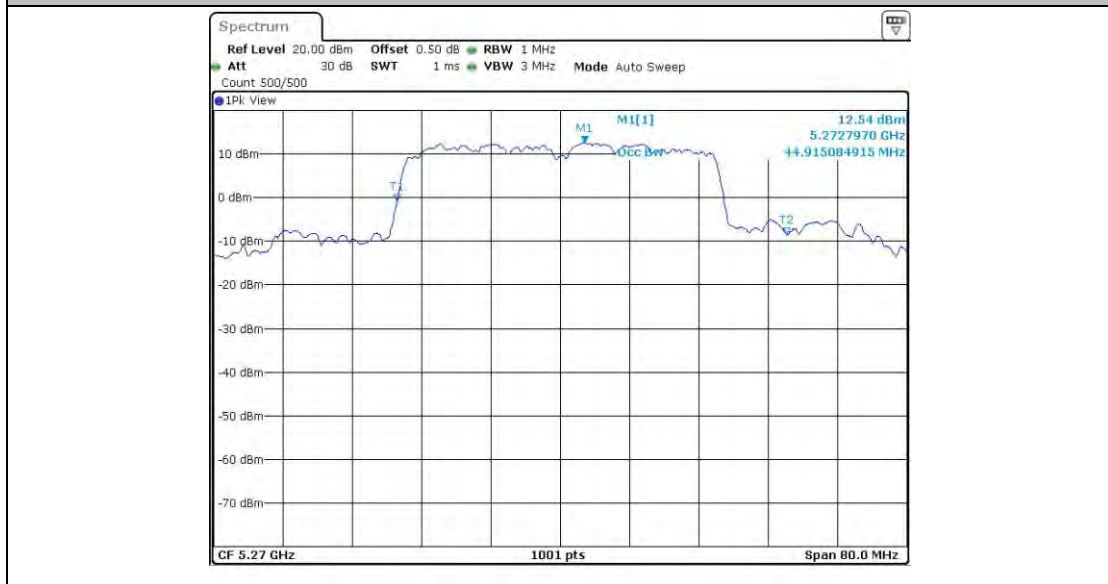
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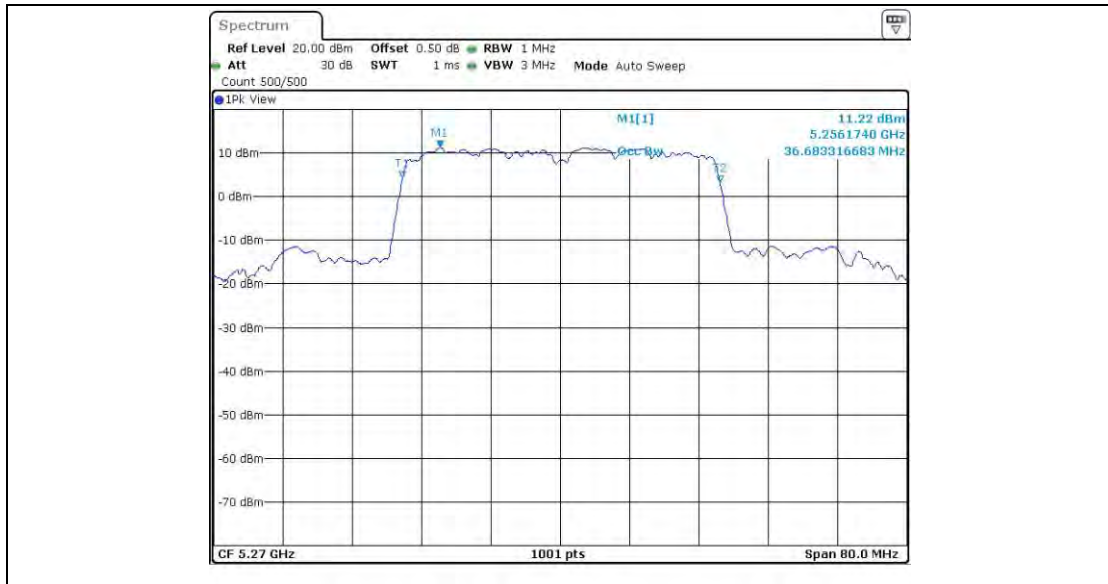
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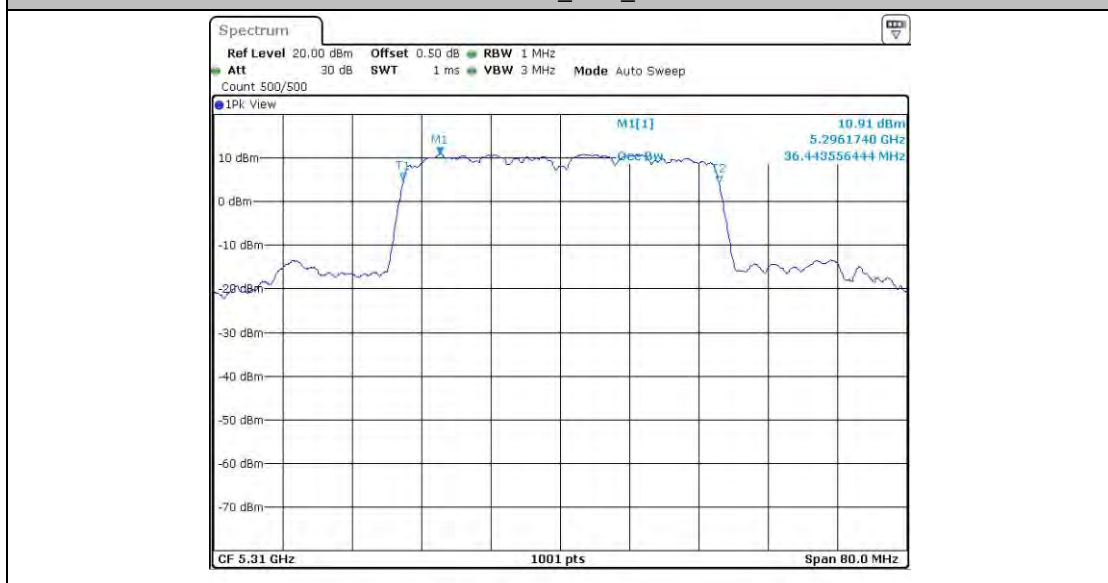
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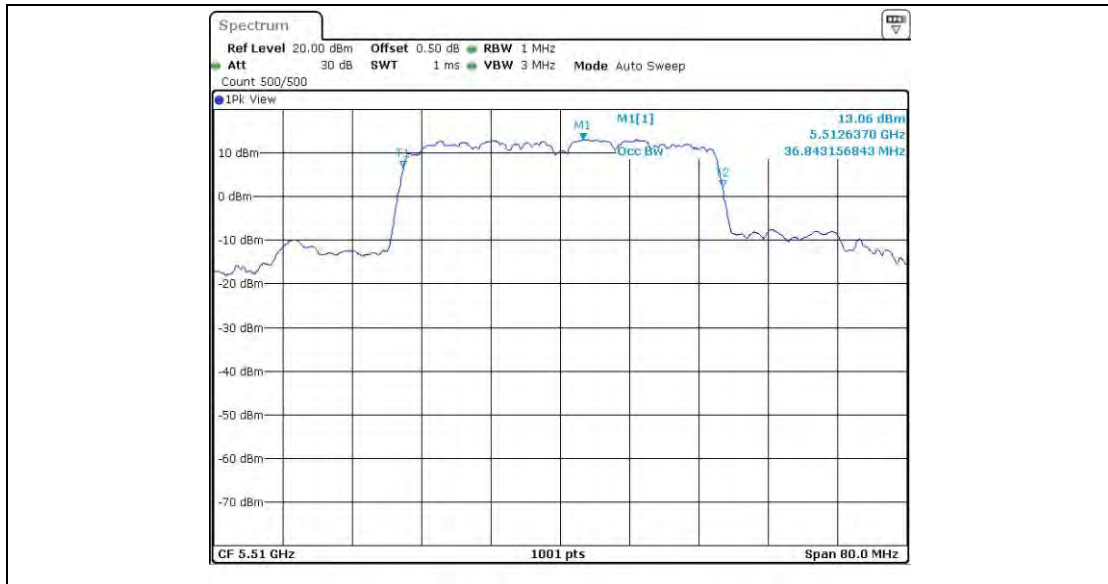
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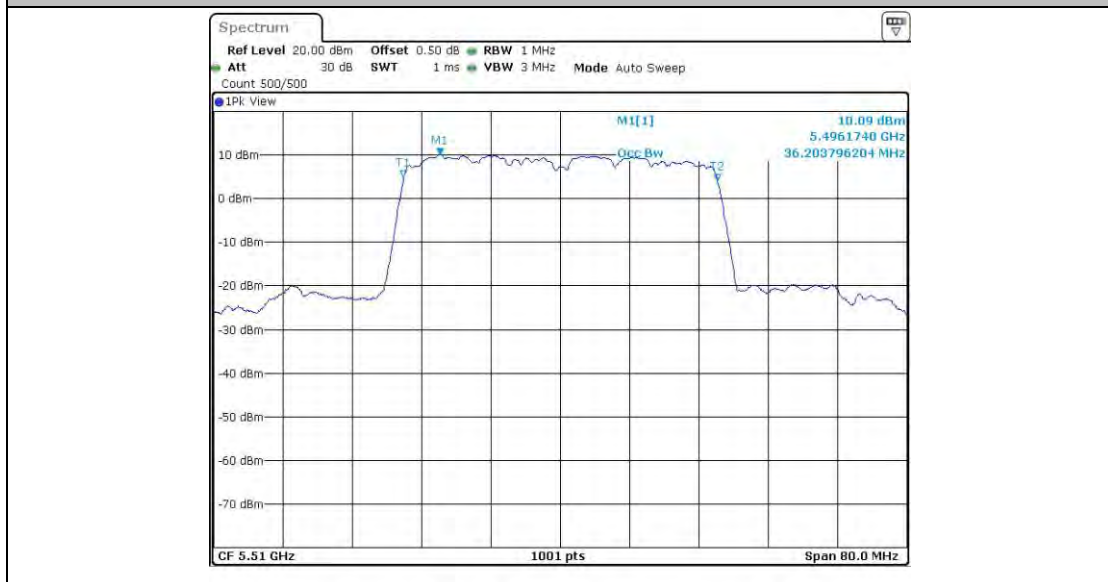
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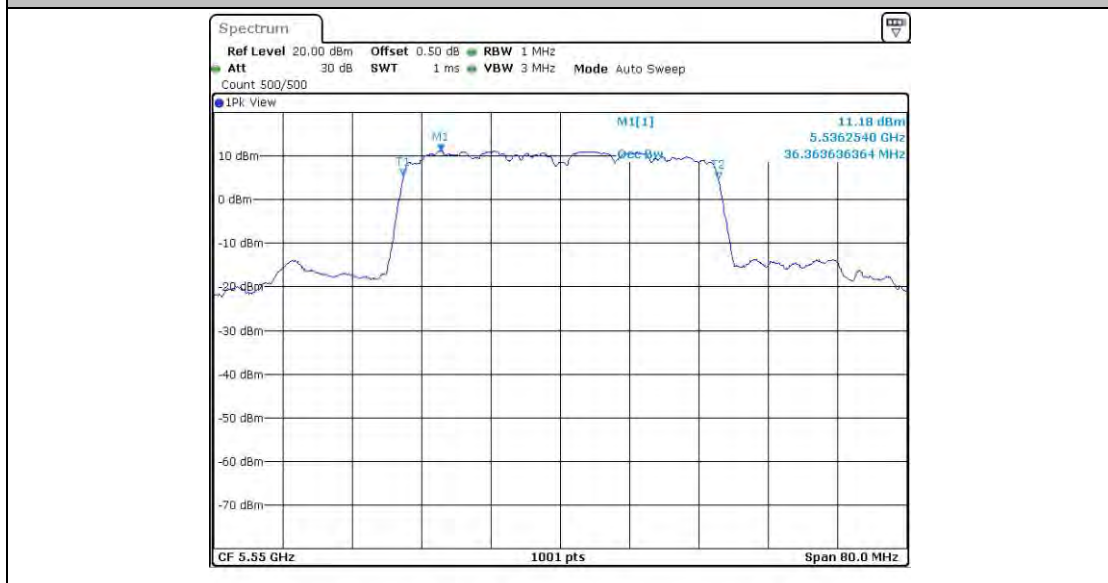
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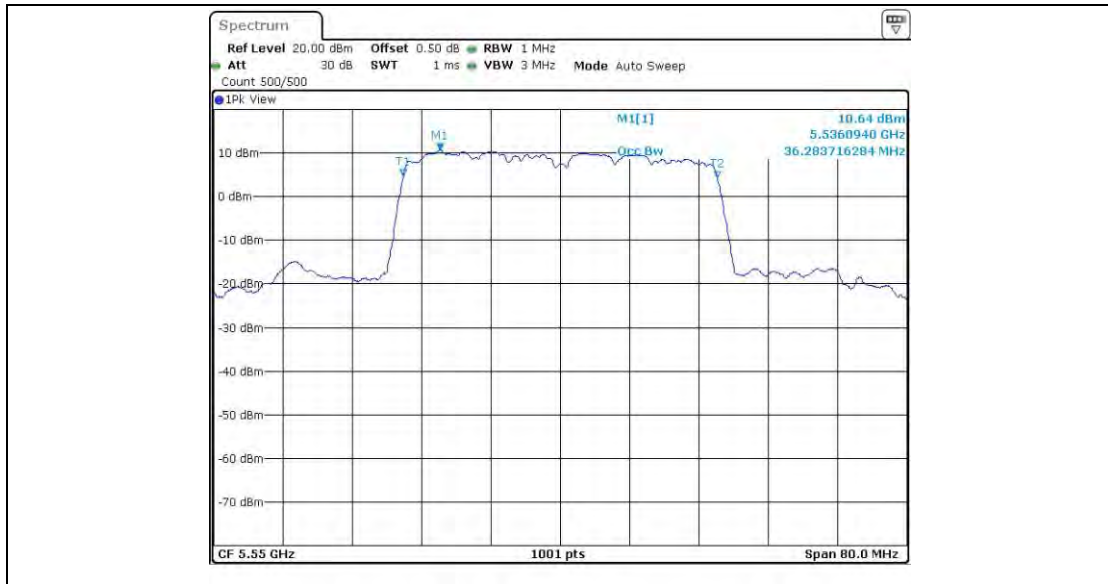
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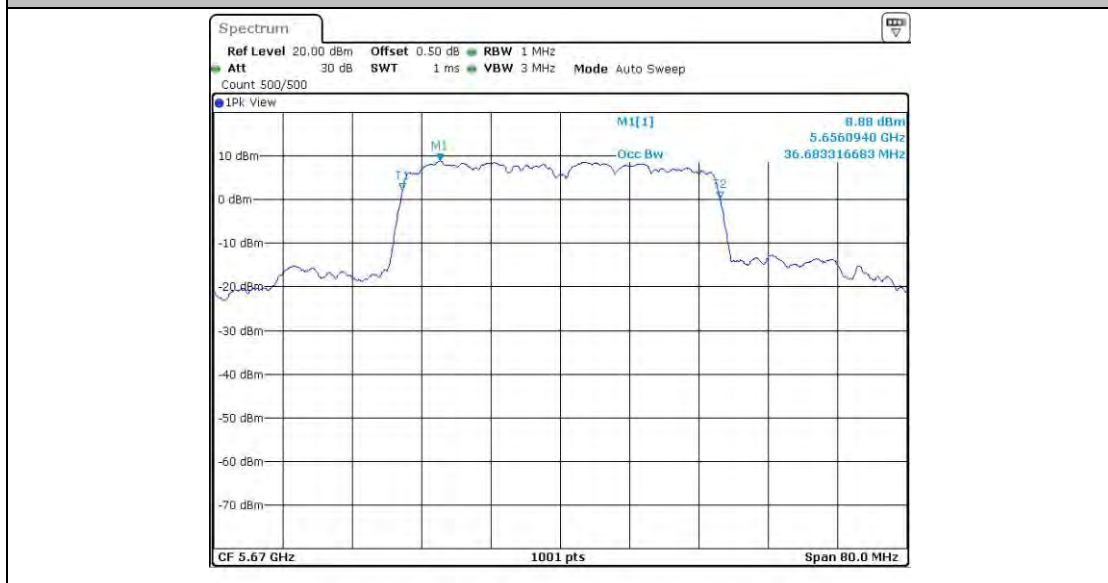
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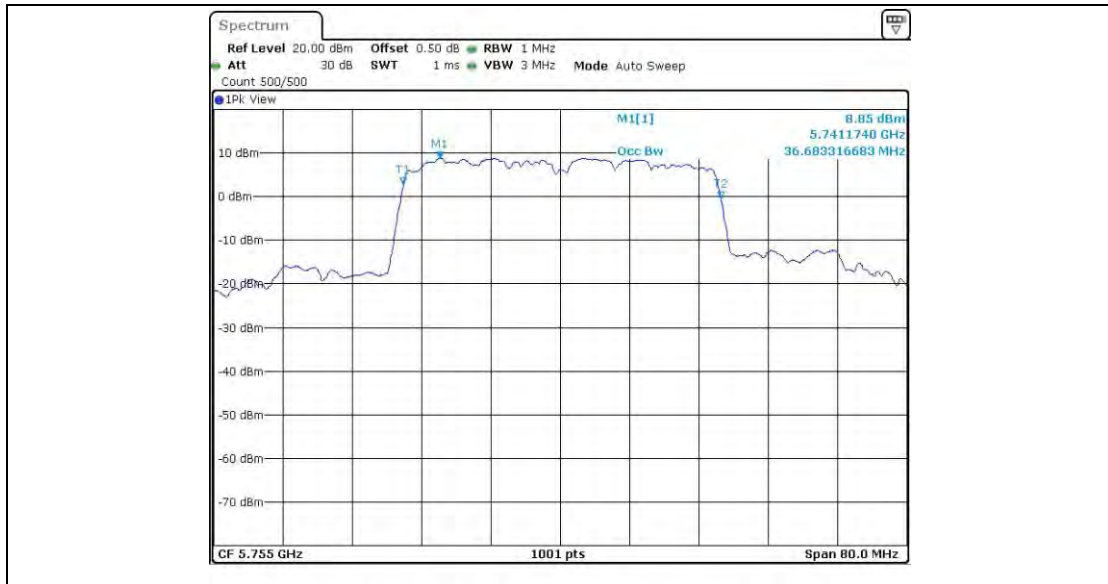
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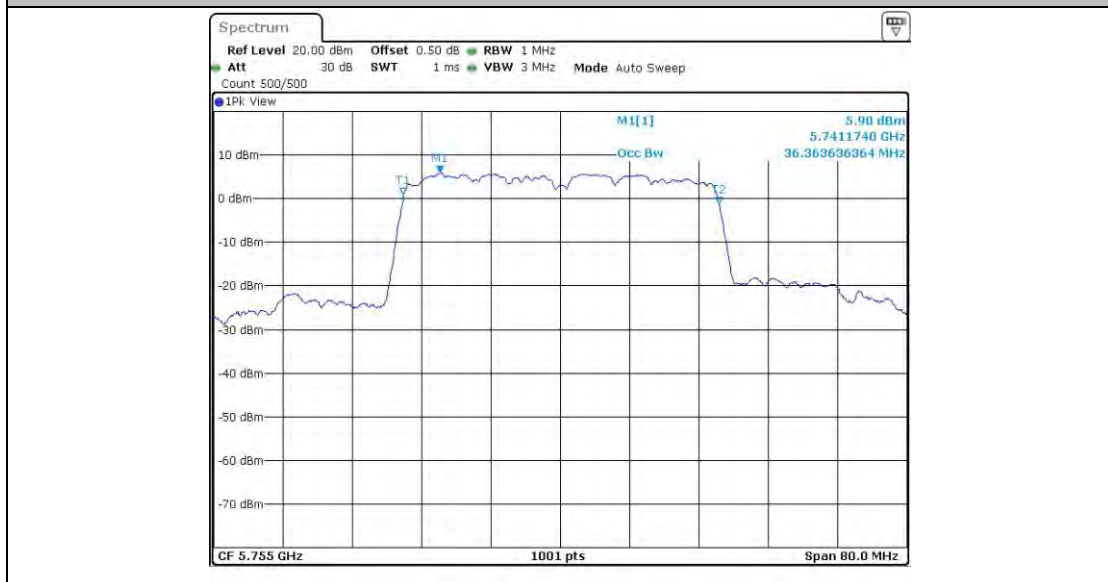
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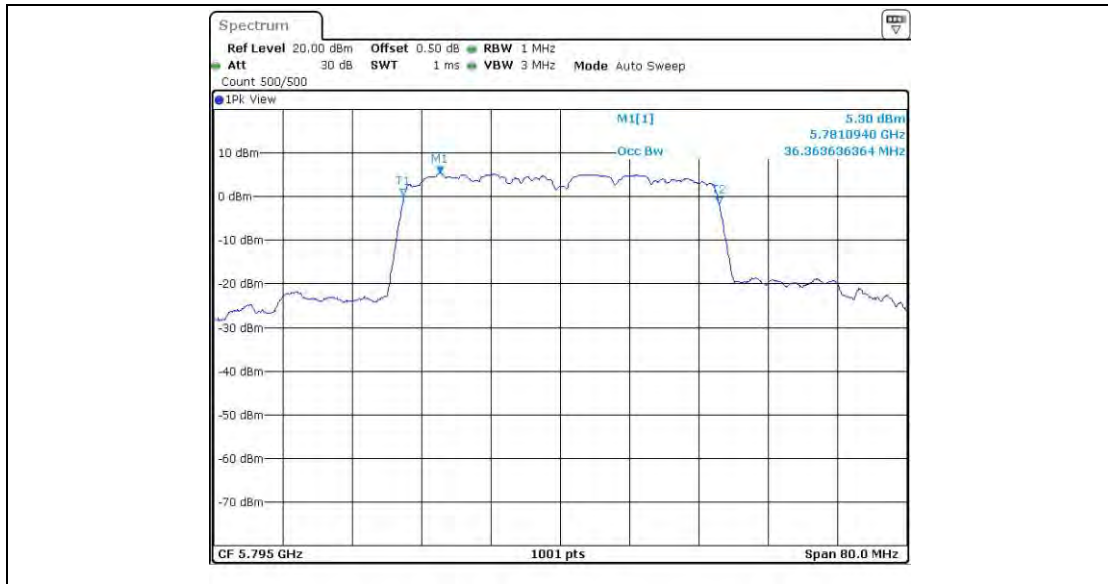
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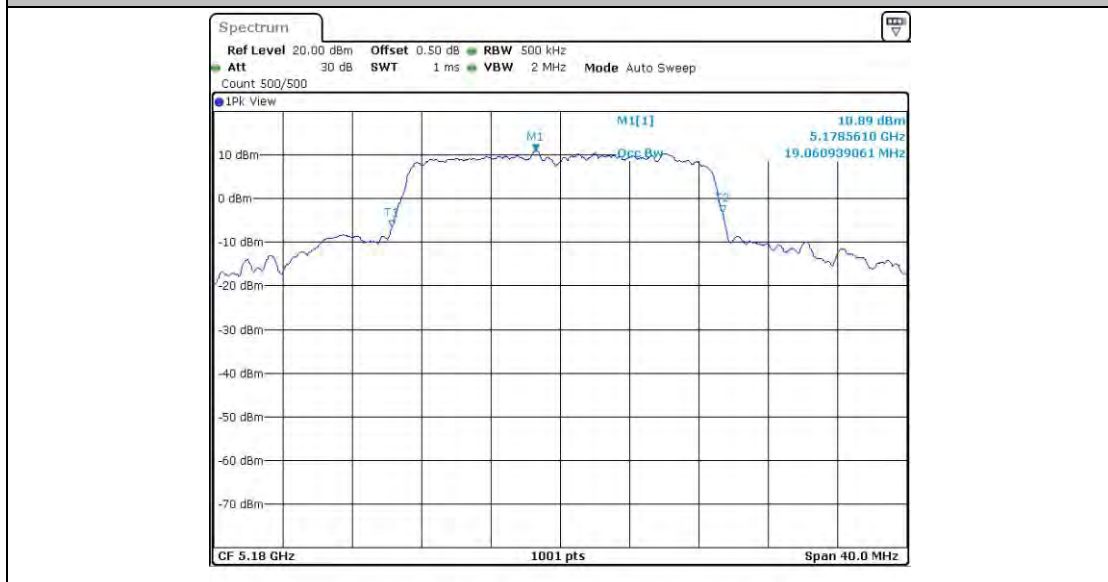
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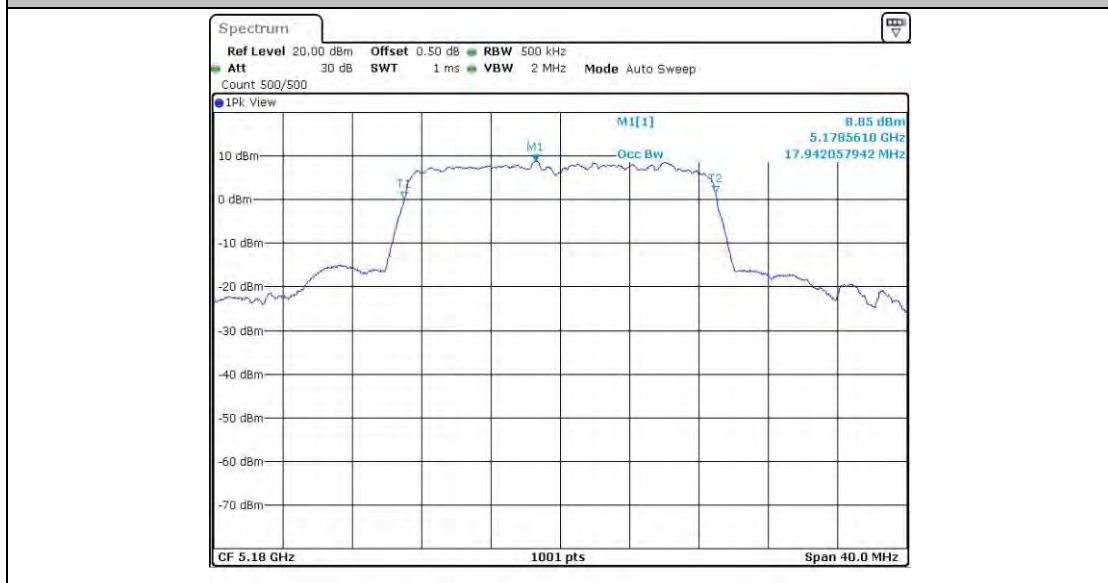
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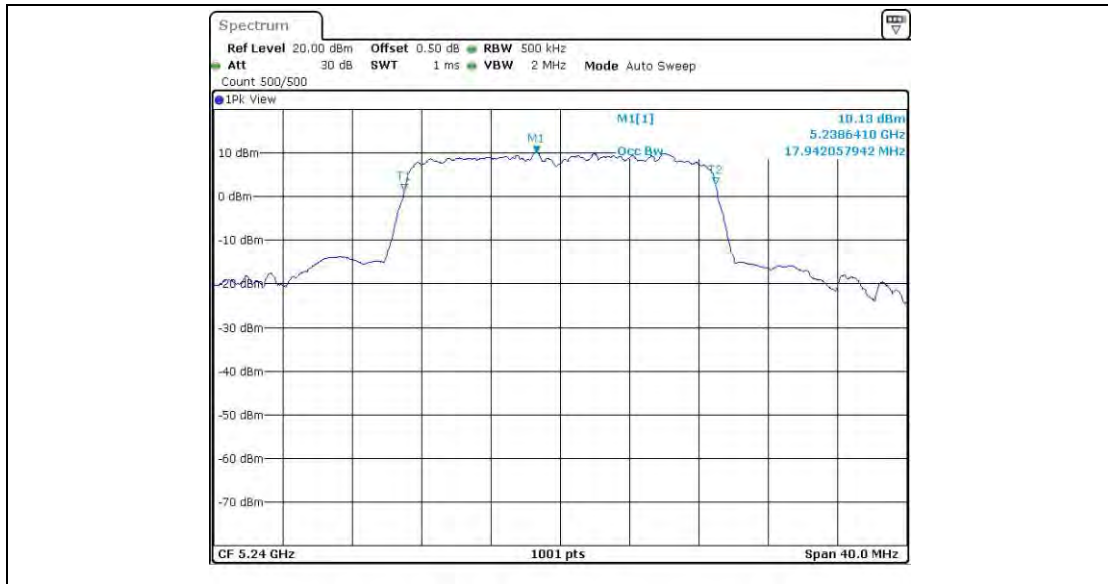
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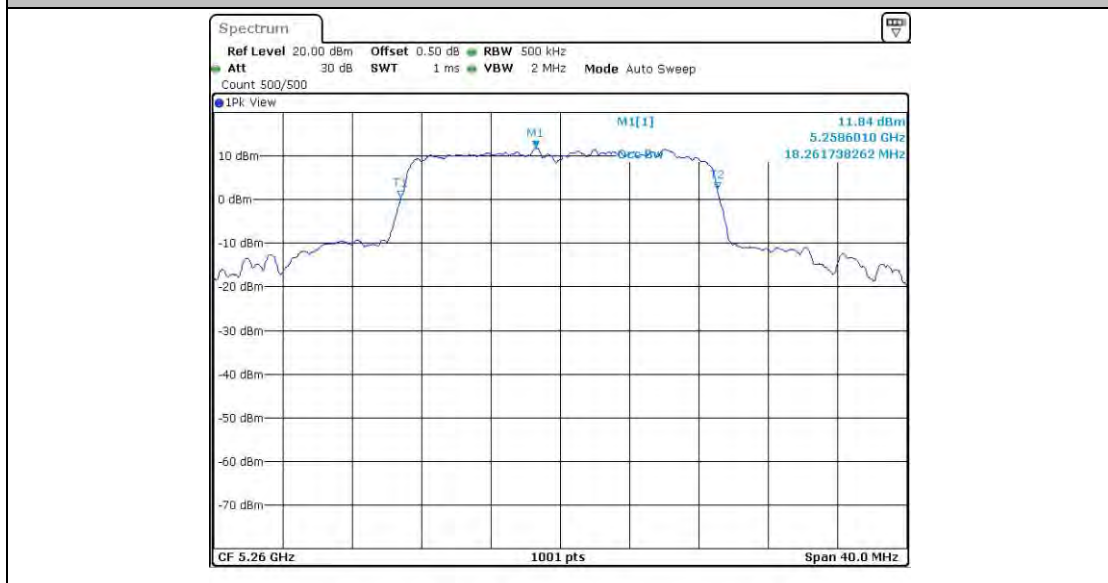
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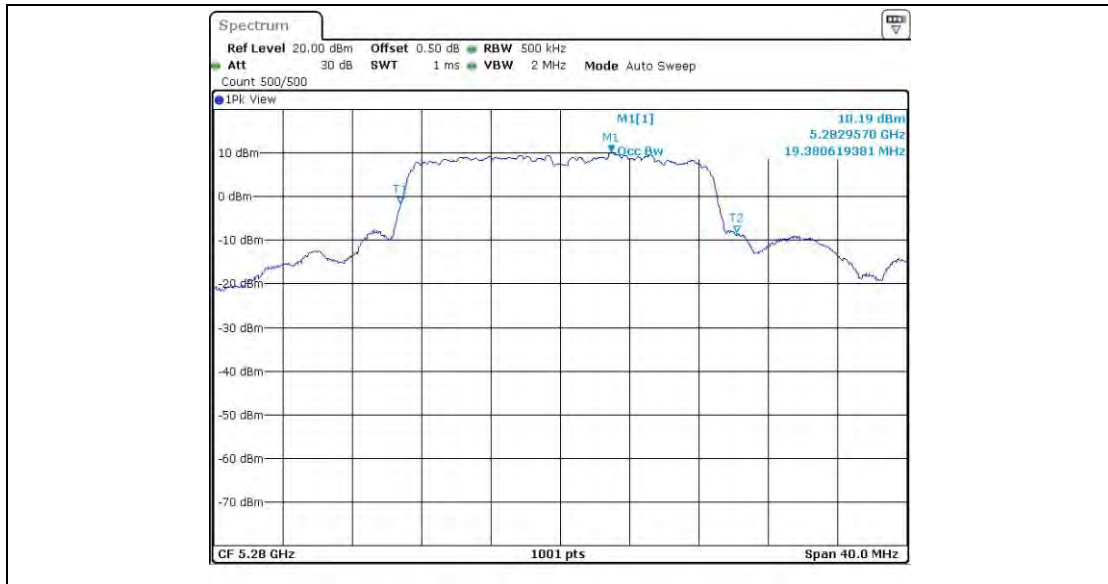
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11AC20SISO_Ant0_5260



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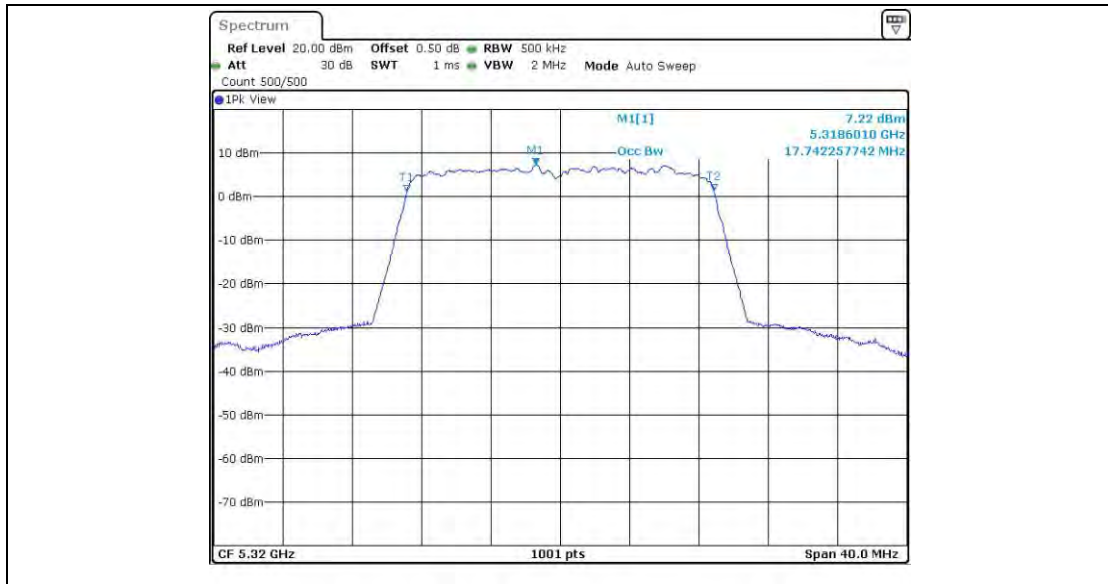
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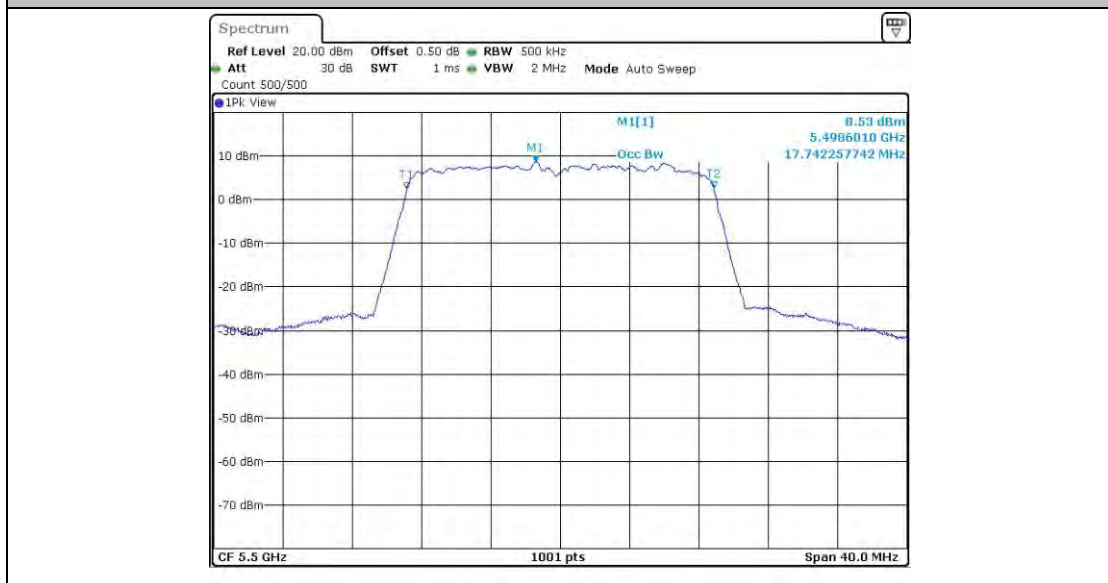
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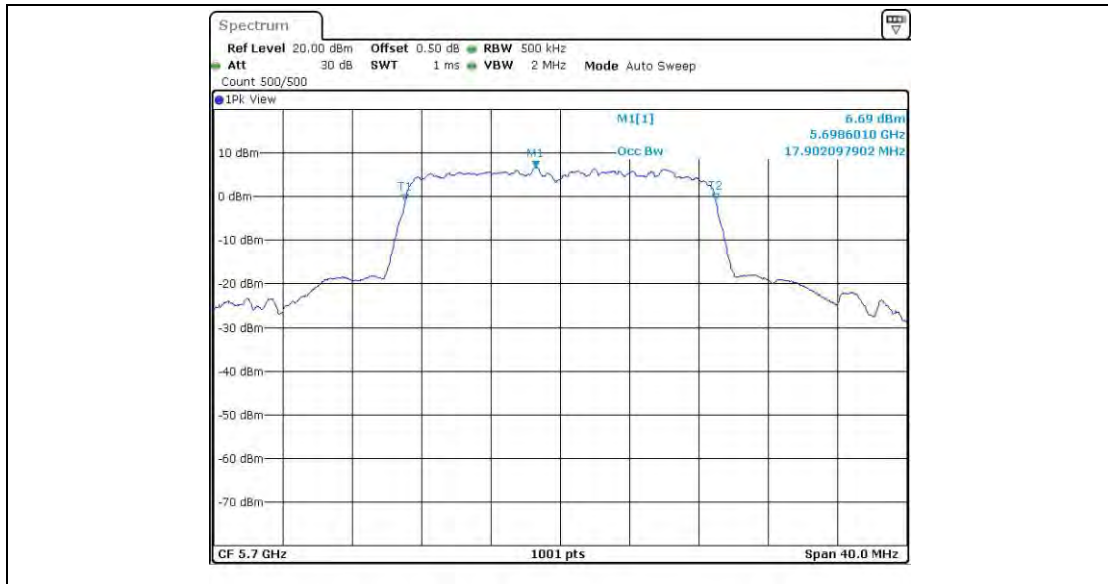
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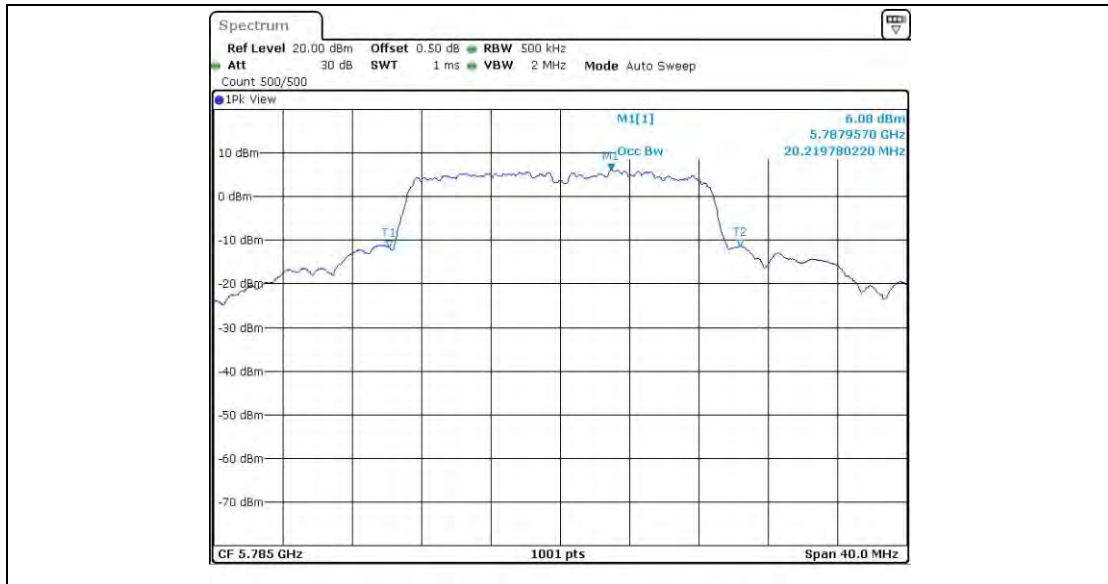
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11AC20SISO_Ant0_5745



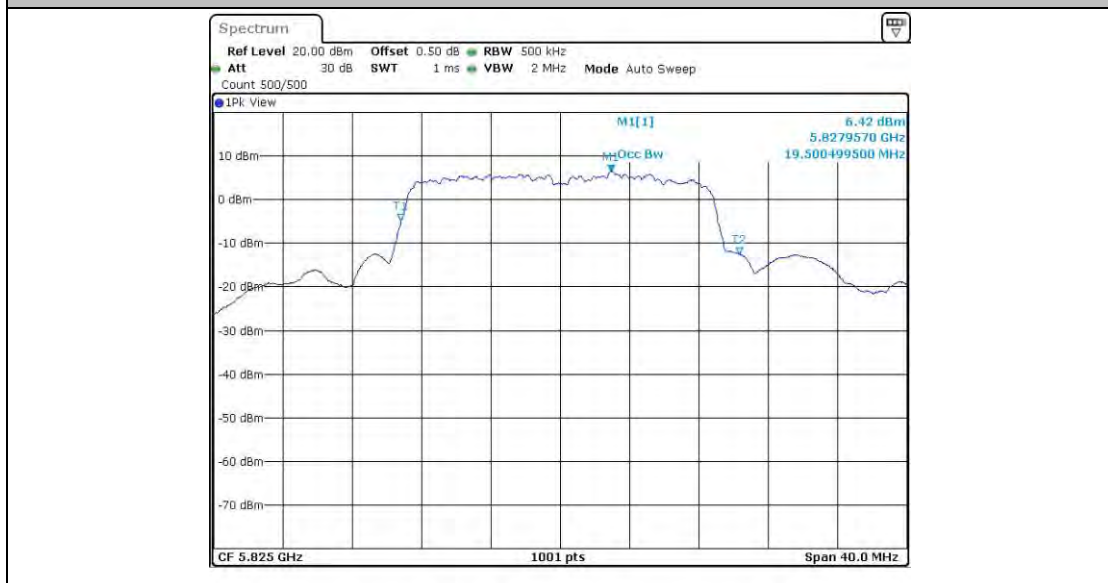
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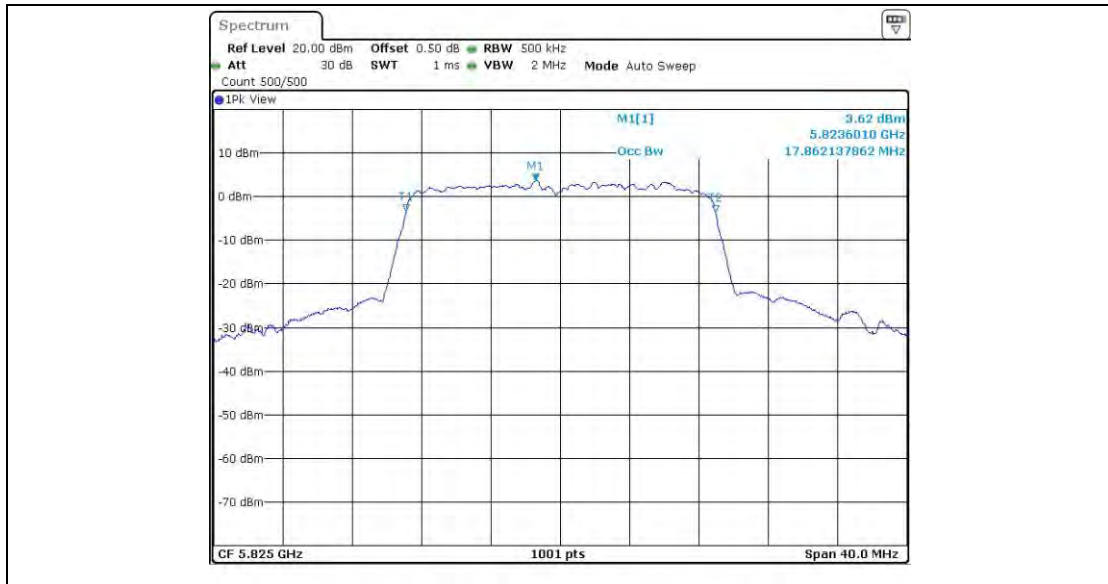
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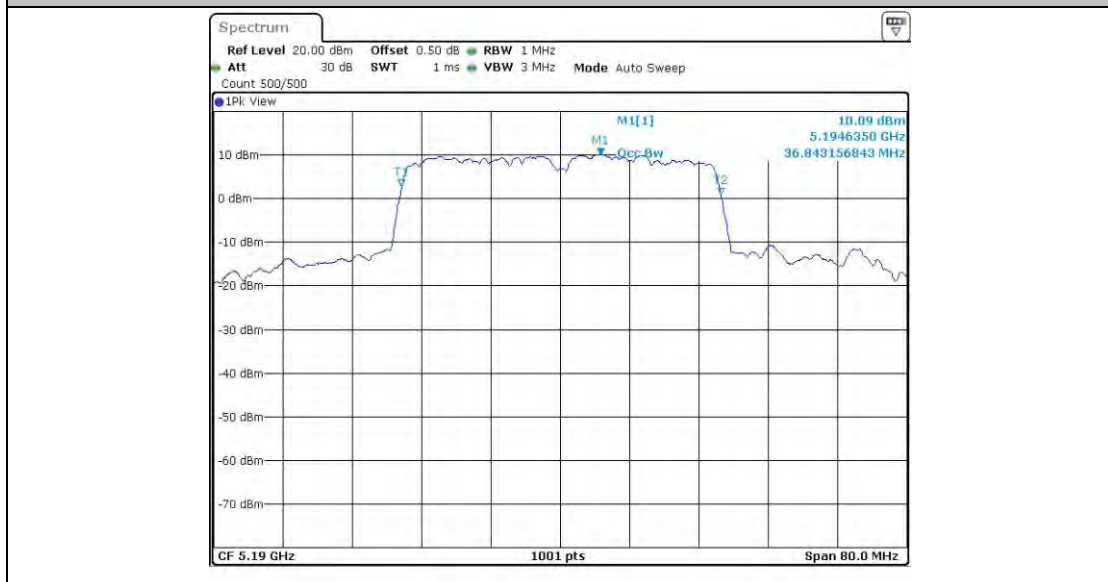
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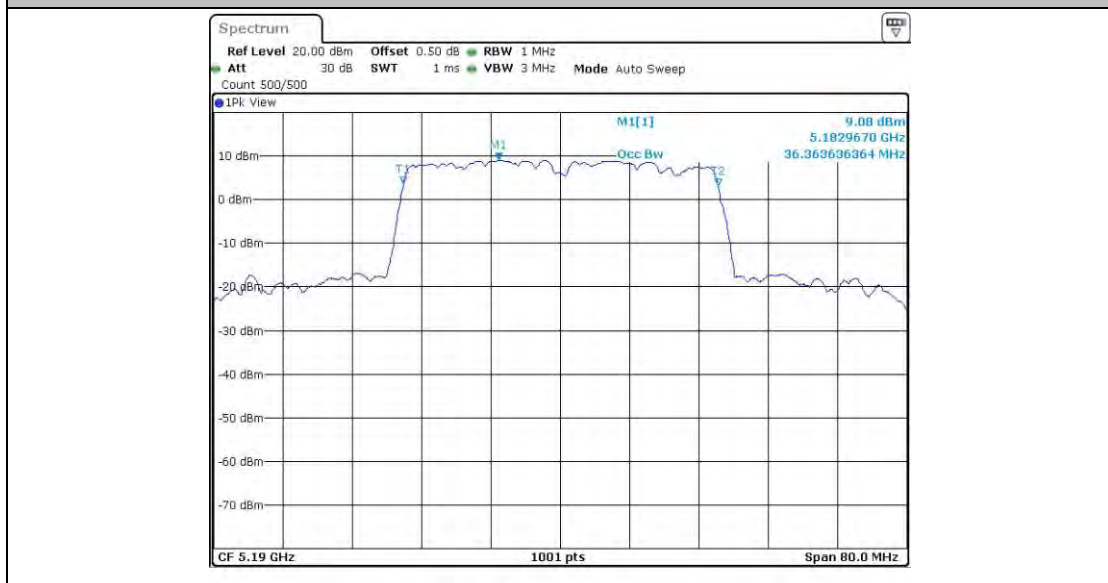
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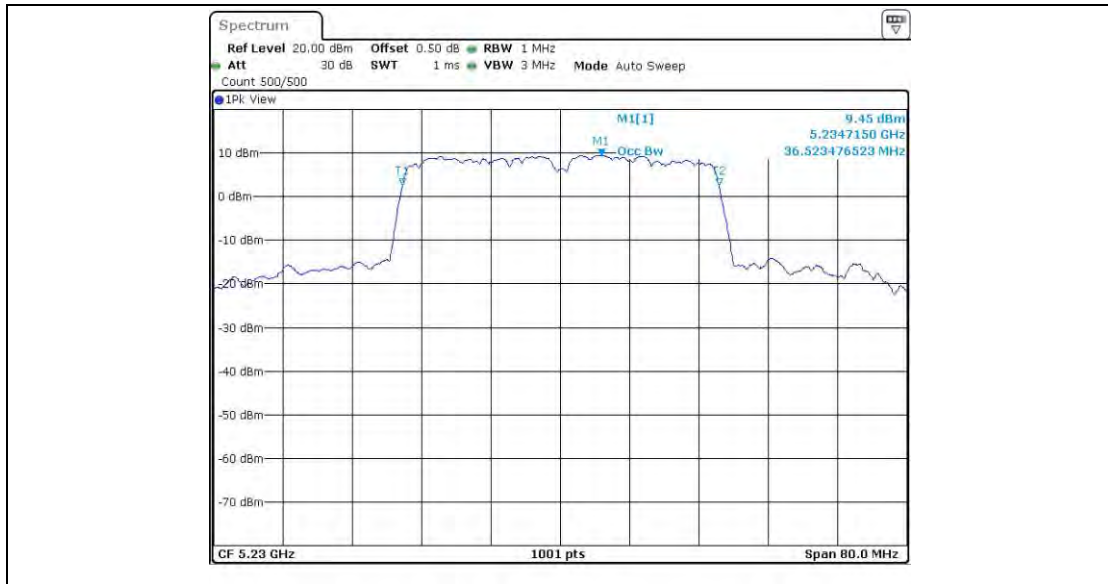
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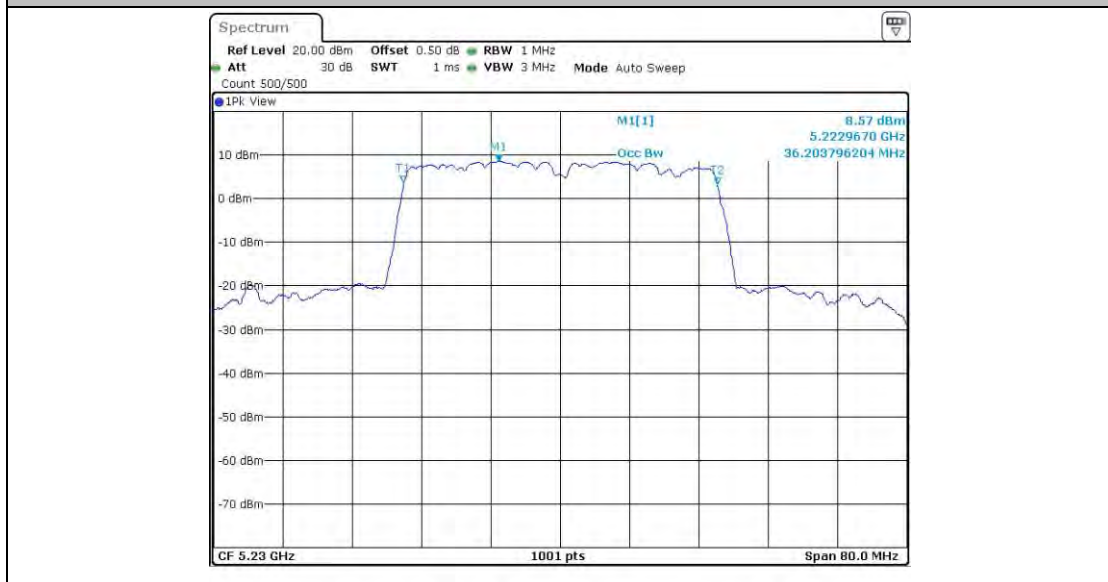
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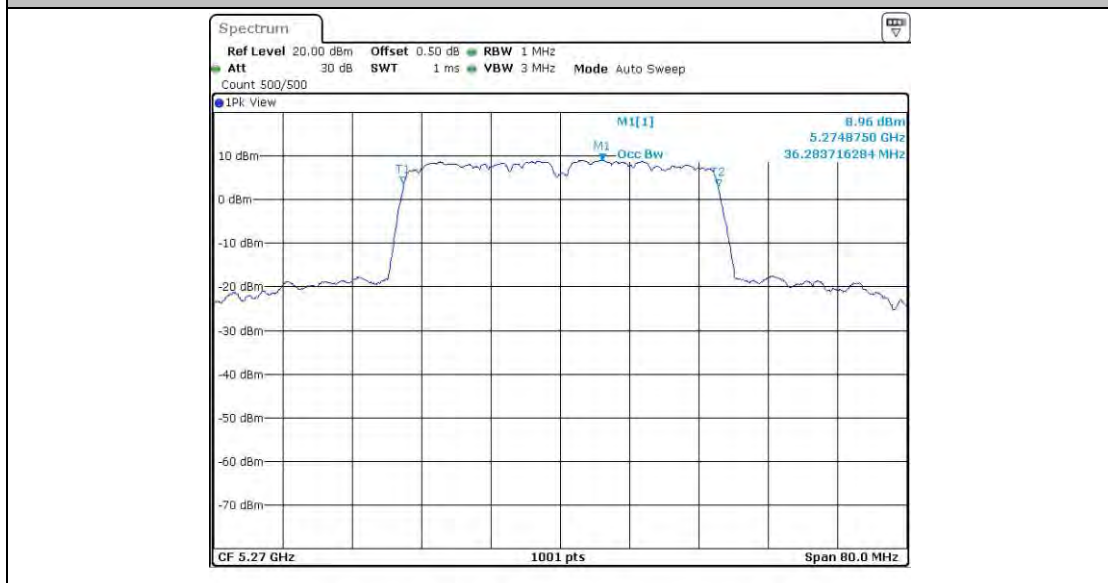
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11AC40SISO_Ant0_5230



11AC40SISO_Ant1_5230



11AC40SISO_Ant0_5270



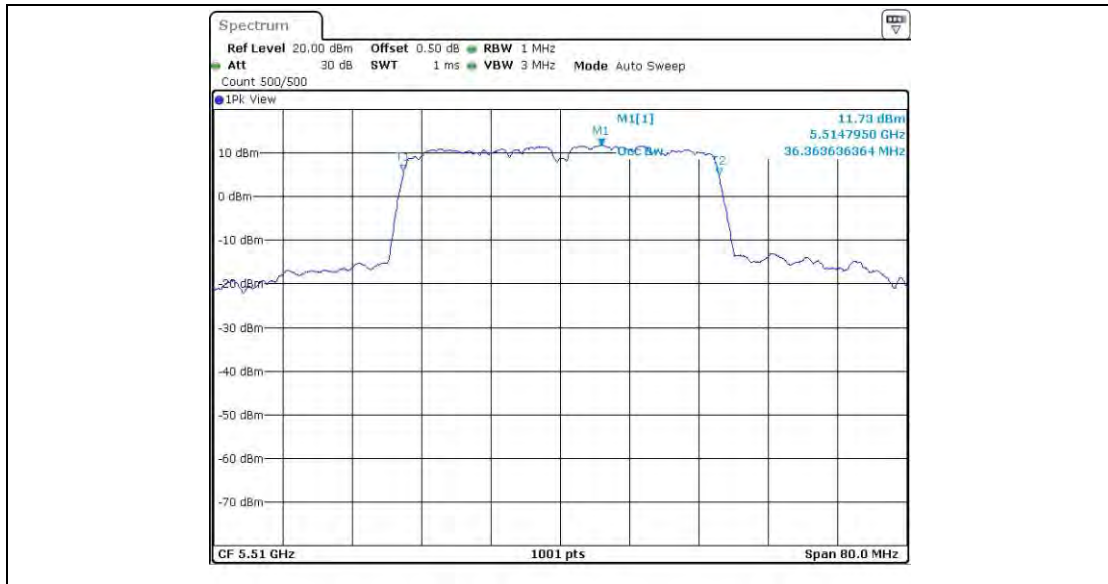
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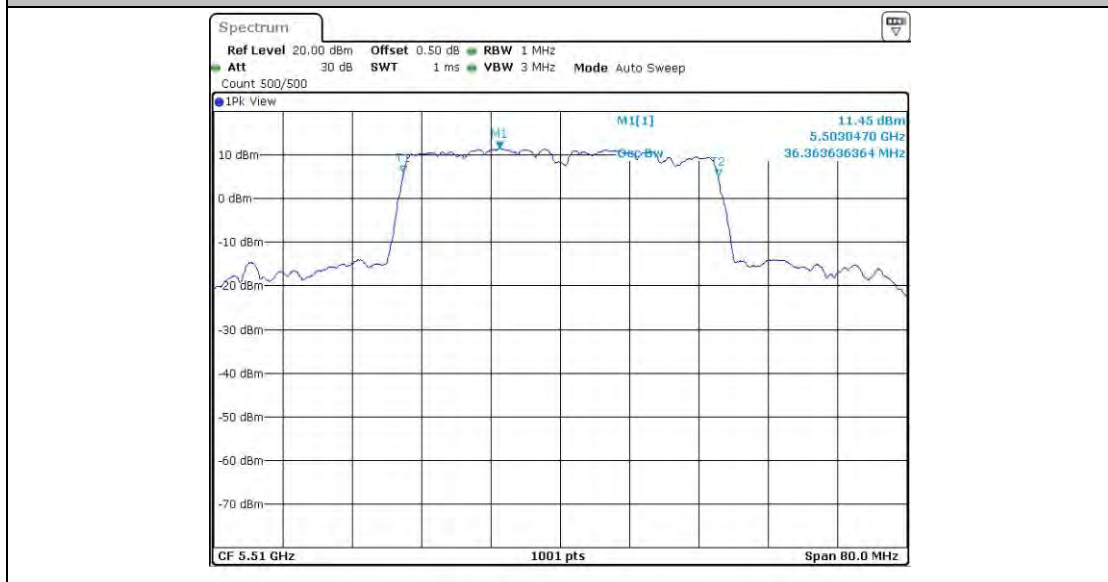
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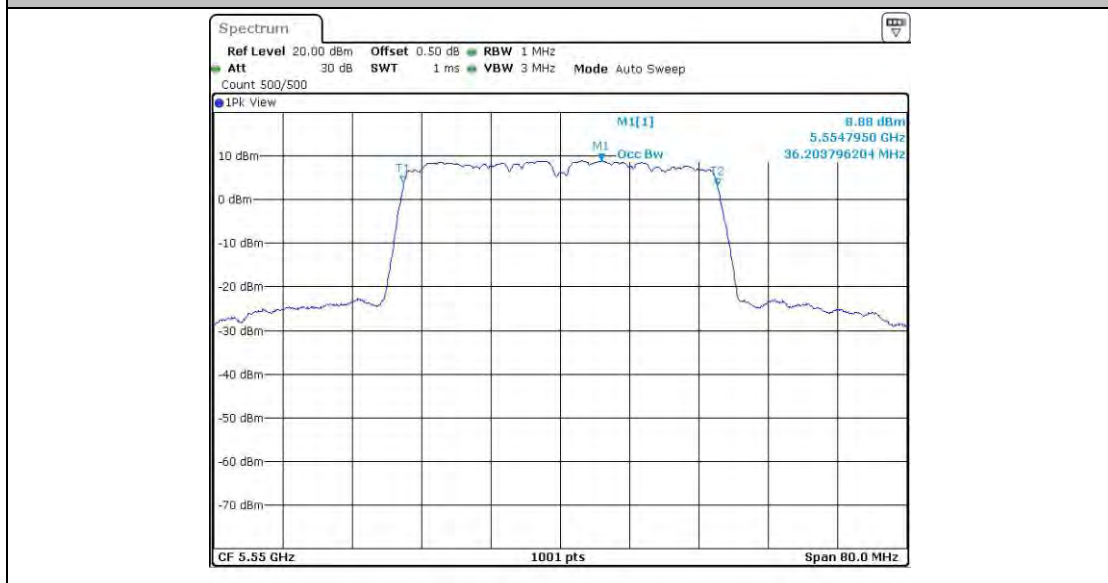
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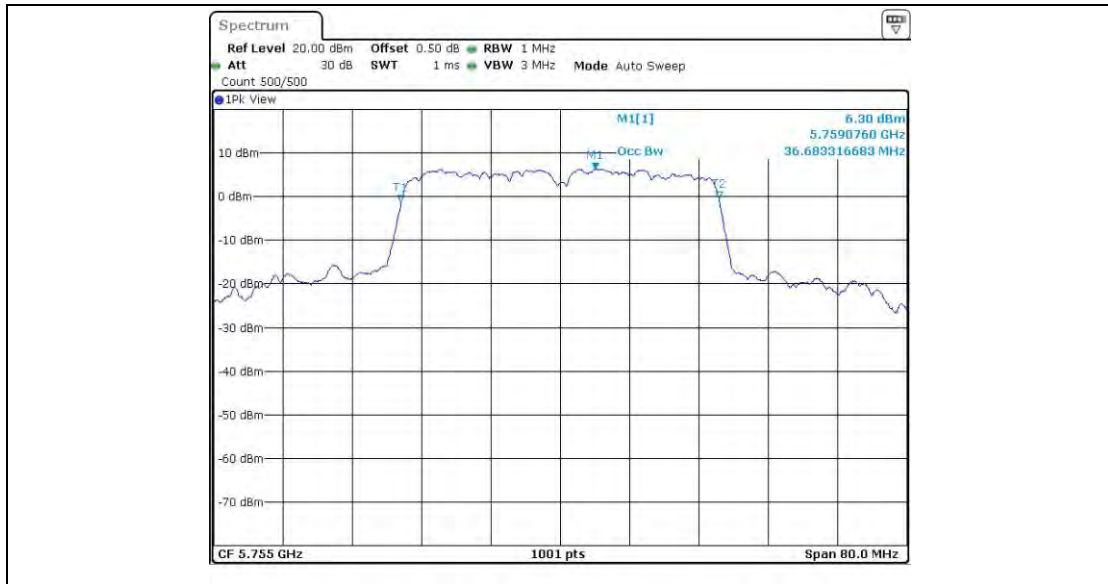
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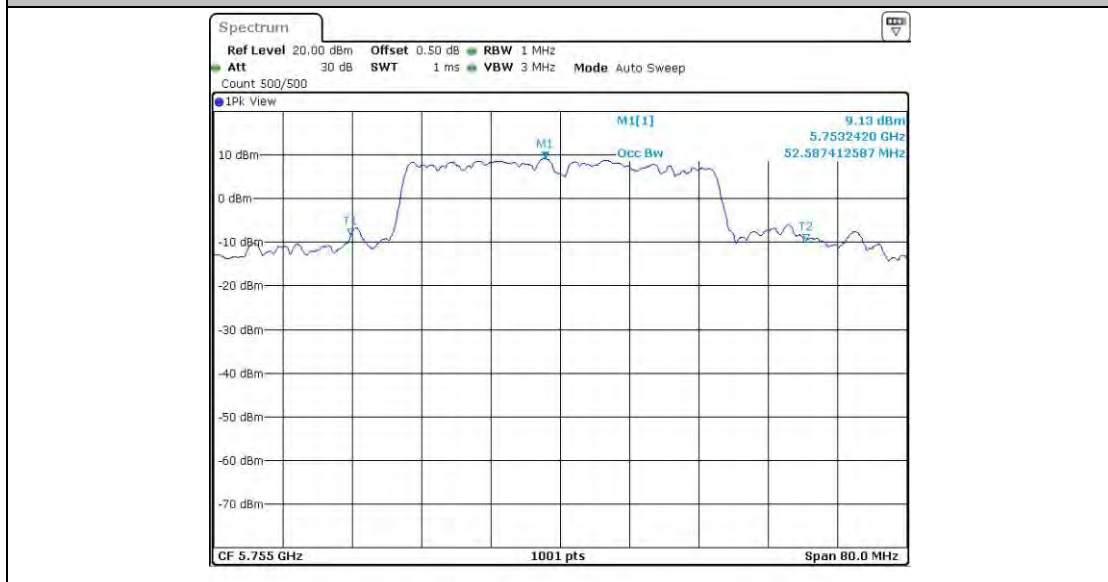
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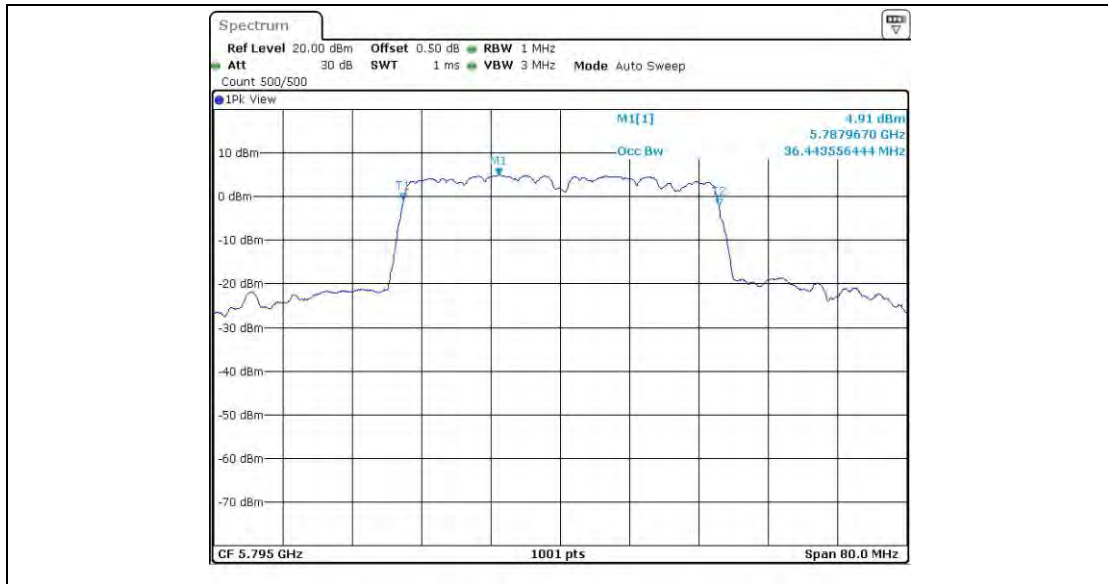
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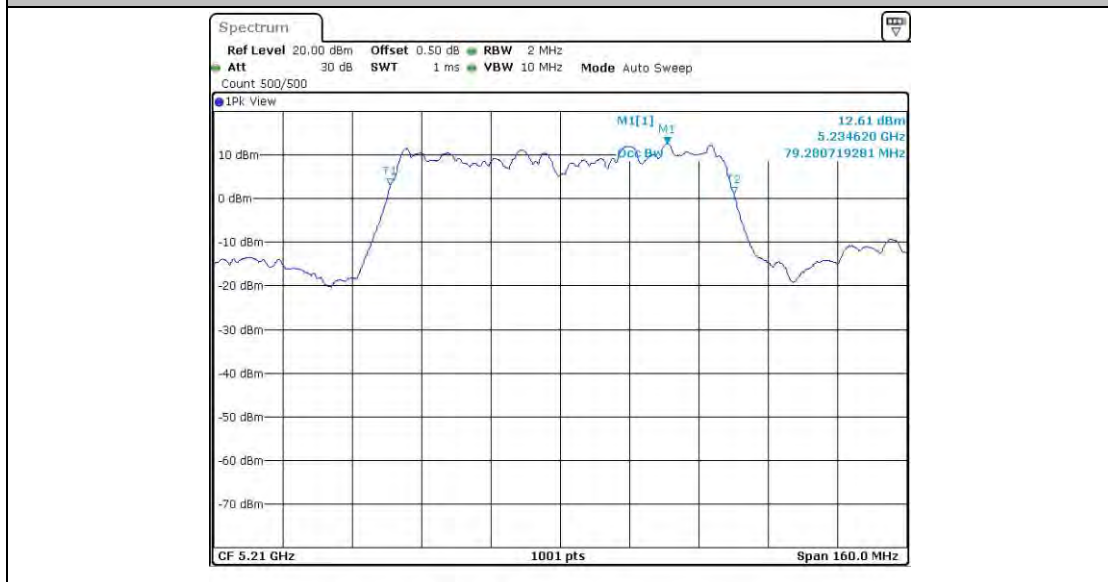
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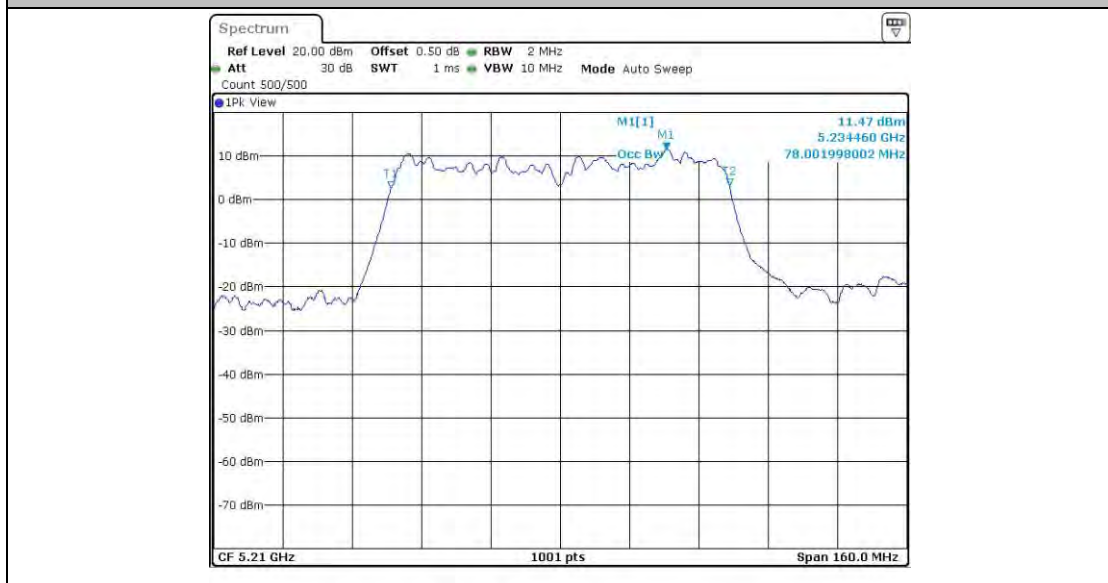
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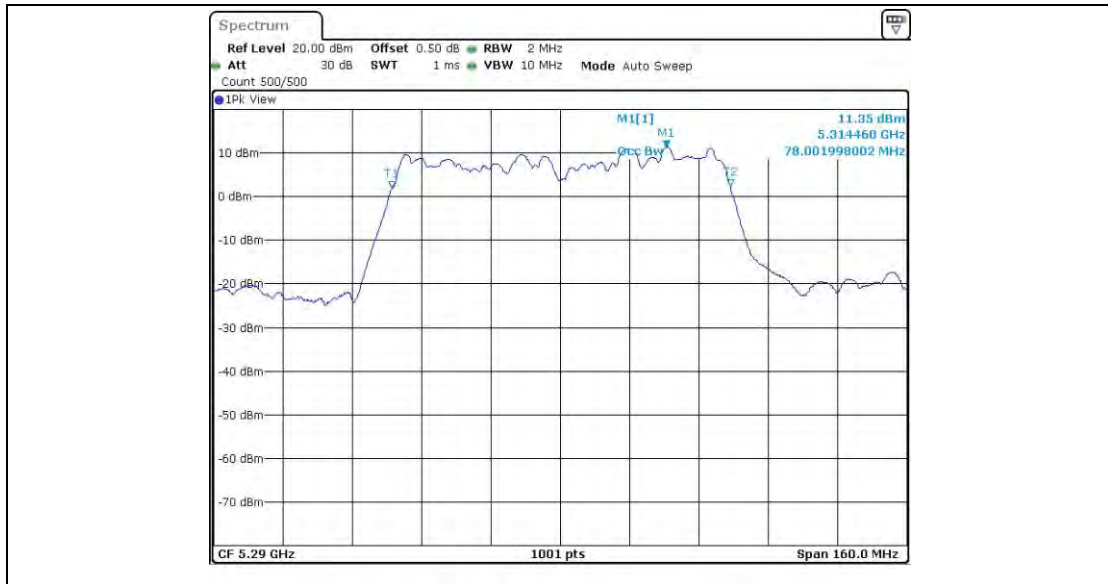
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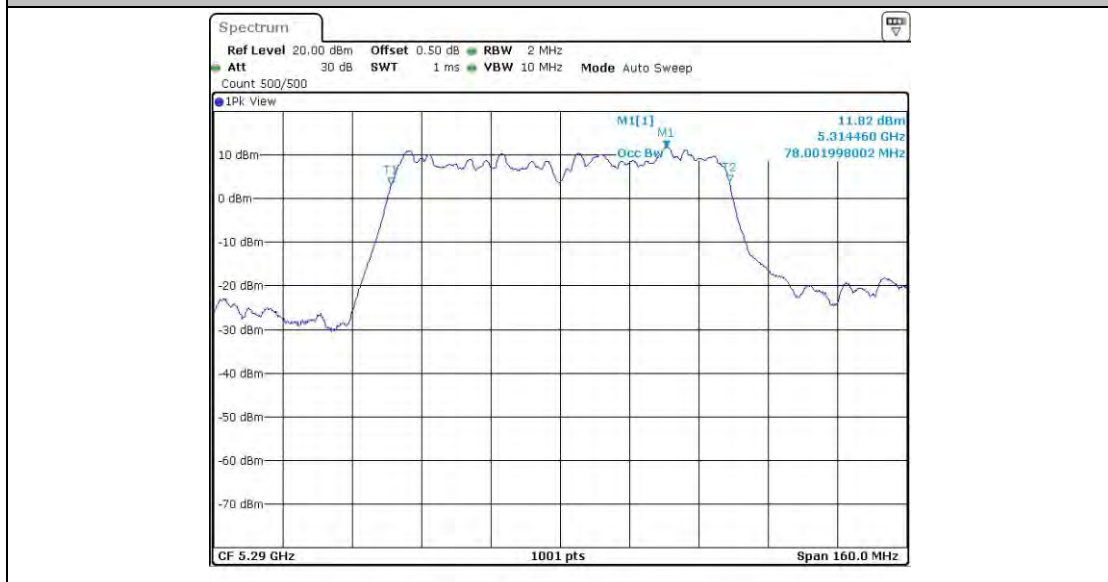
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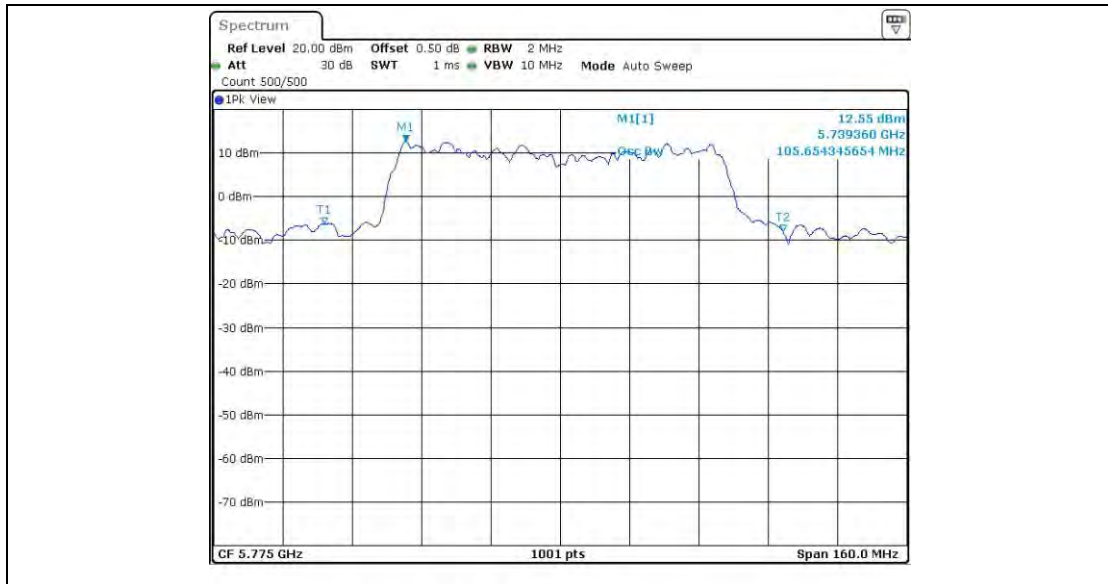
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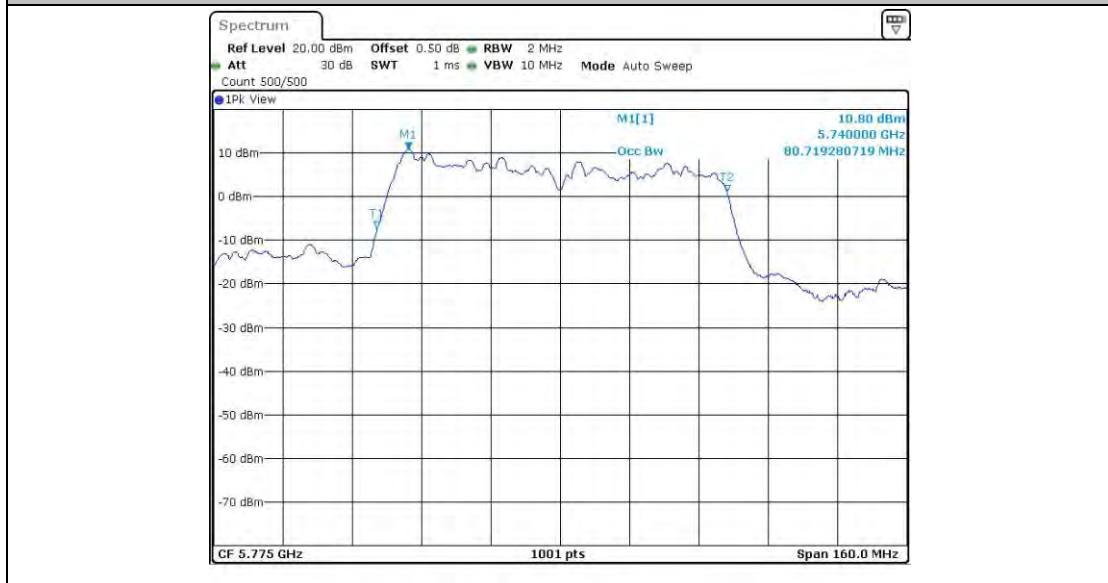
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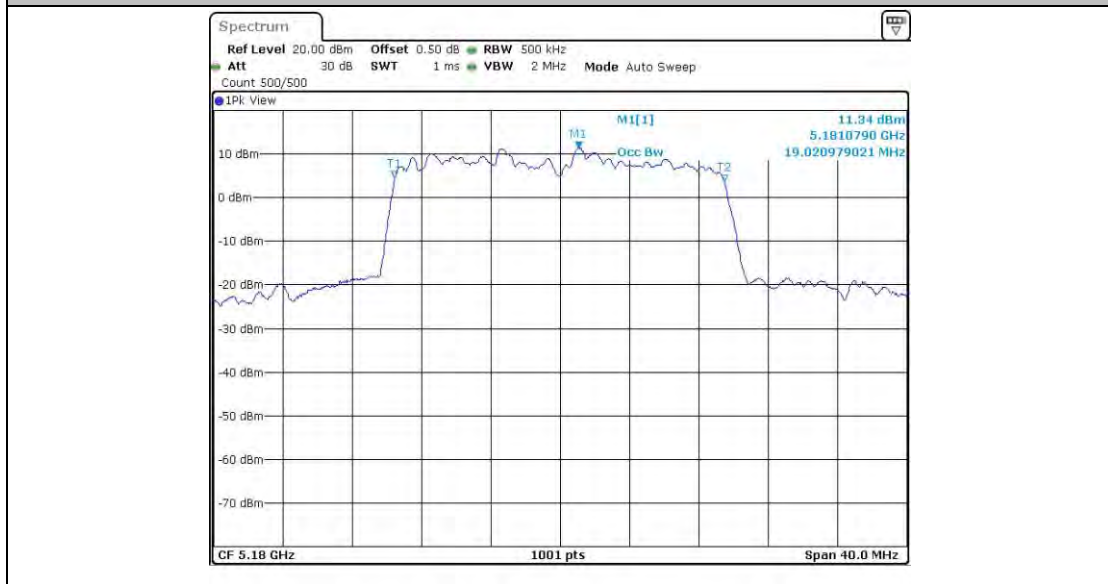
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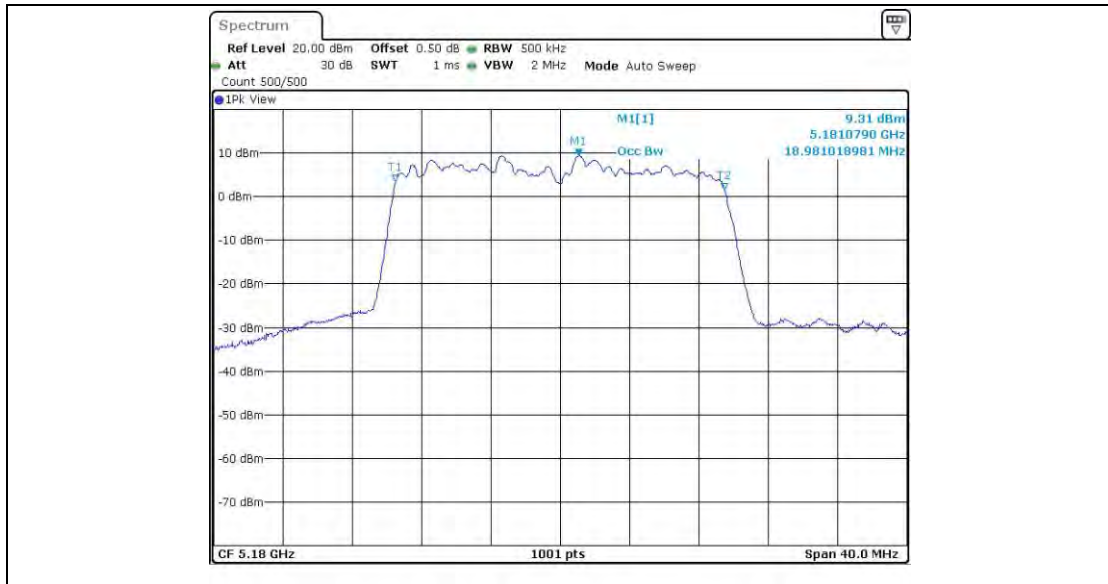
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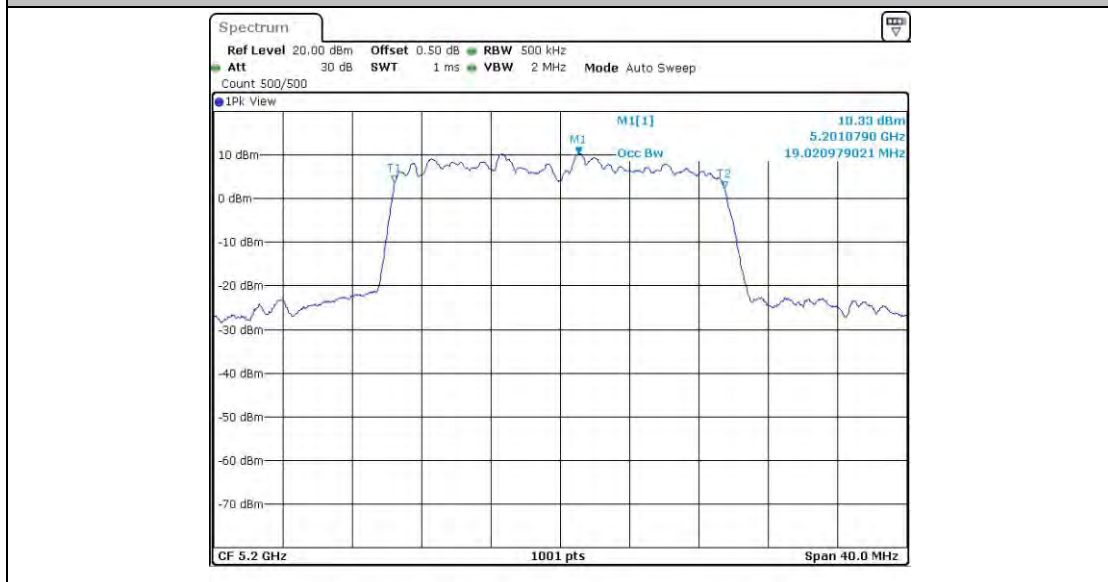
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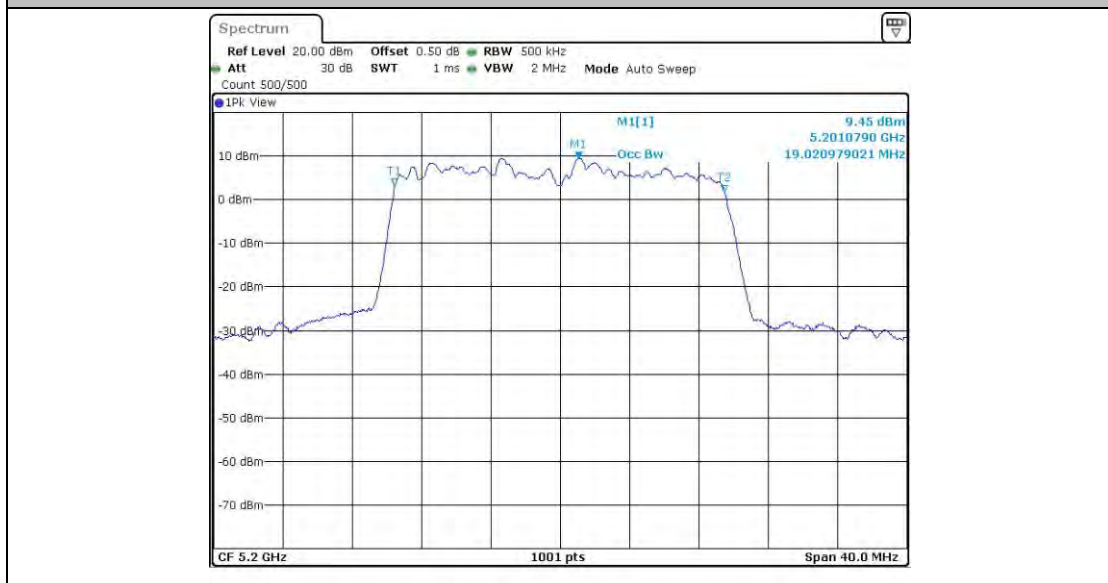
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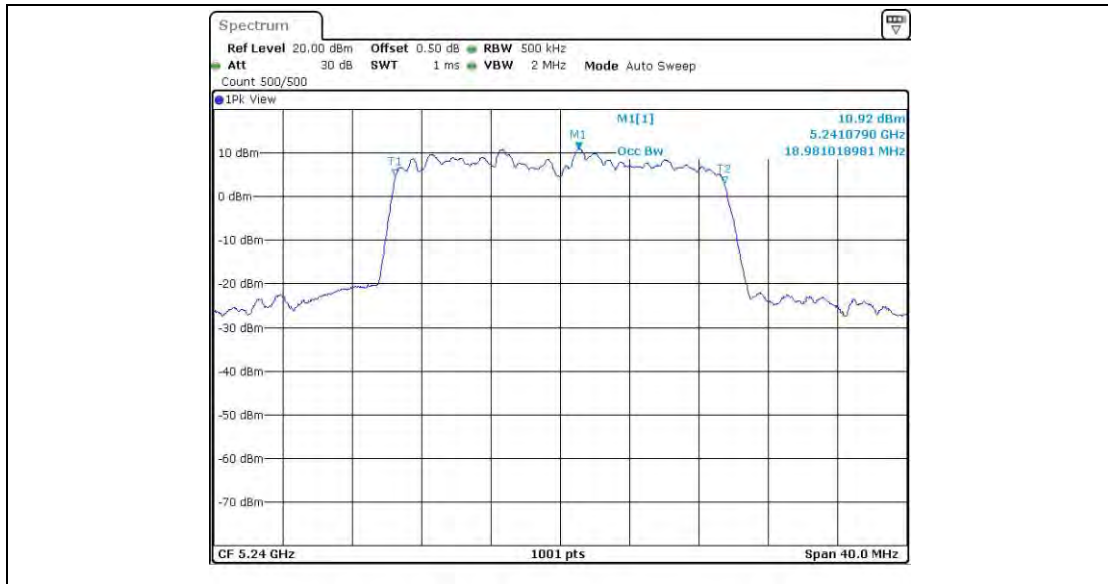
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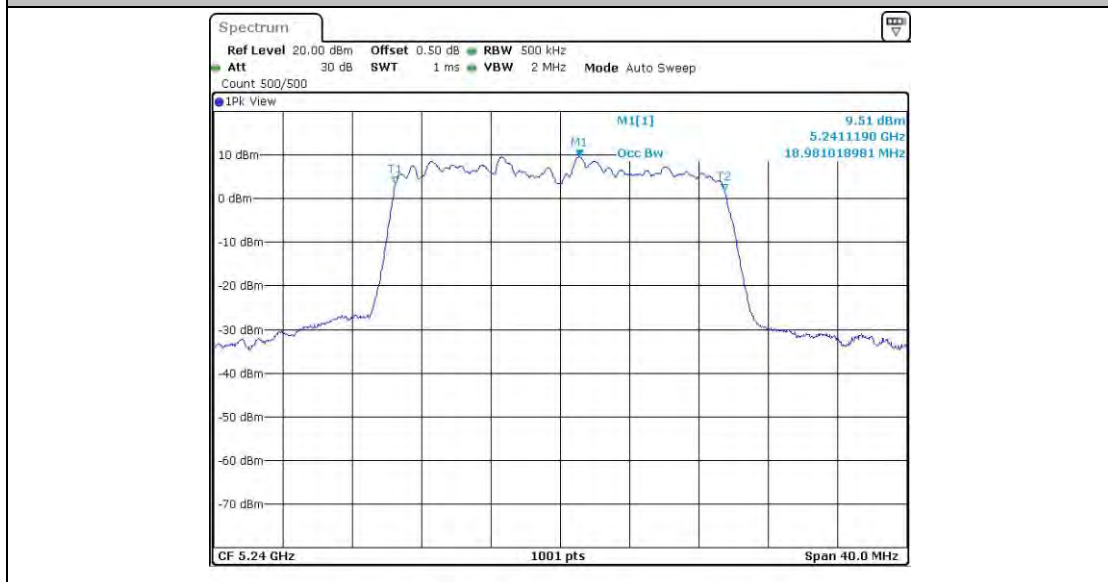
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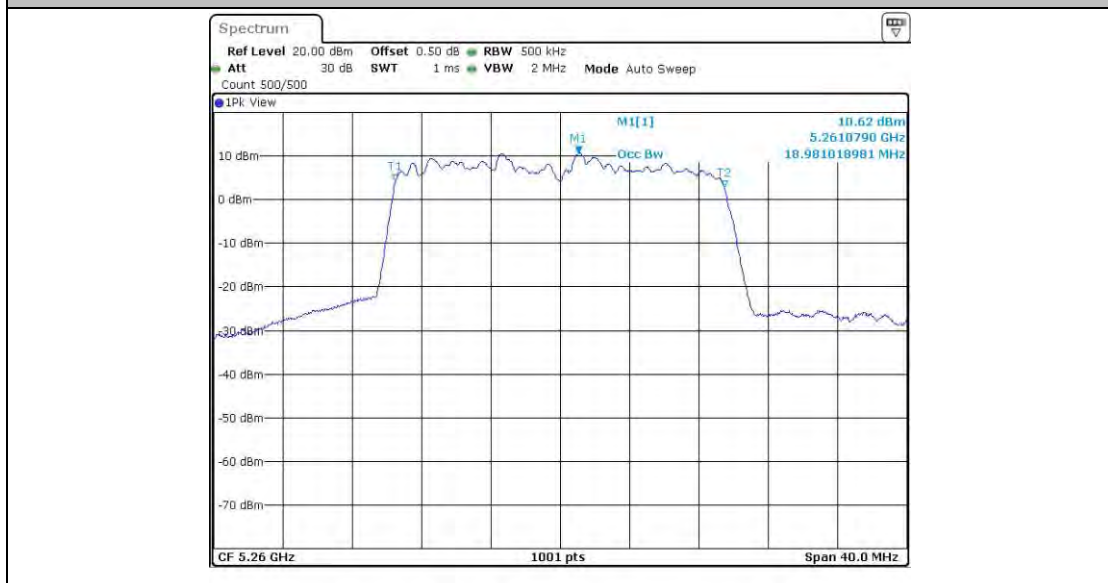
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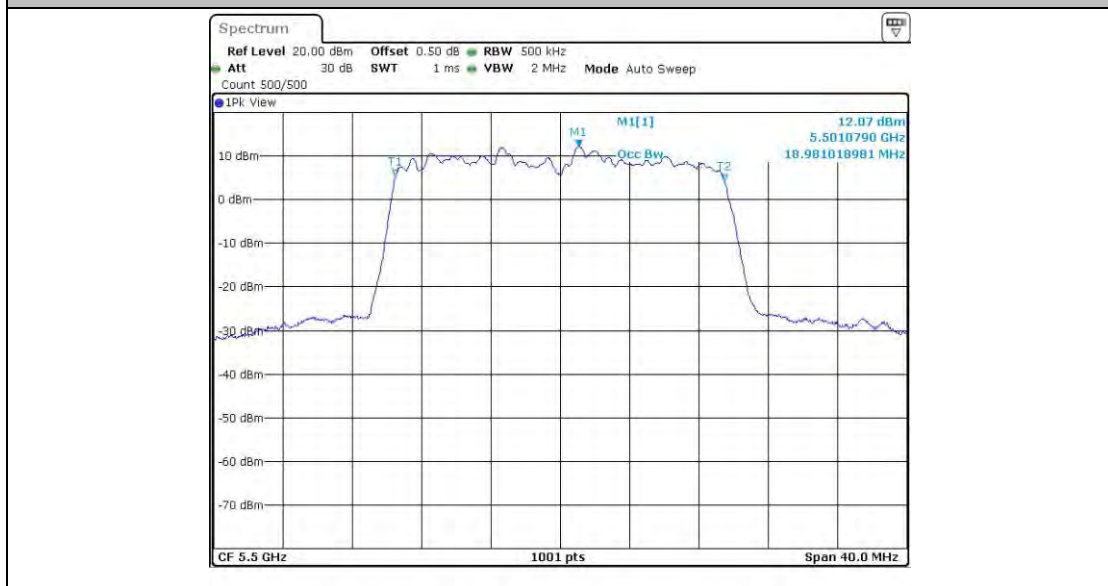
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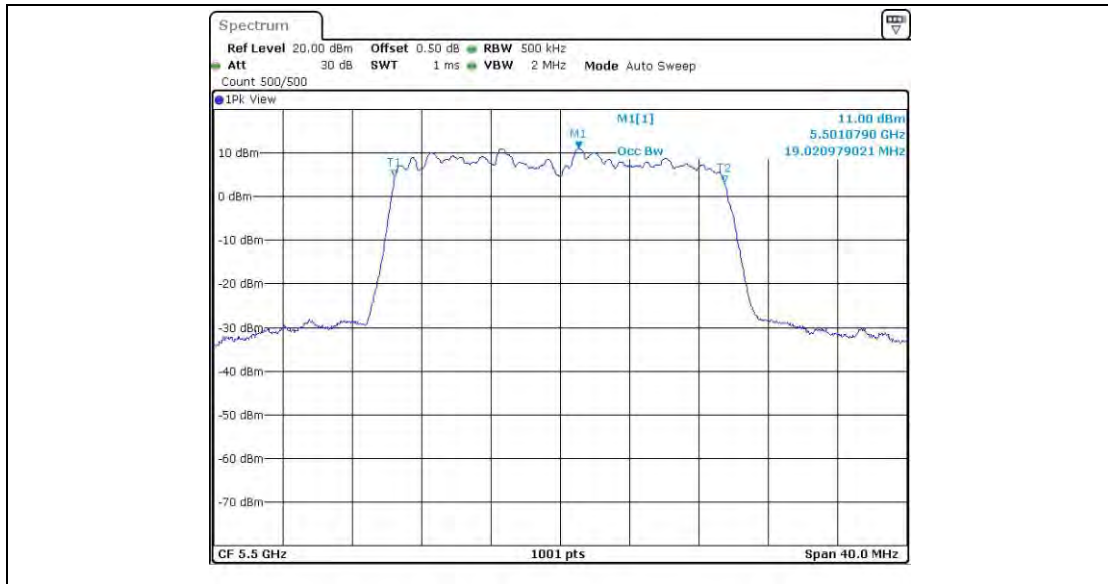
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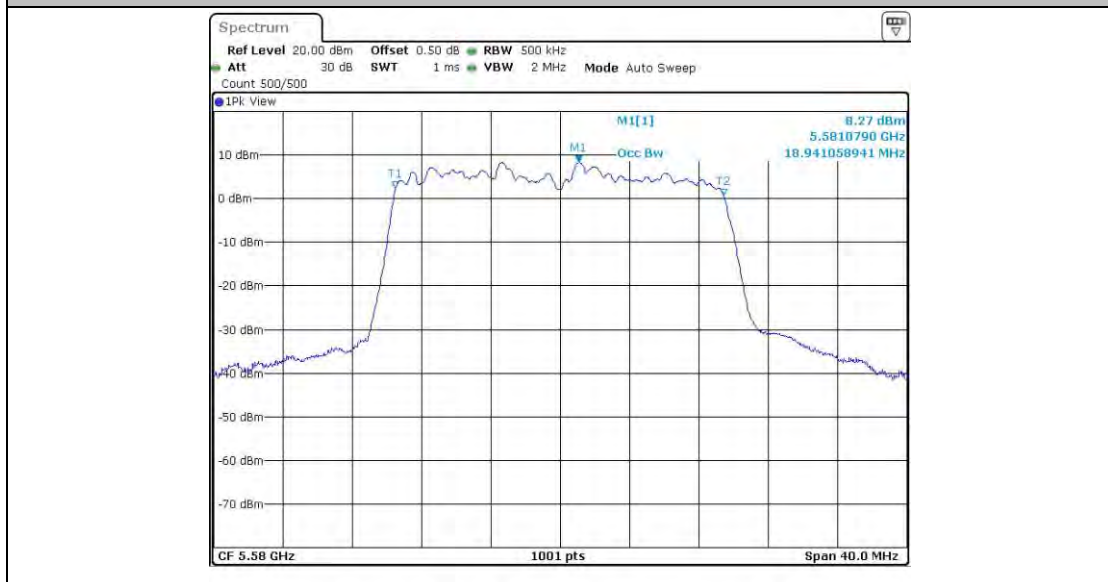
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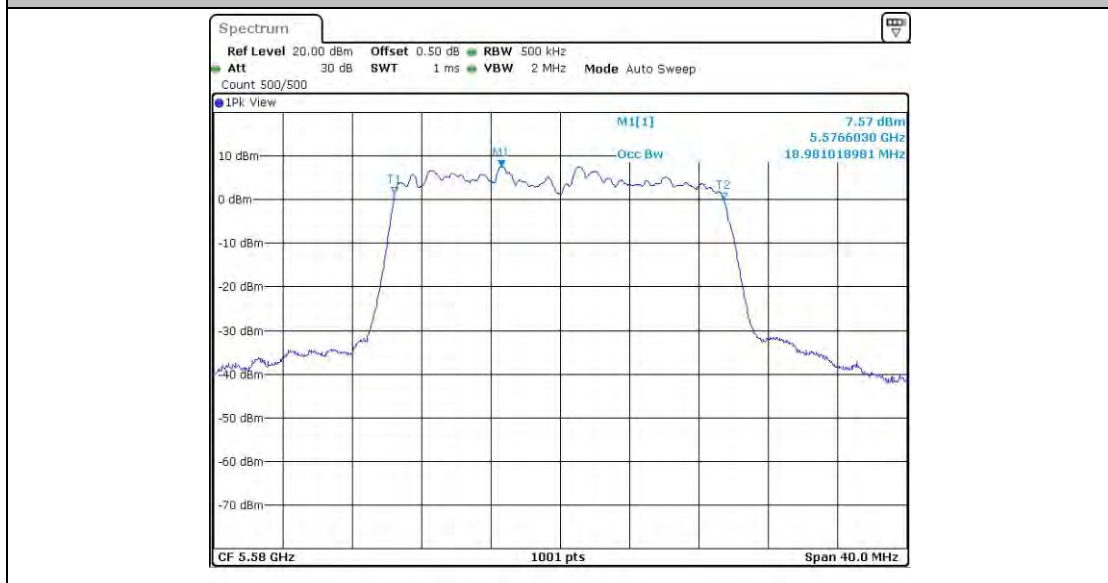
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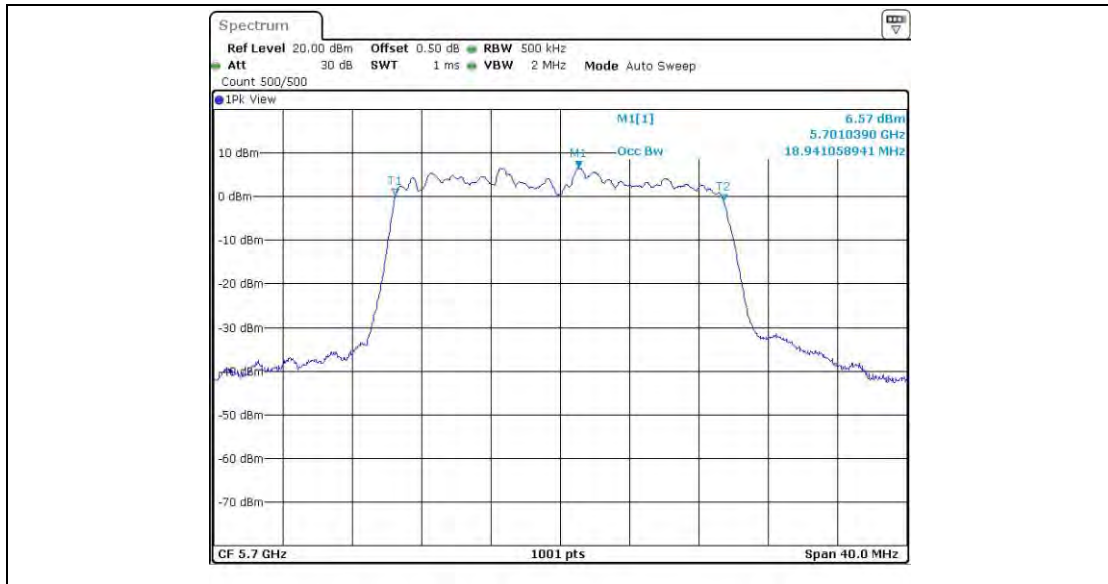
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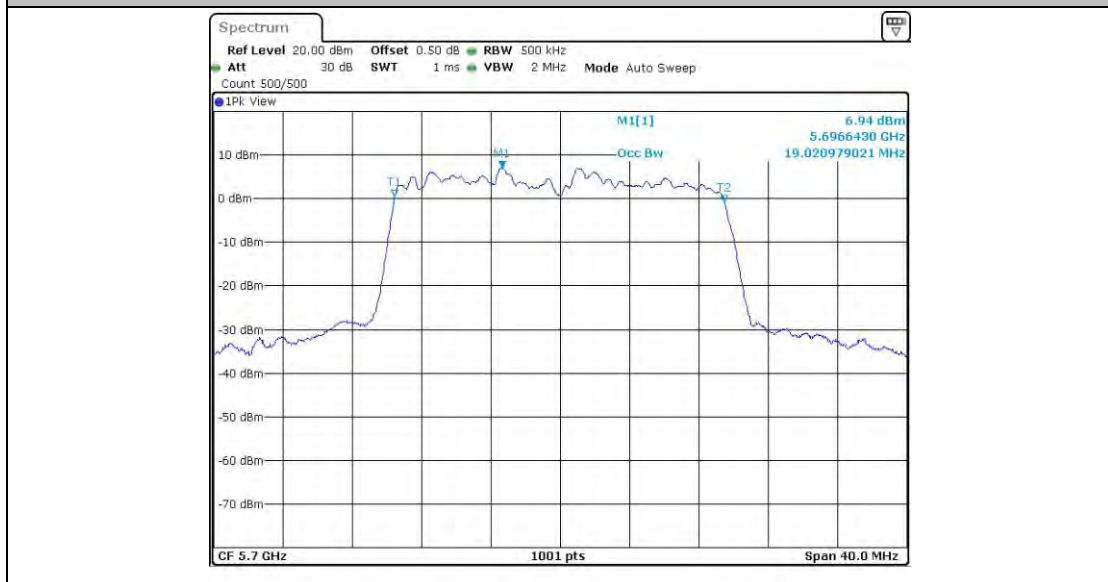
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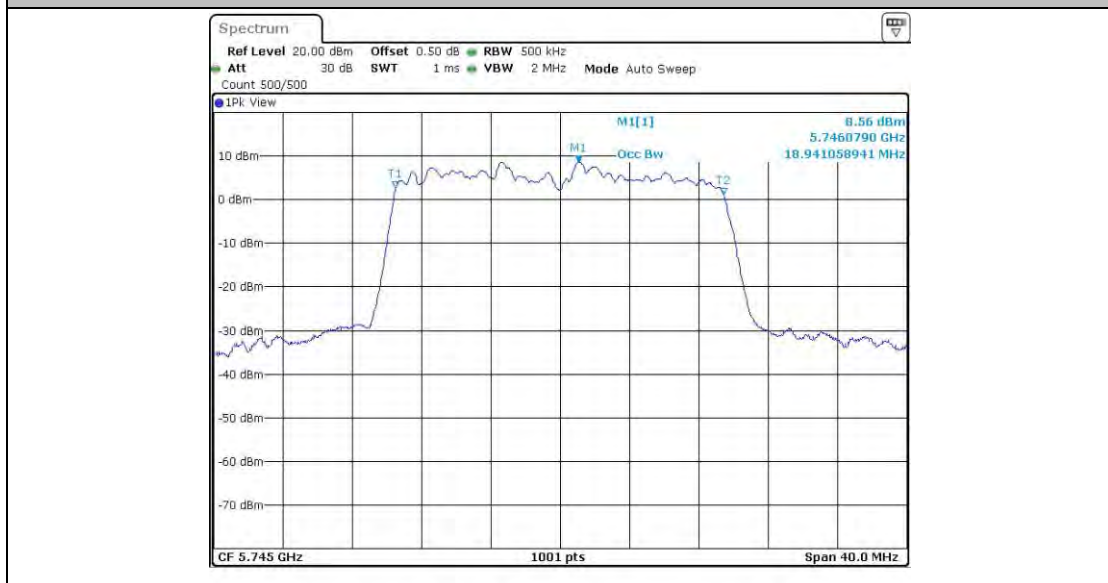
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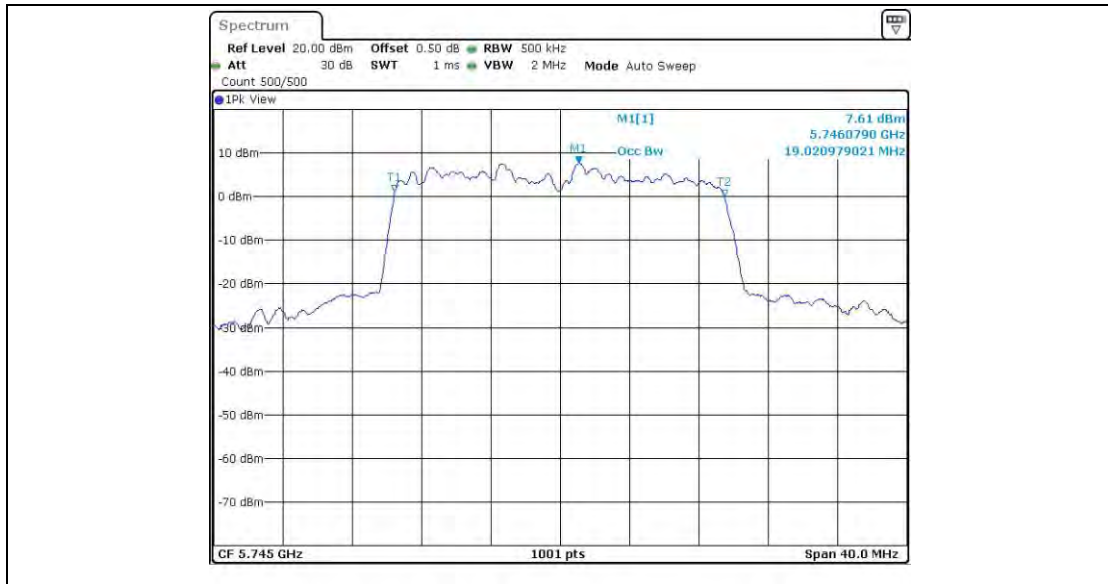
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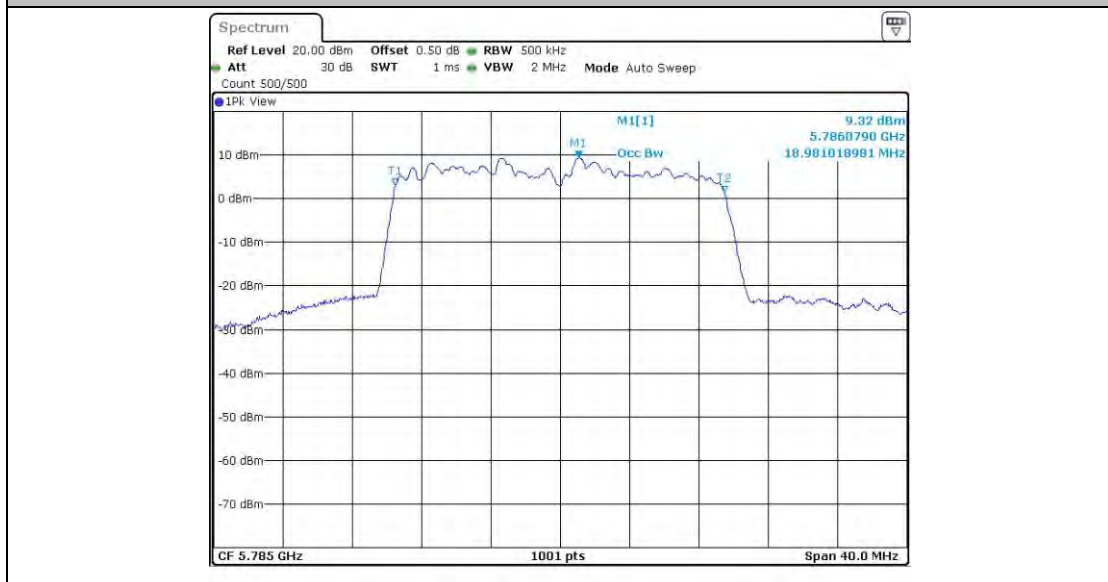
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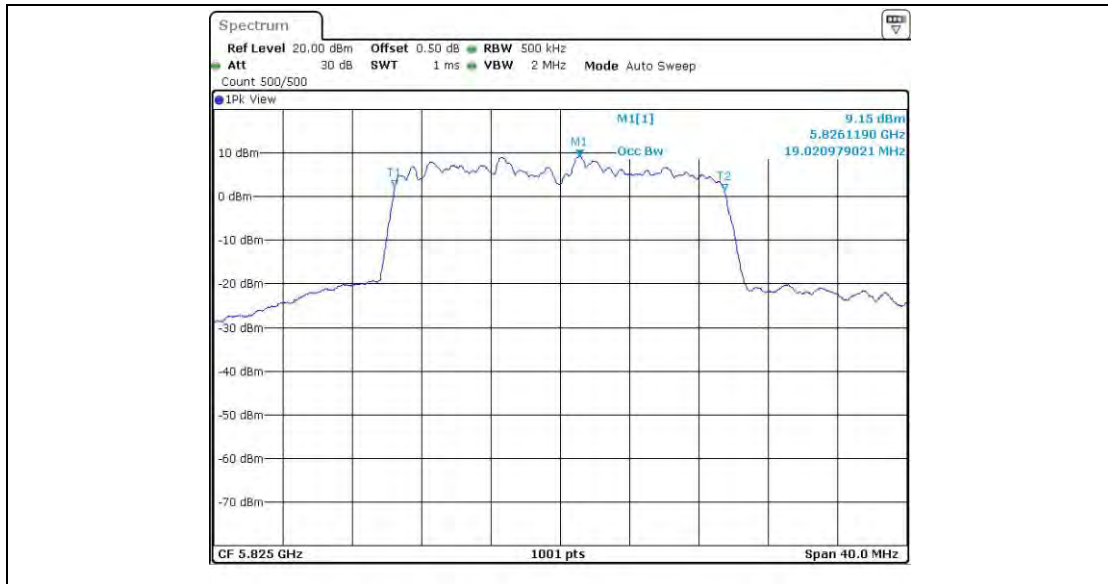
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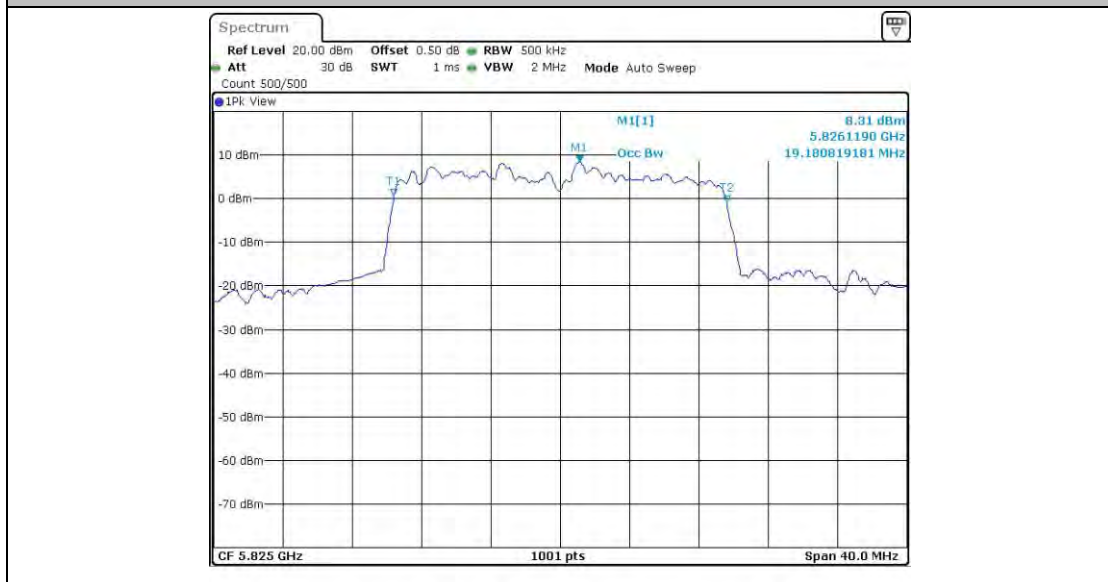
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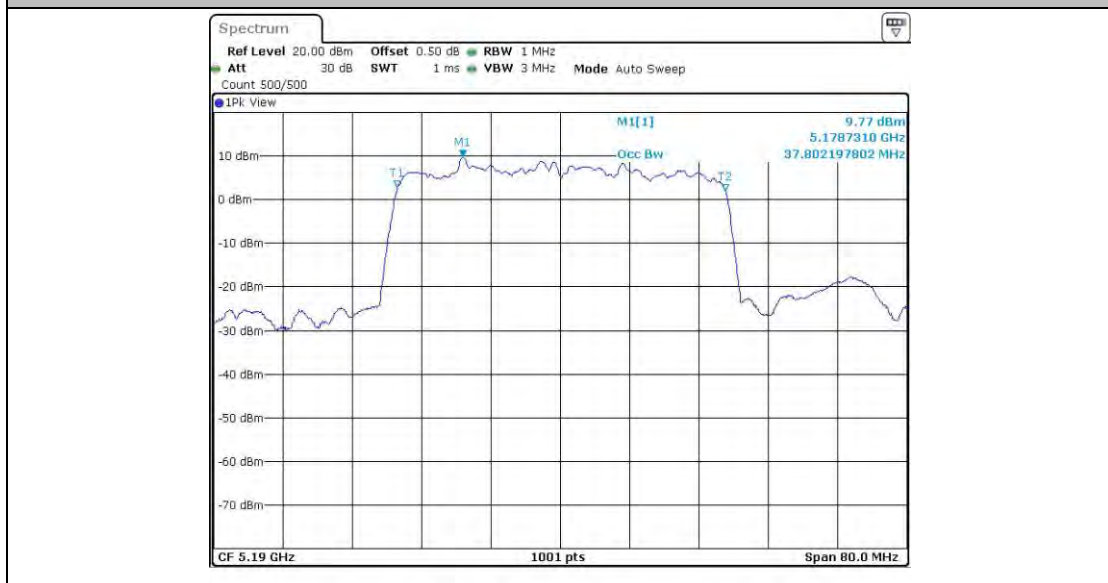
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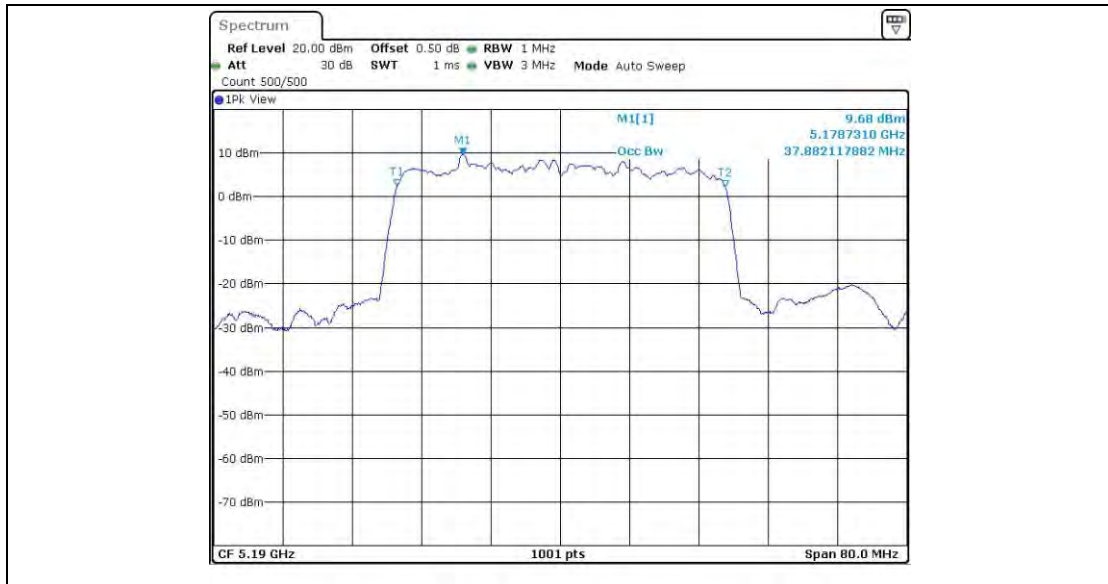
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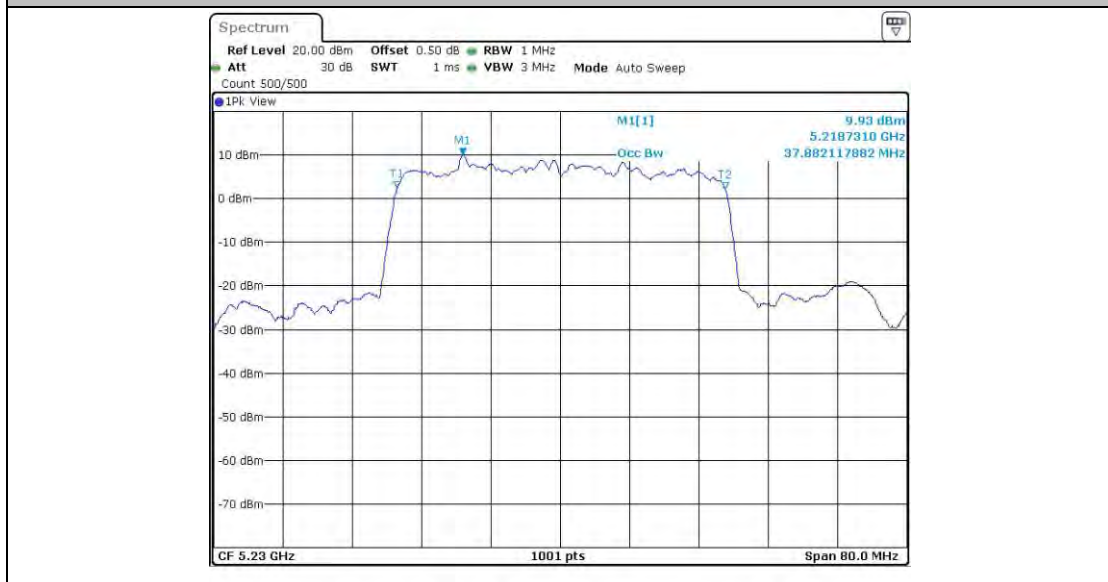
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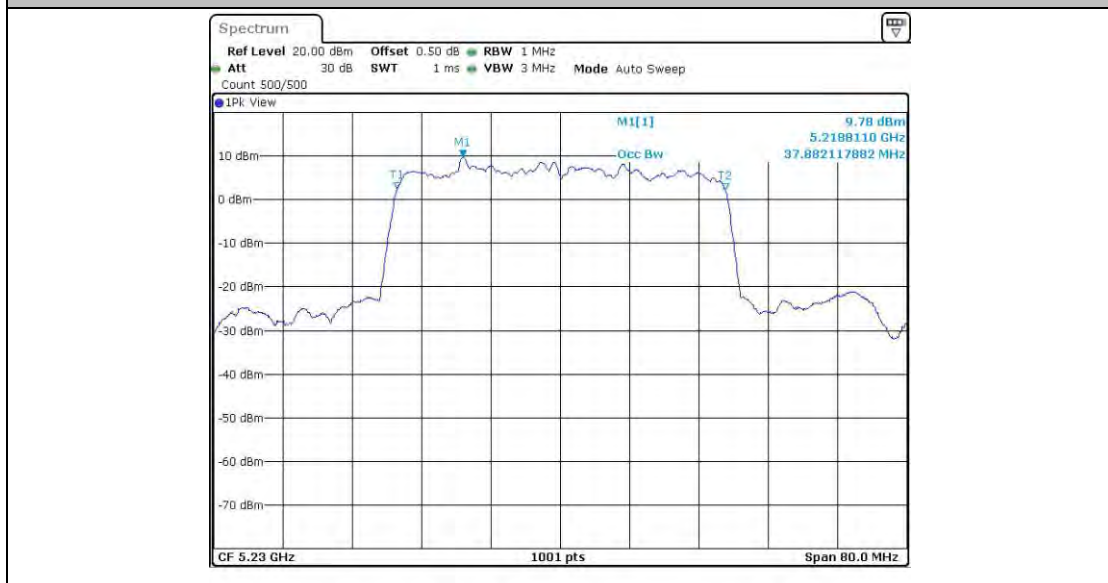
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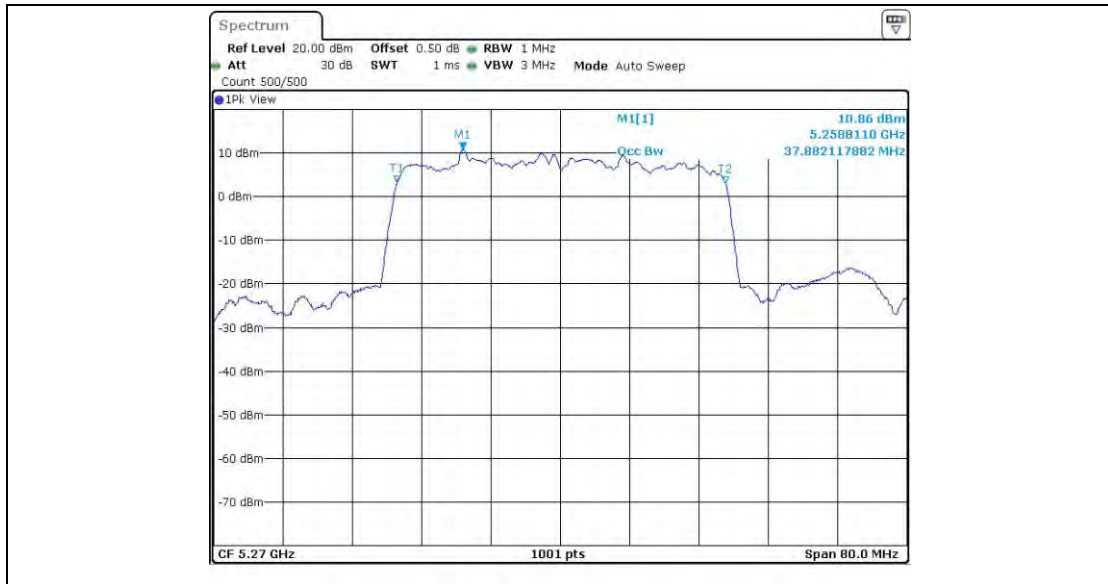
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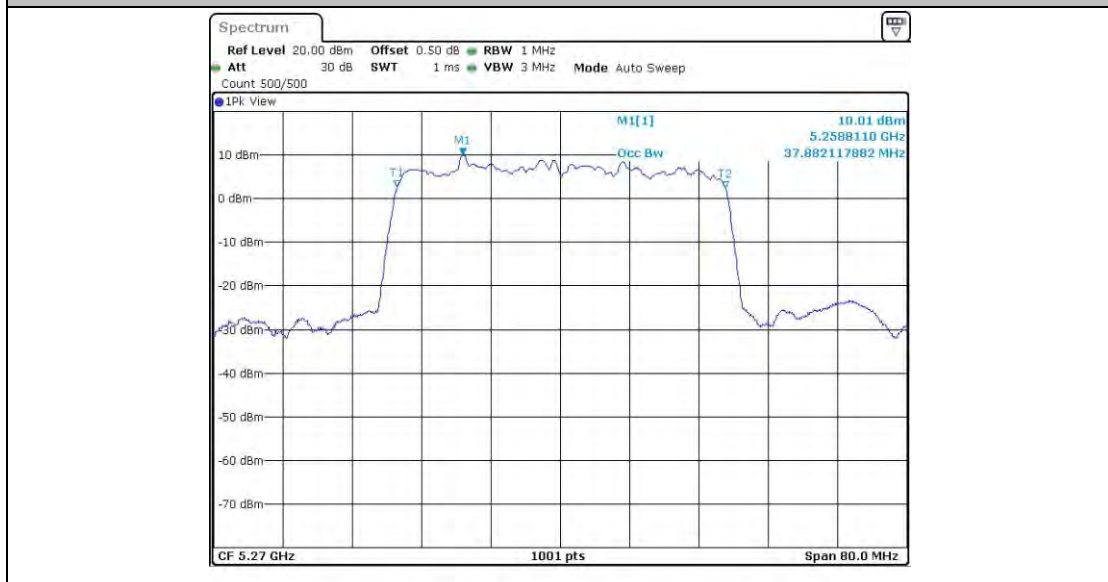
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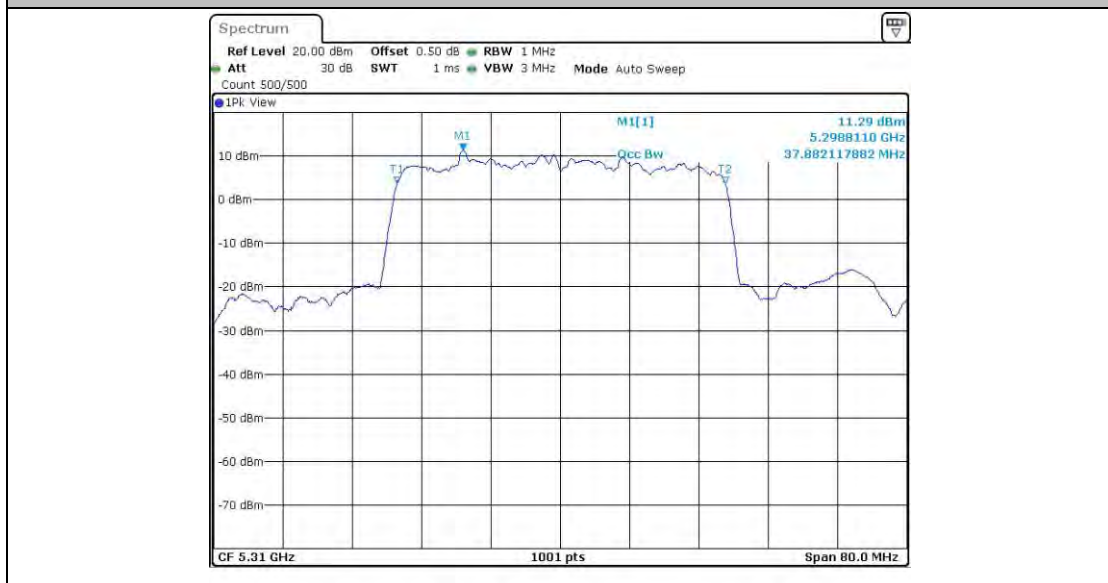
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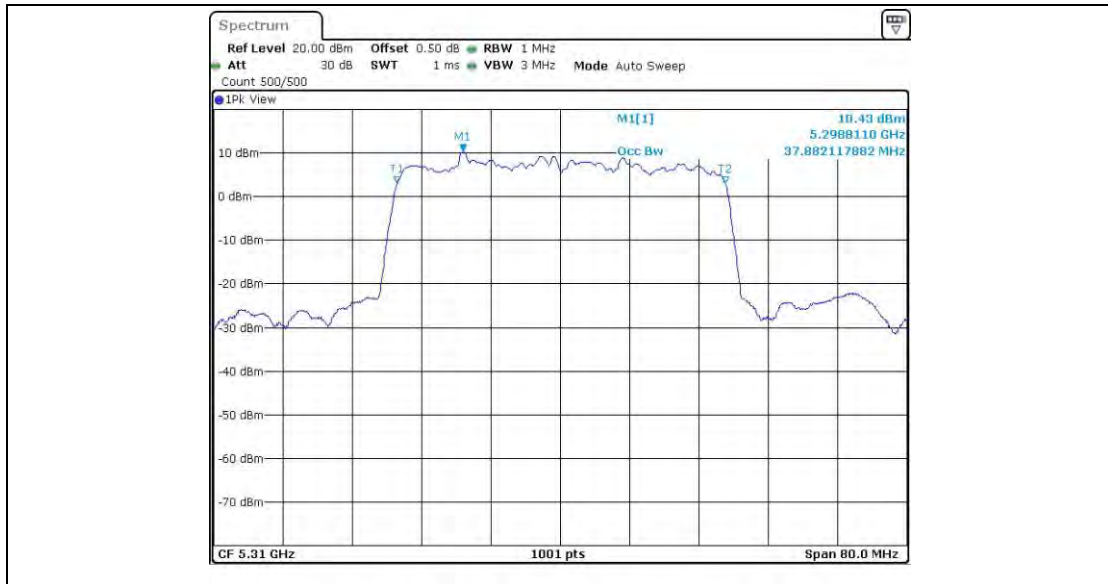
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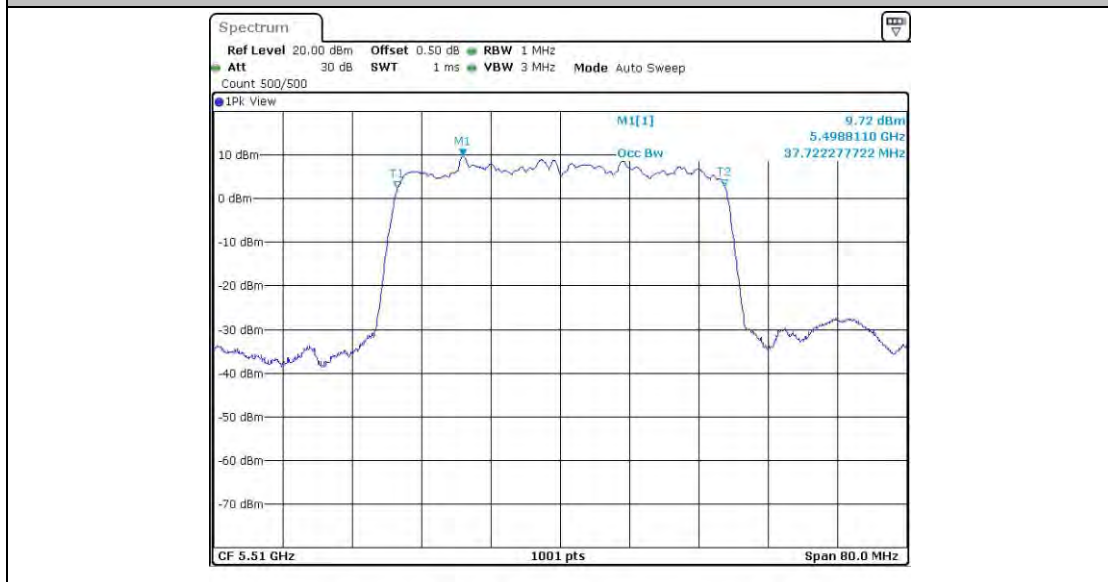
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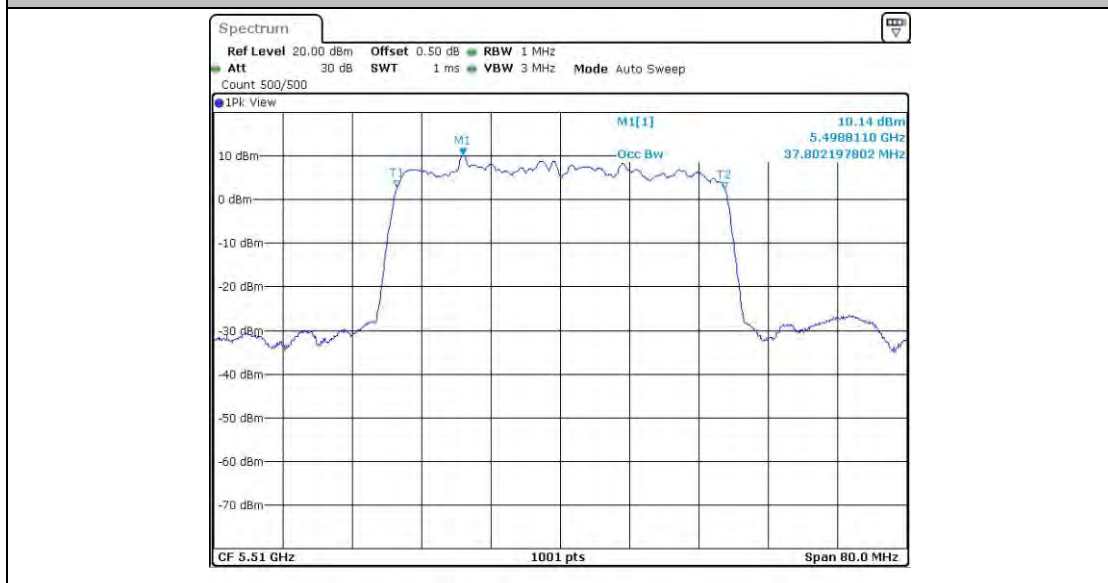
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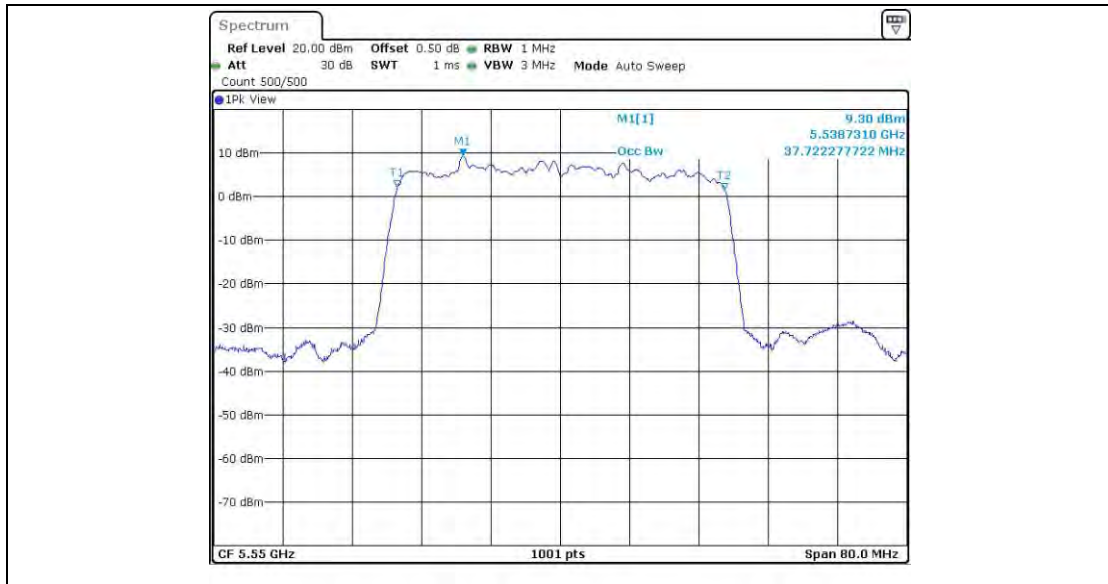
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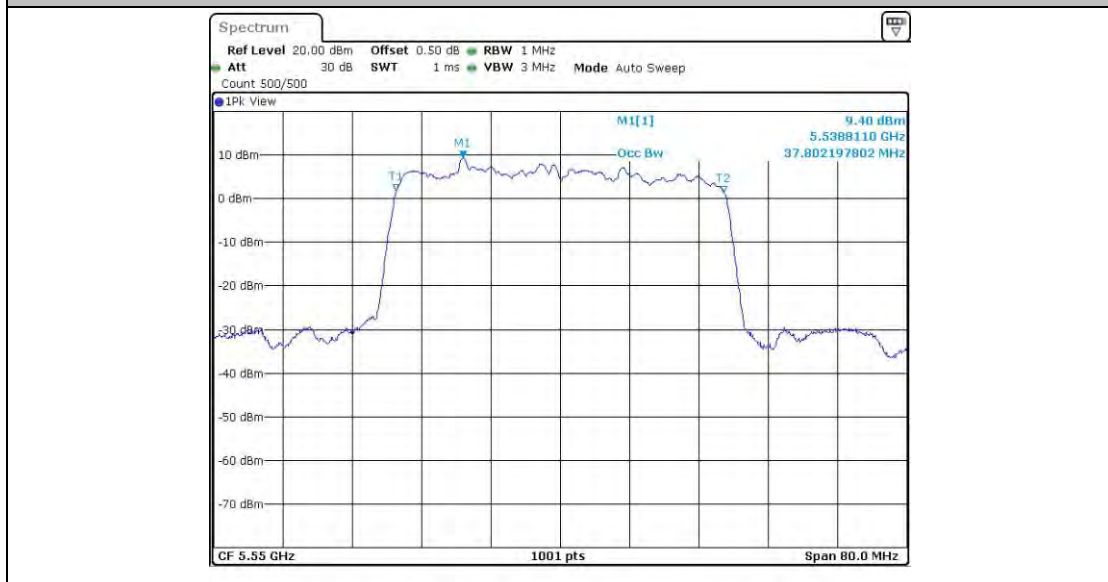
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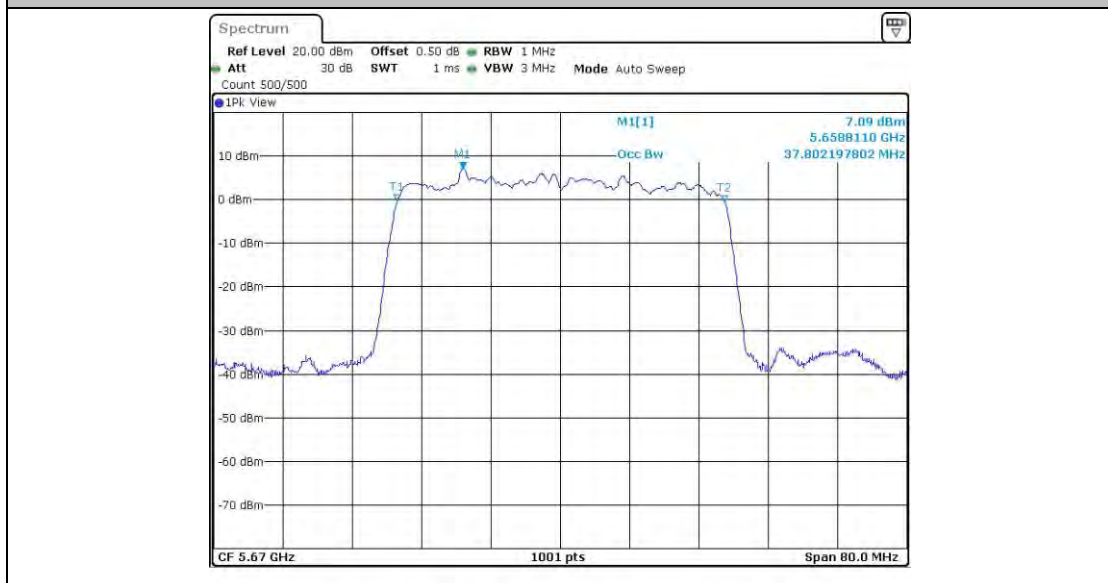
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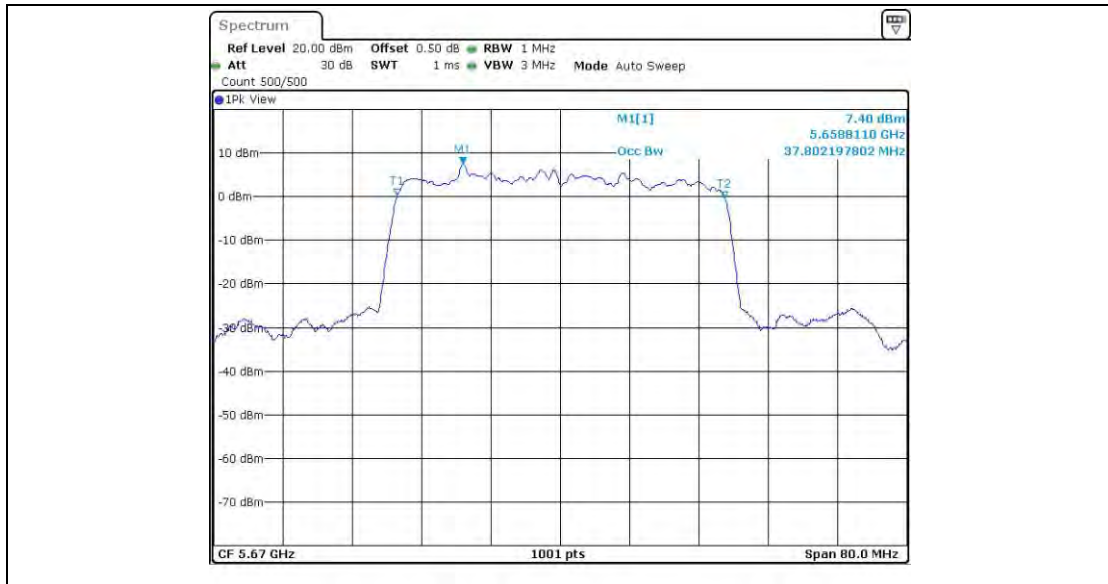
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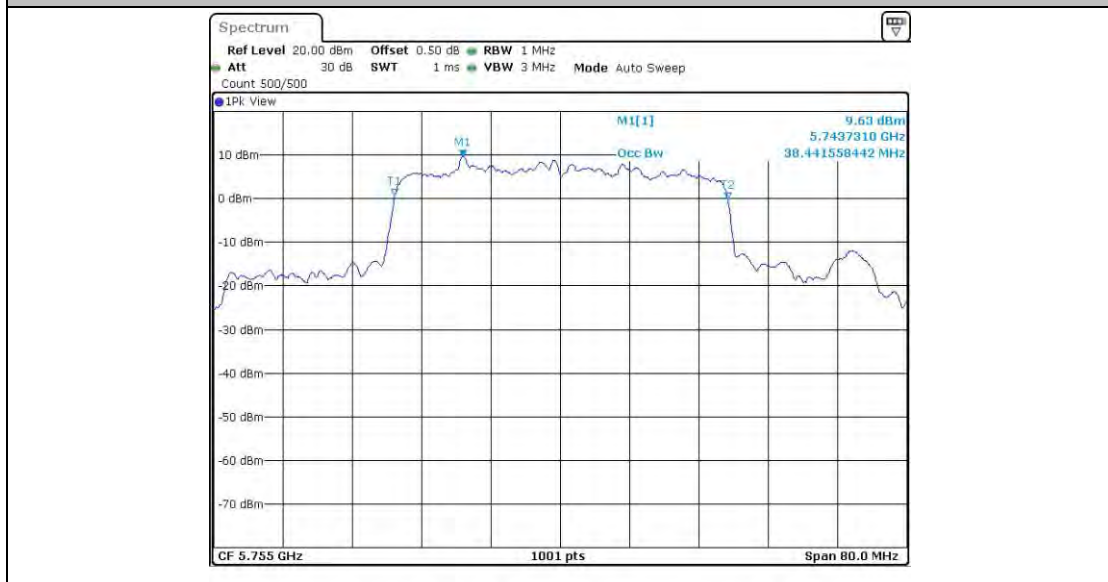
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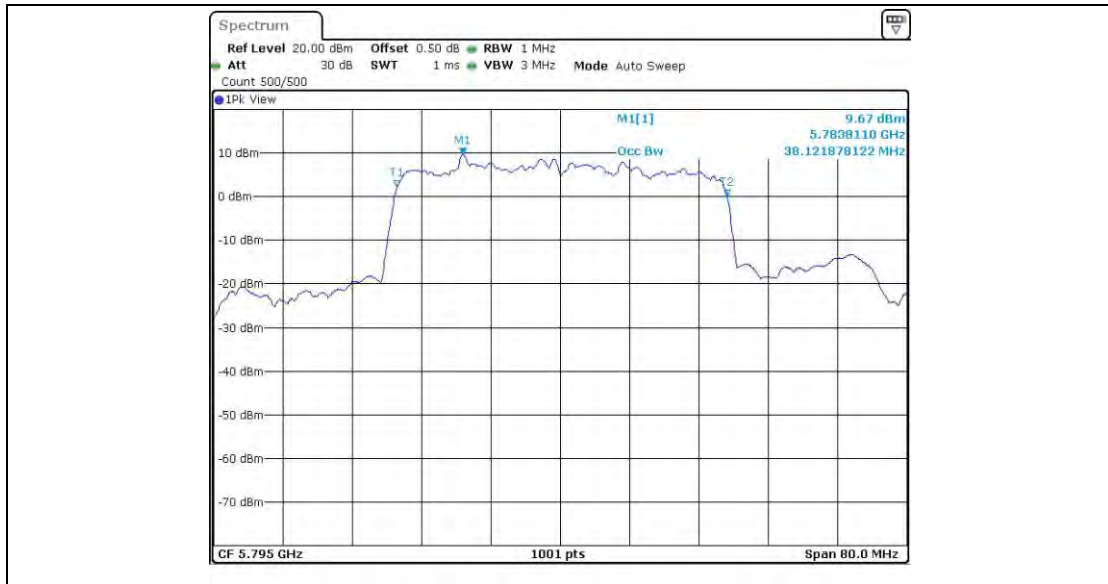
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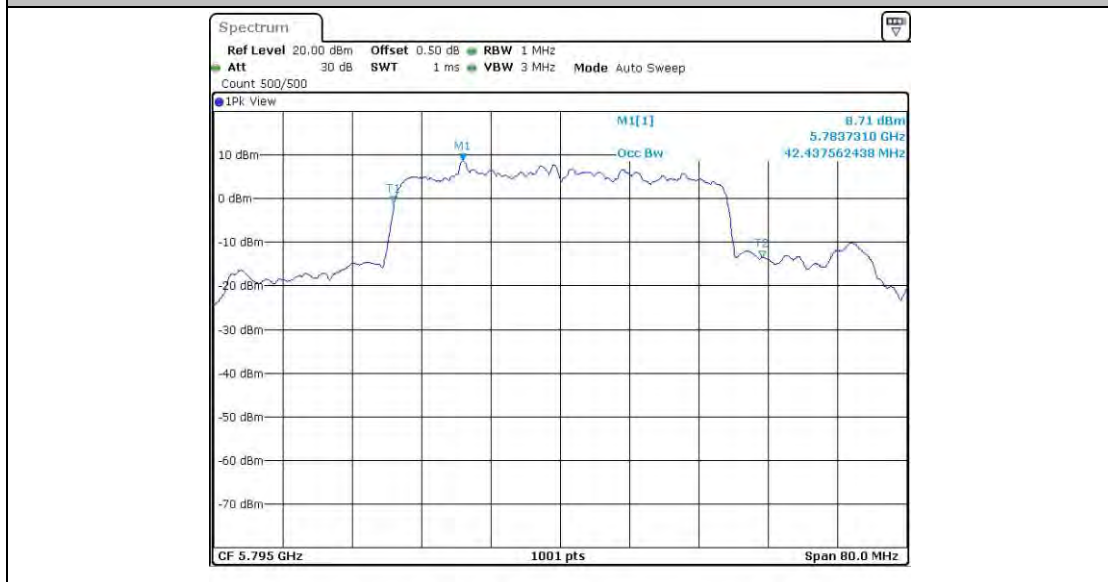
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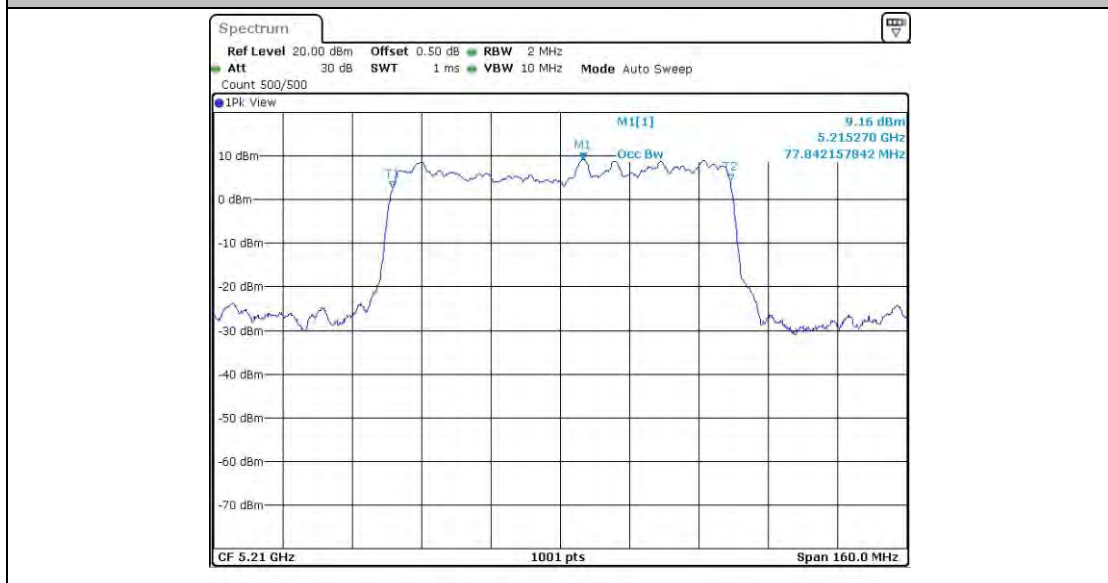
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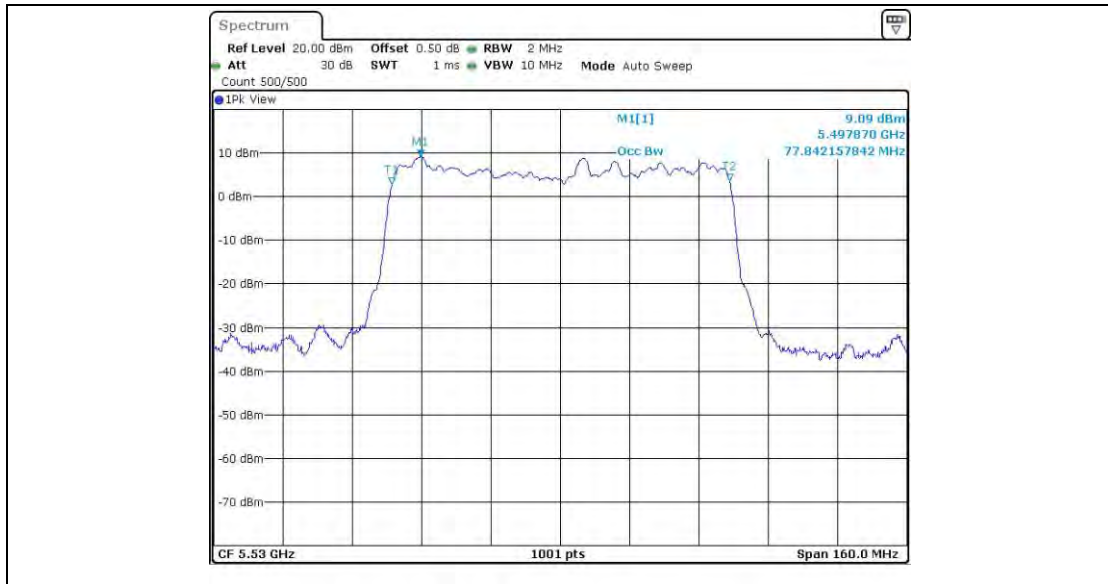
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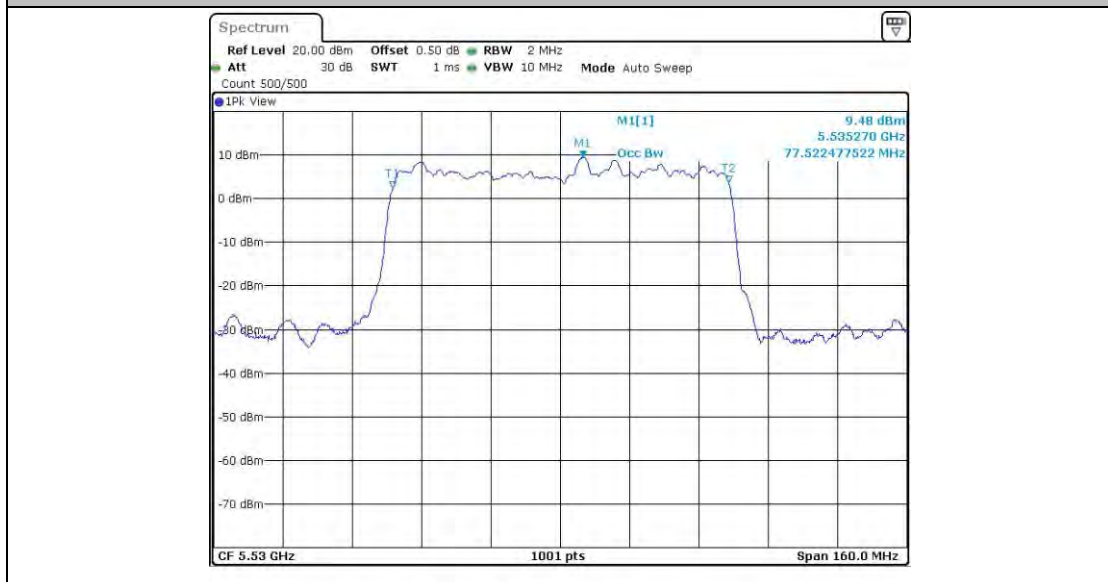
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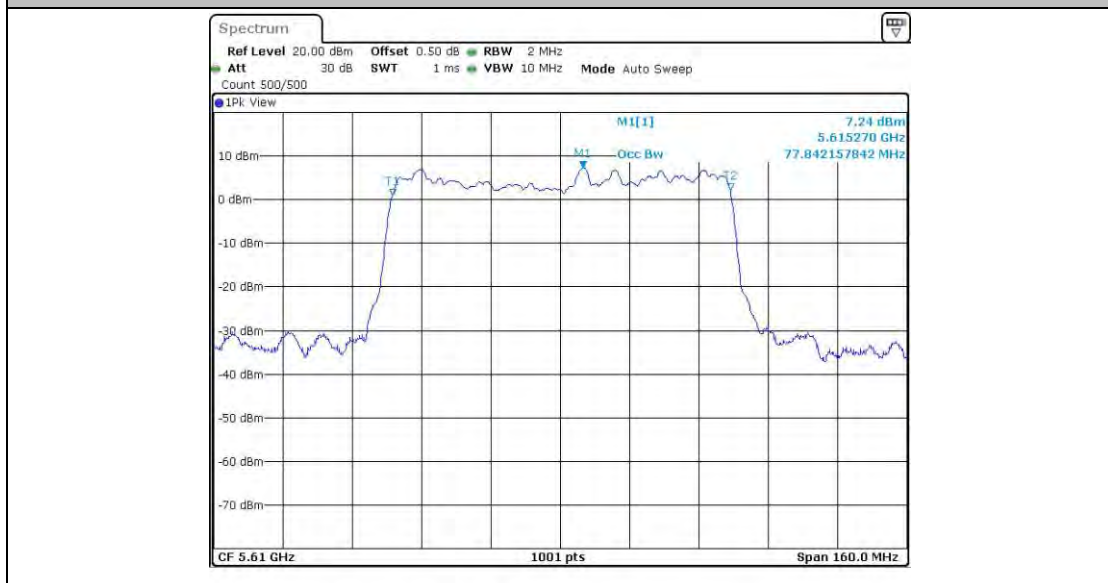
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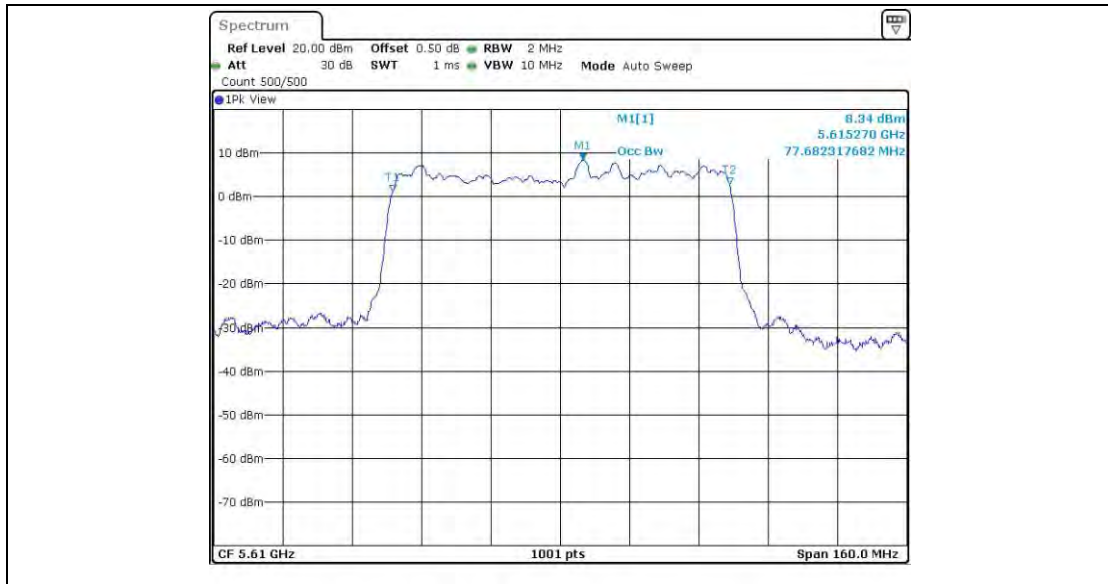
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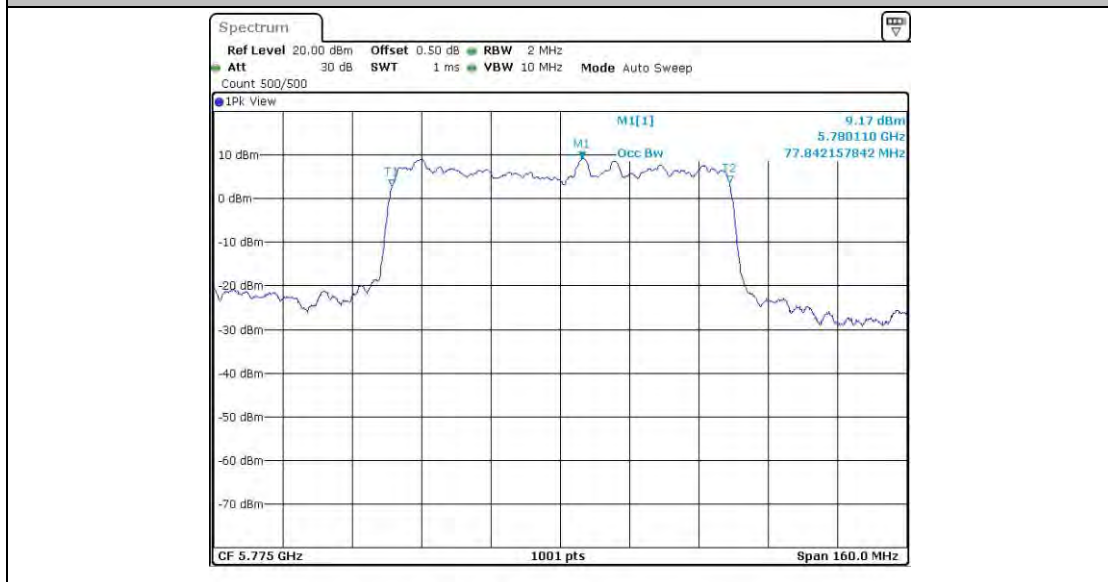
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11AX80SISO_Ant0_5610



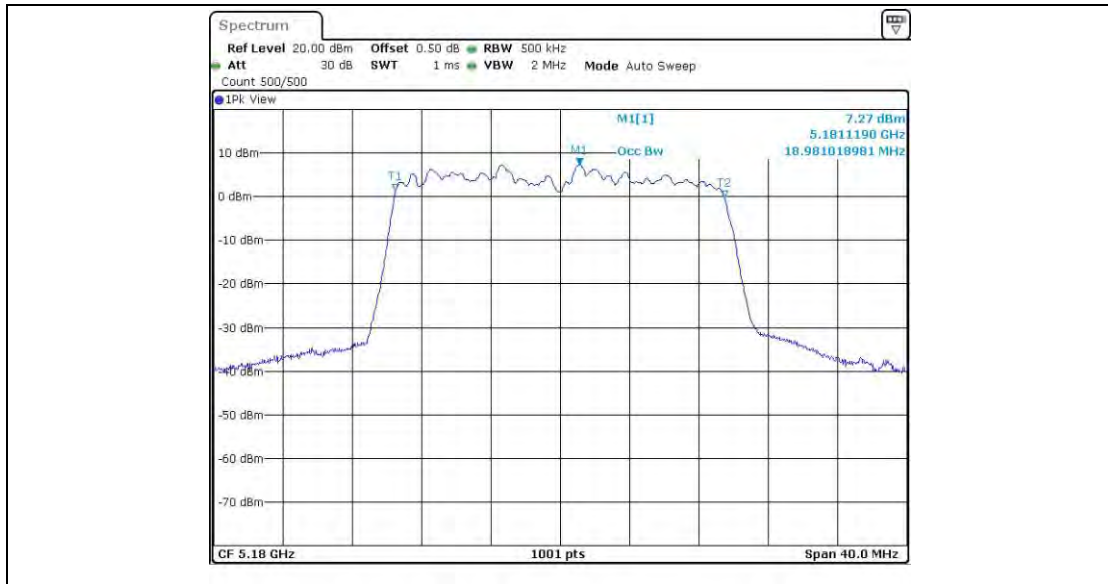
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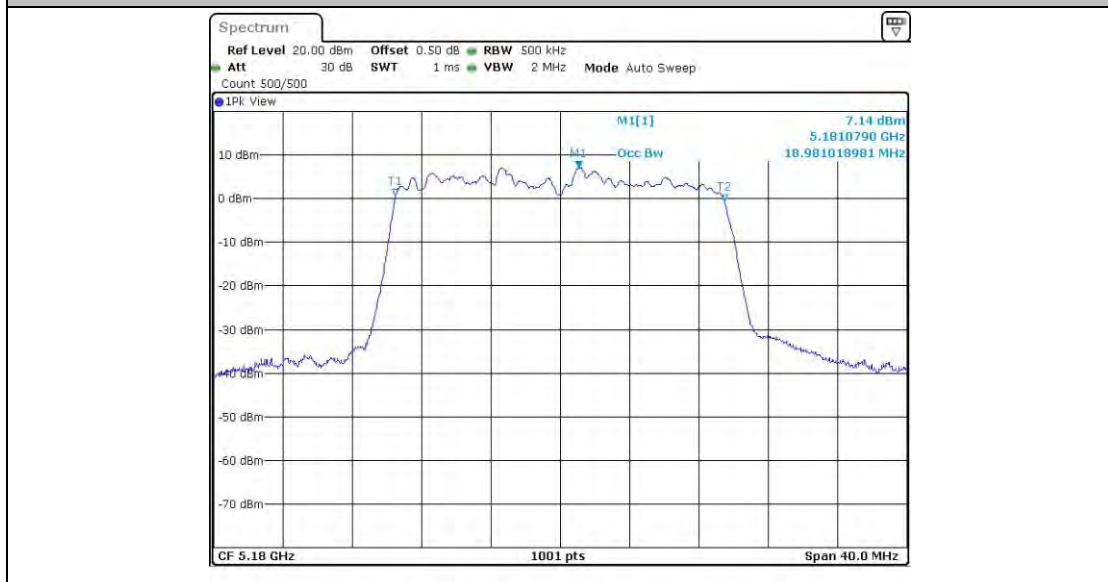
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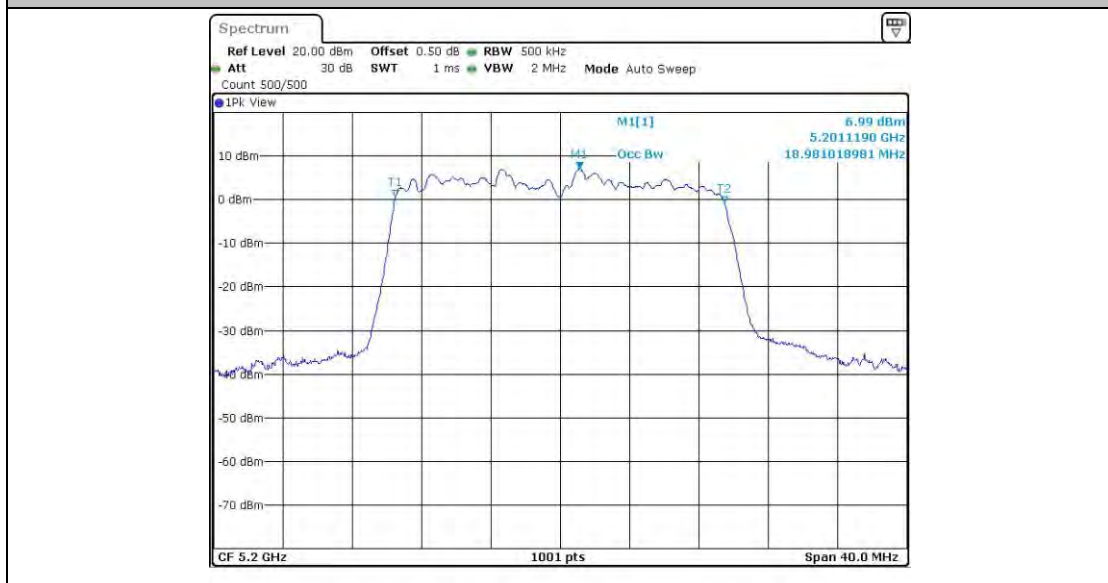
11AX80SISO_Ant1_5775



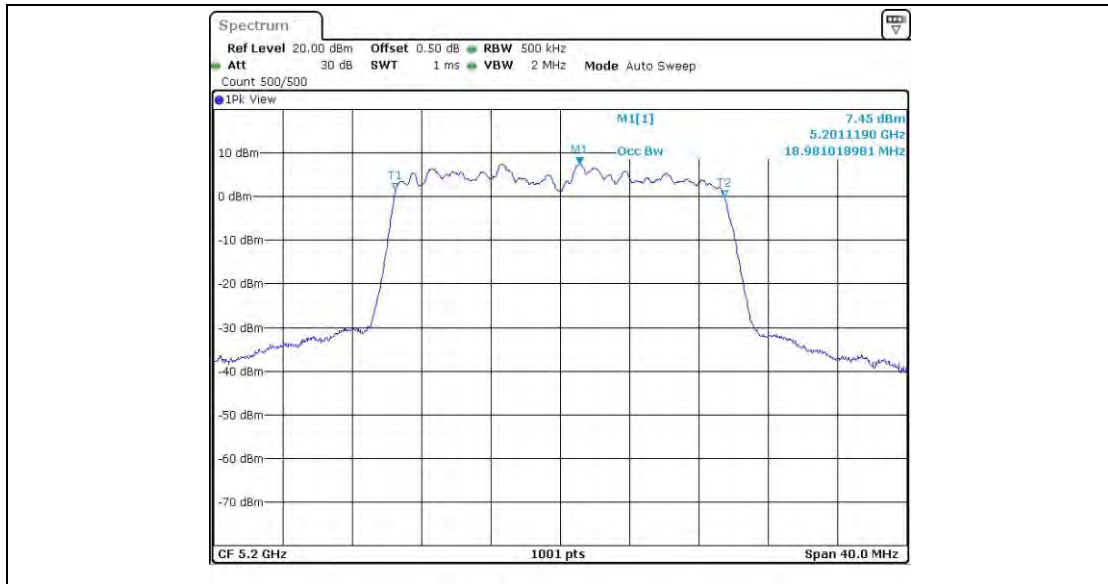
11N20MIMO_Ant0_5180



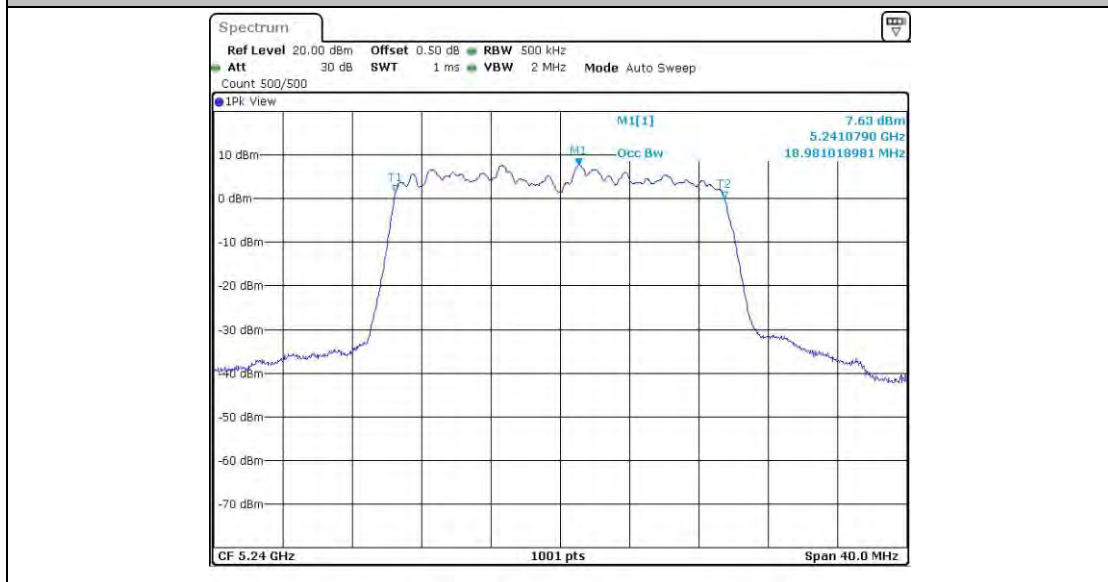
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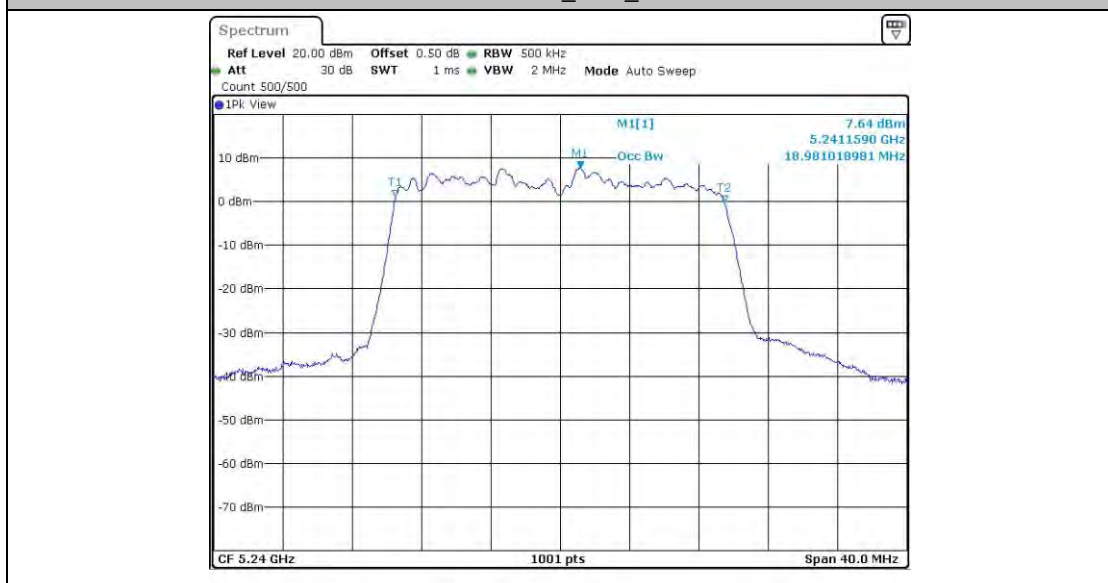
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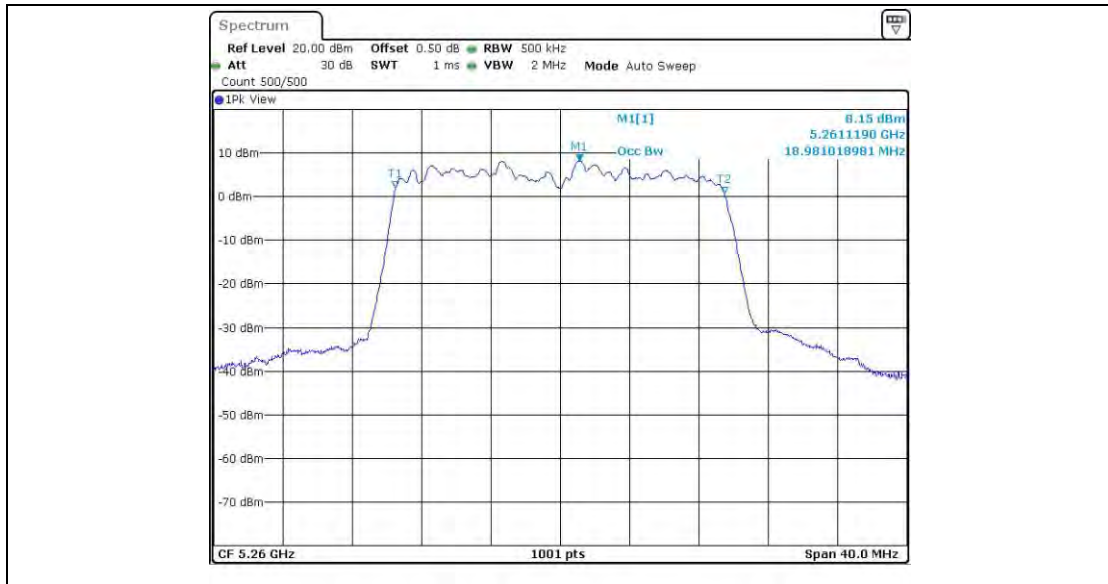
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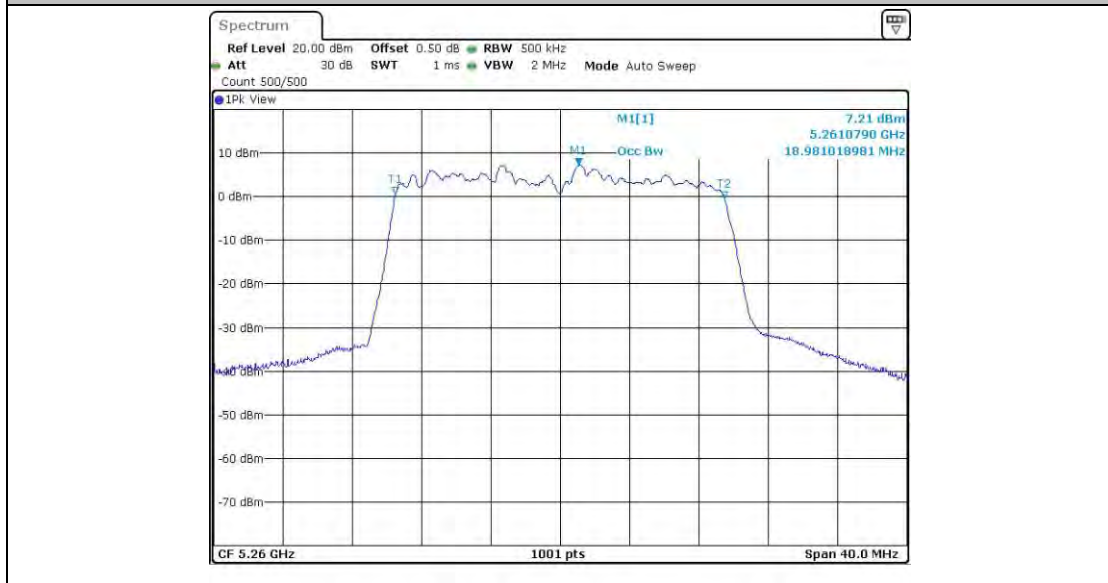
11N20MIMO_Ant0_5240



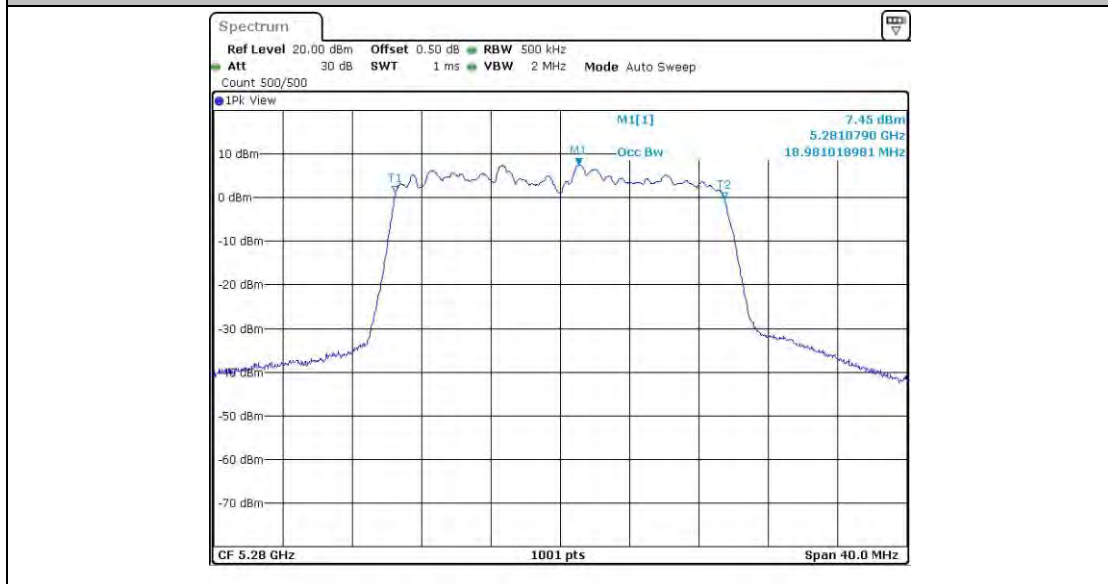
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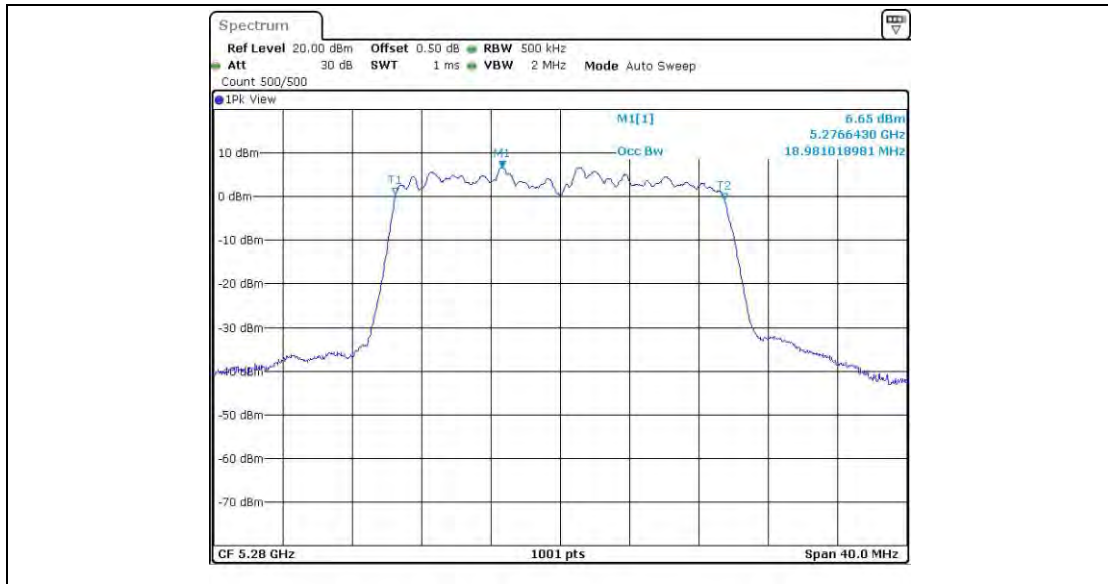
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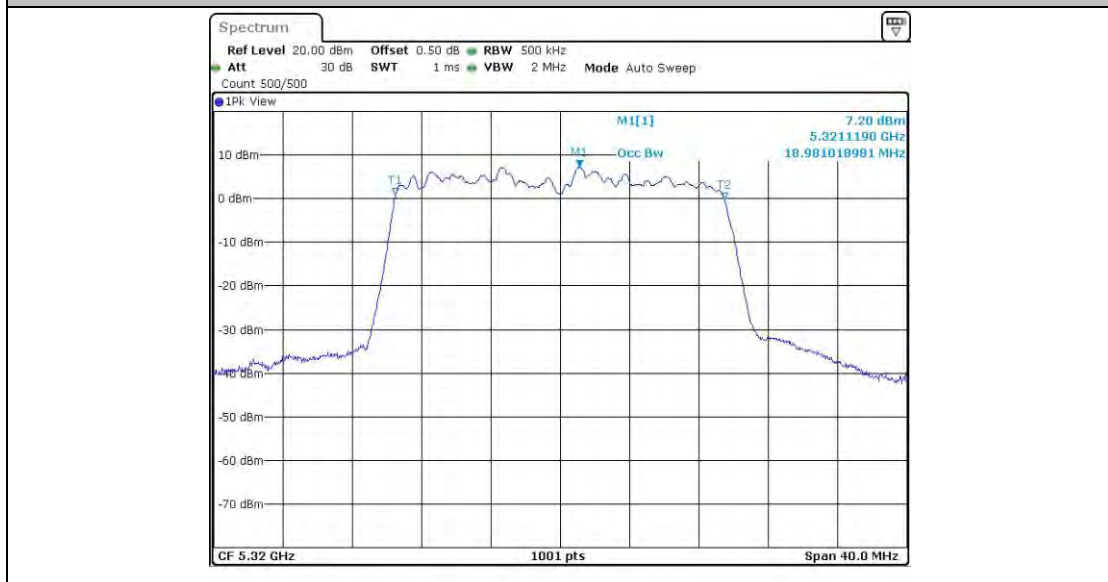
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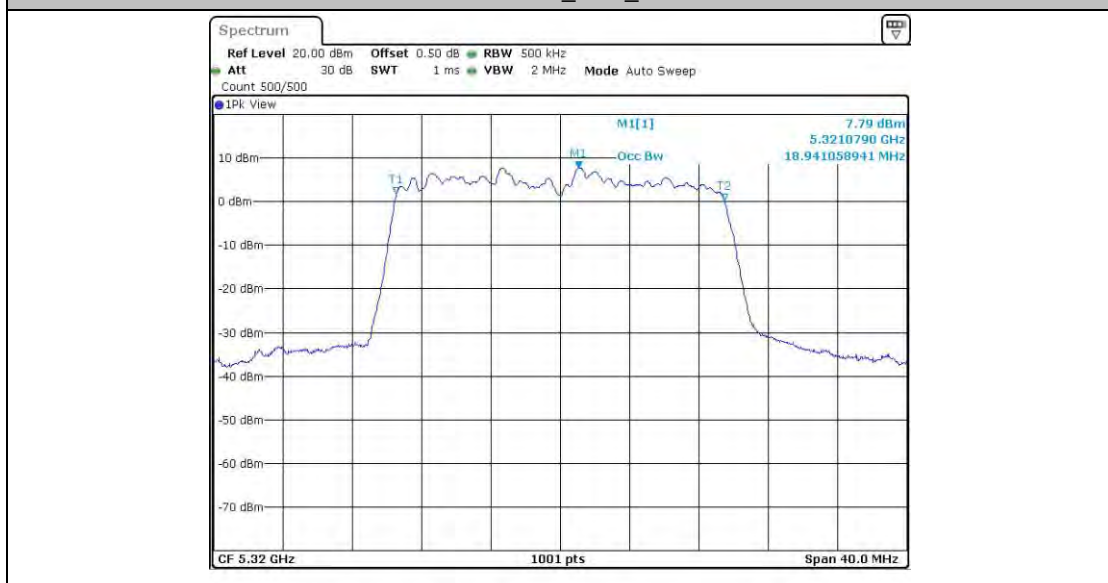
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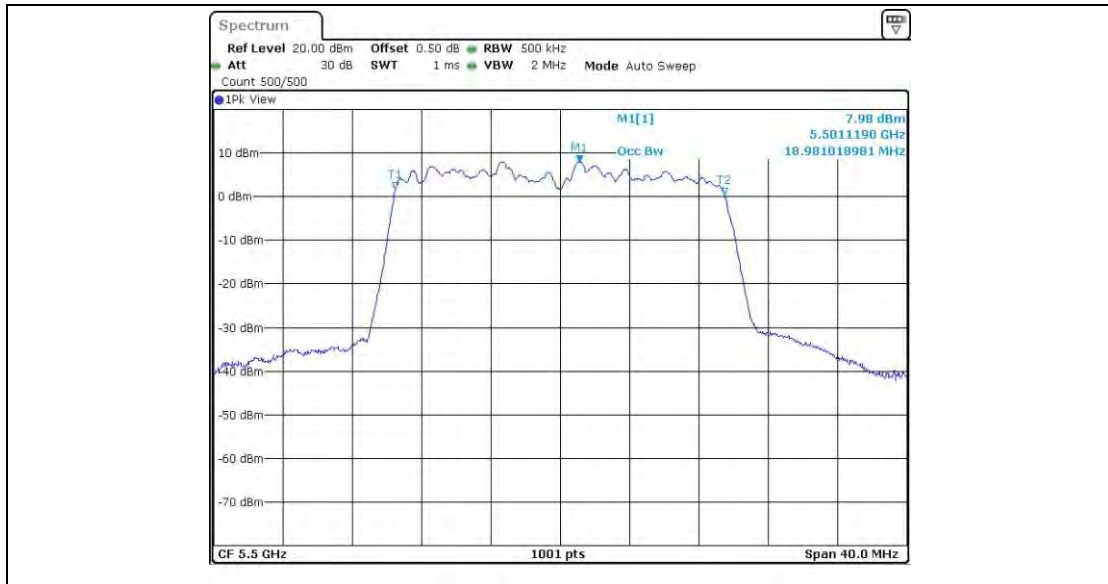
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11N20MIMO_Ant0_5320



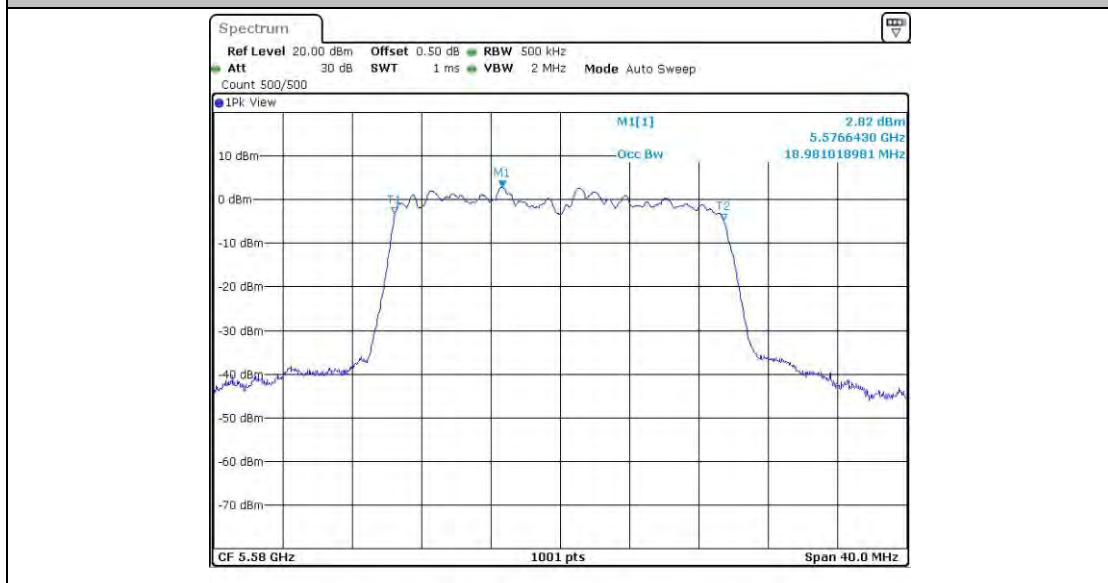
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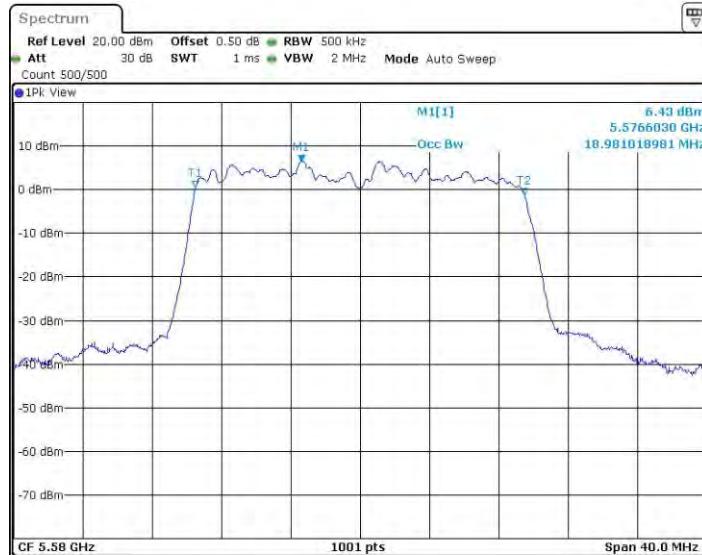
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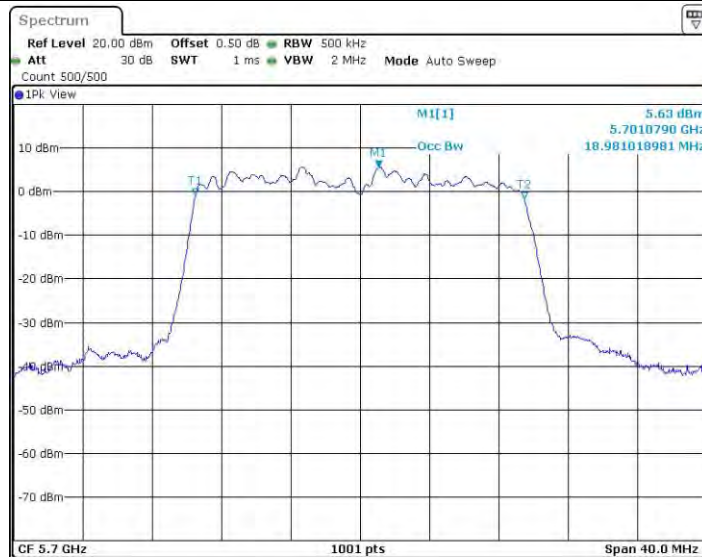
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11N20MIMO_Ant0_5580



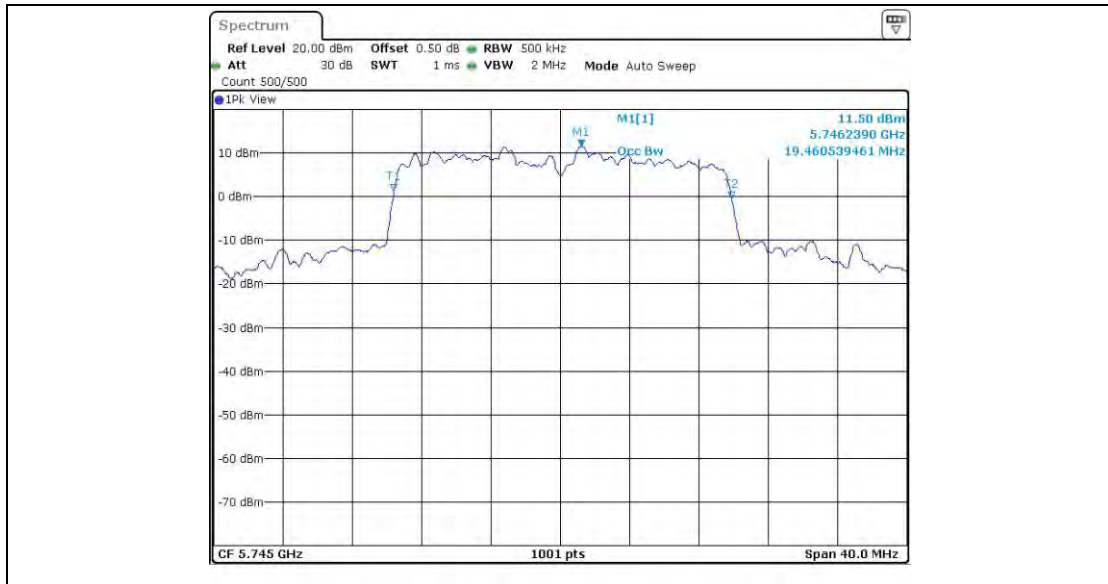
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11N20MIMO_Ant0_5700



11N20MIMO_Ant1_5700



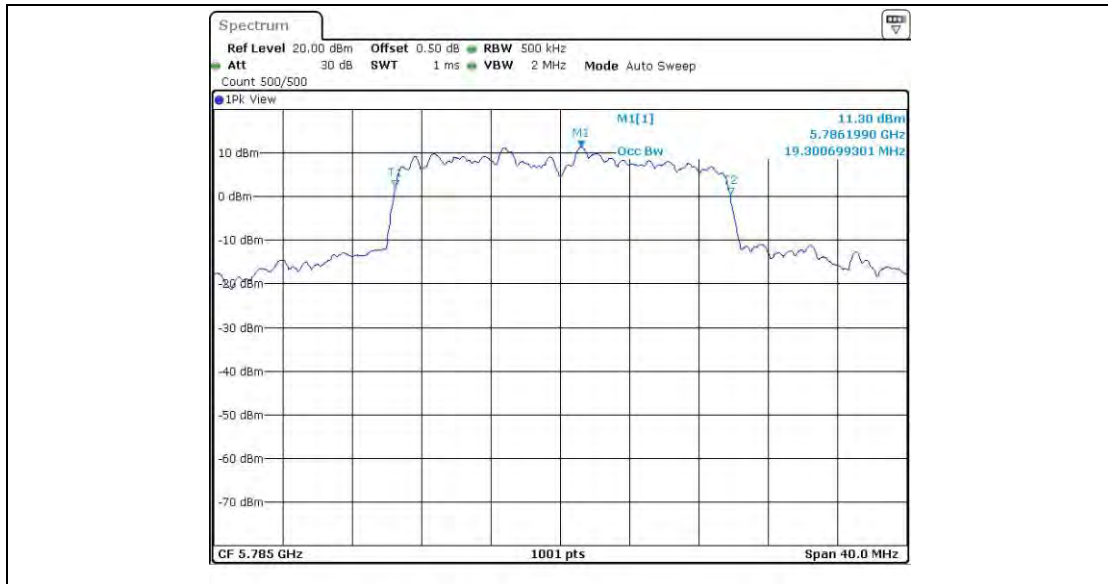
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11N20MIMO_Ant1_5745



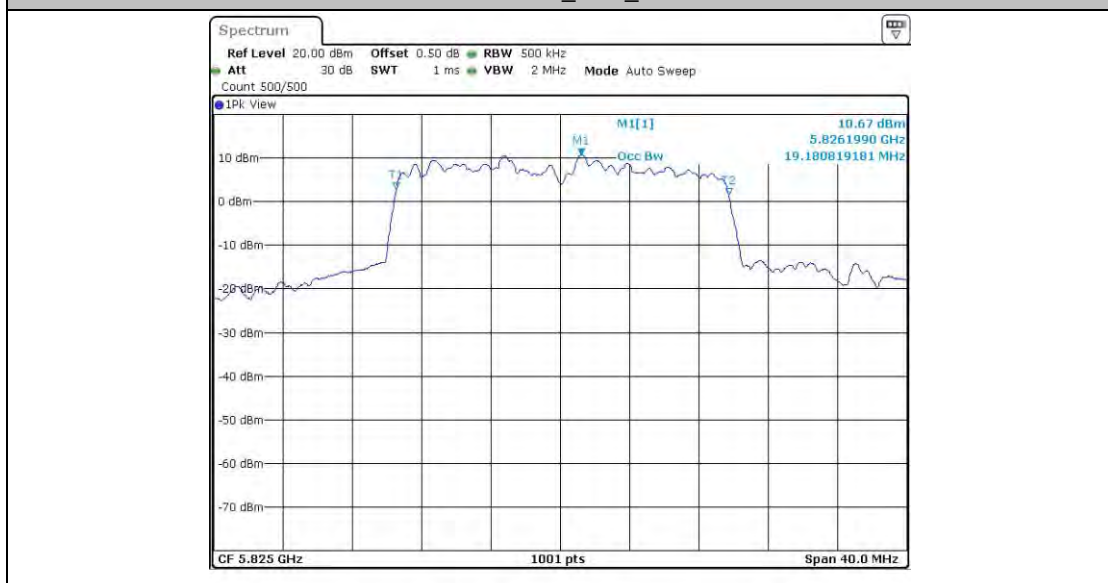
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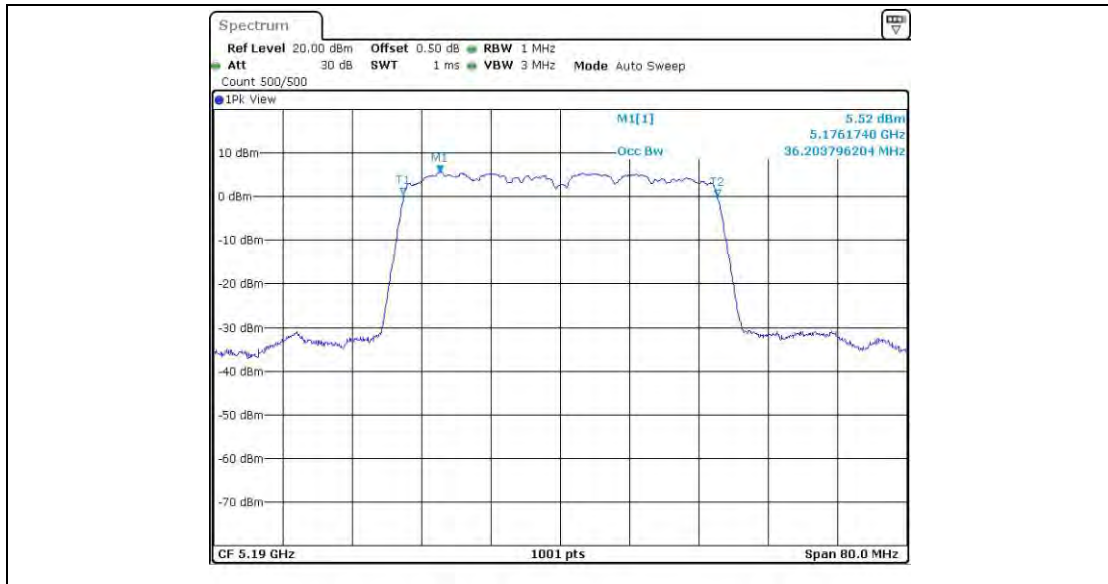
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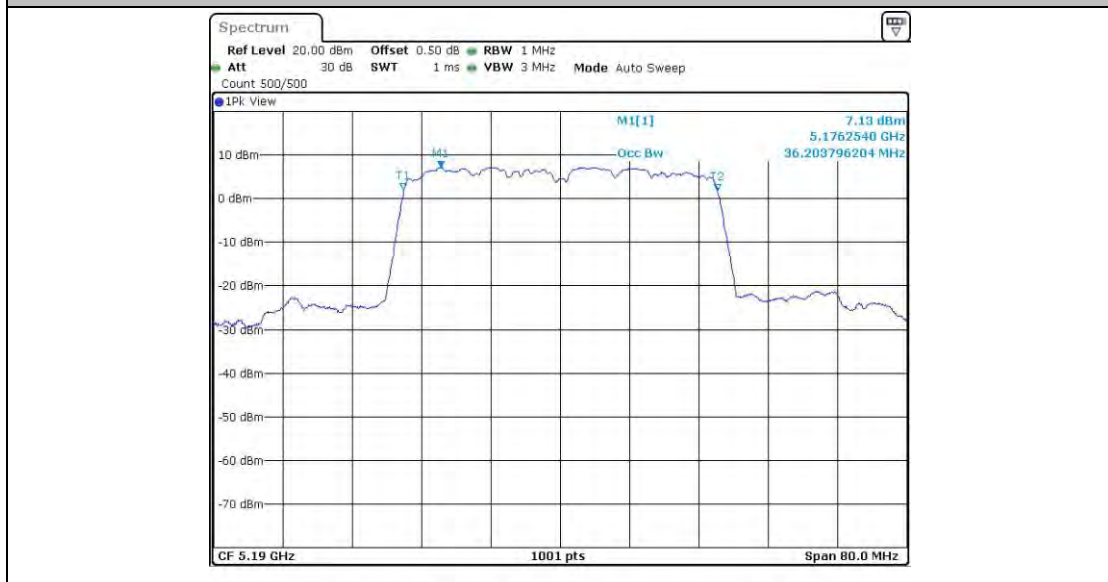
11N20MIMO_Ant0_5825



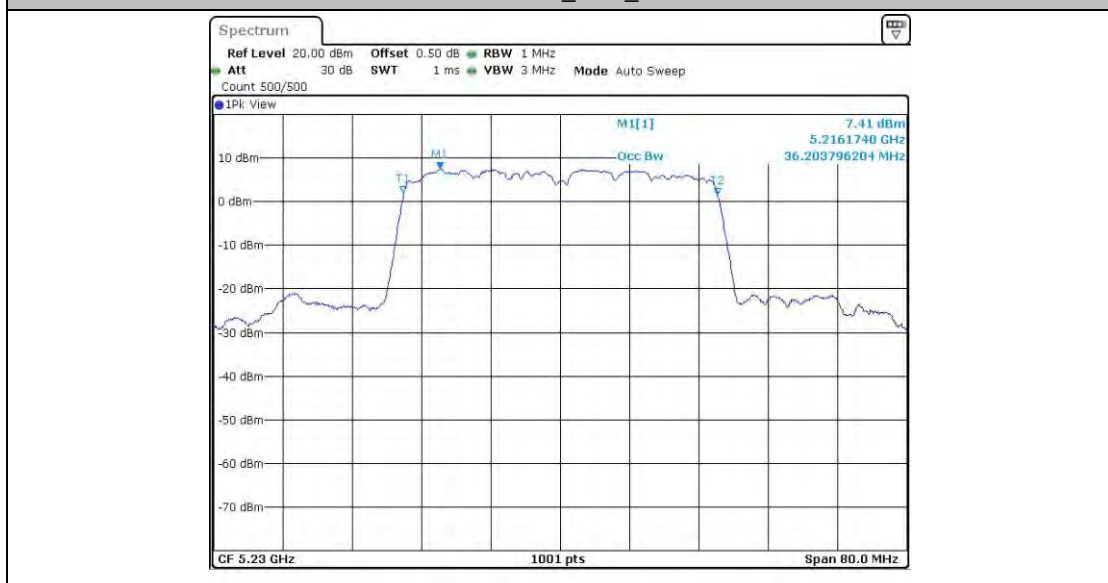
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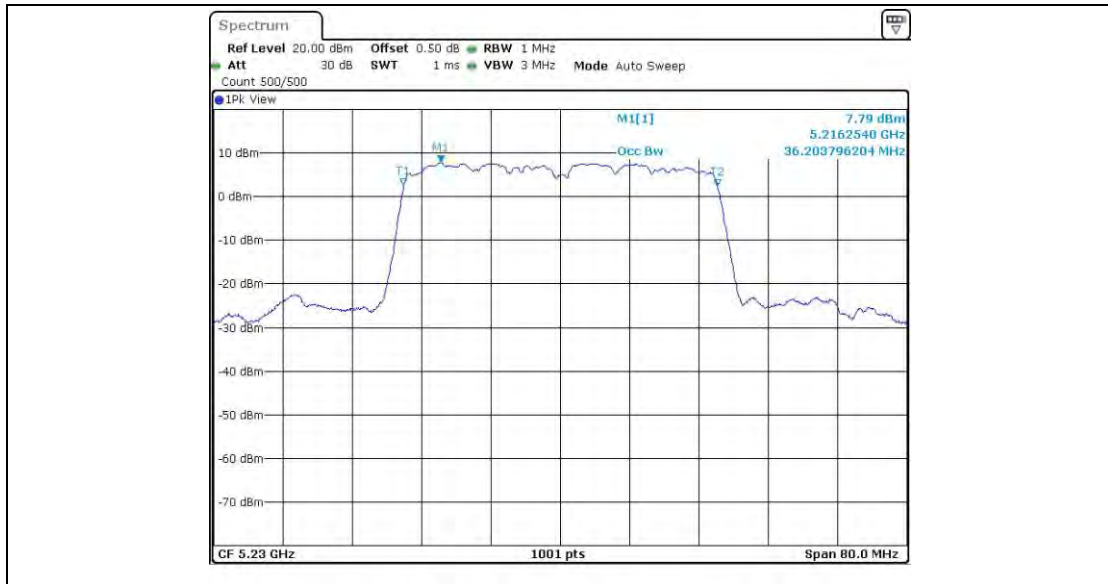
11N40MIMO_Ant0_5190



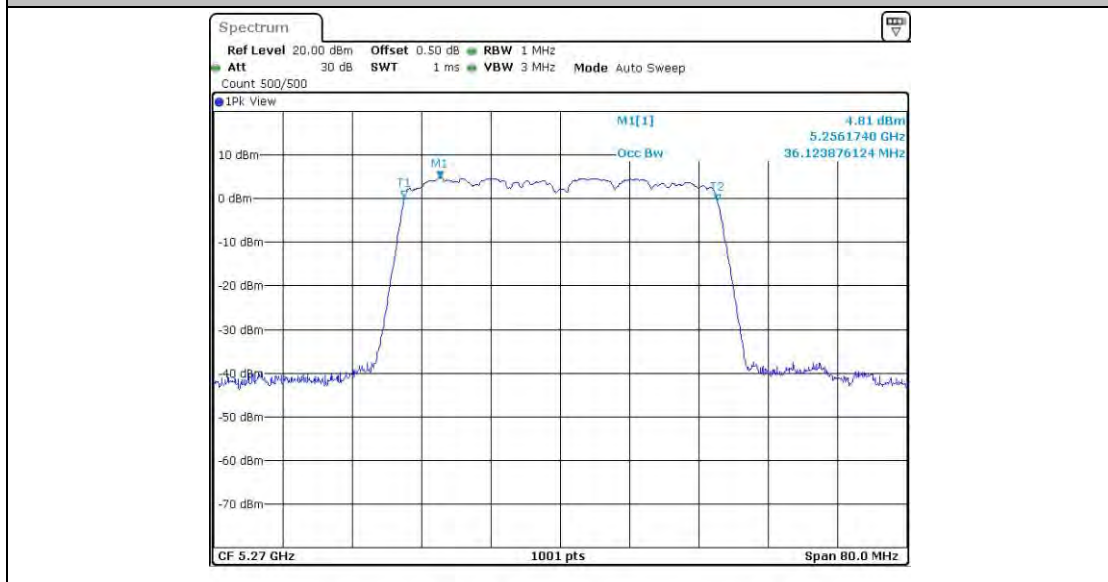
11N40MIMO_Ant1_5190



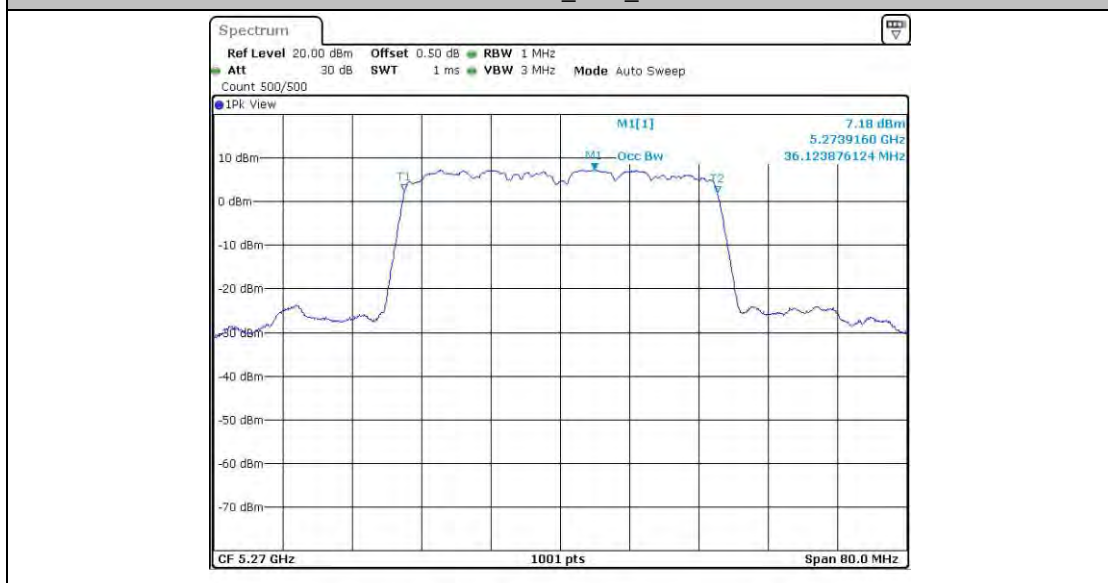
11N40MIMO_Ant0_5230



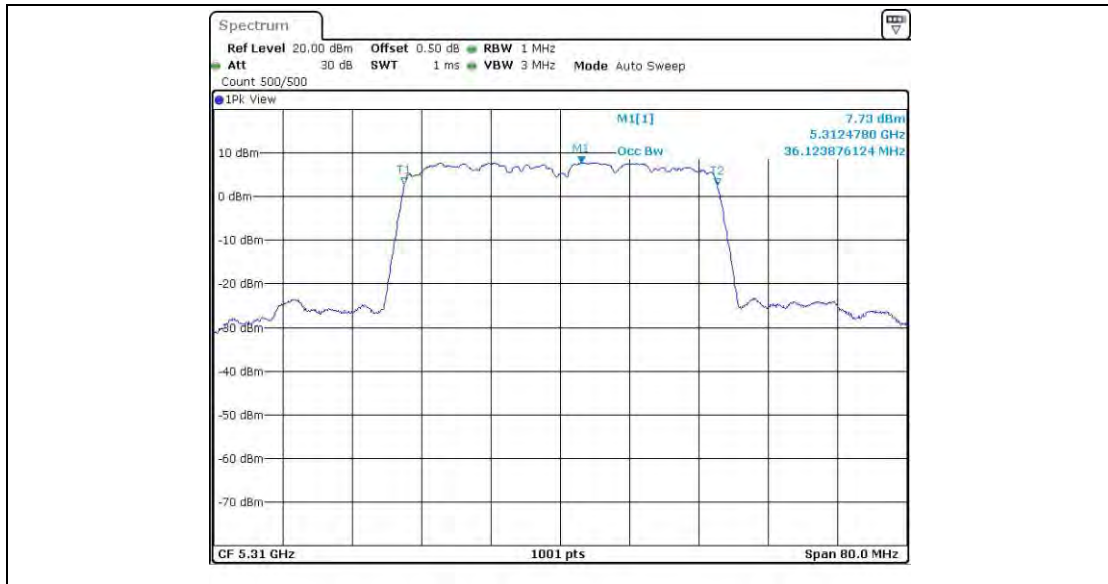
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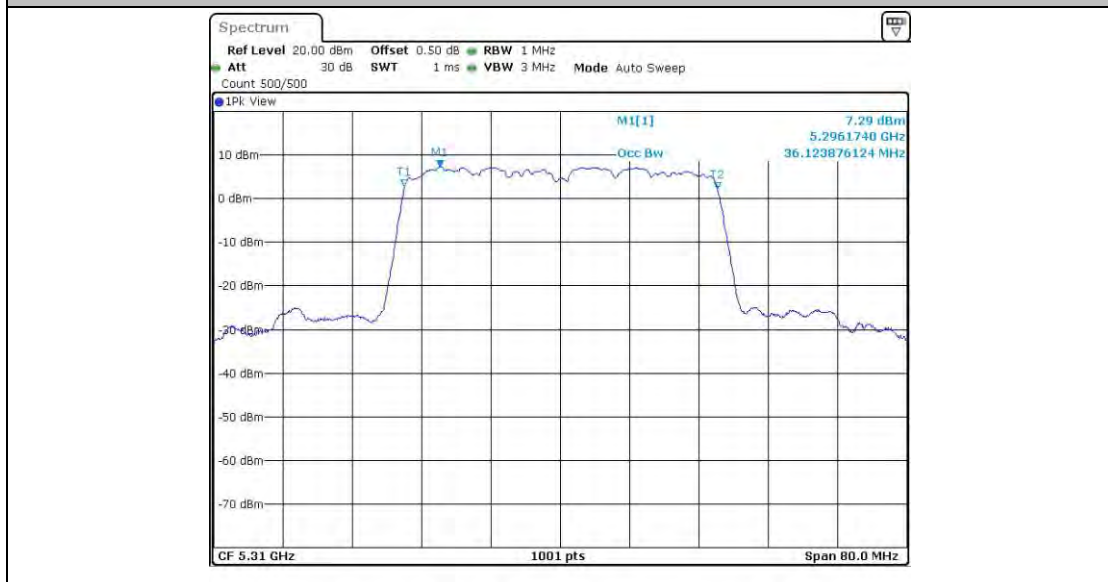
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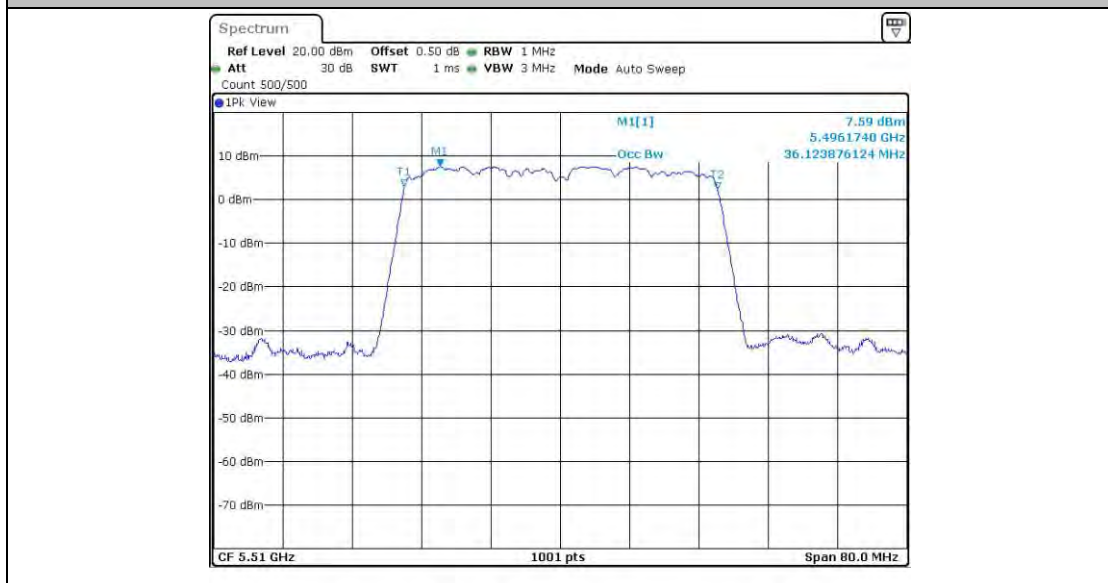
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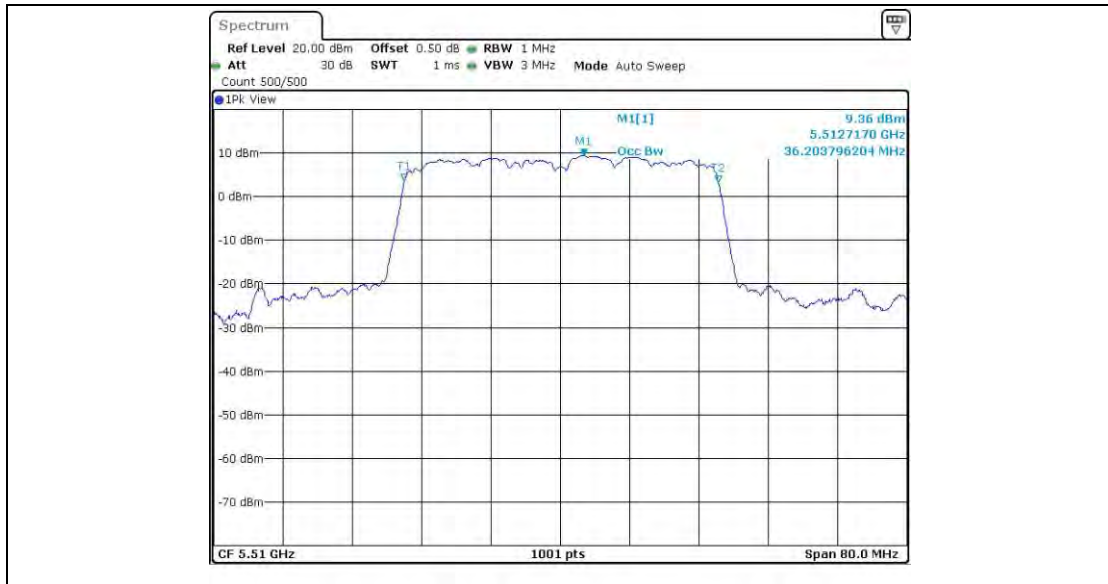
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11N40MIMO_Ant1_5310



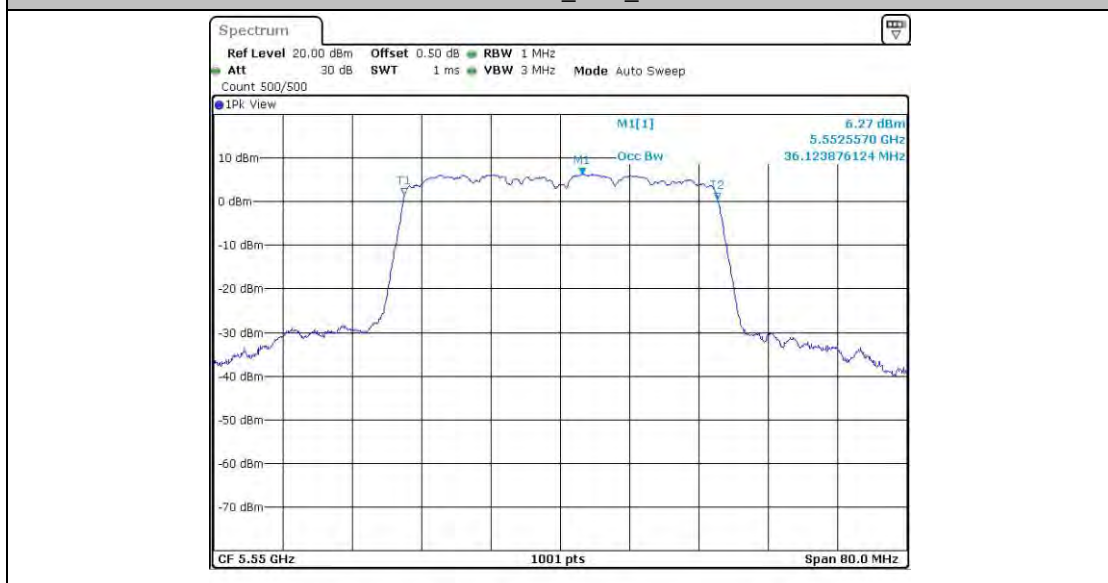
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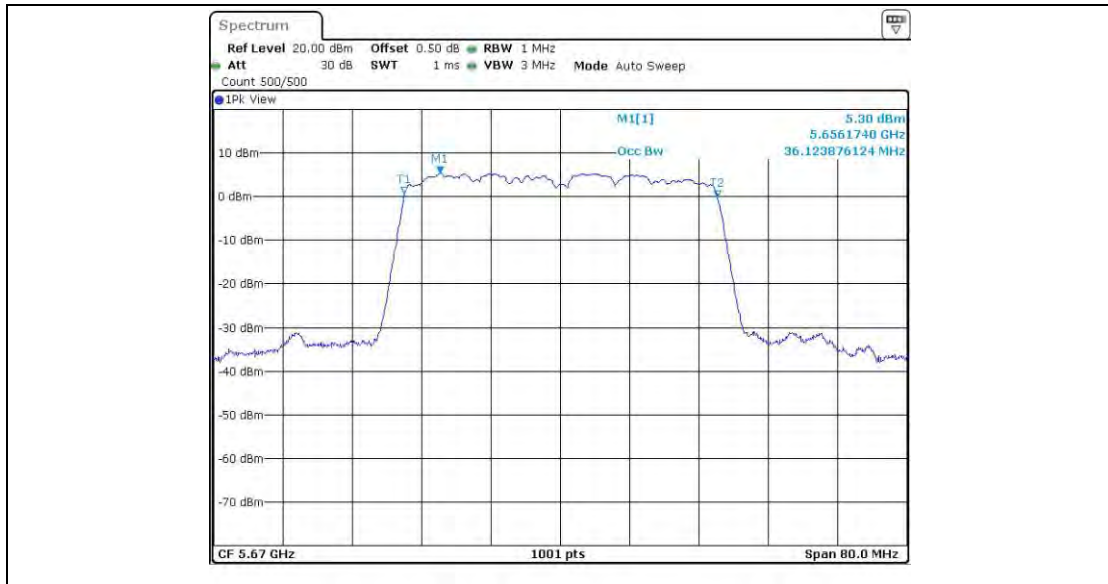
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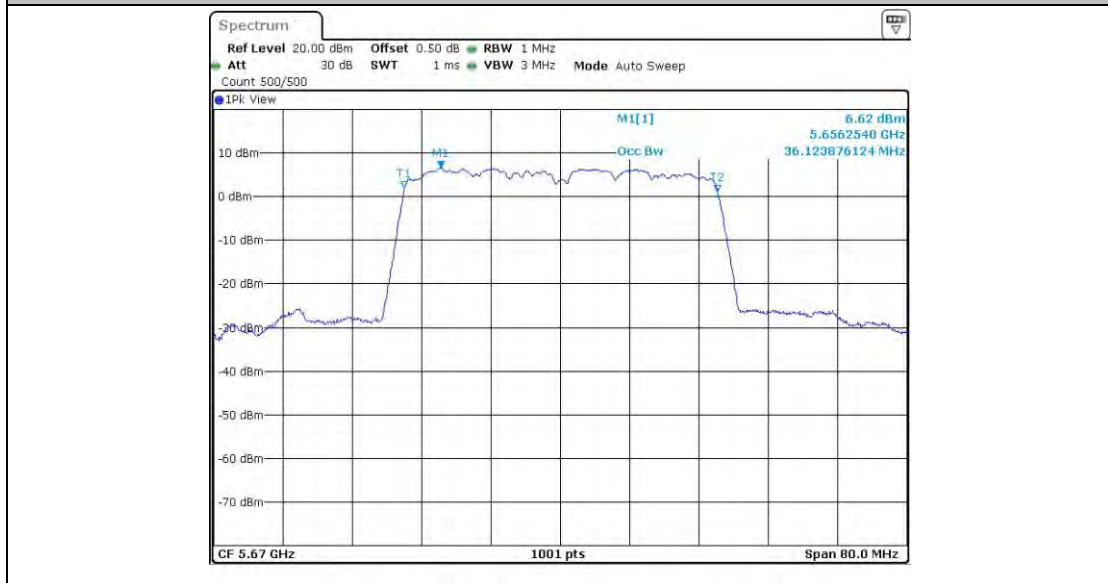
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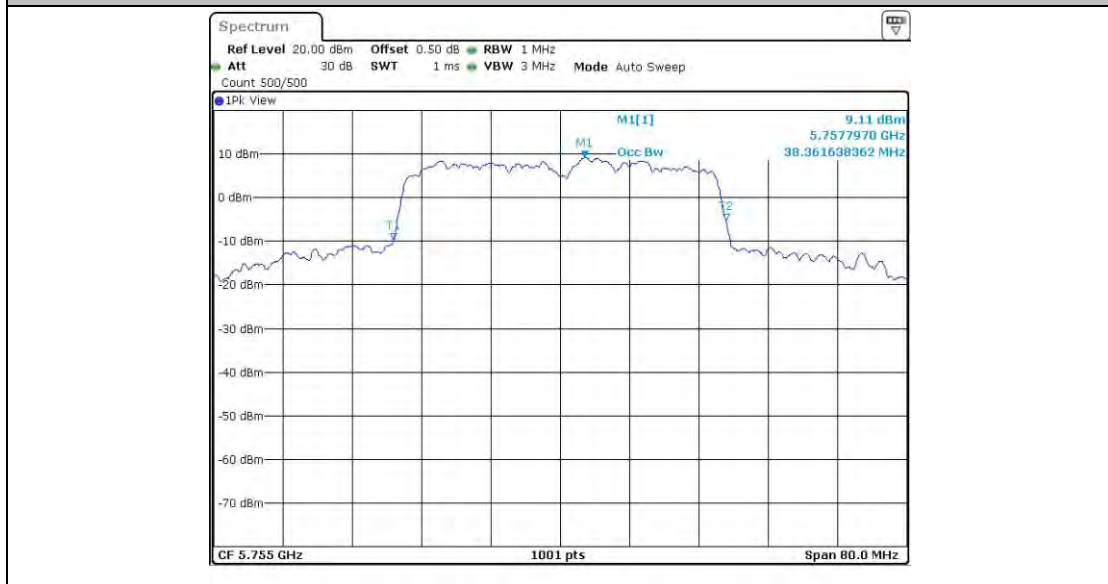
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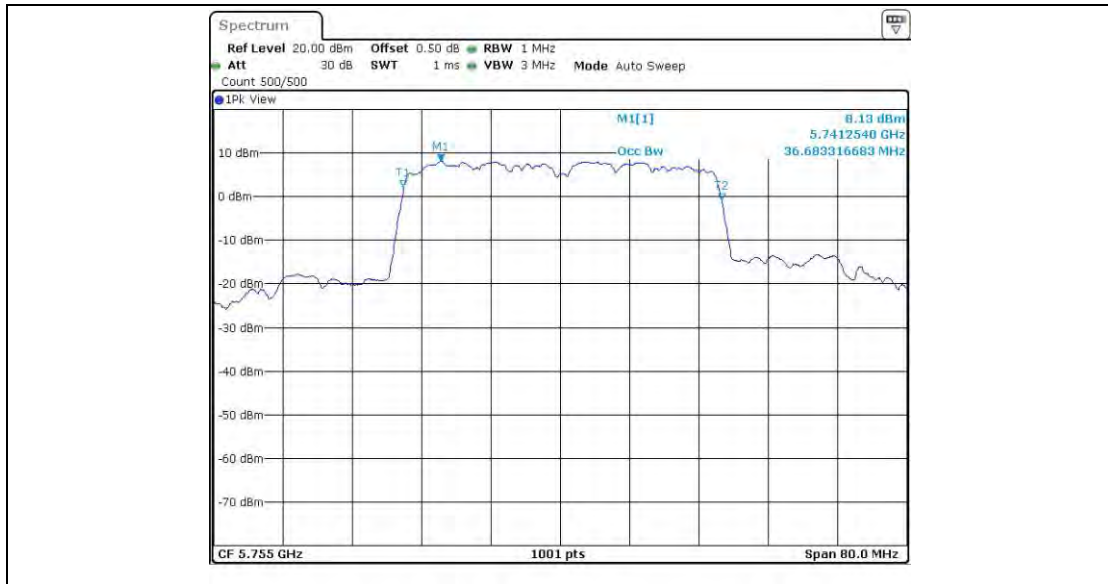
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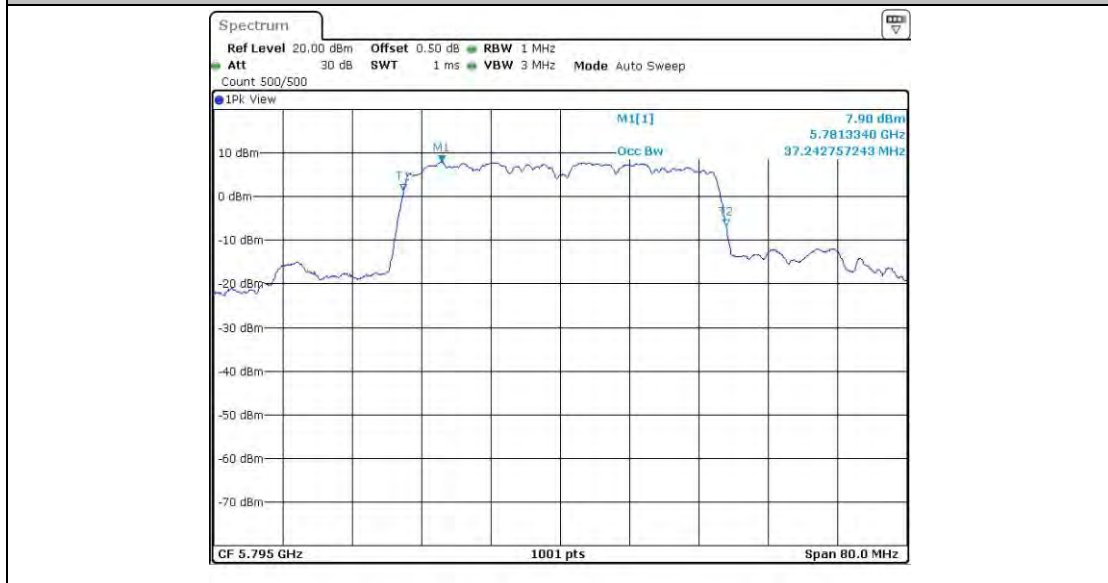
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11N40MIMO_Ant0_5755



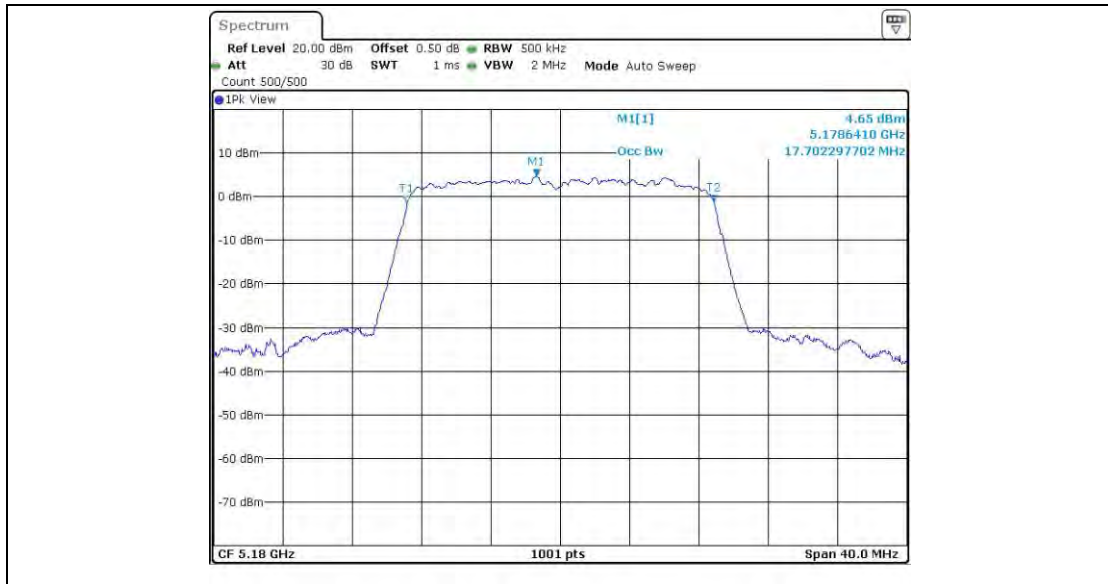
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11N40MIMO_Ant0_5795



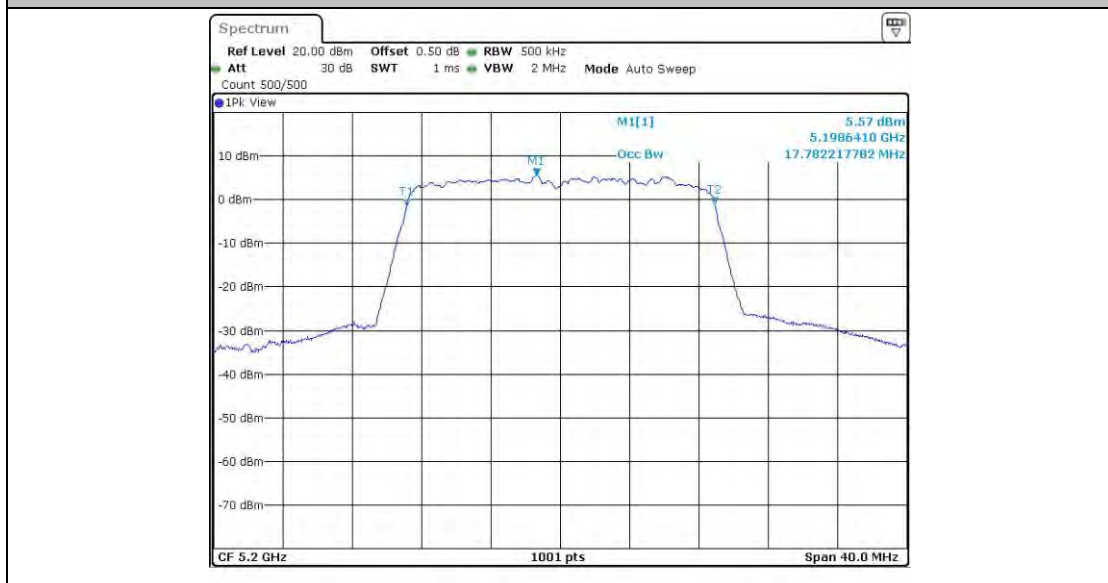
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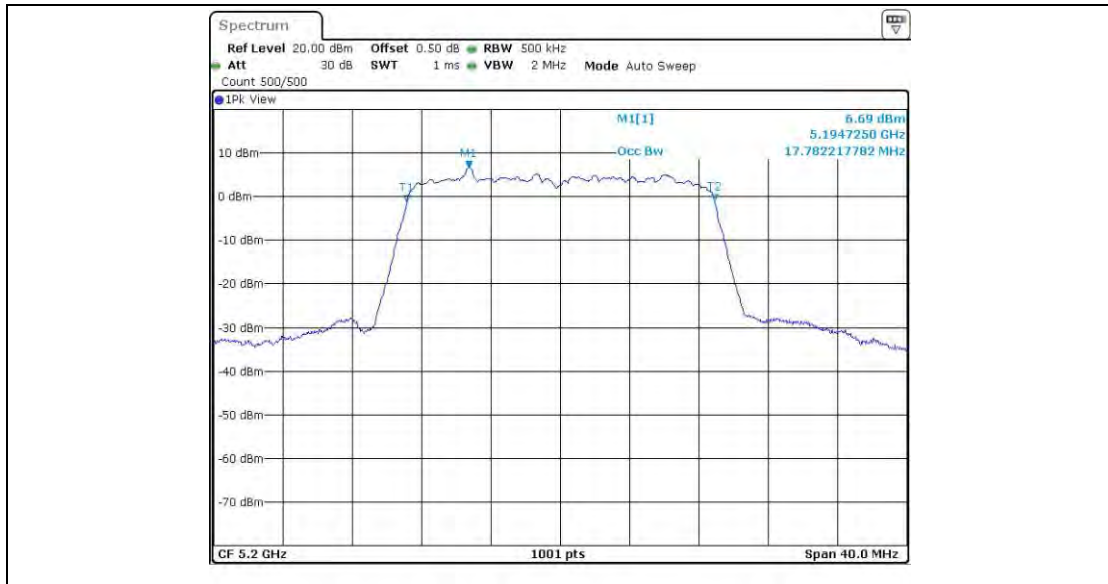
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11AC20MIMO_Ant1_5180



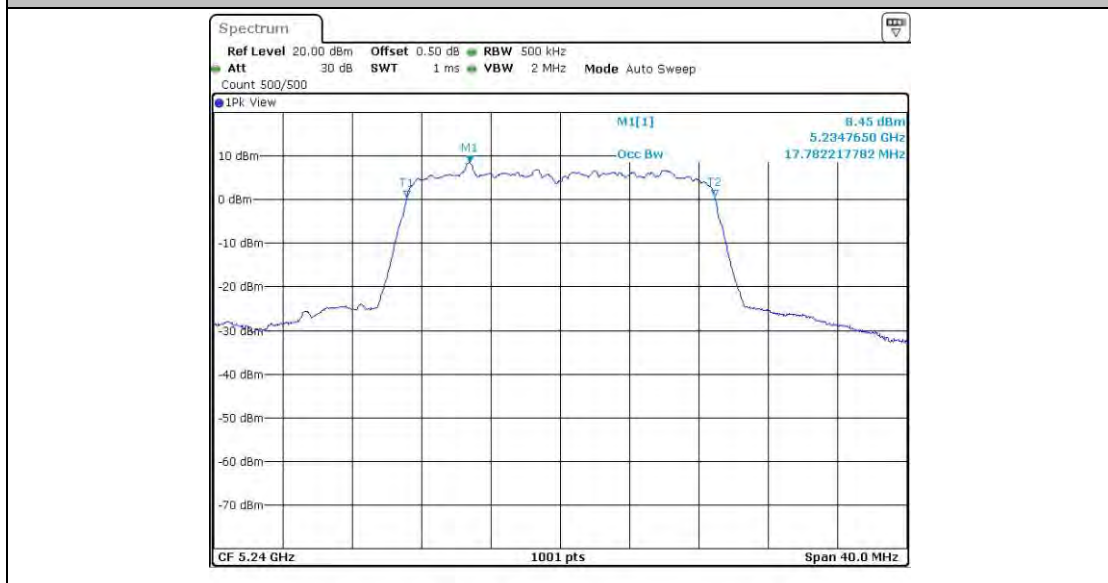
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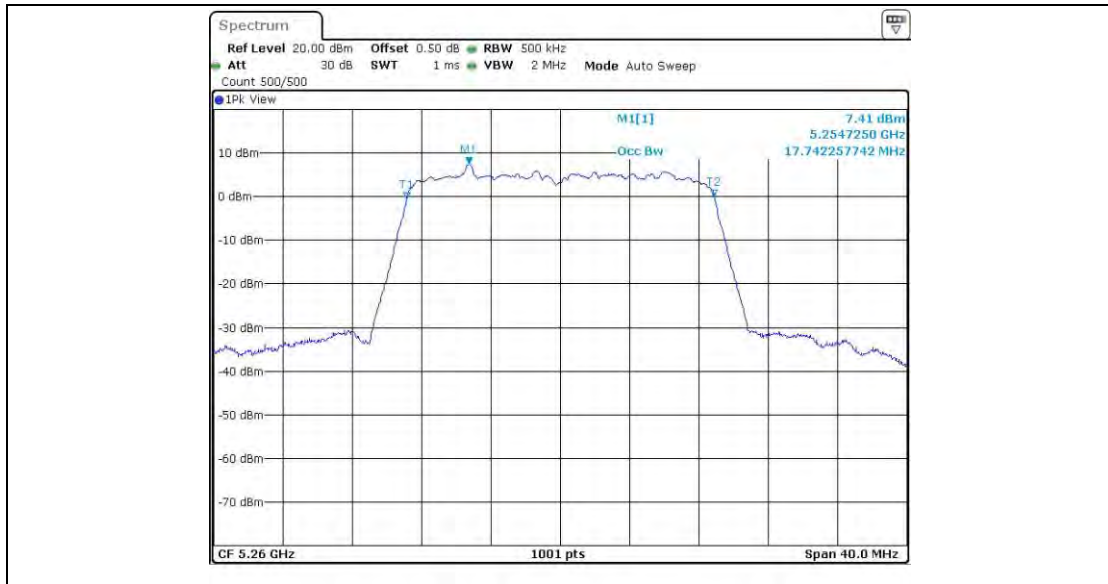
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11AC20MIMO_Ant0_5240



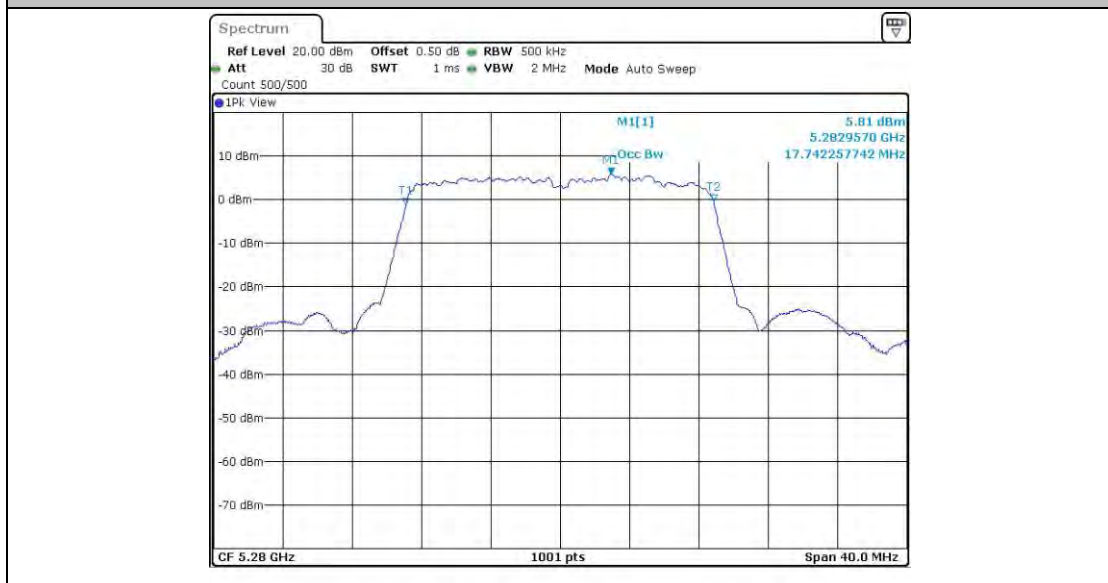
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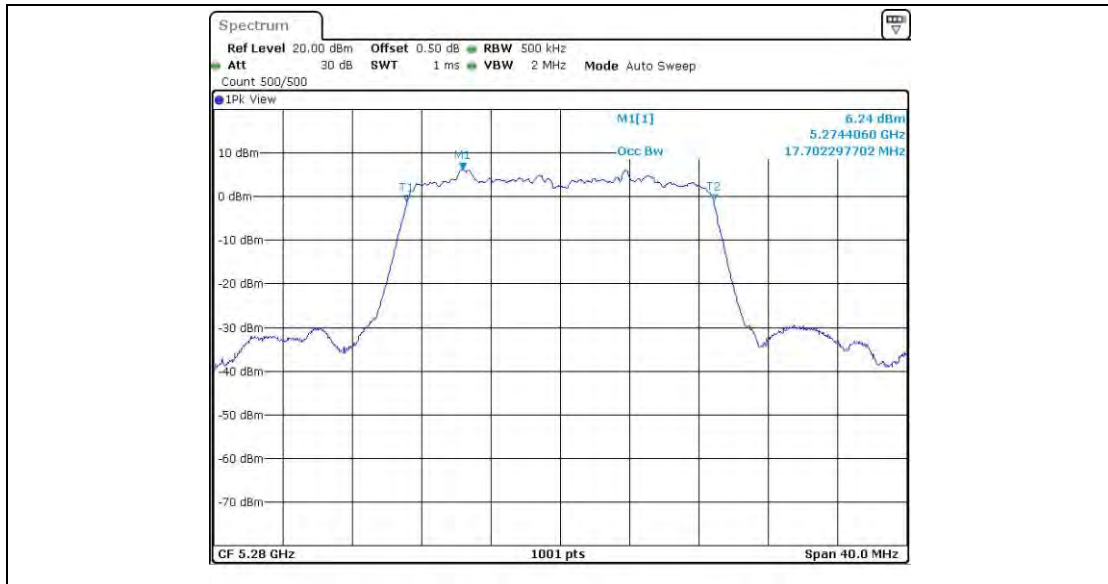
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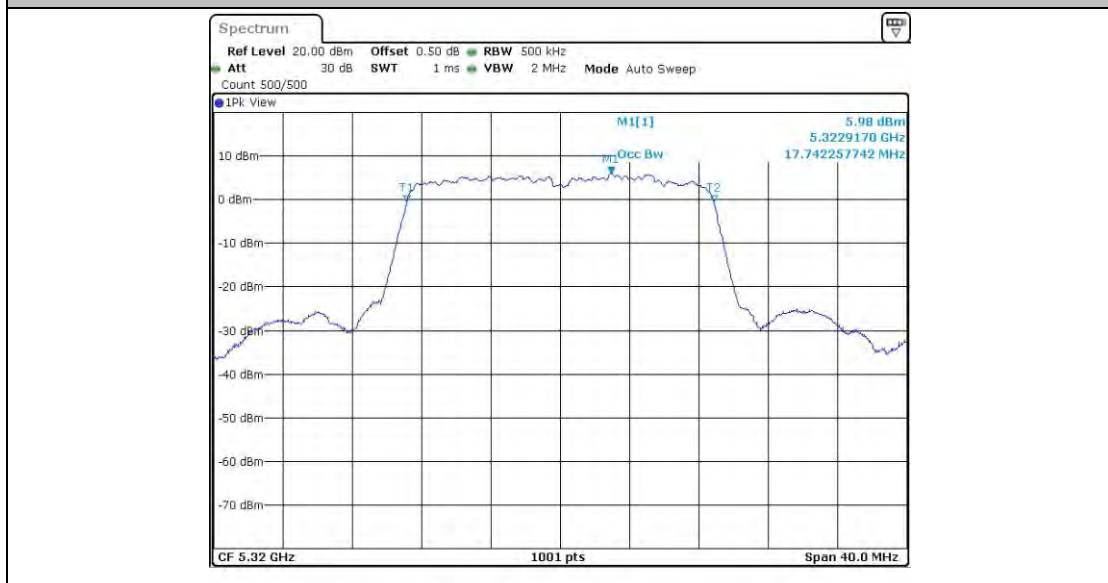
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11AC20MIMO_Ant1_5280



11AC20MIMO_Ant0_5320



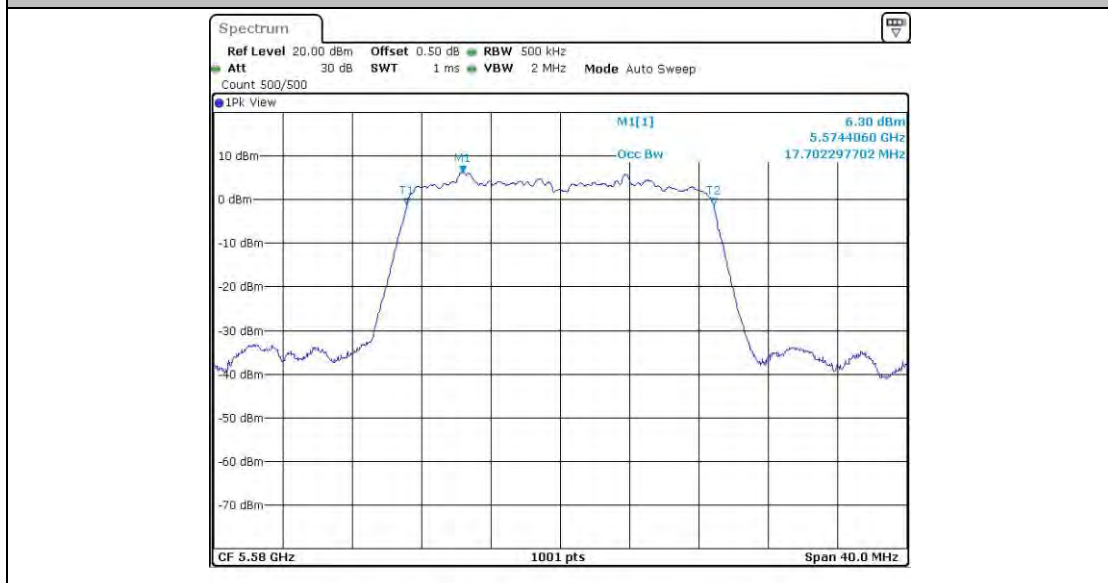
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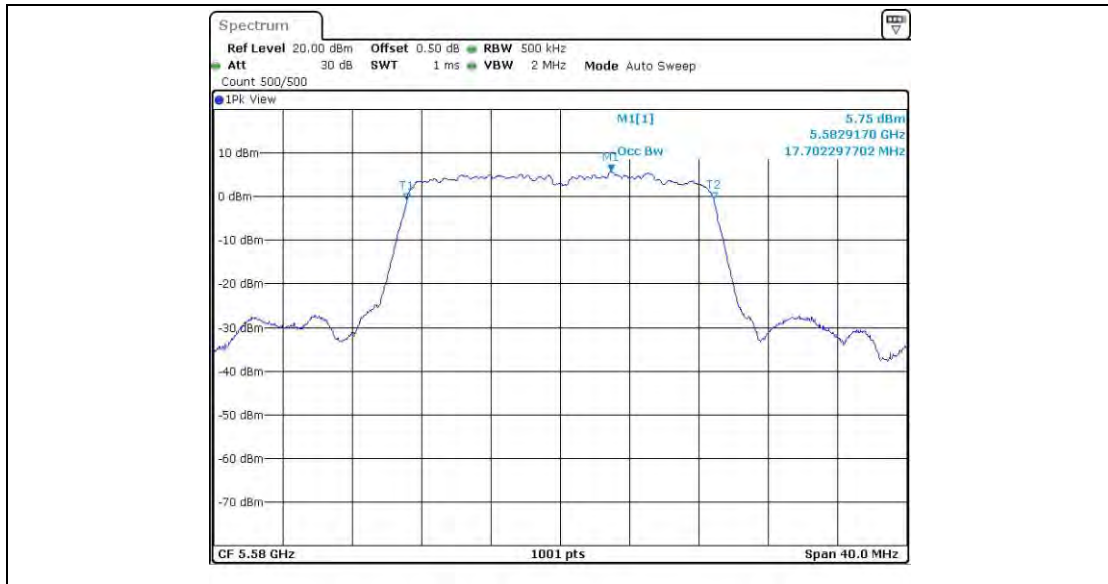
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11AC20MIMO_Ant1_5500



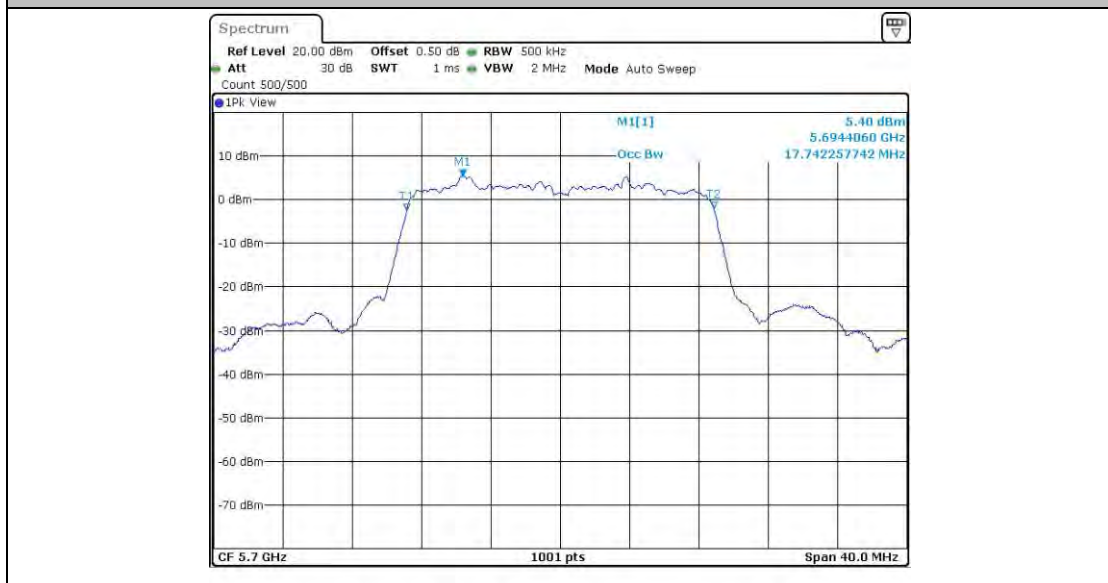
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11AC20MIMO_Ant1_5580



11AC20MIMO_Ant0_5700



11AC20MIMO_Ant1_5700



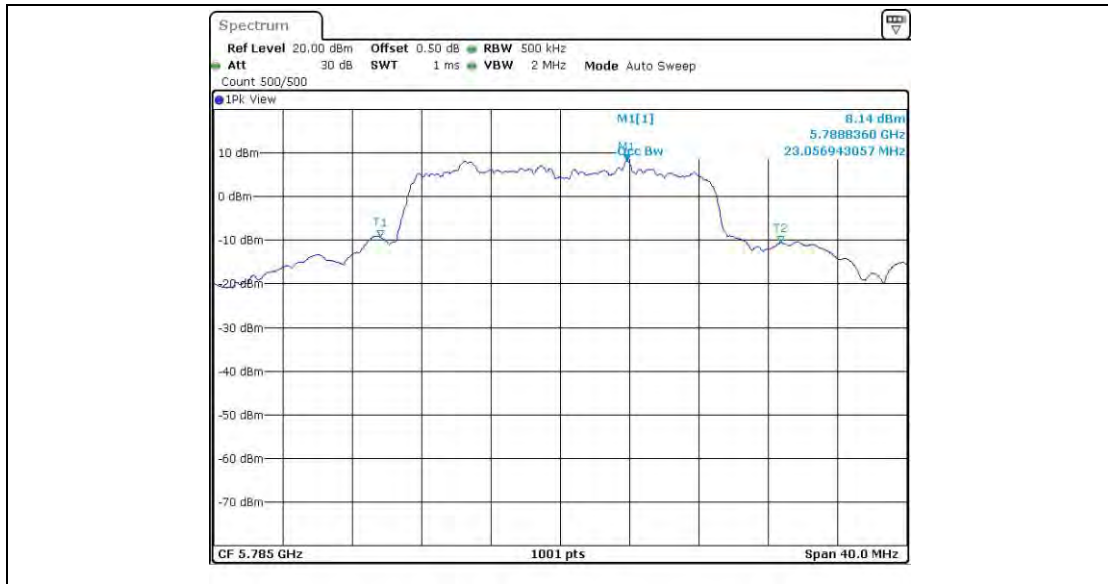
11AC20MIMO_Ant0_5745



11AC20MIMO_Ant1_5745



11AC20MIMO_Ant0_5785



11AC20MIMO_Ant1_5785



11AC20MIMO_Ant0_5825



11AC20MIMO_Ant1_5825



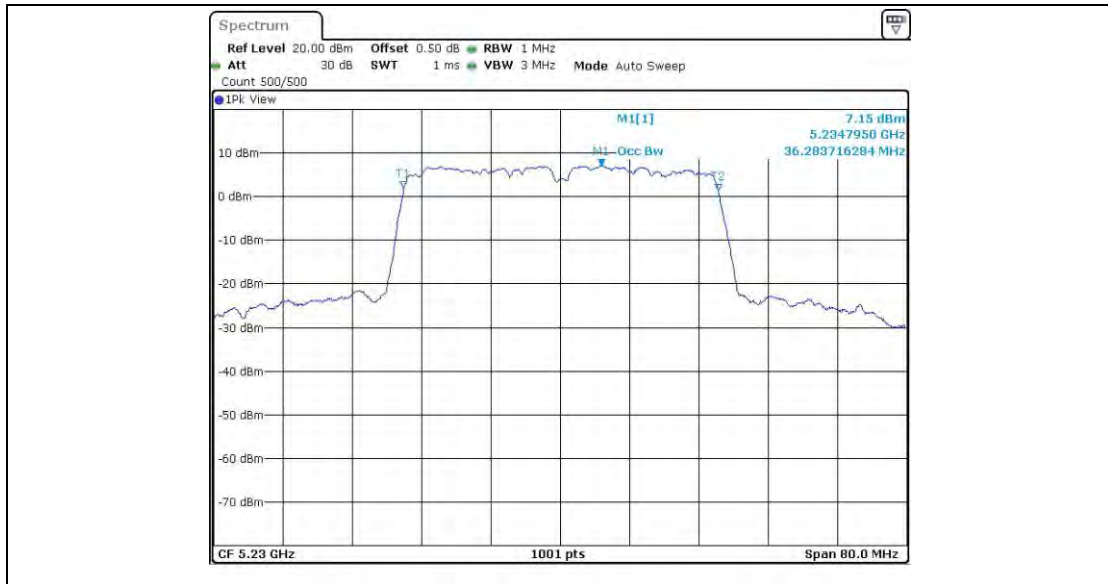
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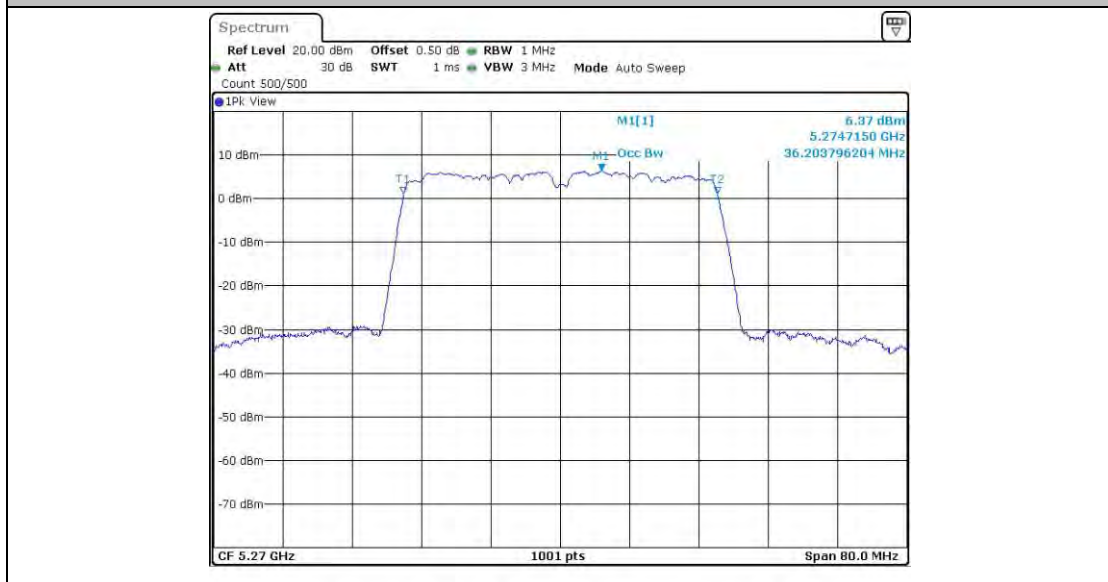
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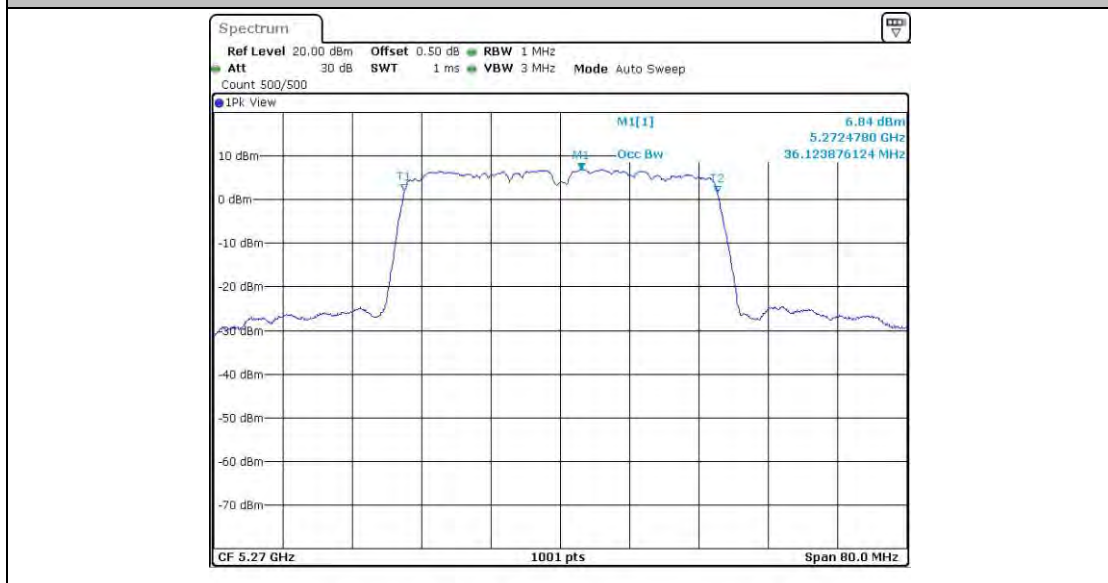
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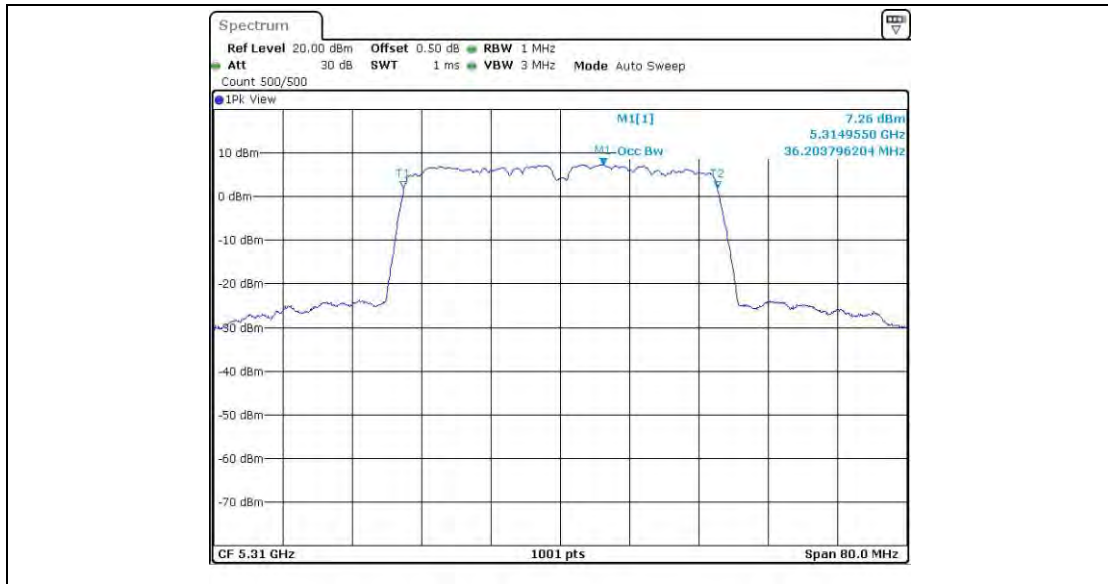
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11AC40MIMO_Ant0_5270



11AC40MIMO_Ant1_5270



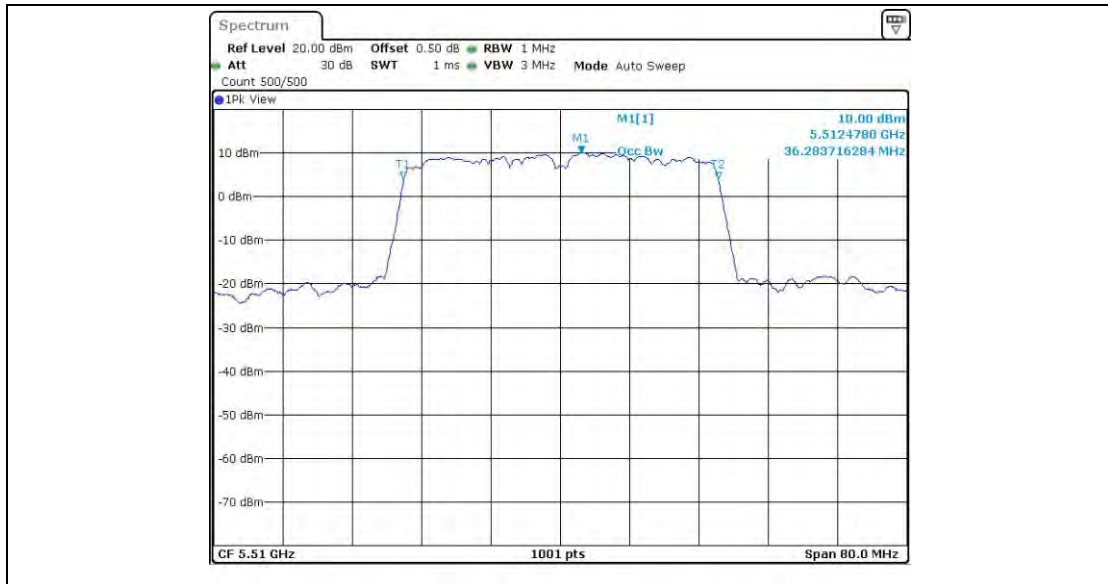
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11AC40MIMO_Ant1_5310



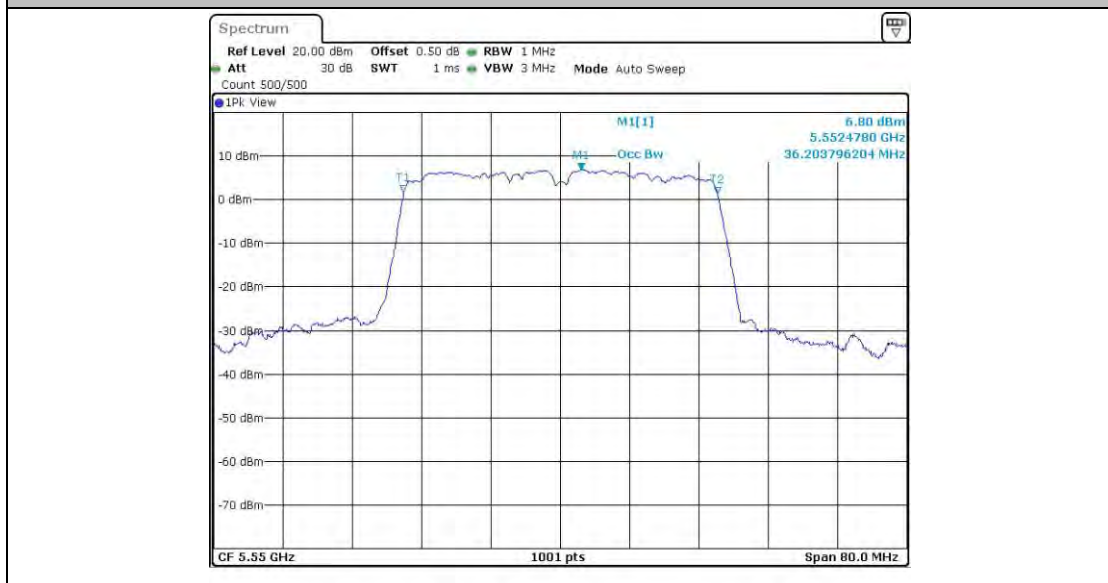
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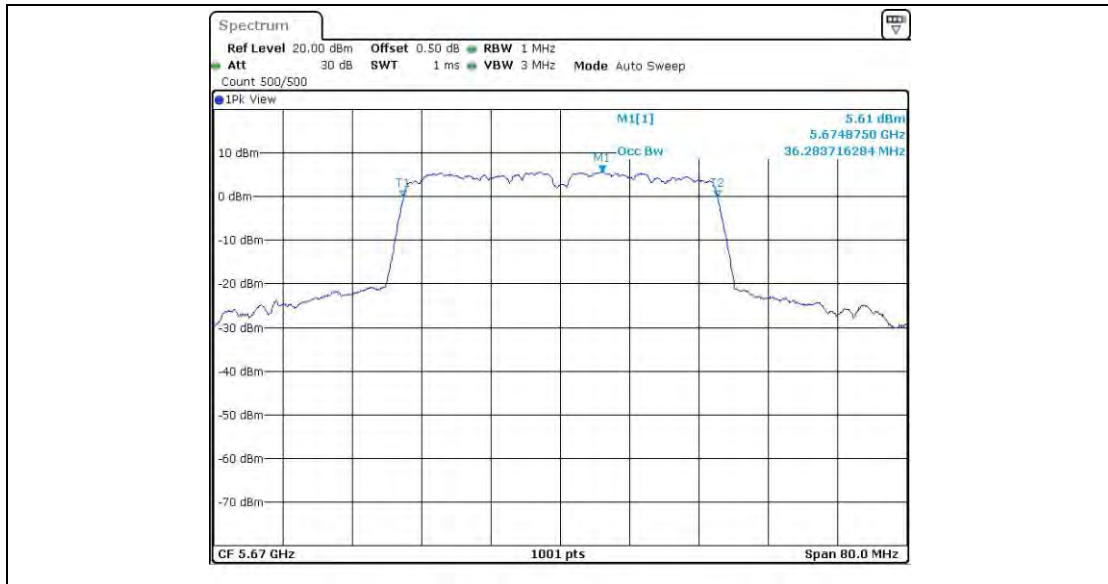
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11AC40MIMO_Ant0_5550



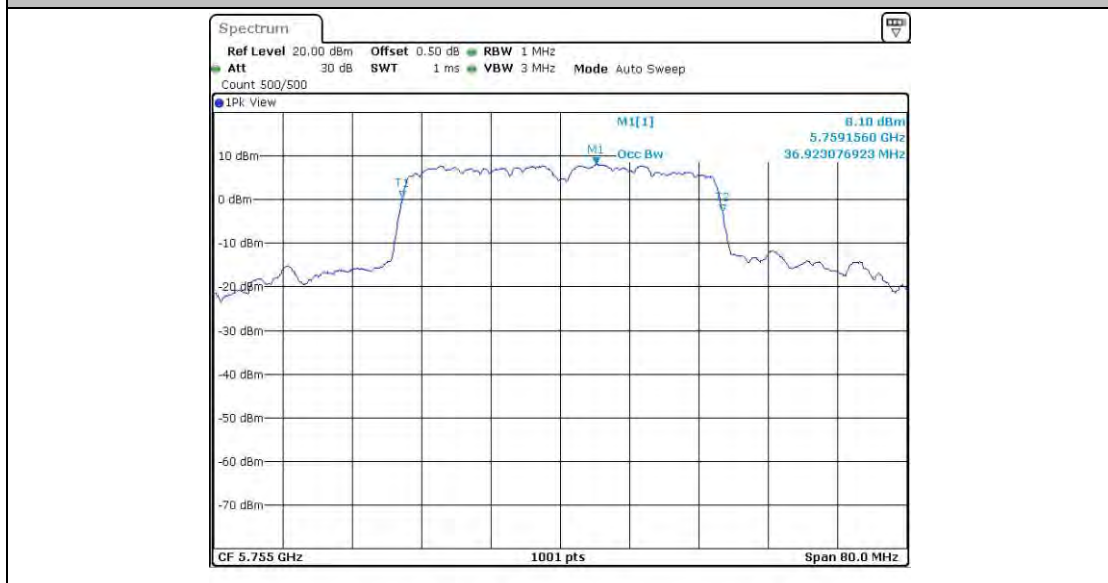
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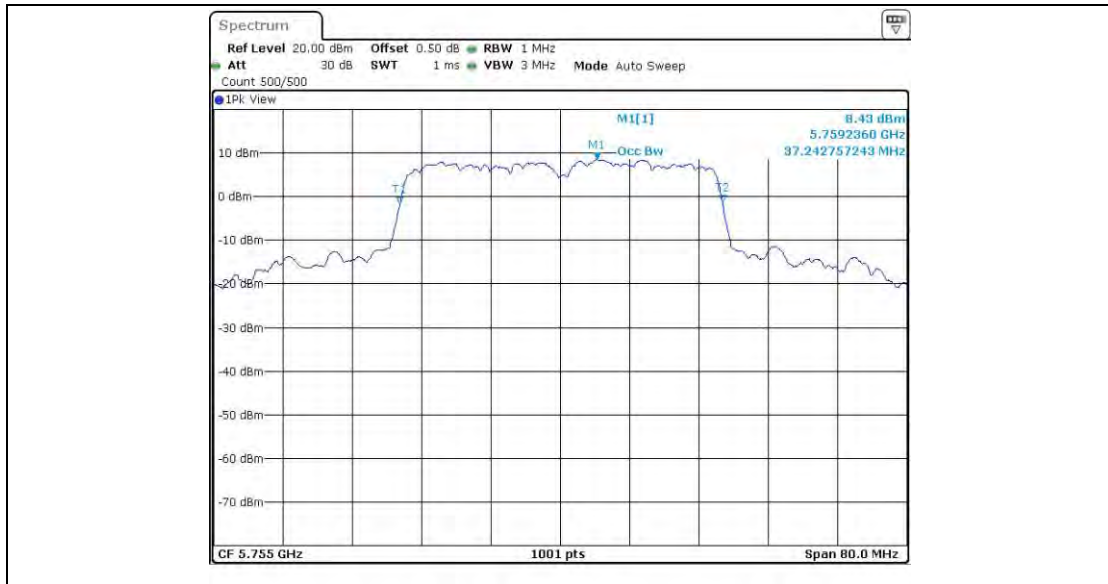
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11AC40MIMO_Ant1_5670



11AC40MIMO_Ant0_5755



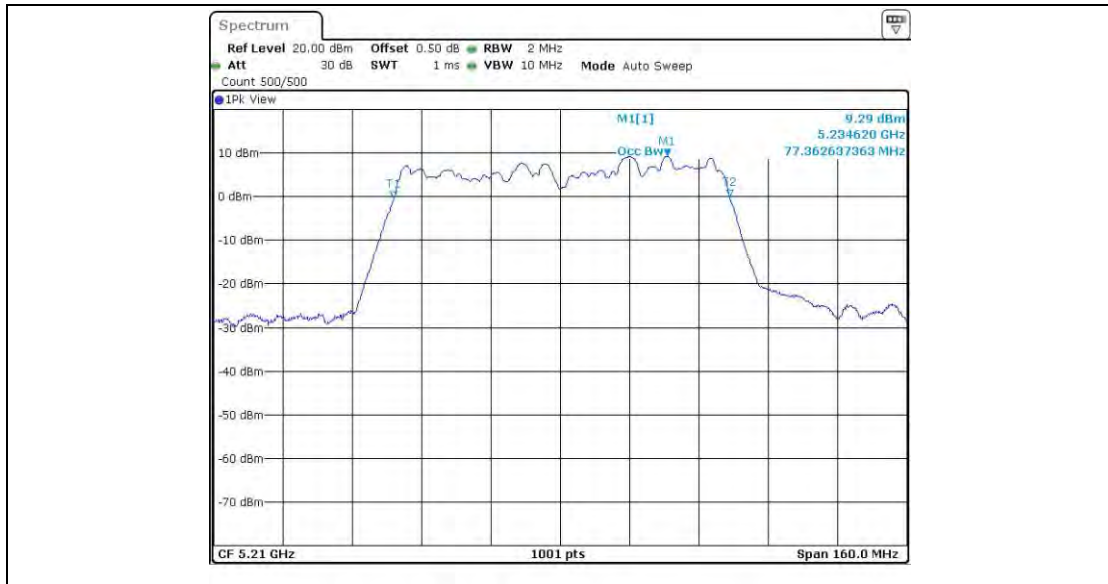
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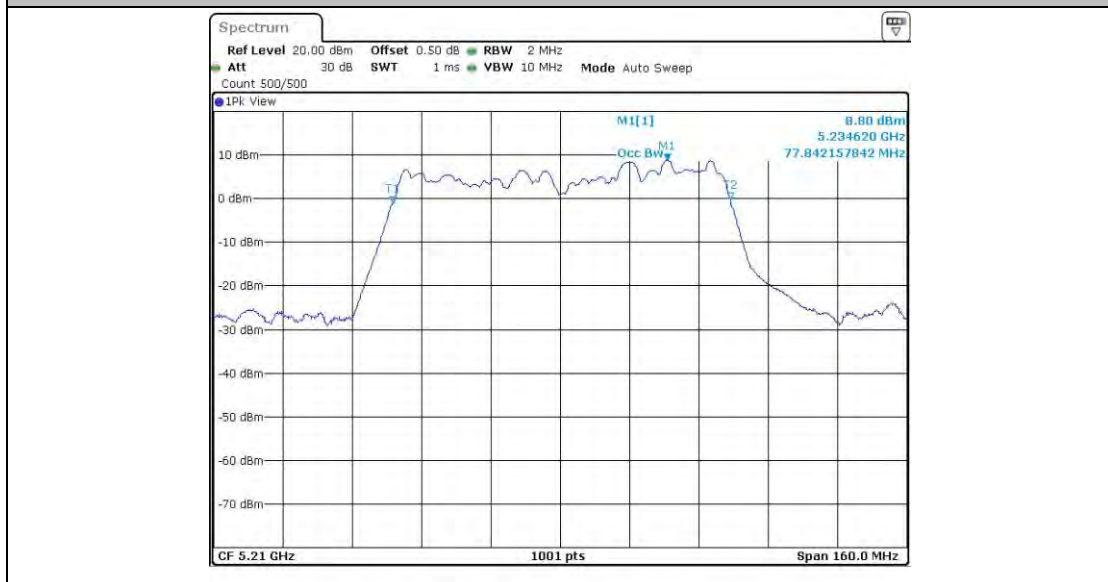
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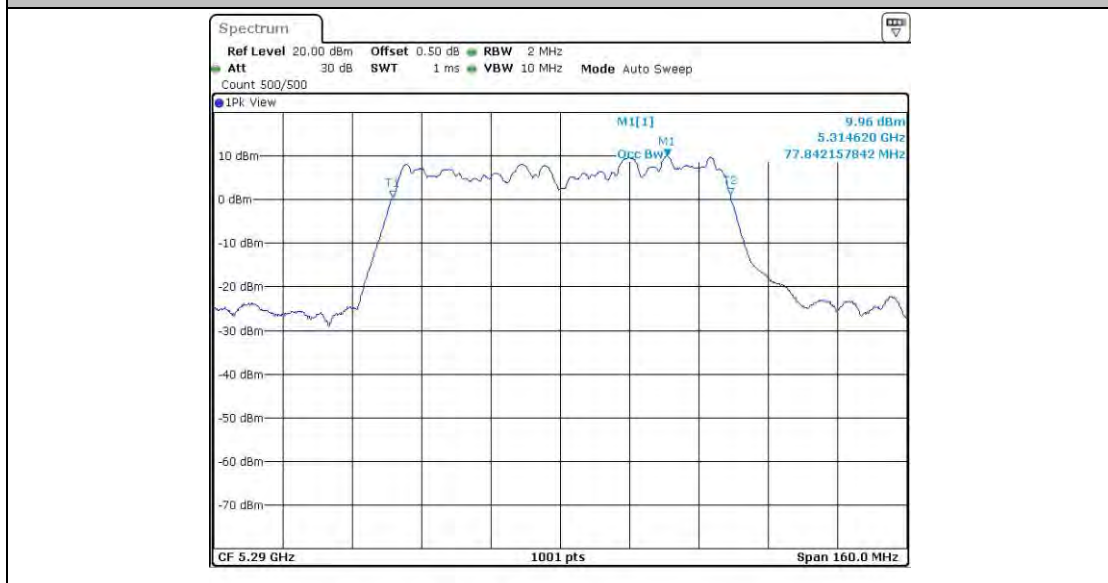
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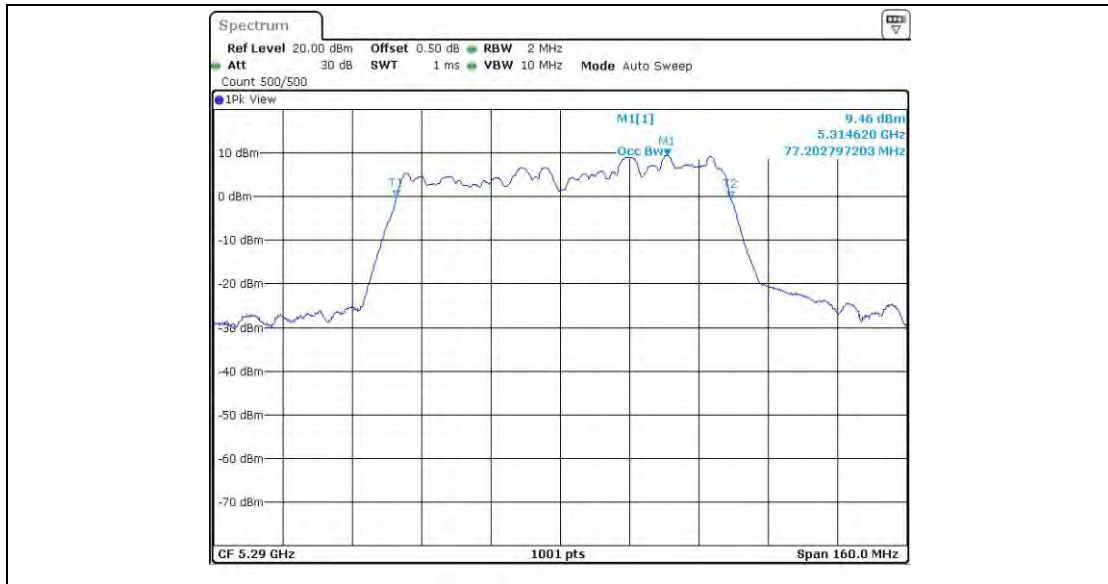
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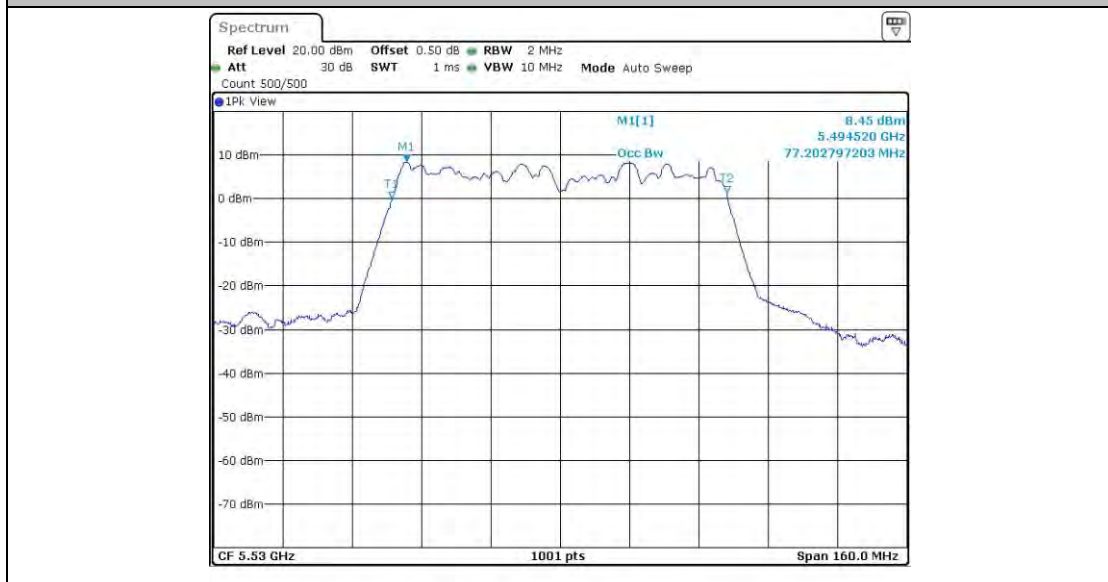
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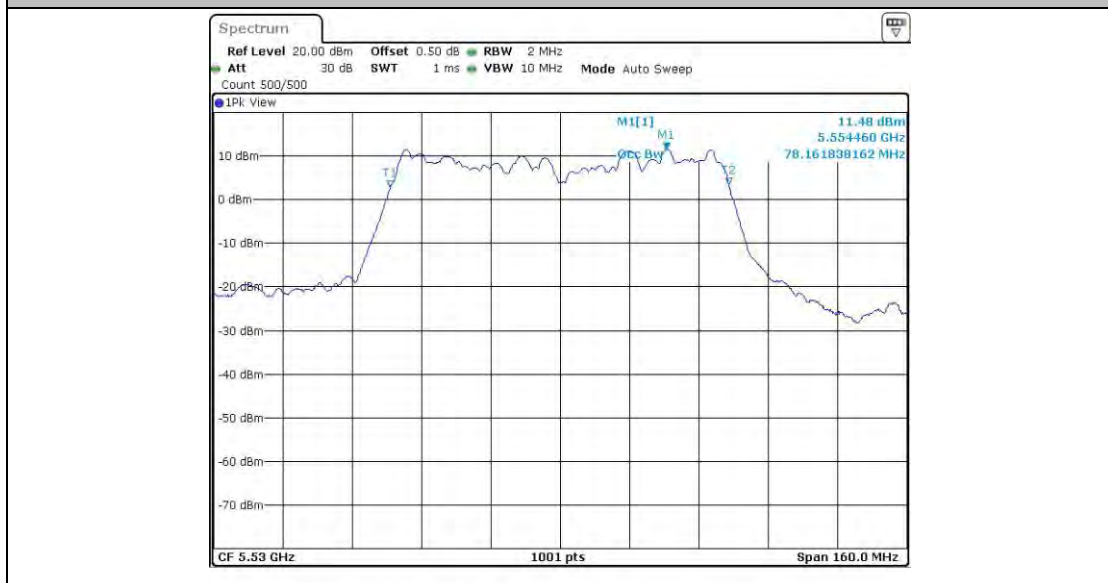
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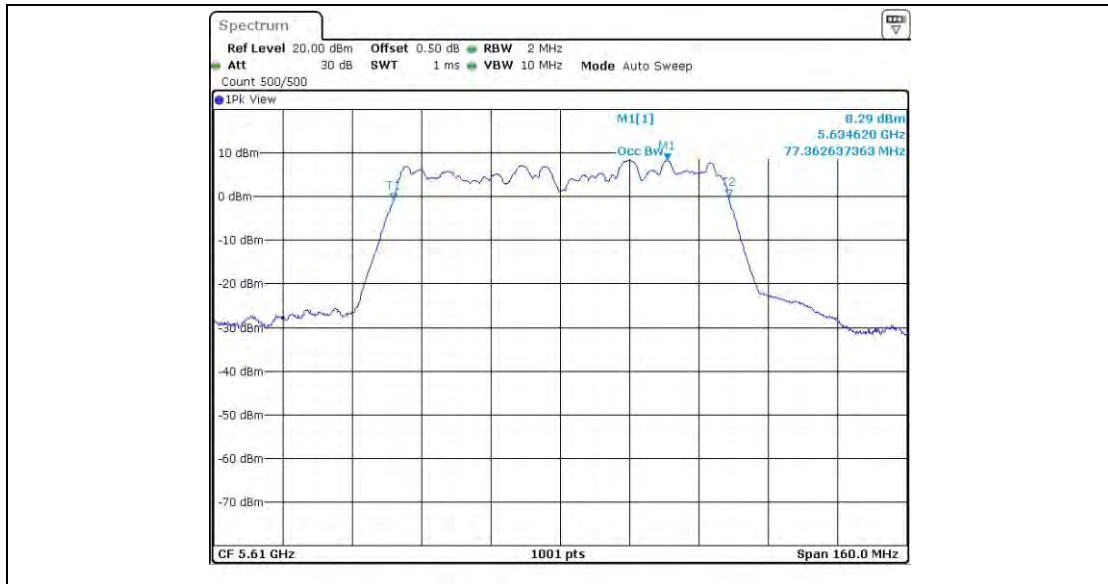
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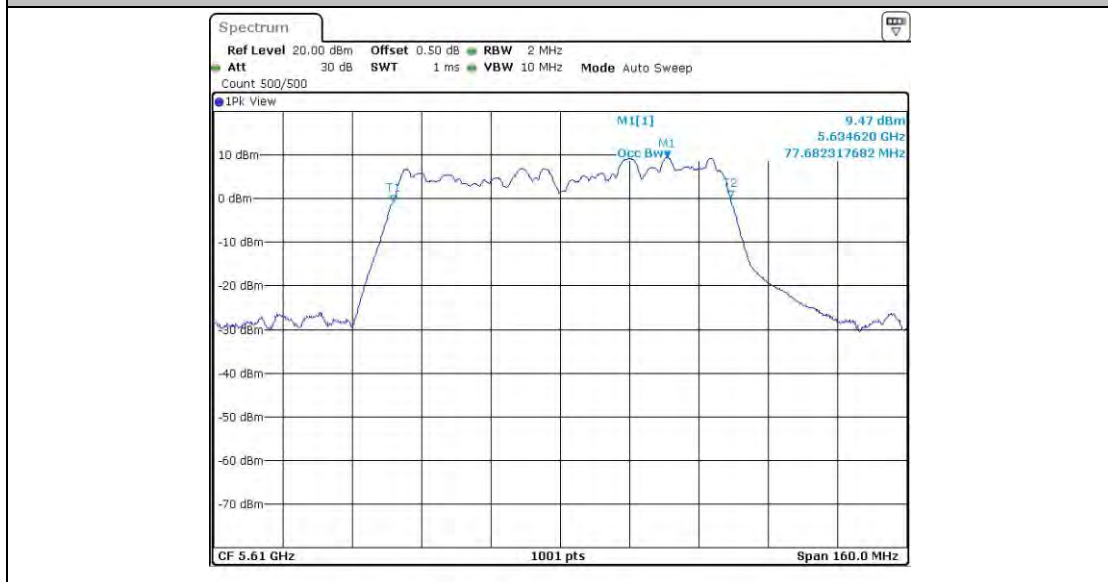
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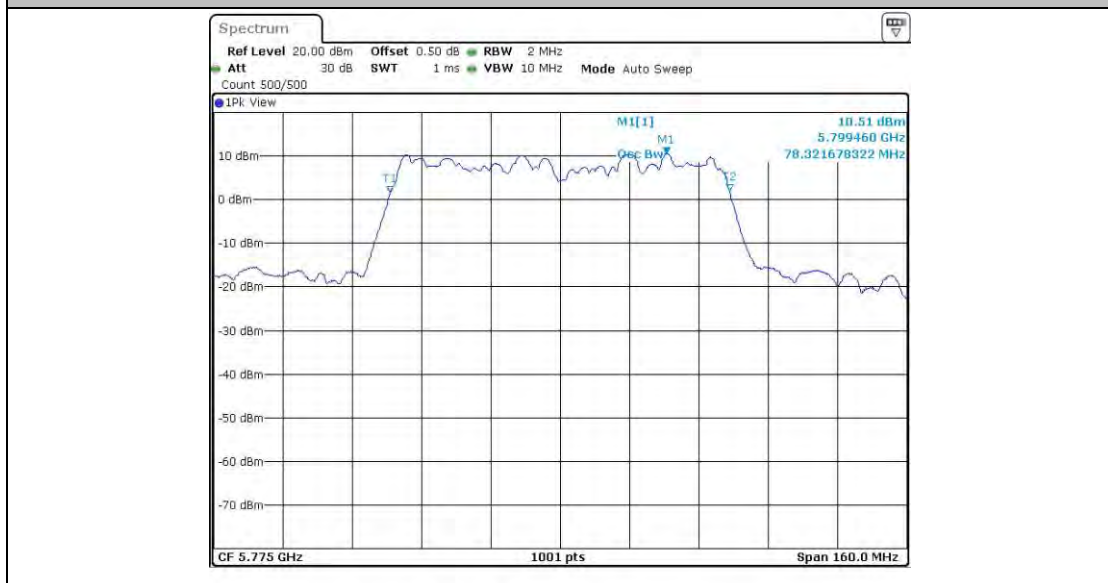
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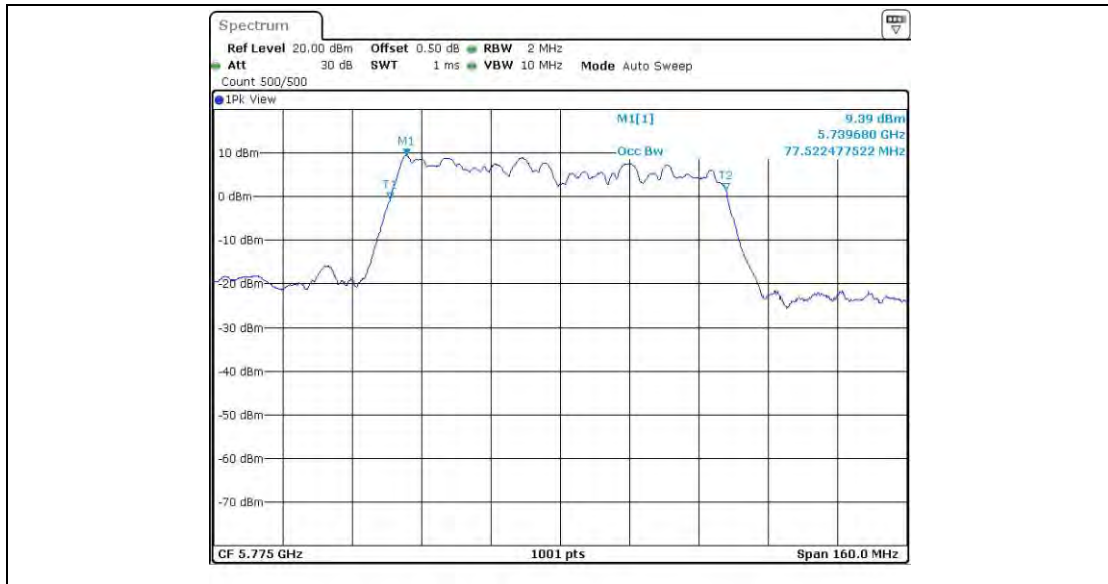
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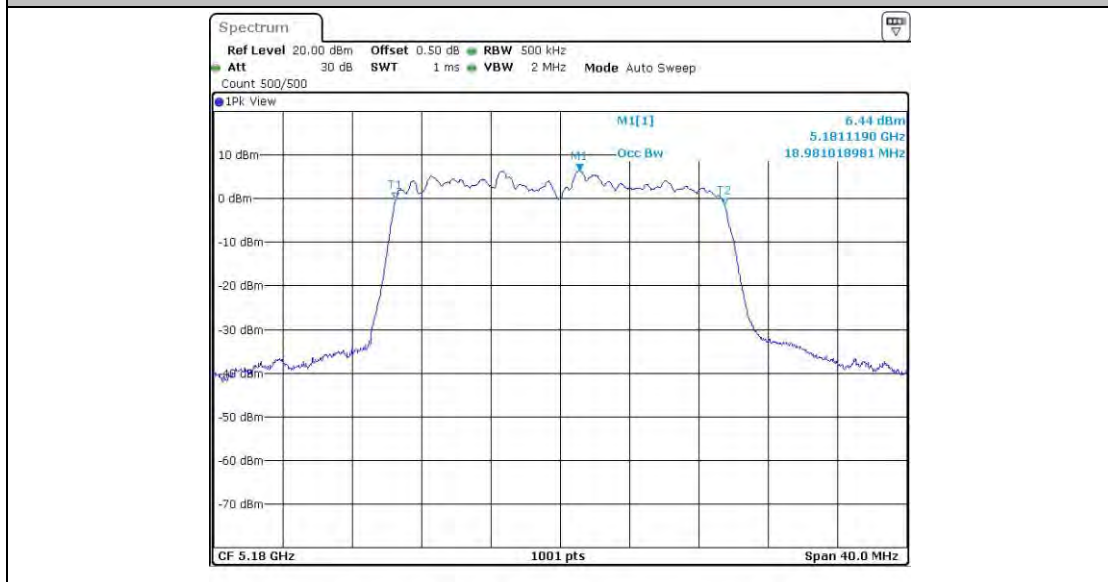
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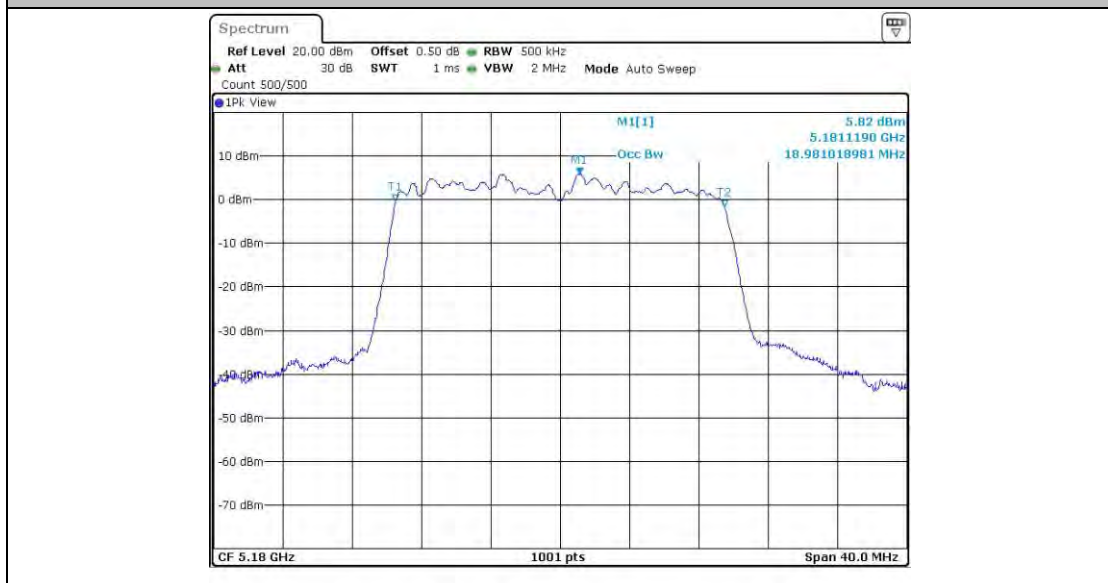
11AC80MIMO_Ant0_5775



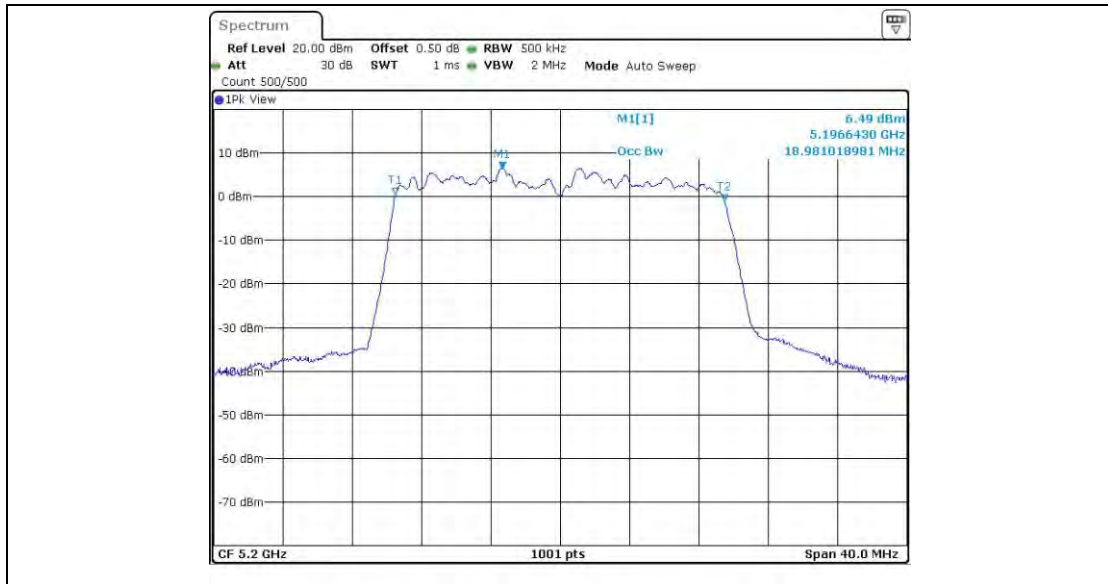
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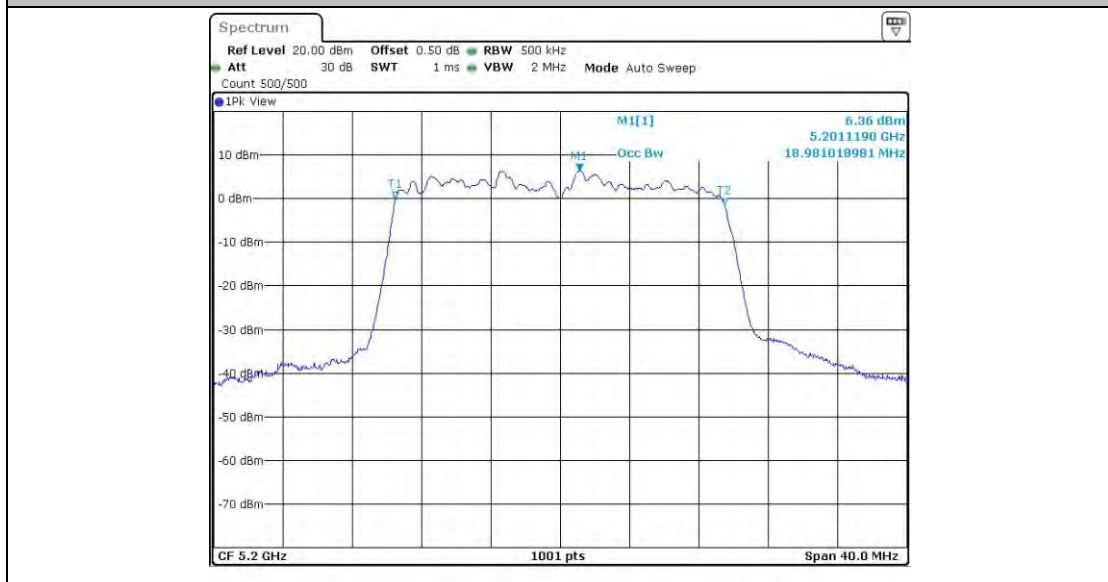
11AX20MIMO_Ant0_5180



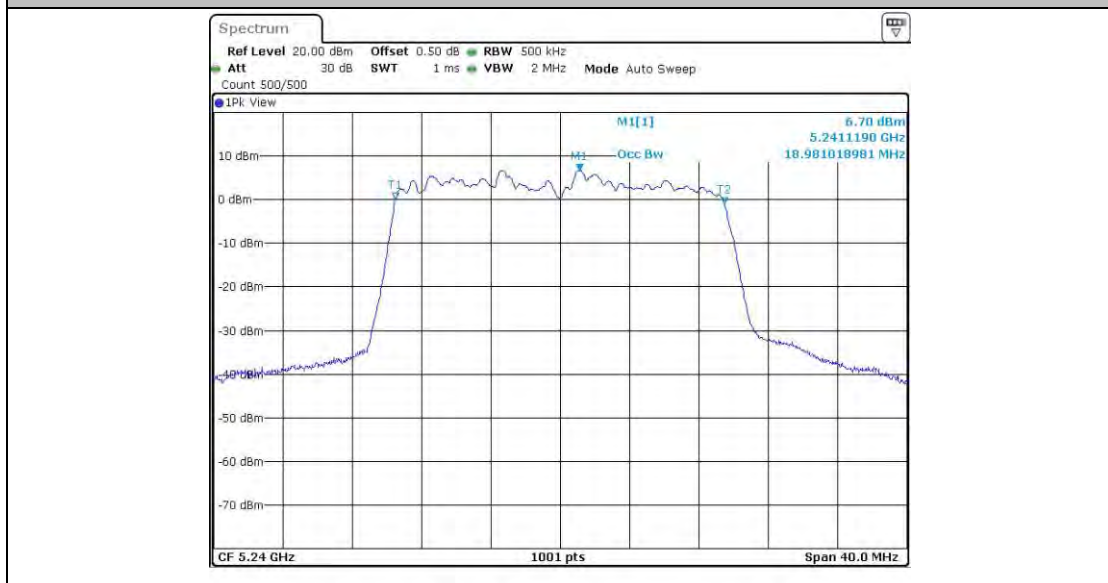
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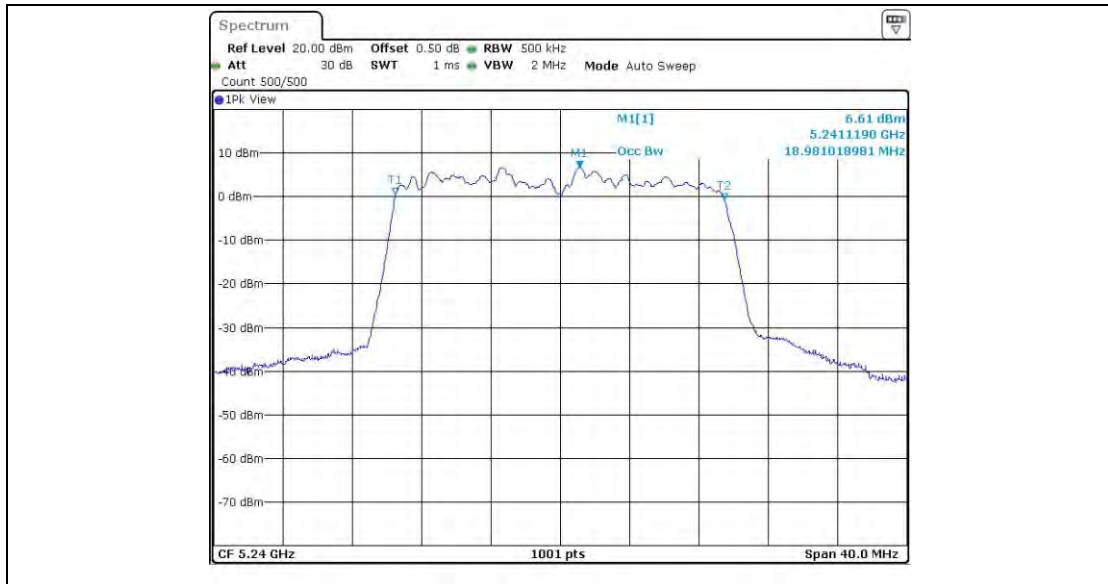
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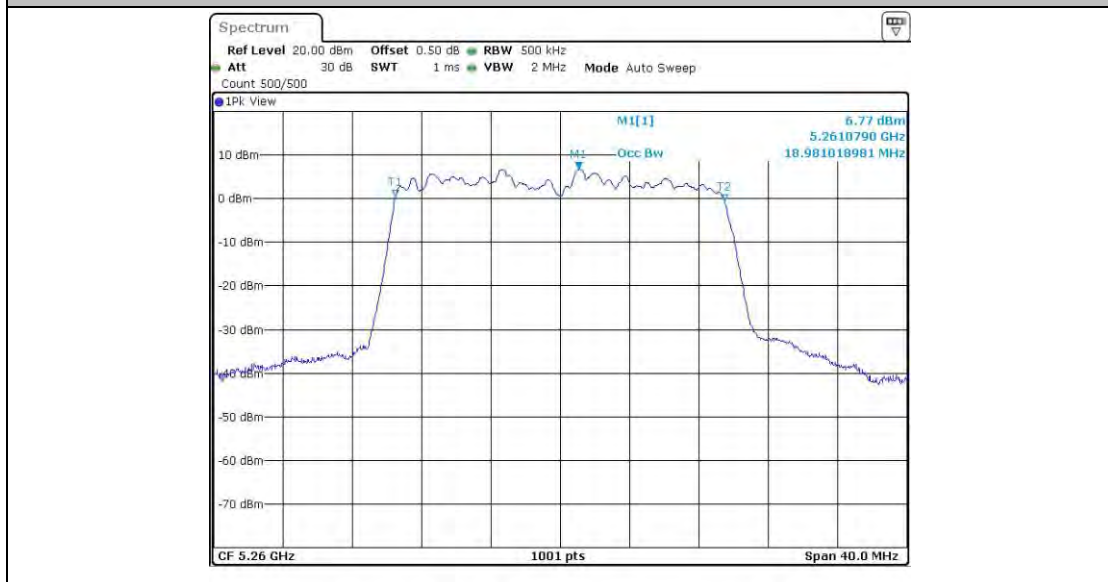
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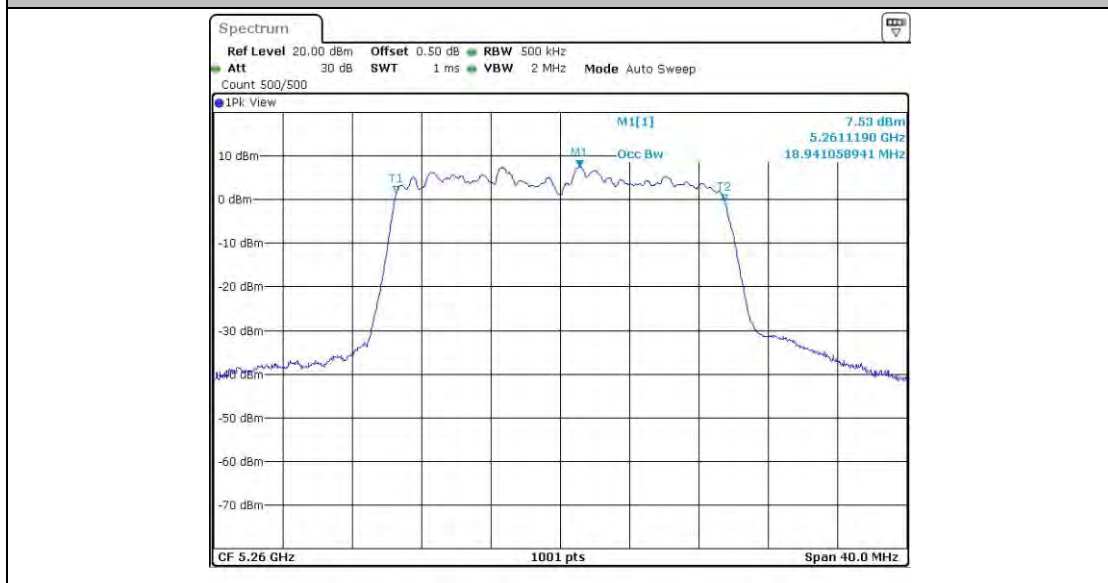
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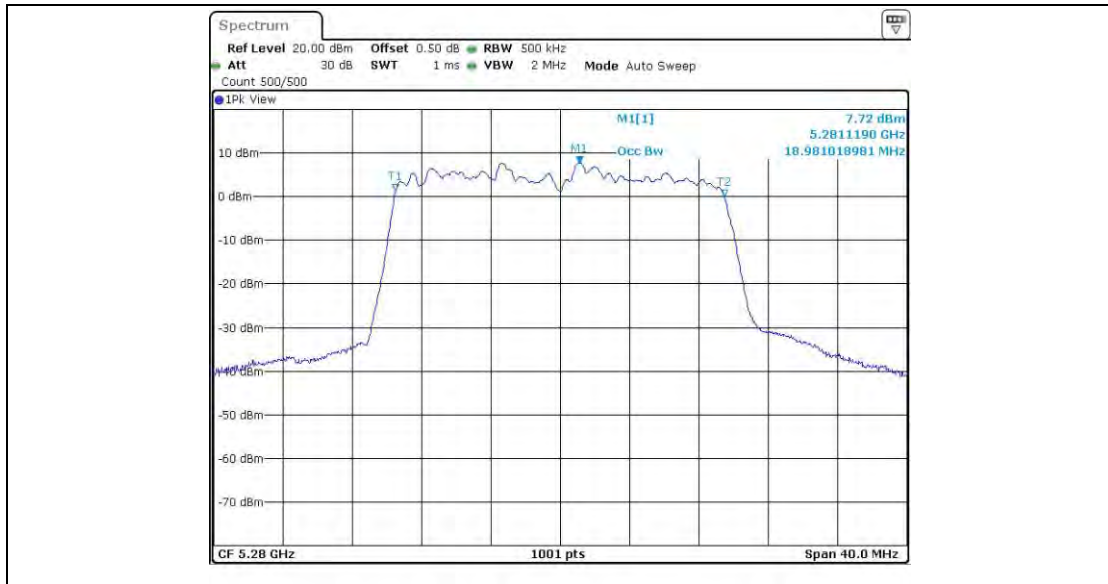
11AX20MIMO_Ant1_5240



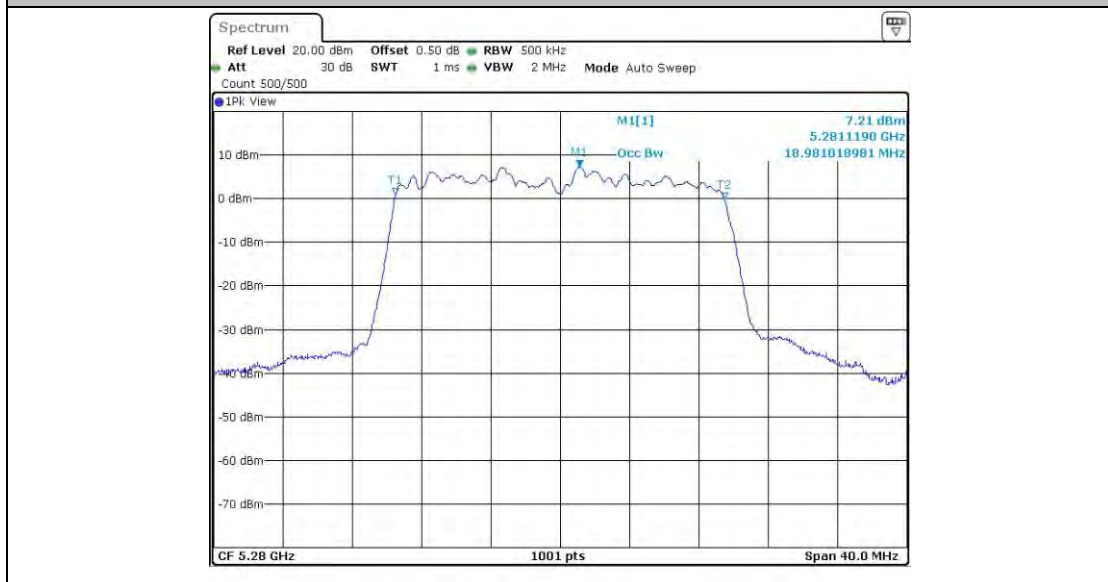
11AX20MIMO_Ant0_5260



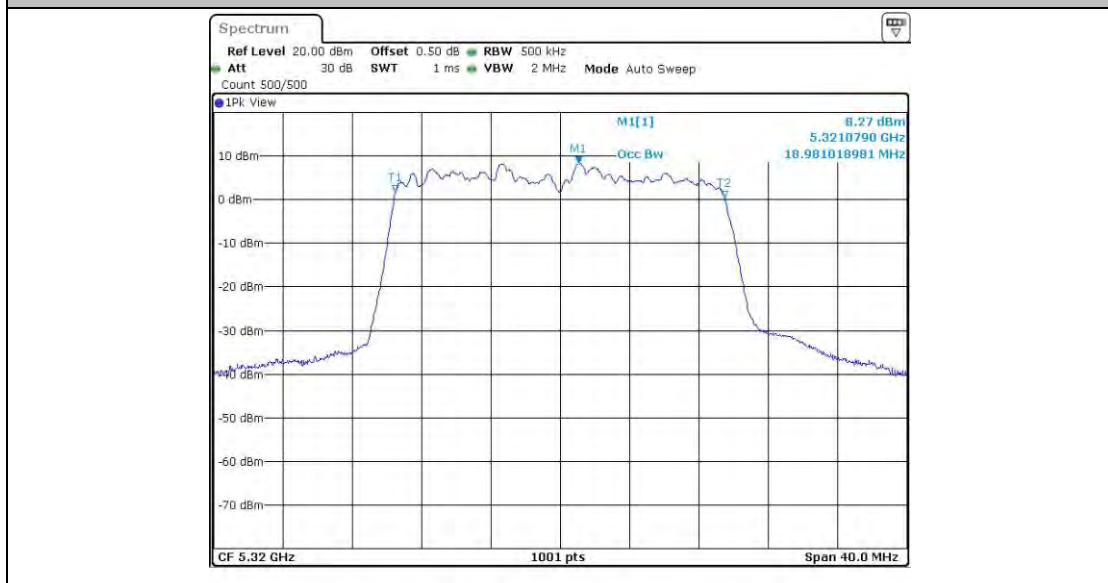
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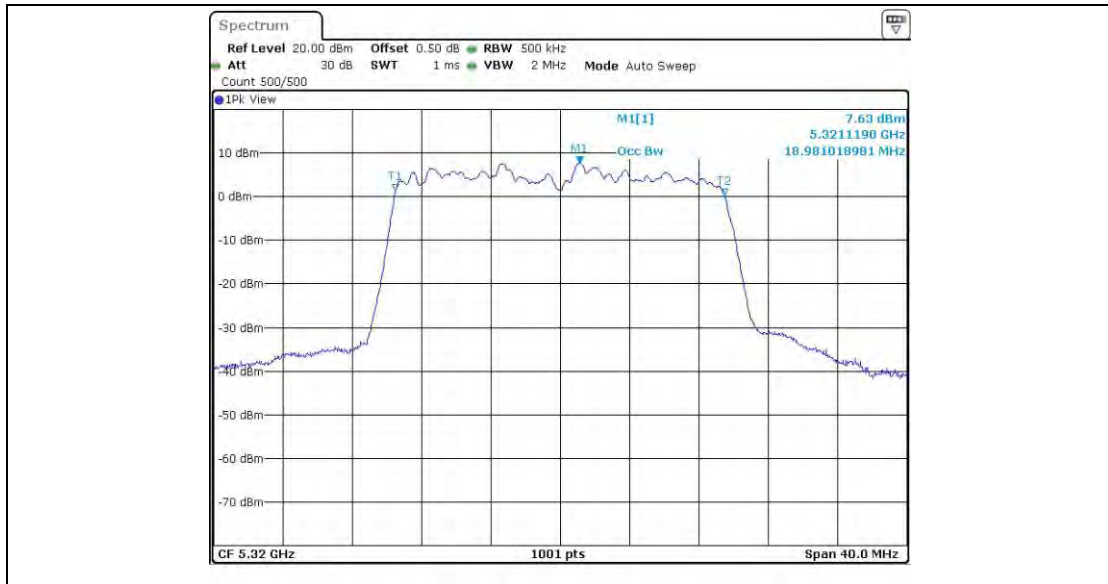
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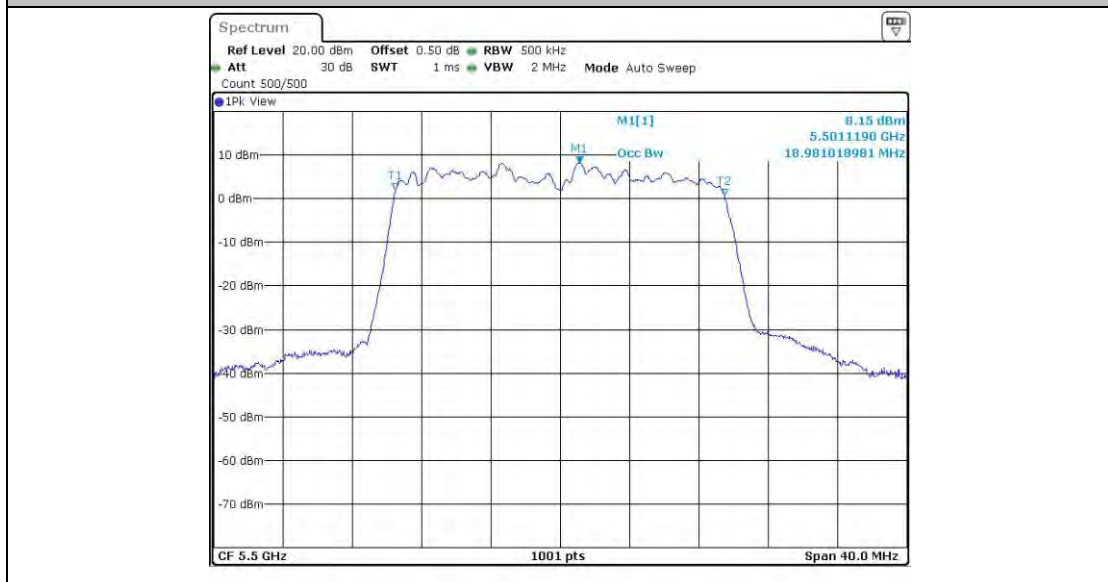
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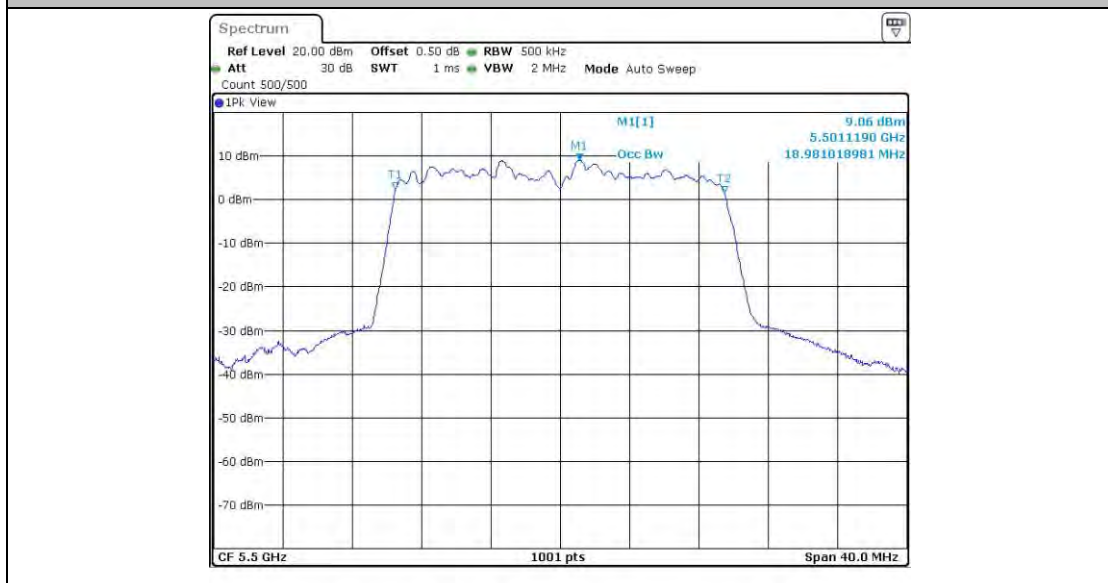
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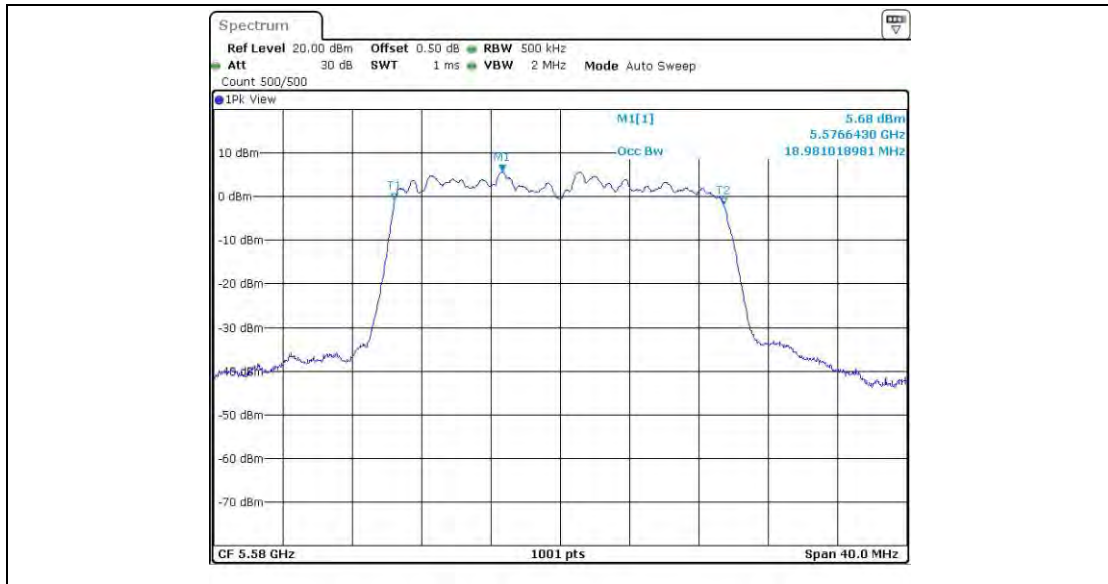
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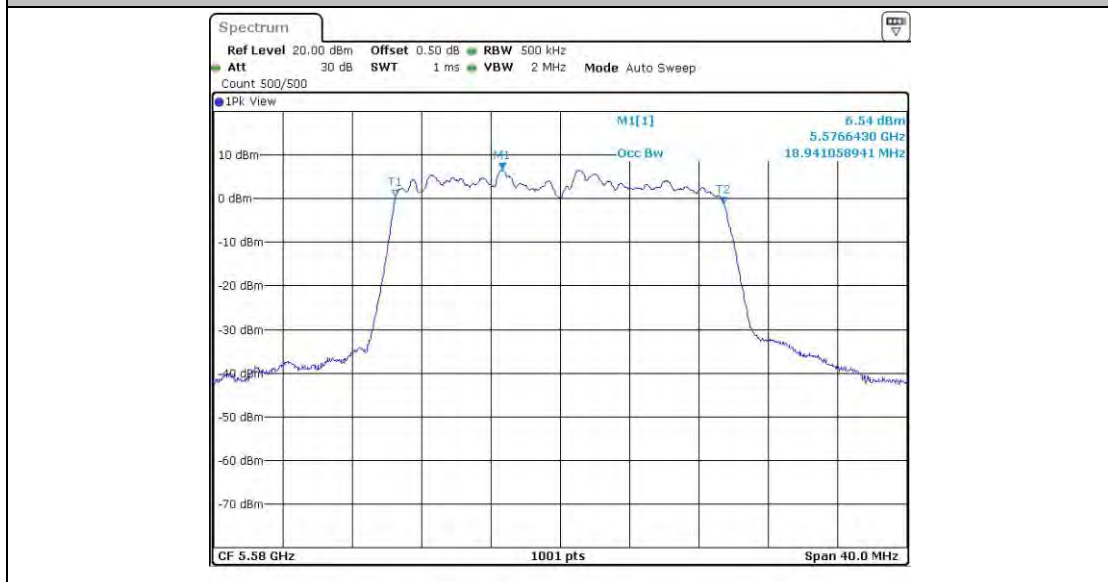
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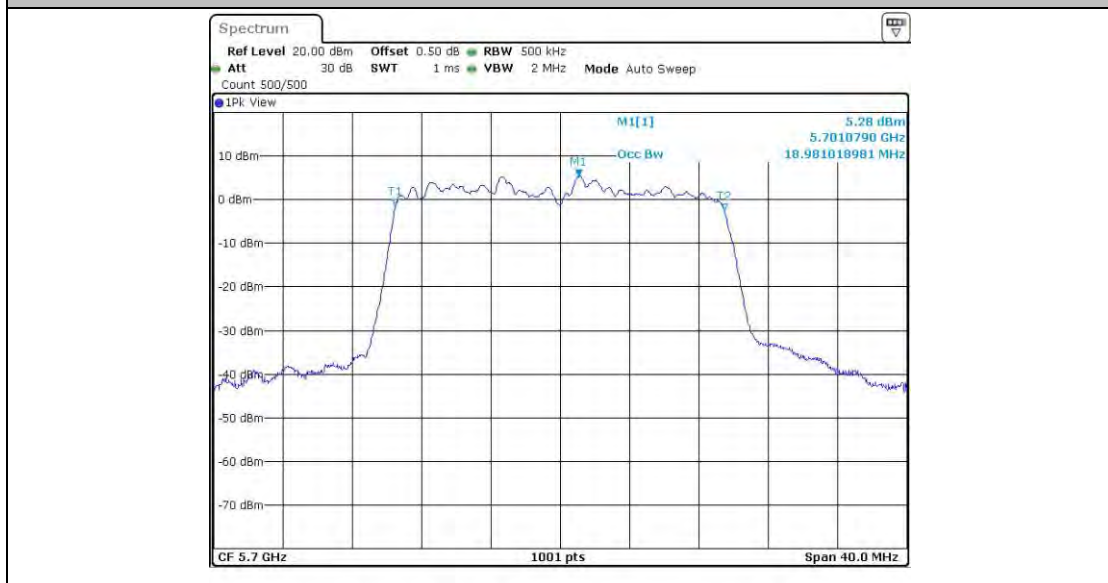
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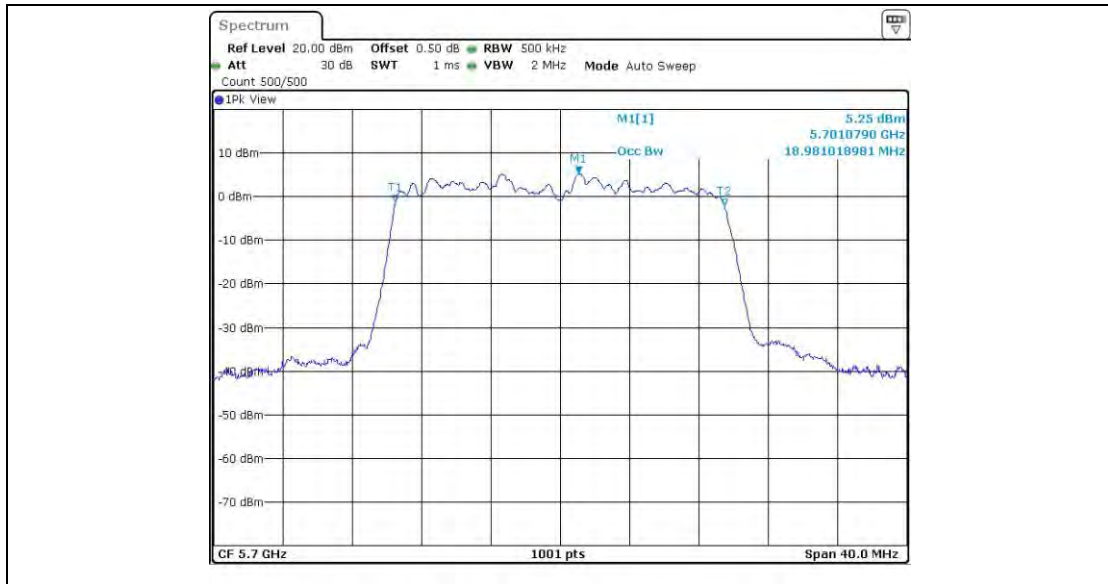
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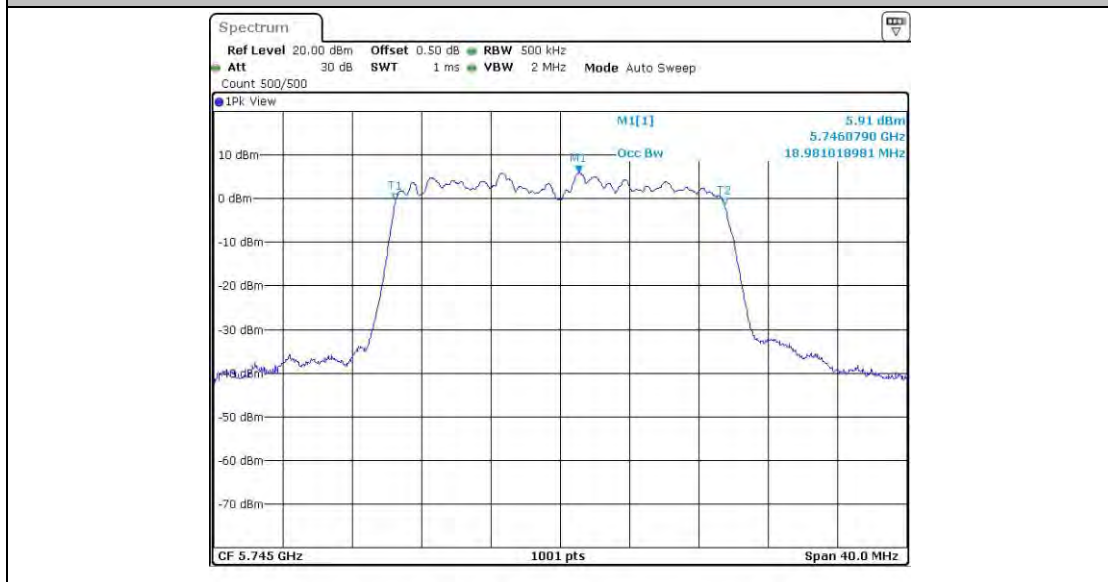
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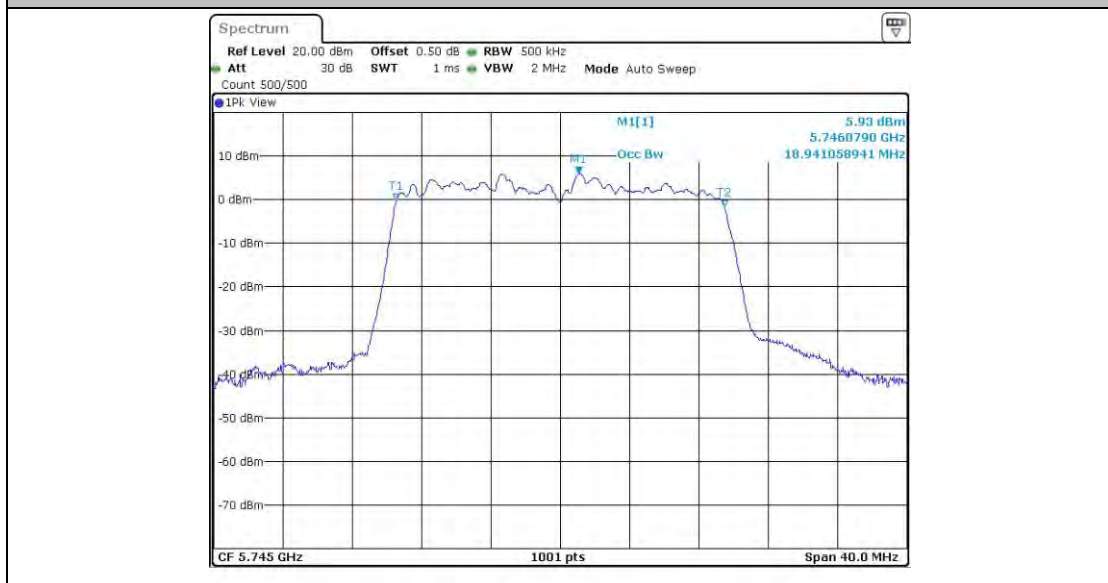
11AX20MIMO_Ant0_5700



11AX20MIMO_Ant1_5700



11AX20MIMO_Ant0_5745



11AX20MIMO_Ant1_5745