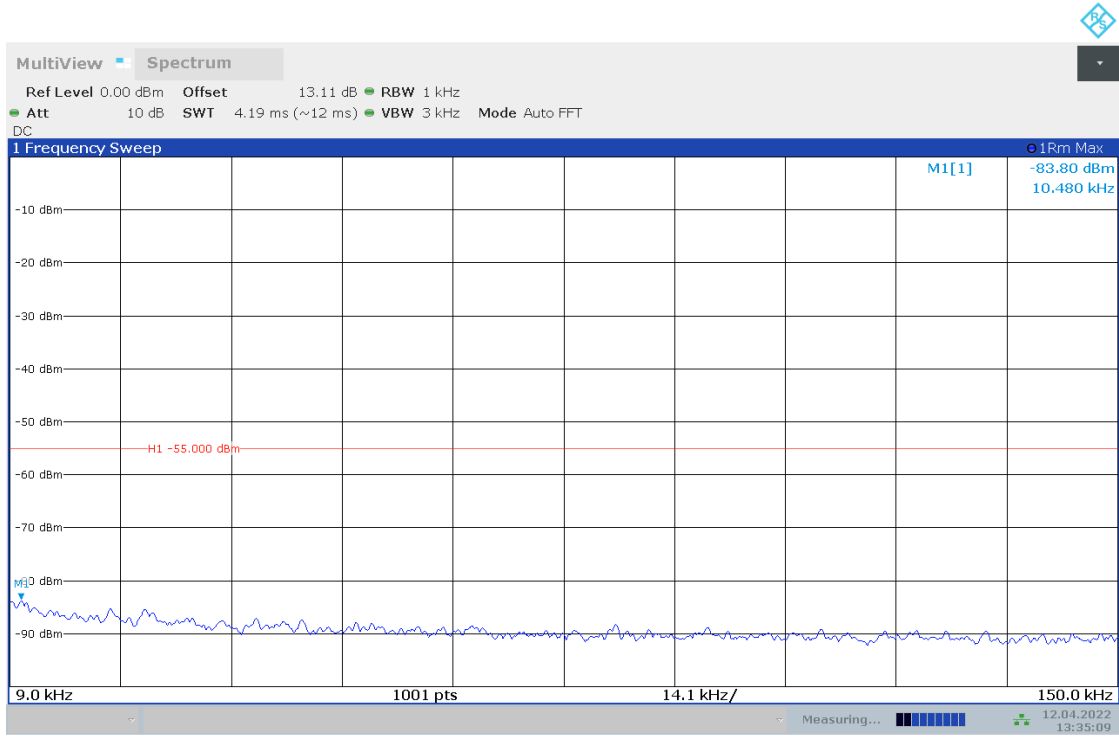
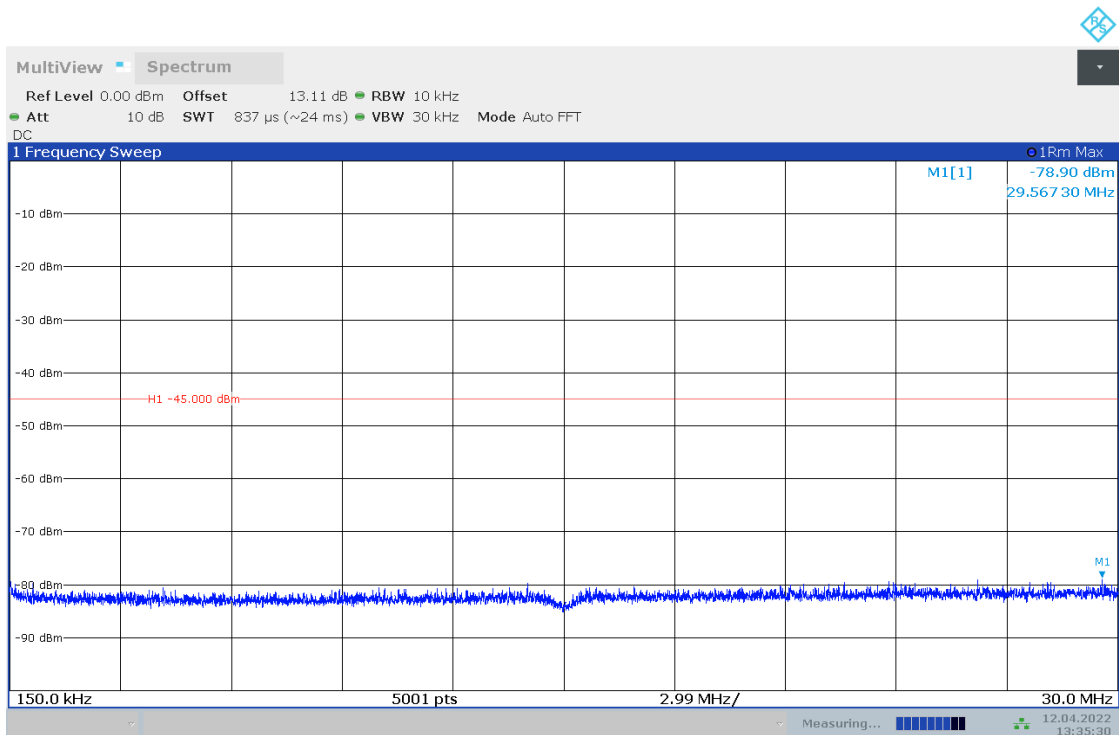


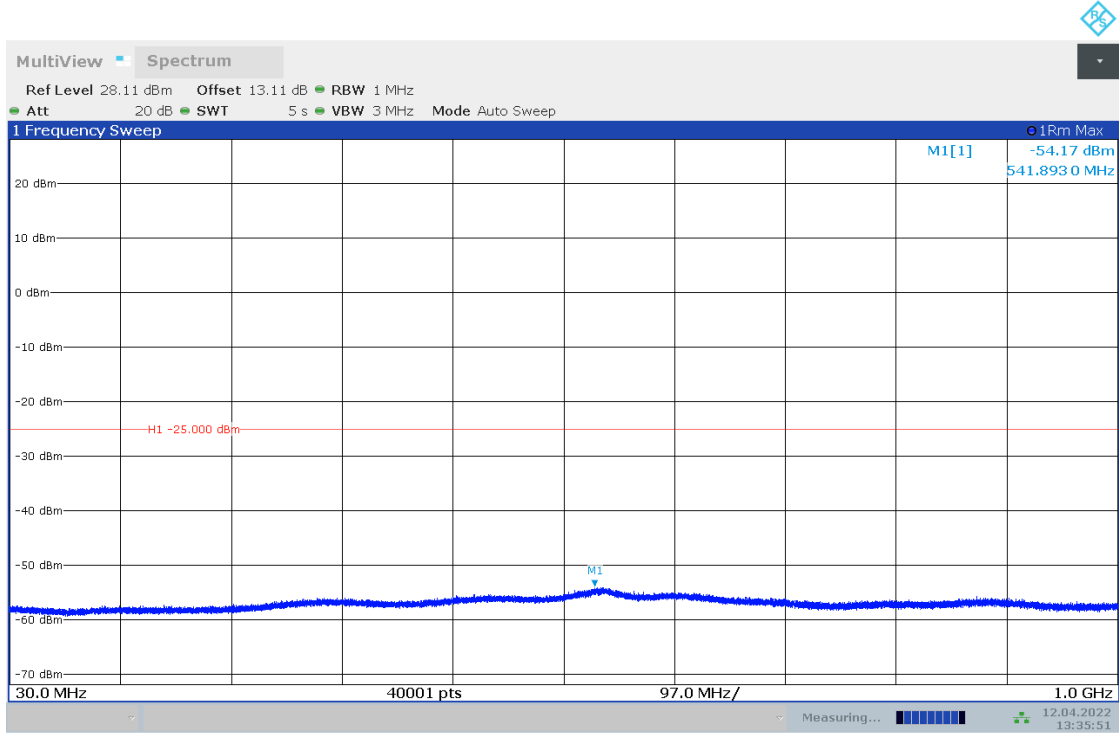
## 4.2.17 TM2\_HCH\_15MHZ+15MHZ\_PCCRB75#0\_SCCRB75#0



13:35:09 12.04.2022



13:35:30 12.04.2022

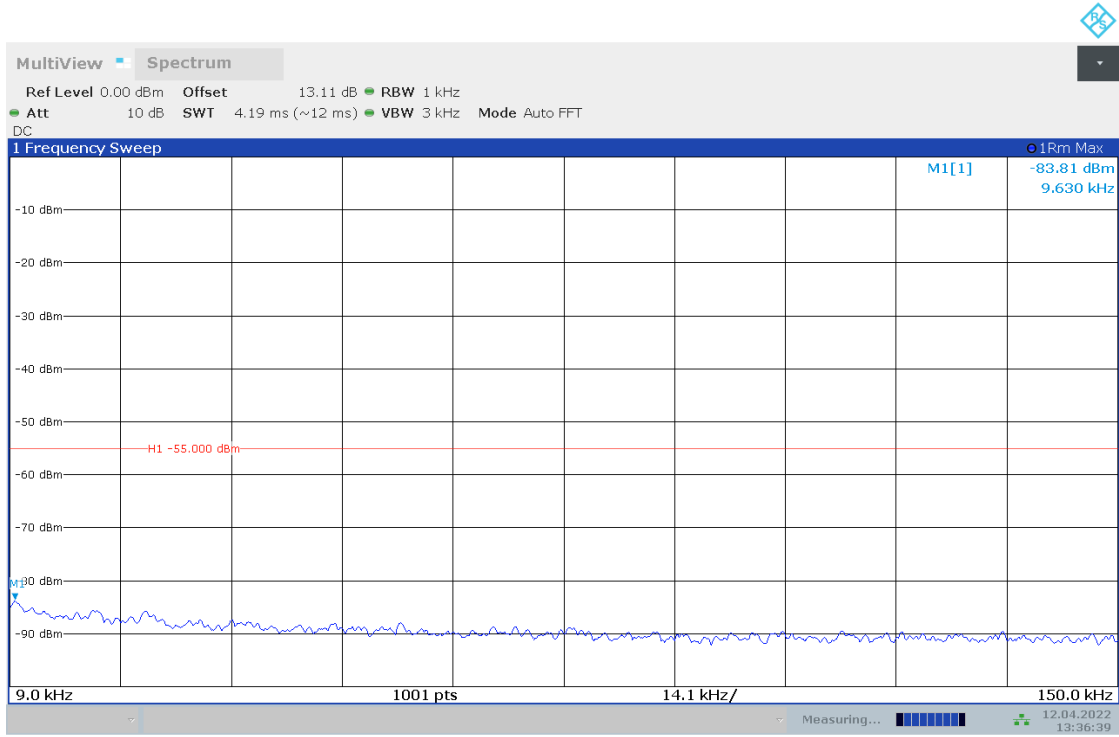


13:35:52 12.04.2022

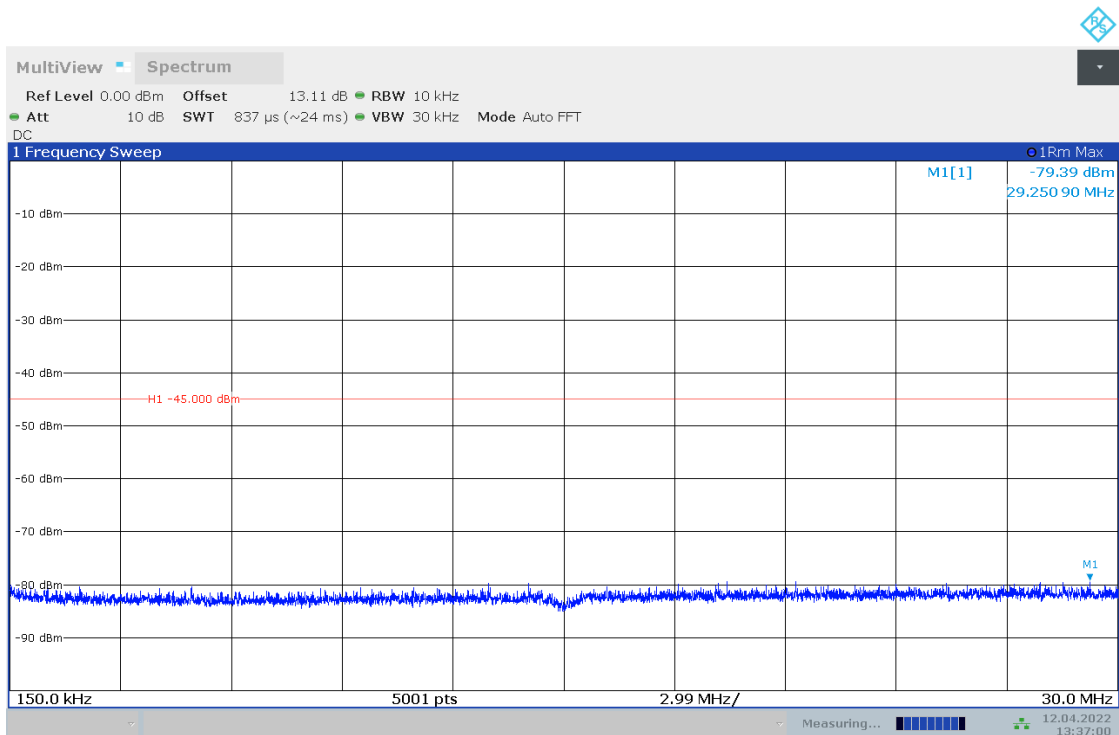


13:36:13 12.04.2022

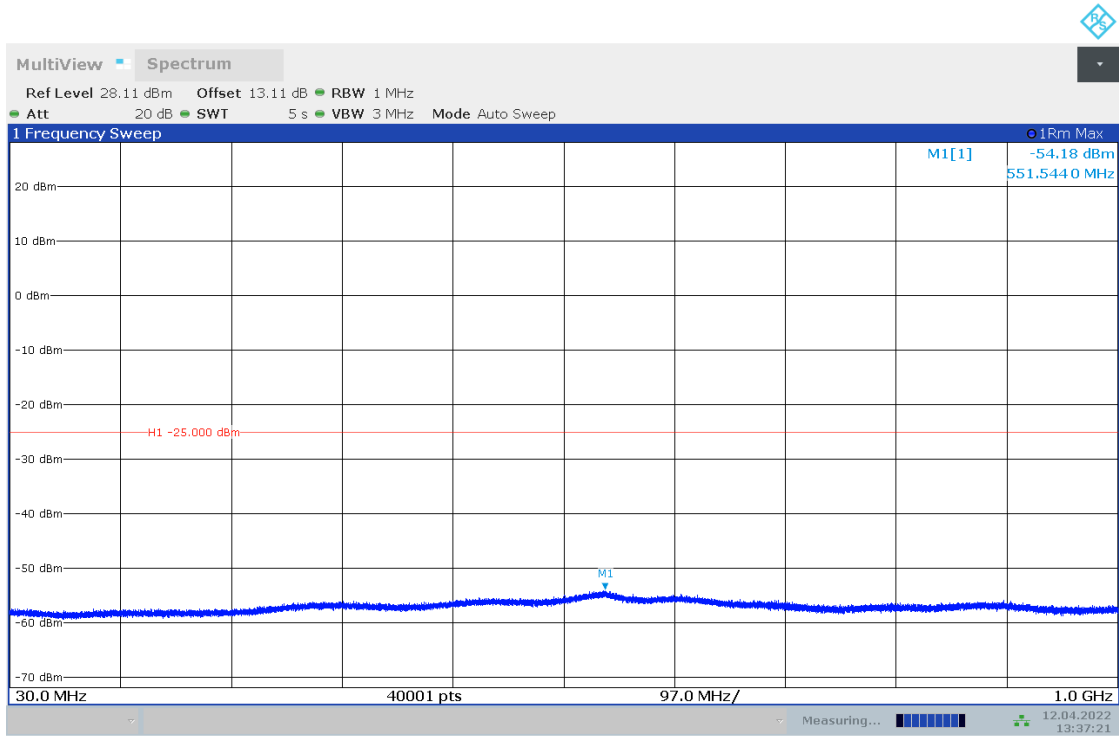
## 4.2.18 TM2\_HCH\_15MHZ+15MHZ\_PCCRB1#0\_SCCRB1#74



13:36:39 12.04.2022



13:37:00 12.04.2022

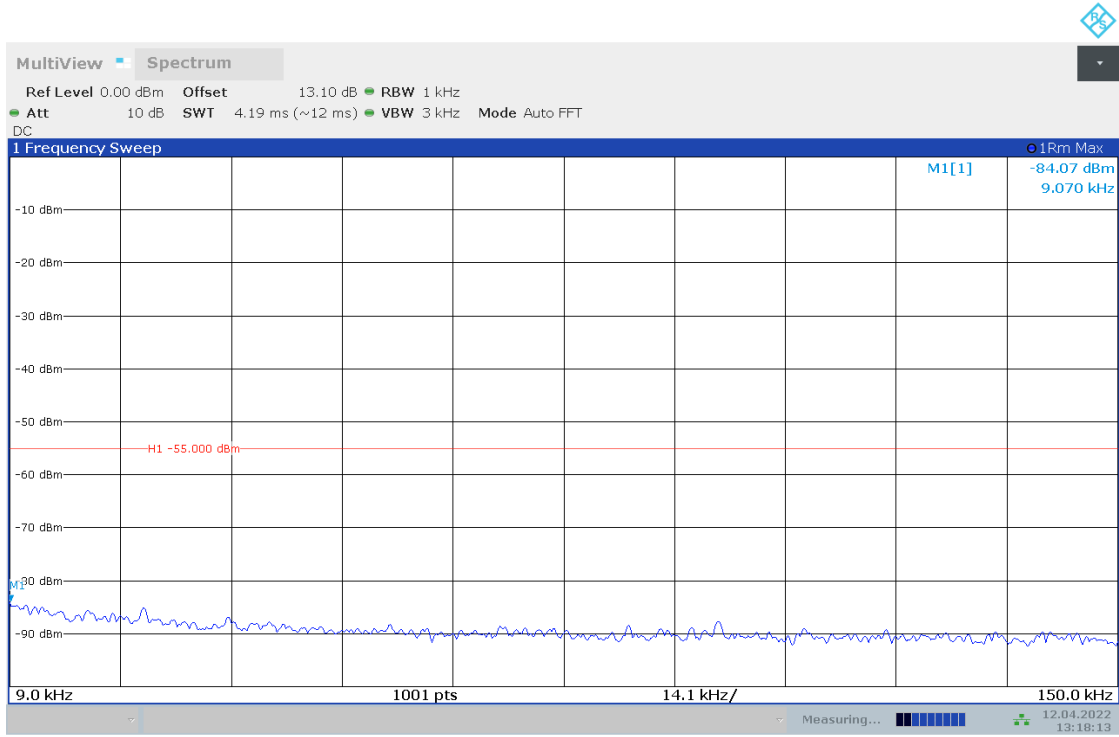


13:37:21 12.04.2022

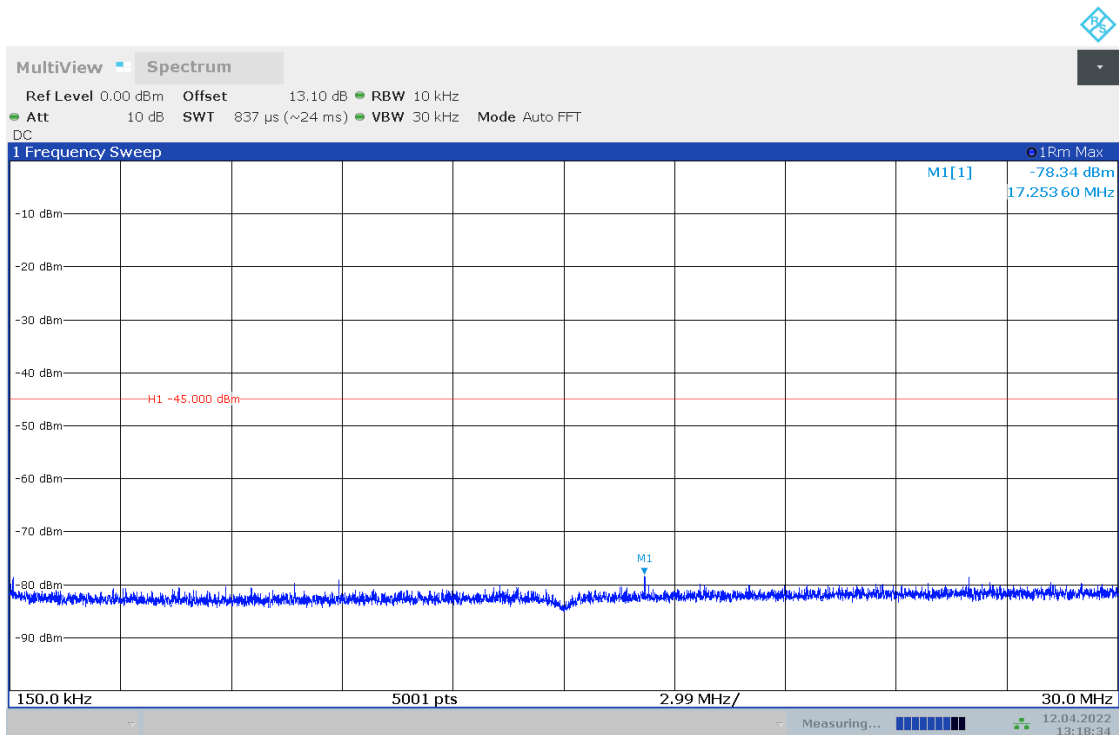


13:37:43 12.04.2022

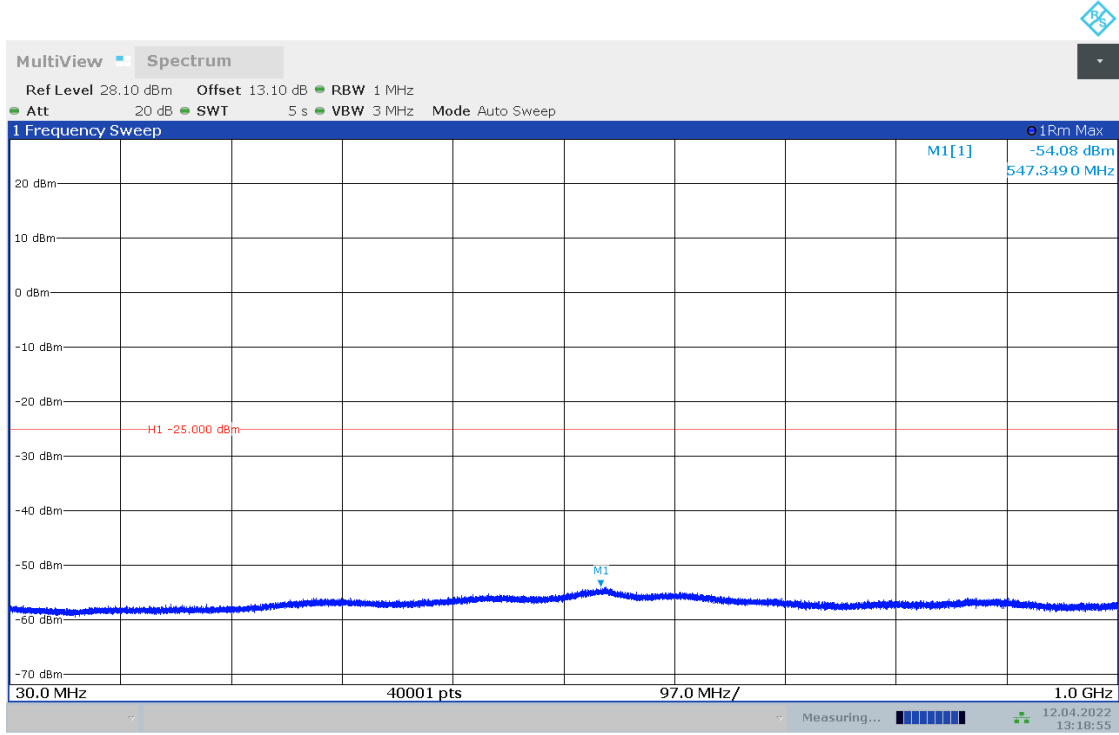
## 4.2.19 TM2\_LCH\_20MHZ+20MHZ\_PCCRB100#0\_SCCRB100#0



13:18:14 12.04.2022



13:18:35 12.04.2022

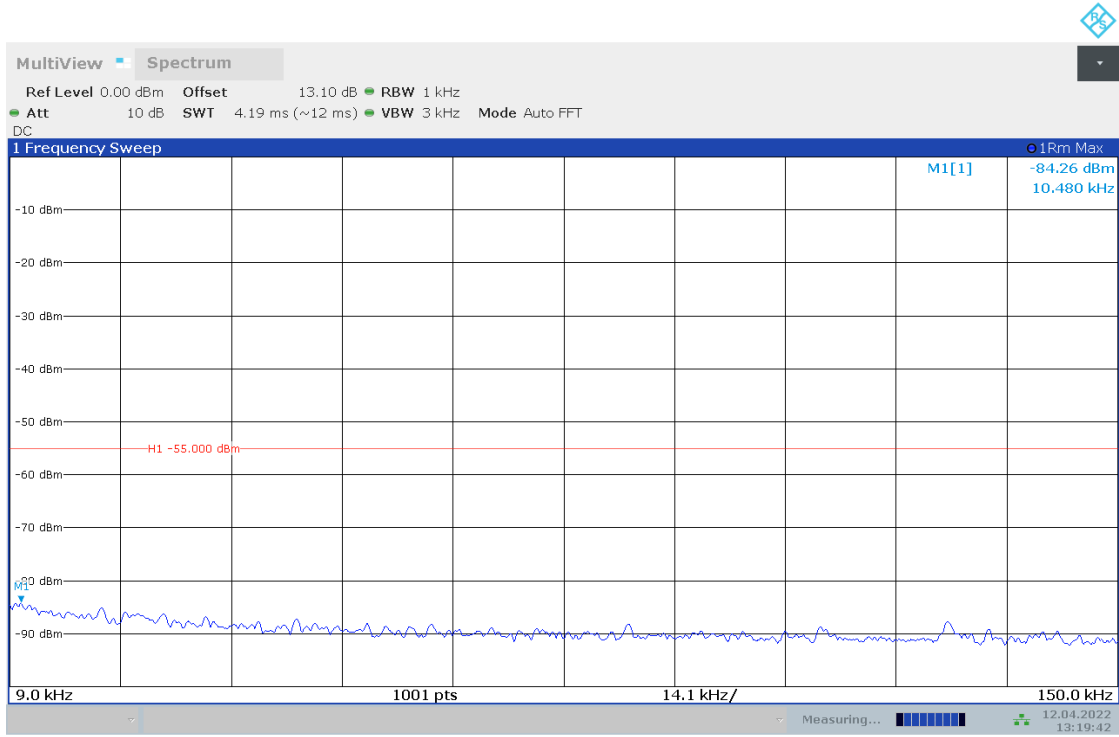


13:18:56 12.04.2022

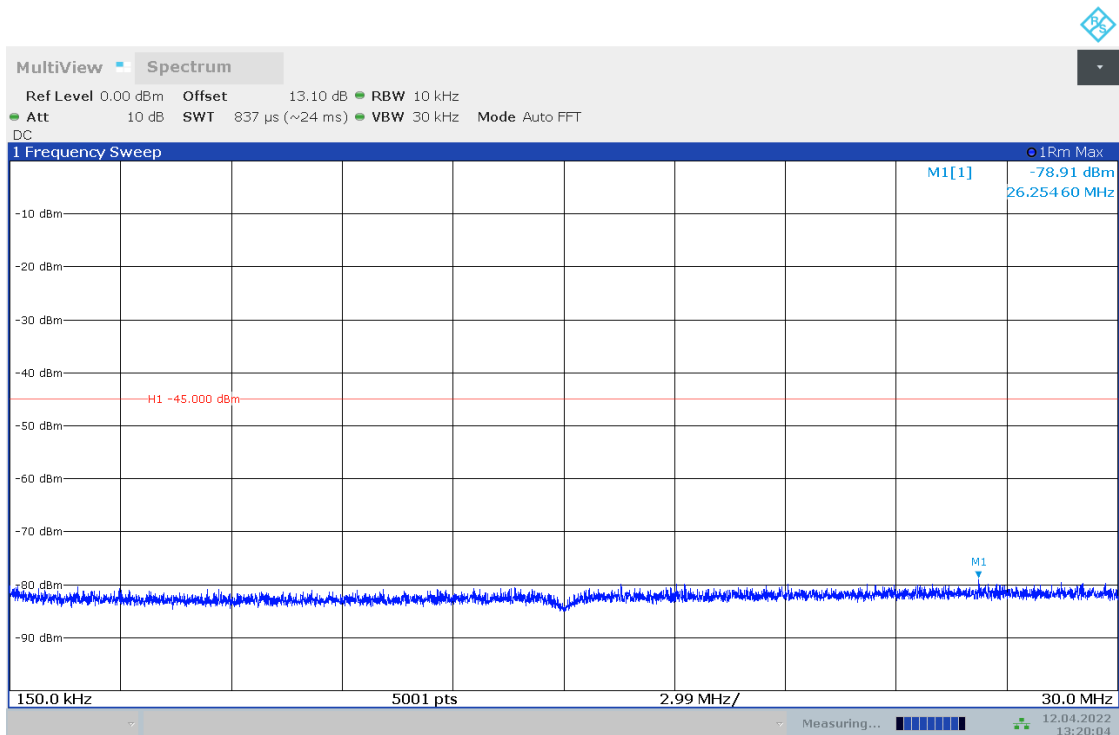


13:19:17 12.04.2022

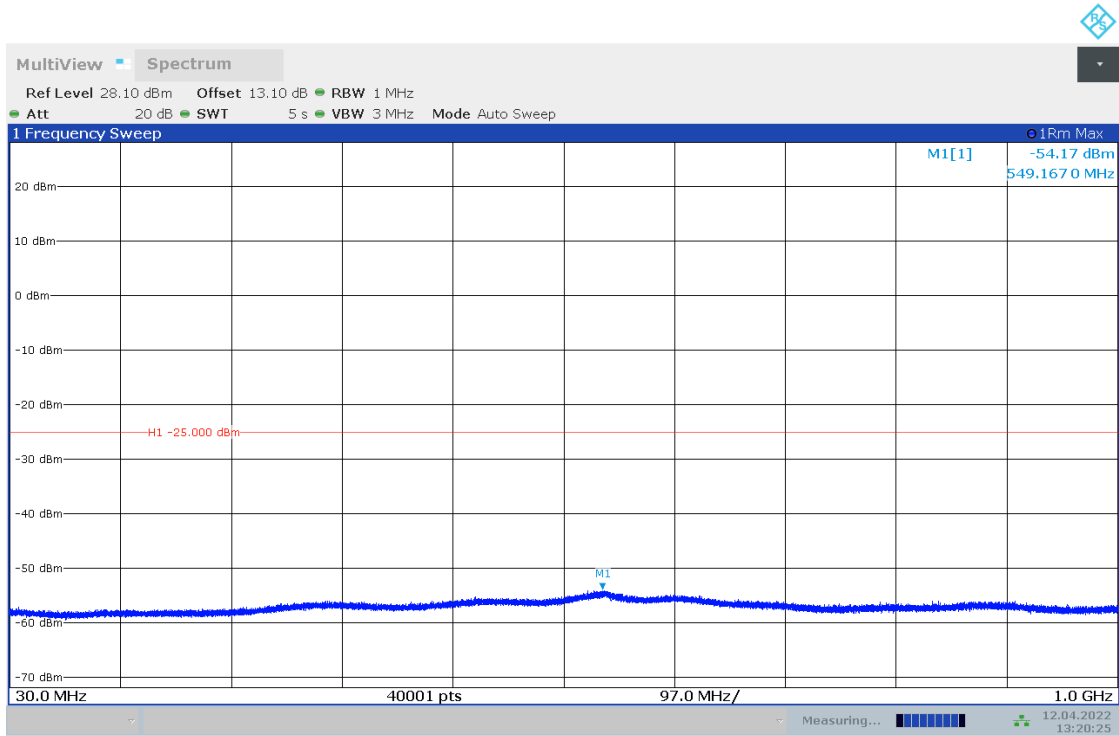
## 4.2.20 TM2\_LCH\_20MHZ+20MHZ\_PCCRB1#0\_SCCRB1#99



13:19:43 12.04.2022



13:20:05 12.04.2022



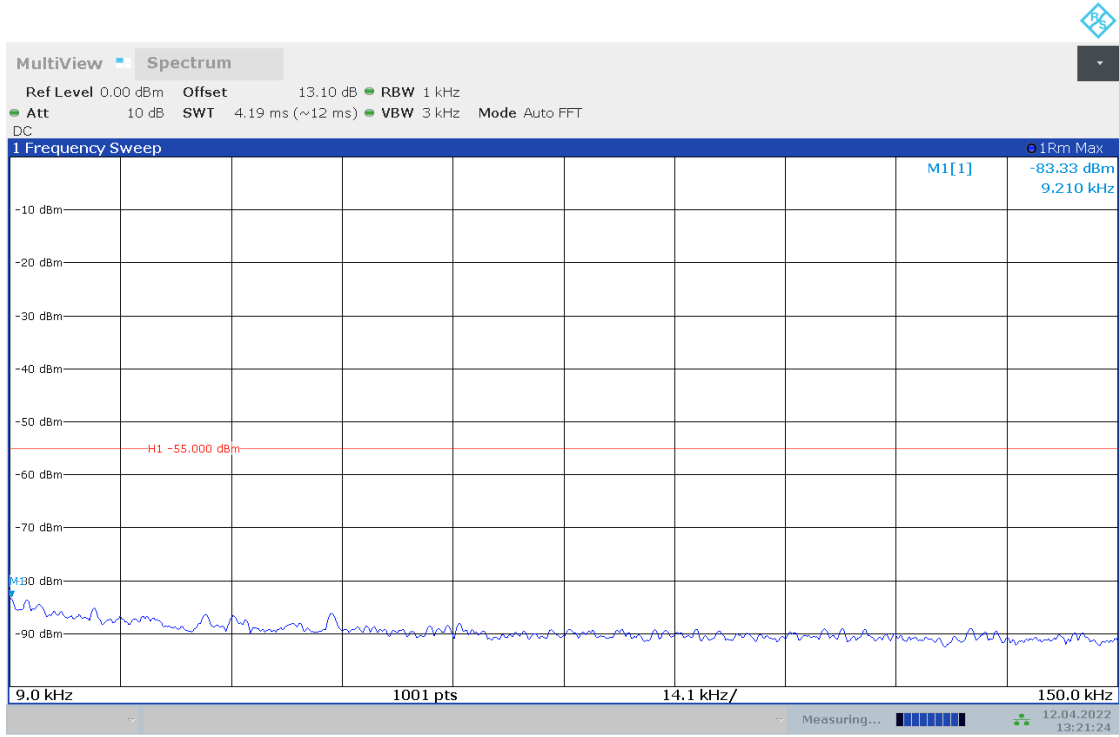
13:20:26 12.04.2022



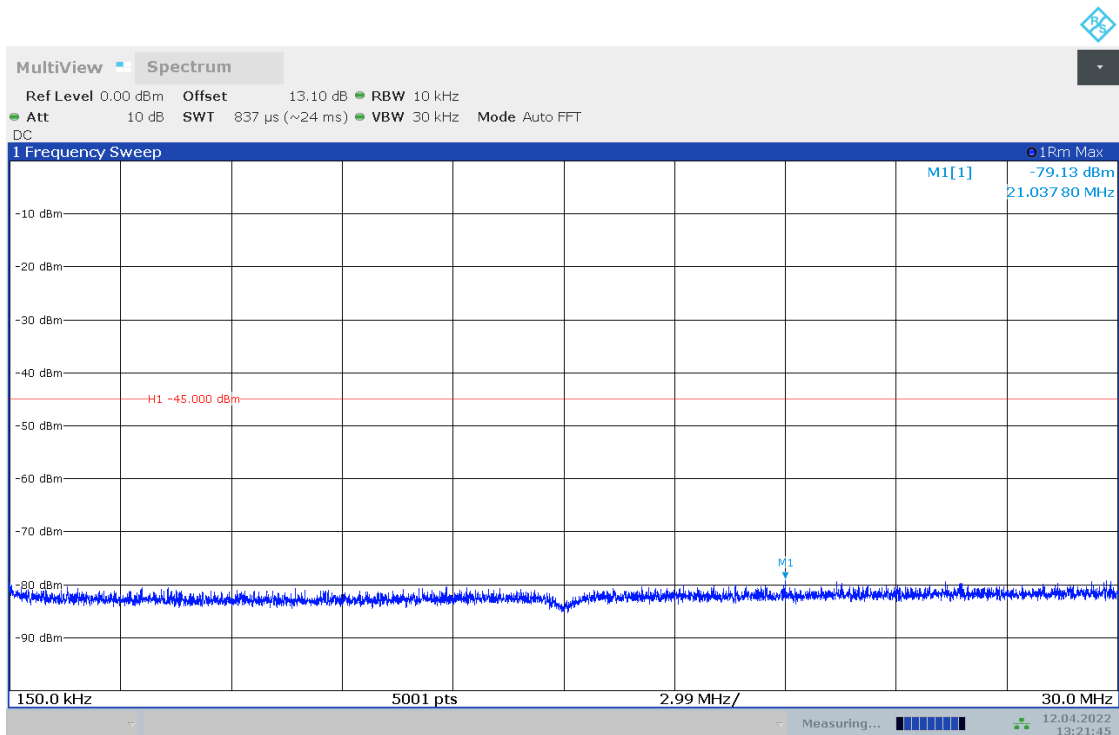
13:20:47 12.04.2022



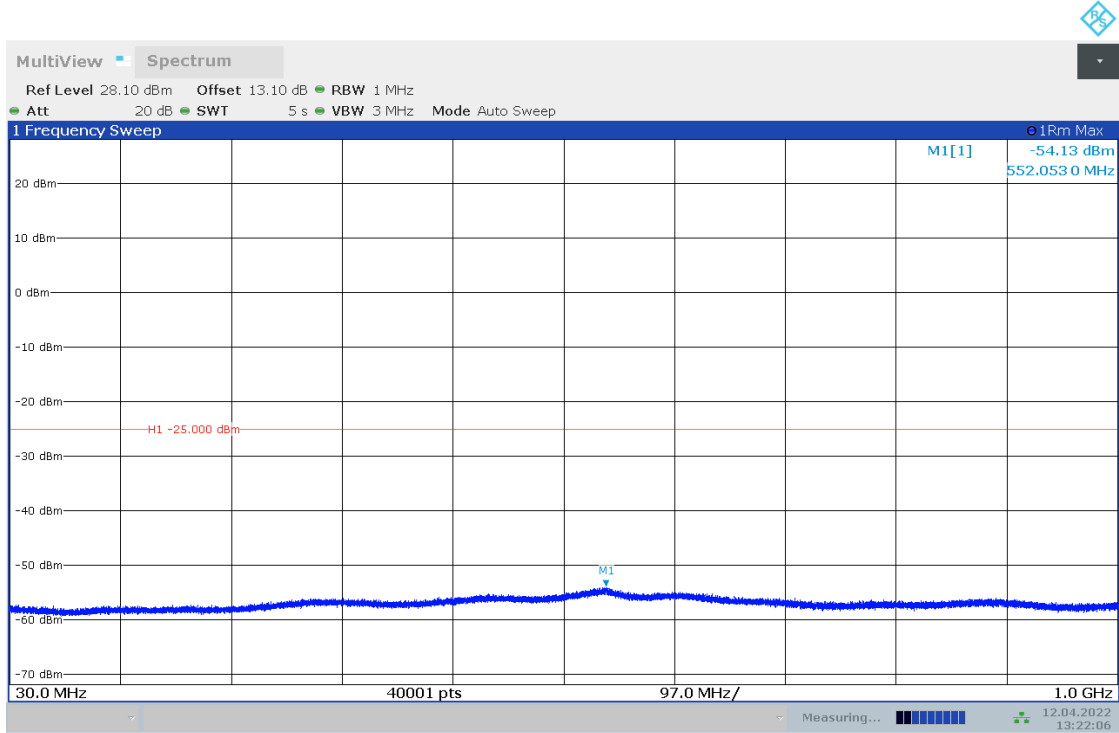
## 4.2.21 TM2\_MCH\_20MHZ+20MHZ\_PCCRB100#0\_SCCRB100#0



13:21:25 12.04.2022



13:21:46 12.04.2022

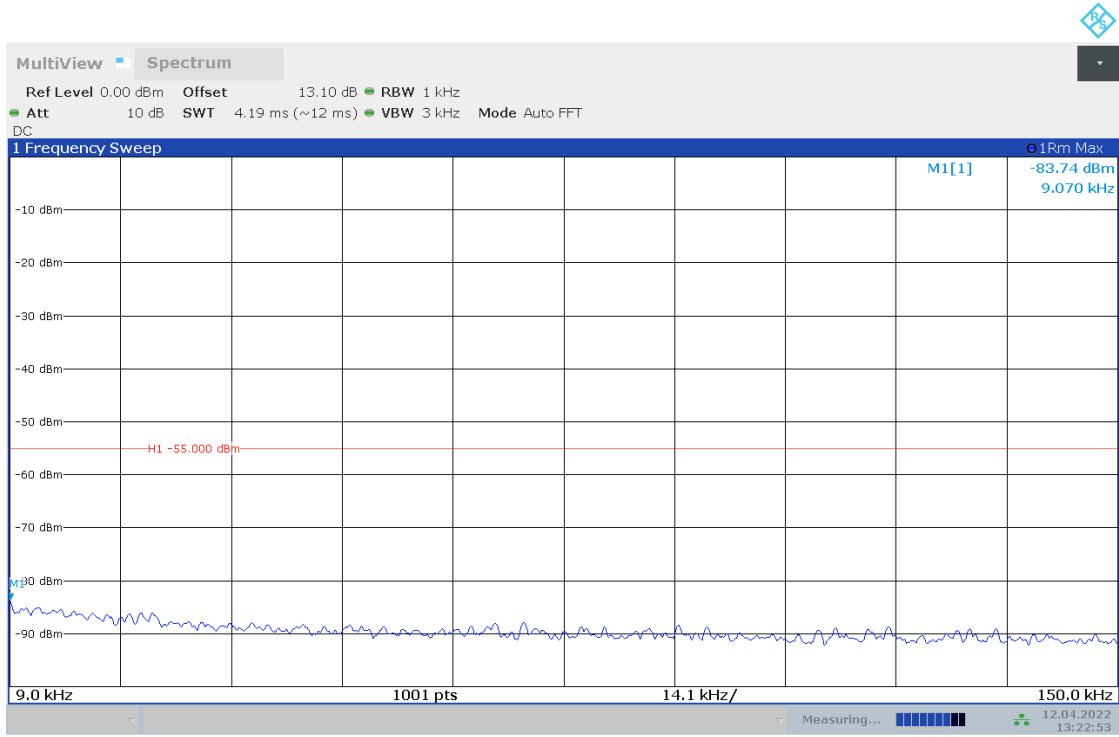


13:22:07 12.04.2022

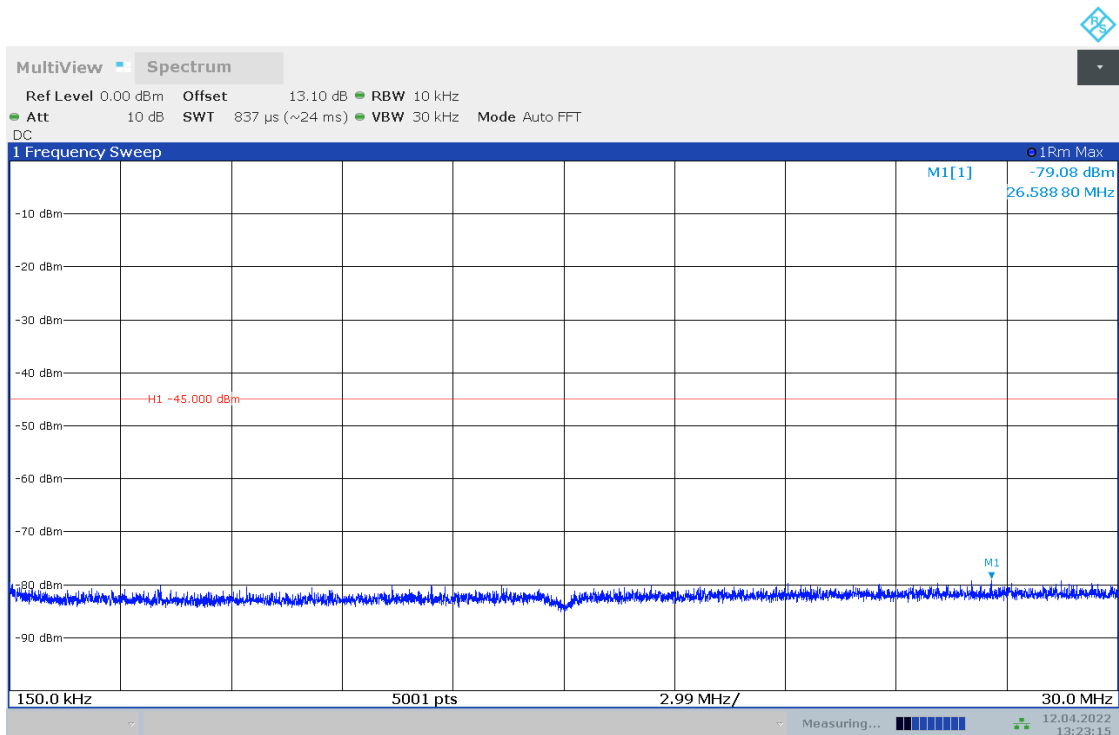


13:22:28 12.04.2022

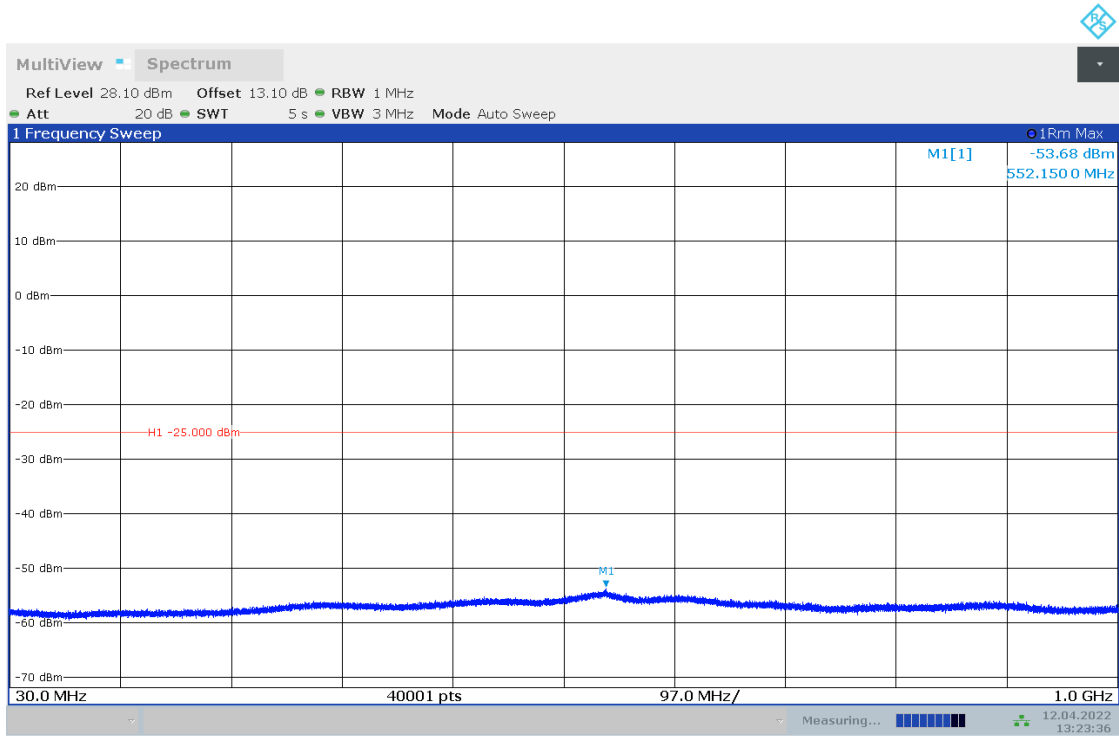
## 4.2.22 TM2\_MCH\_20MHZ+20MHZ\_PCCRB1#0\_SCCRB1#99



13:22:54 12.04.2022



13:23:16 12.04.2022

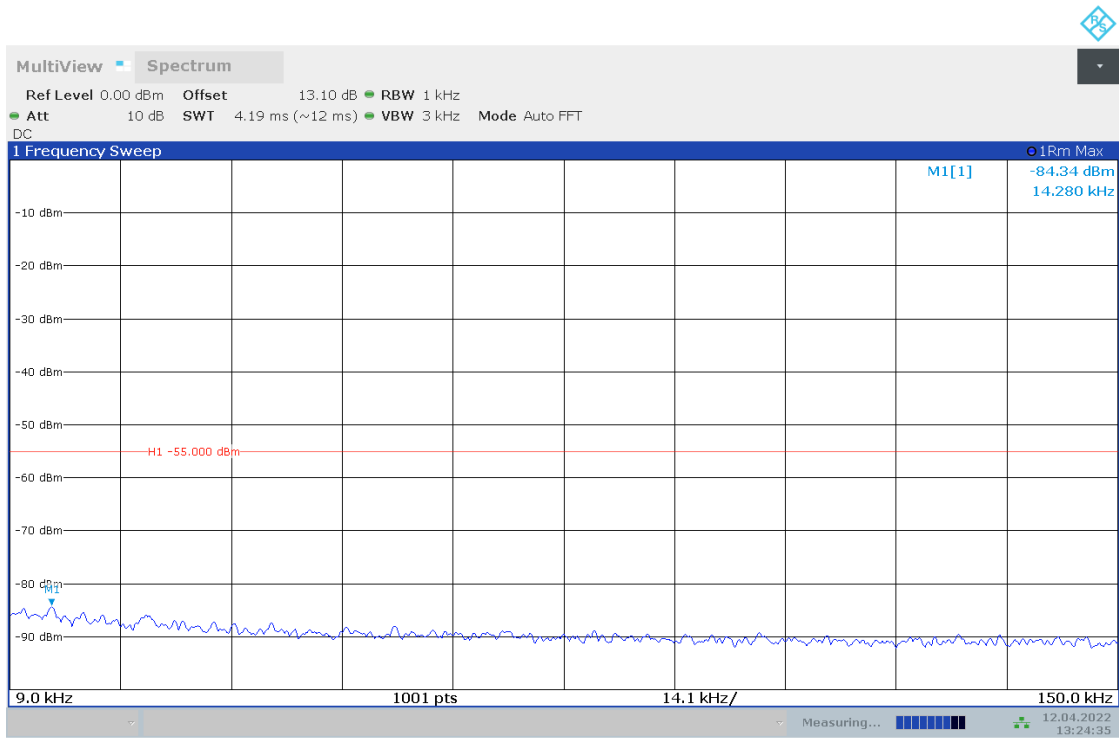


13:23:37 12.04.2022

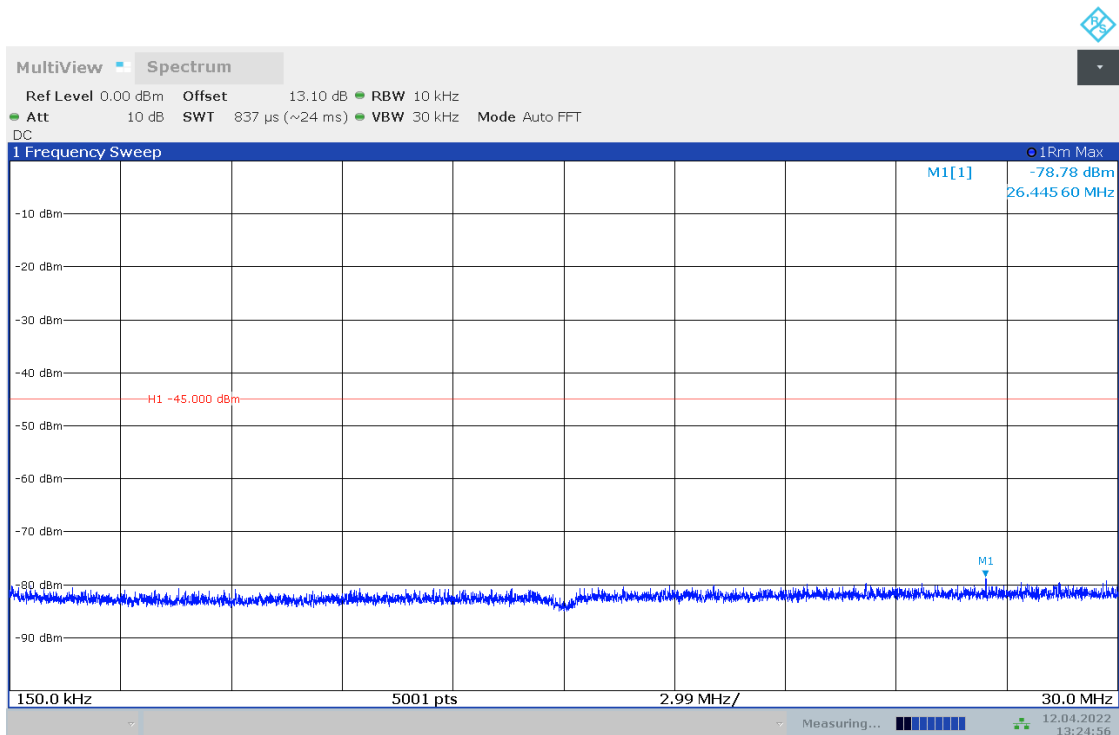


13:23:58 12.04.2022

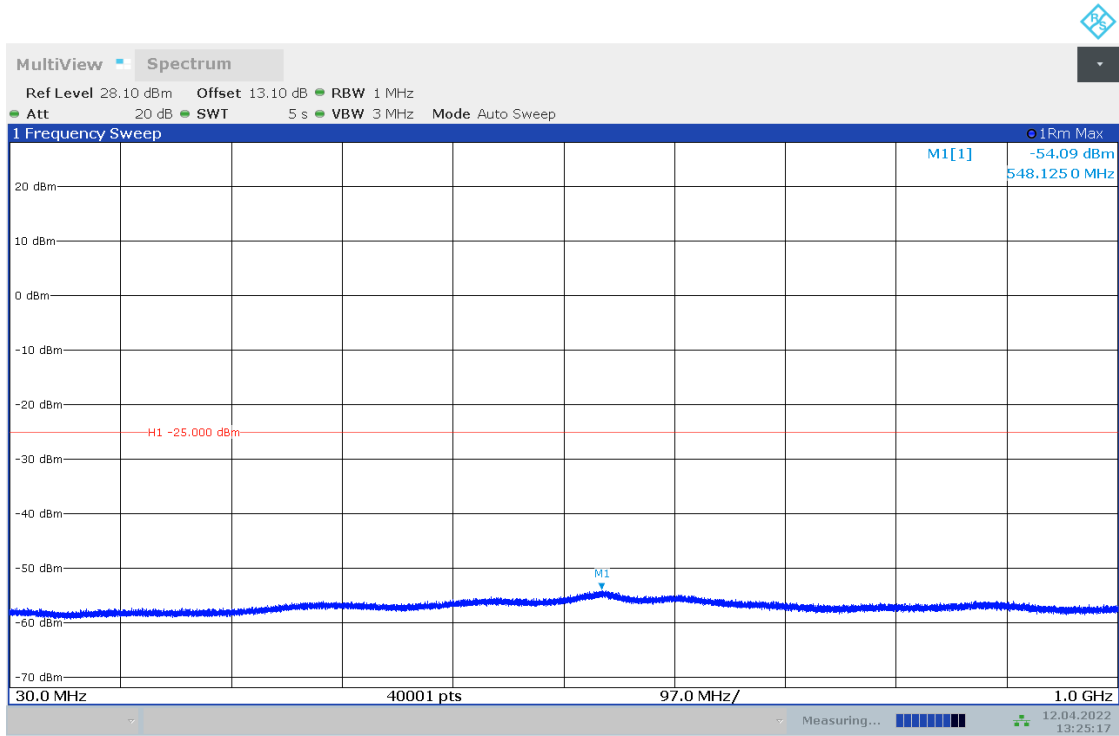
## 4.2.23 TM2\_HCH\_20MHZ+20MHZ\_PCCRB100#0\_SCCRB100#0



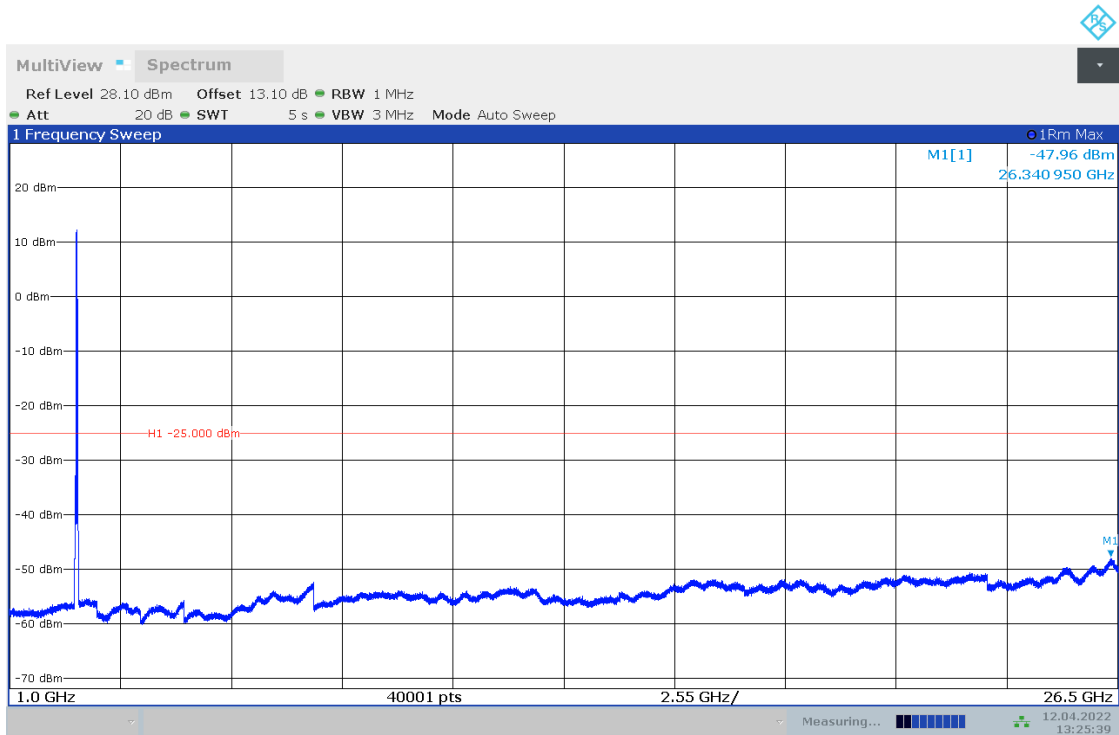
13:24:35 12.04.2022



13:24:57 12.04.2022

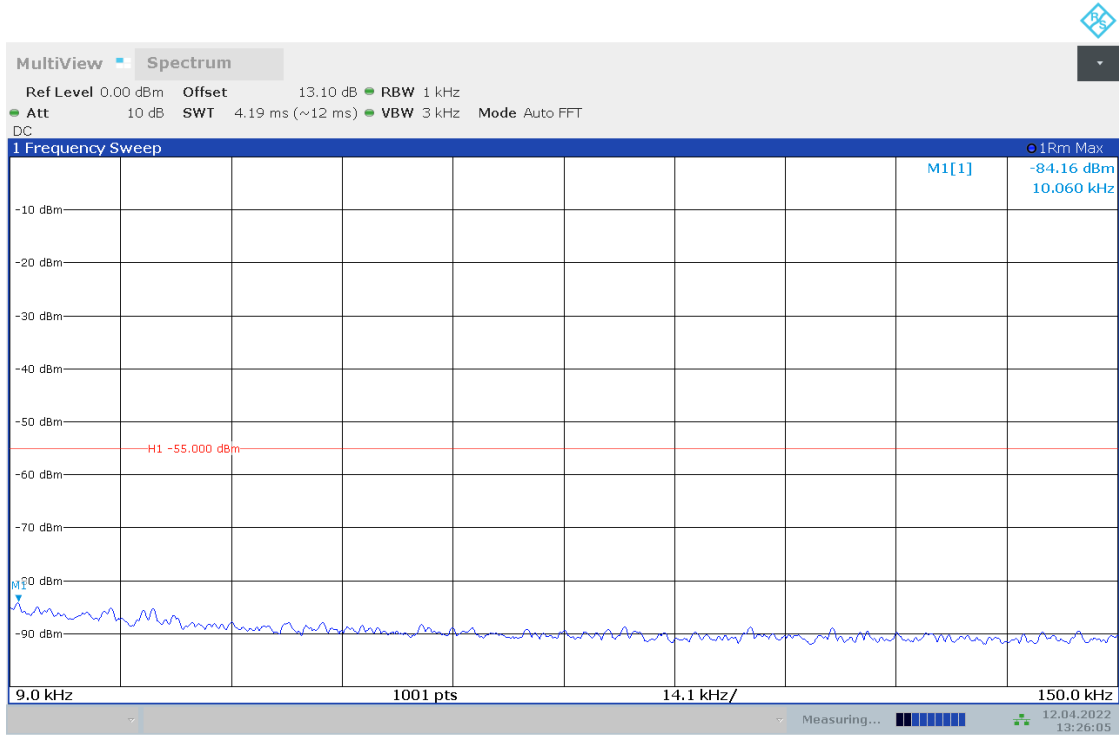


13:25:18 12.04.2022

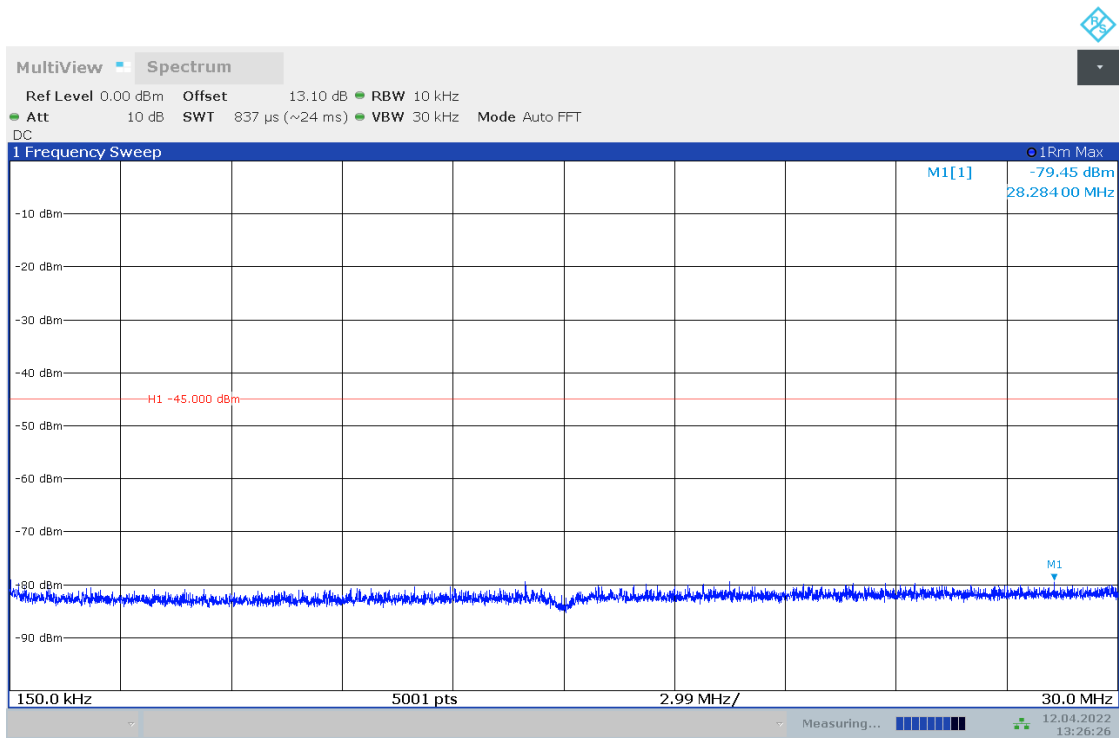


13:25:39 12.04.2022

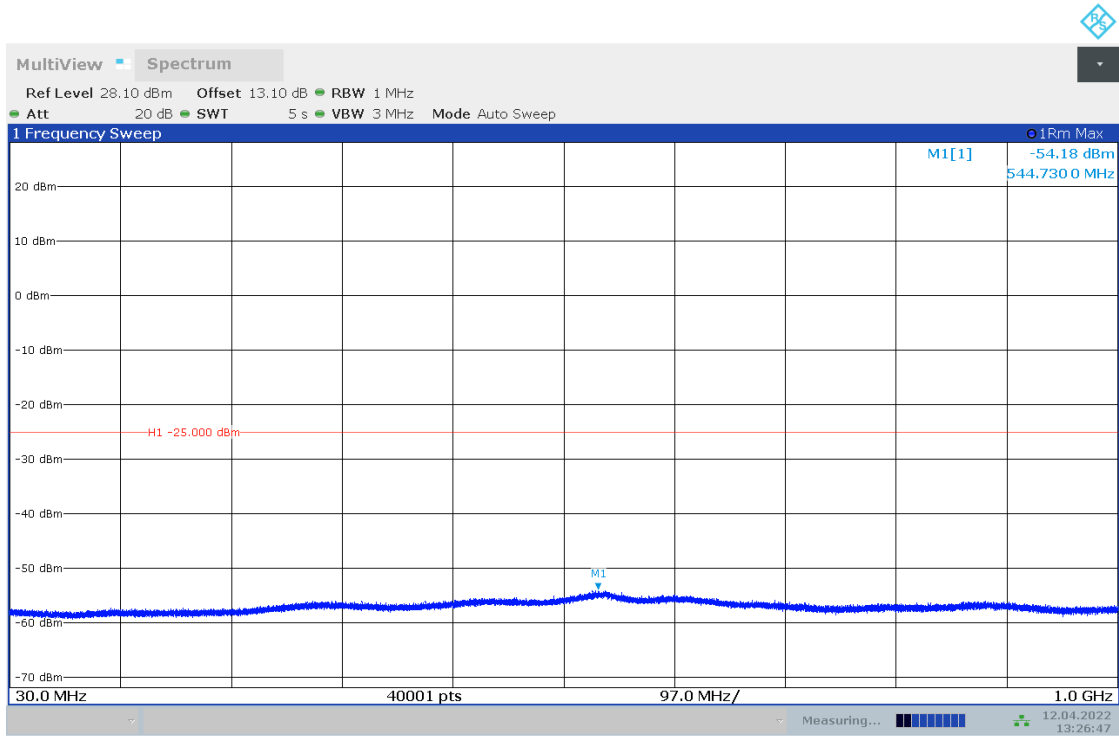
## 4.2.24 TM2\_HCH\_20MHZ+20MHZ\_PCCRB1#0\_SCCRB1#99



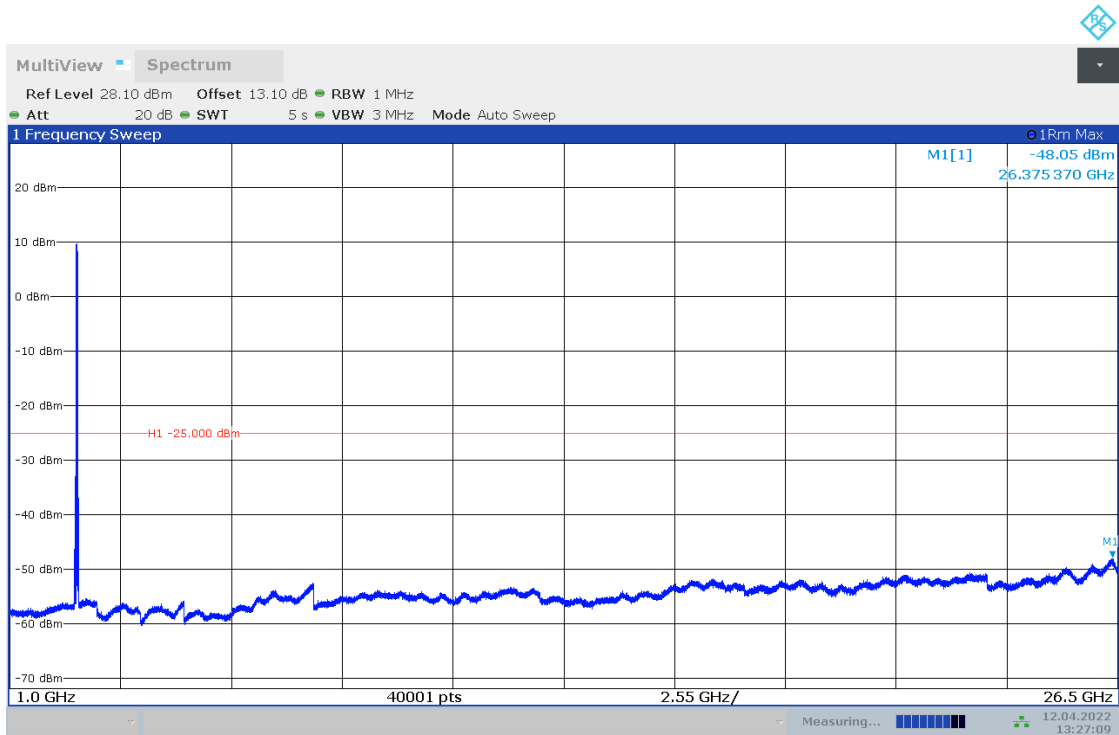
13:26:05 12.04.2022



13:26:26 12.04.2022



13:26:48 12.04.2022



13:27:09 12.04.2022

End of Test Report