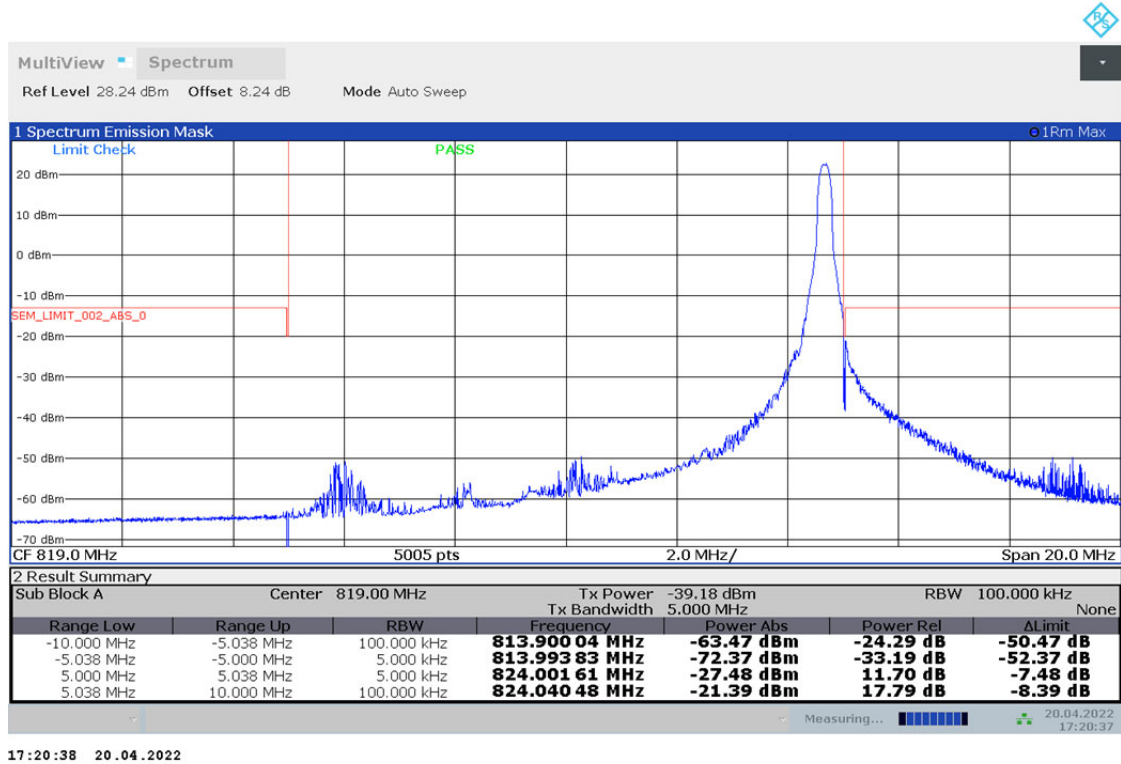
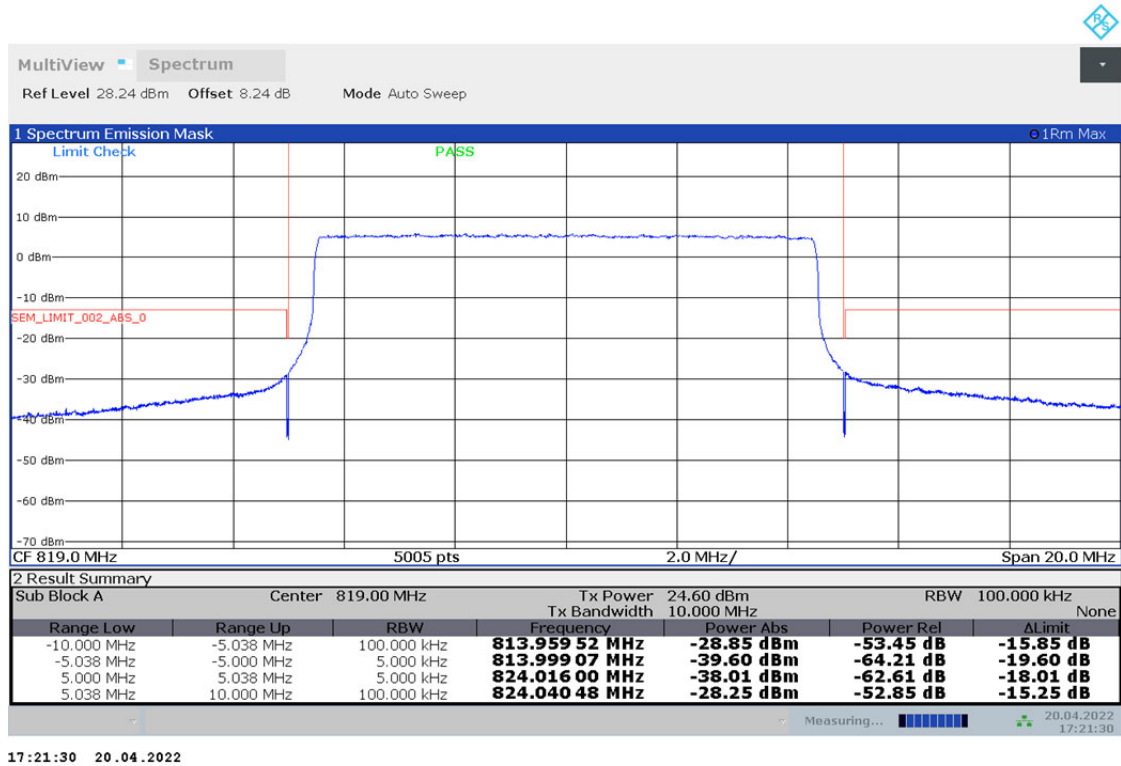


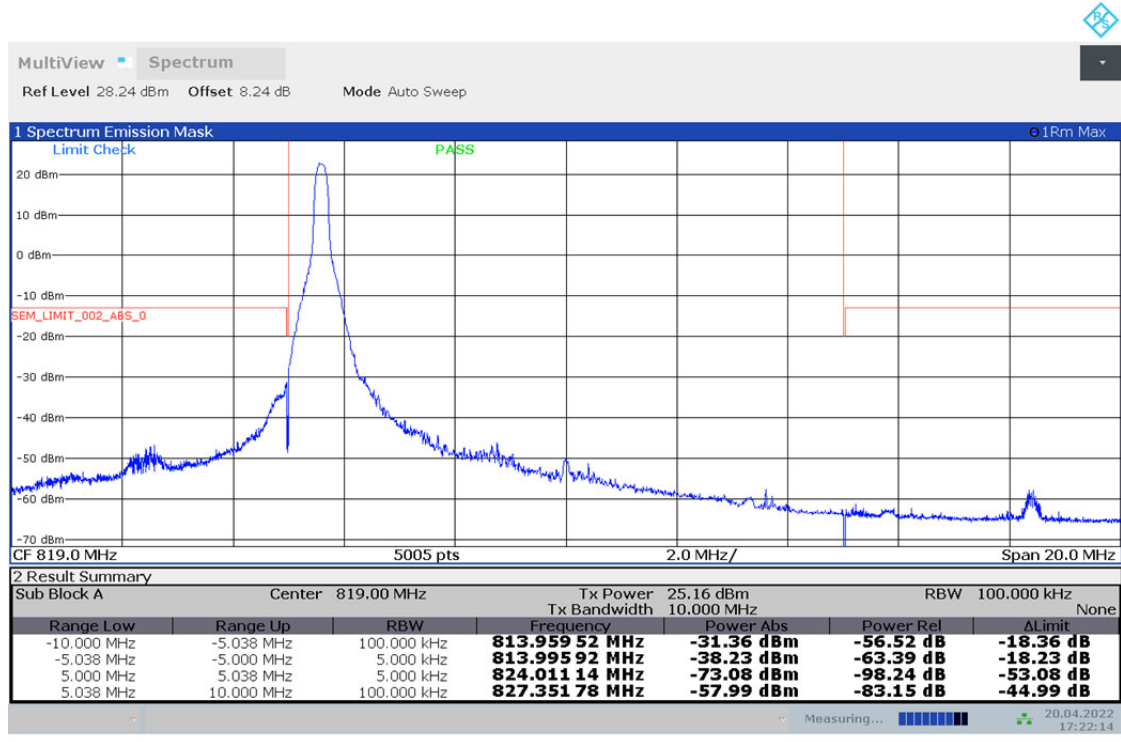
## 5.2.12 TM1\_5MHz\_HCH\_RB1#24



## 5.2.13 TM1\_10MHz\_LCH\_RB50#0

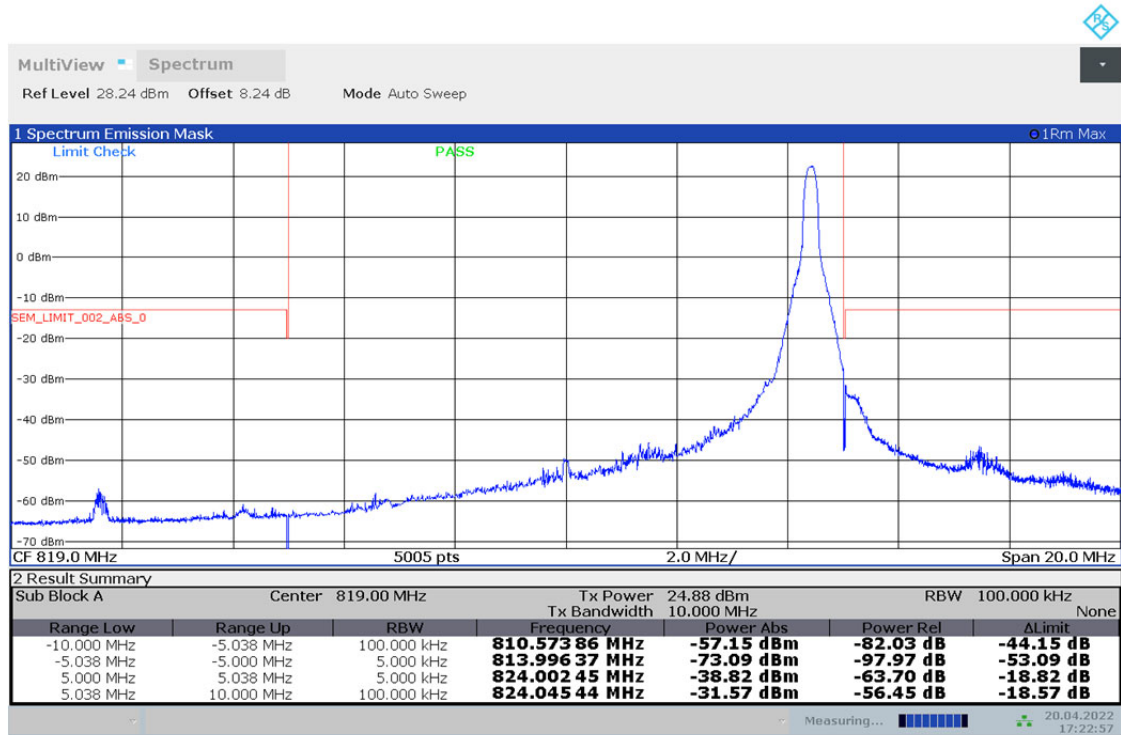


## 5.2.14 TM1\_10MHz\_LCH\_RB1#0



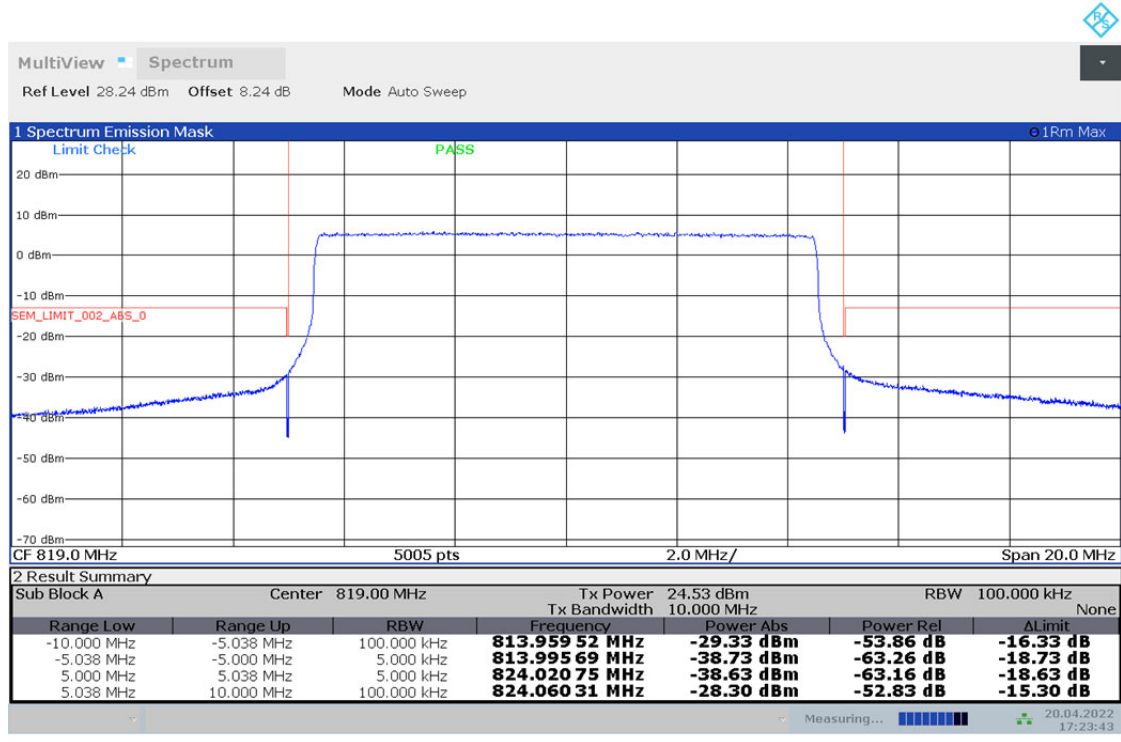
17:22:15 20.04.2022

## 5.2.15 TM1\_10MHz\_LCH\_RB1#49



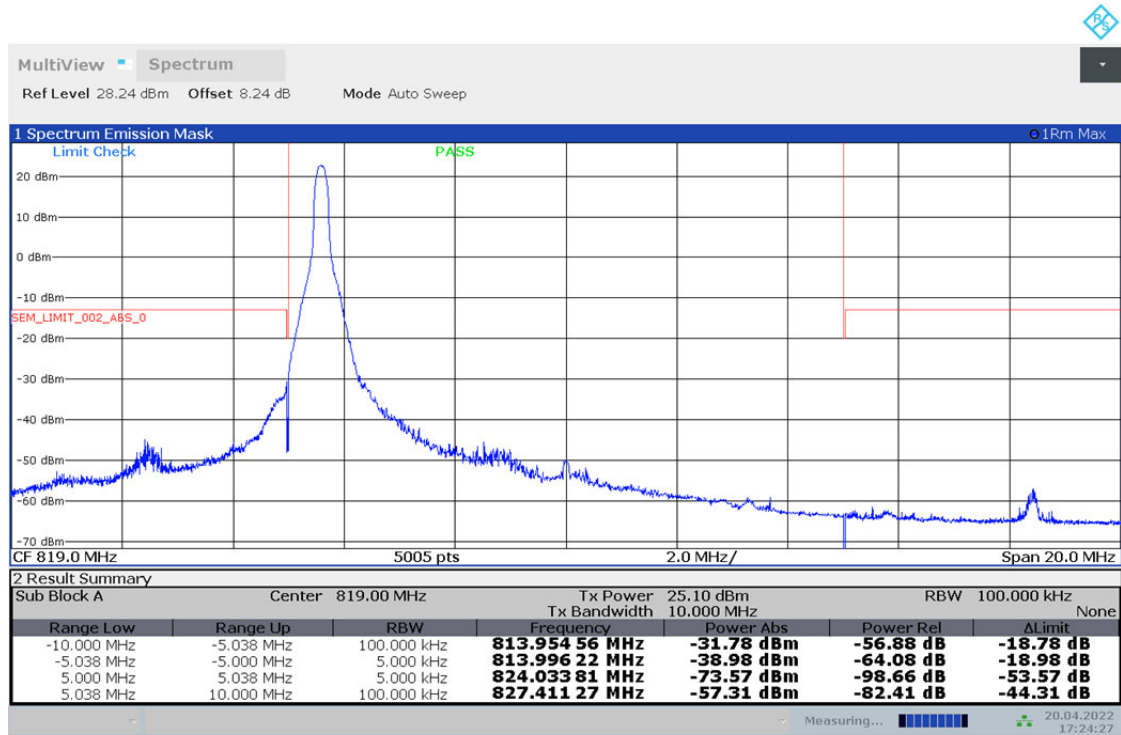
17:22:58 20.04.2022

## 5.2.16 TM1\_10MHz\_HCH\_RB50#0



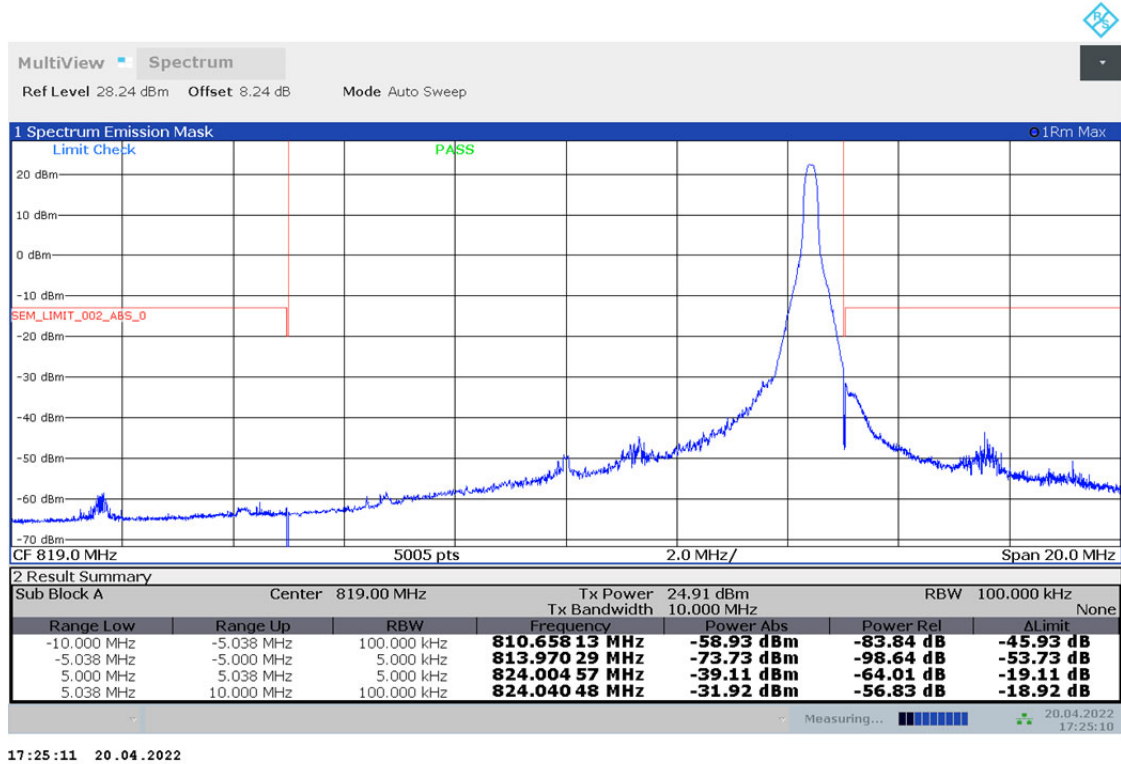
17:23:44 20.04.2022

## 5.2.17 TM1\_10MHz\_HCH\_RB1#0

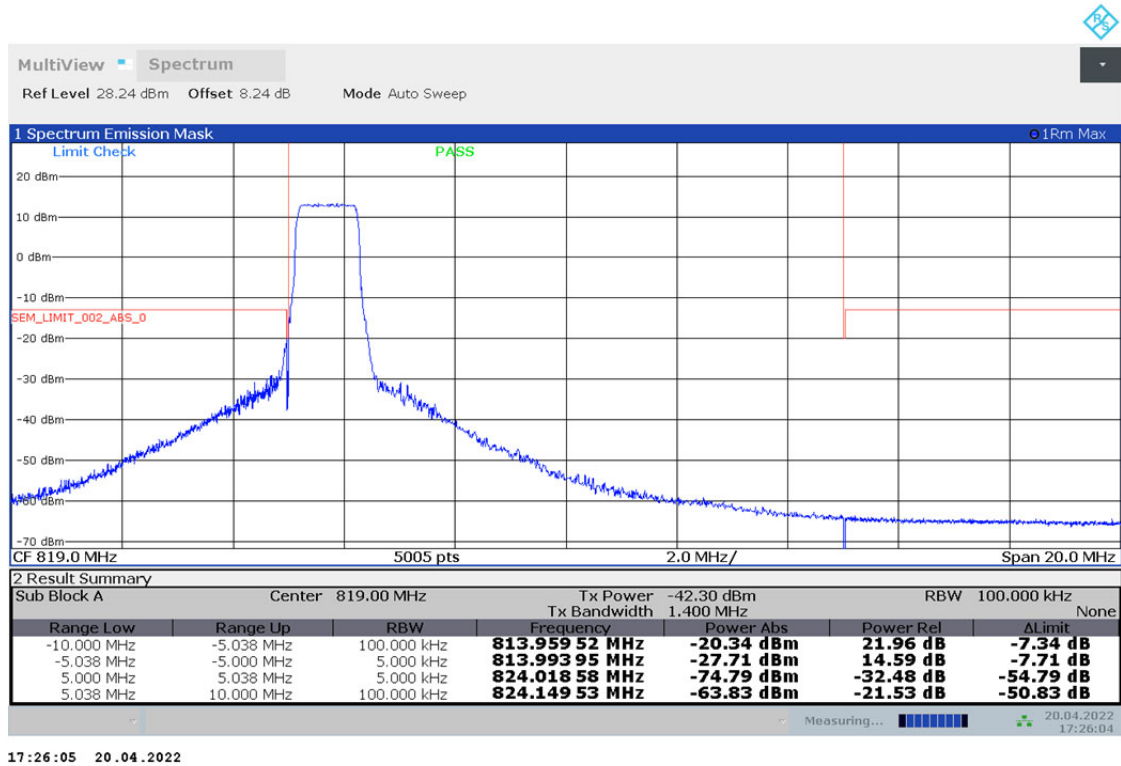


17:24:28 20.04.2022

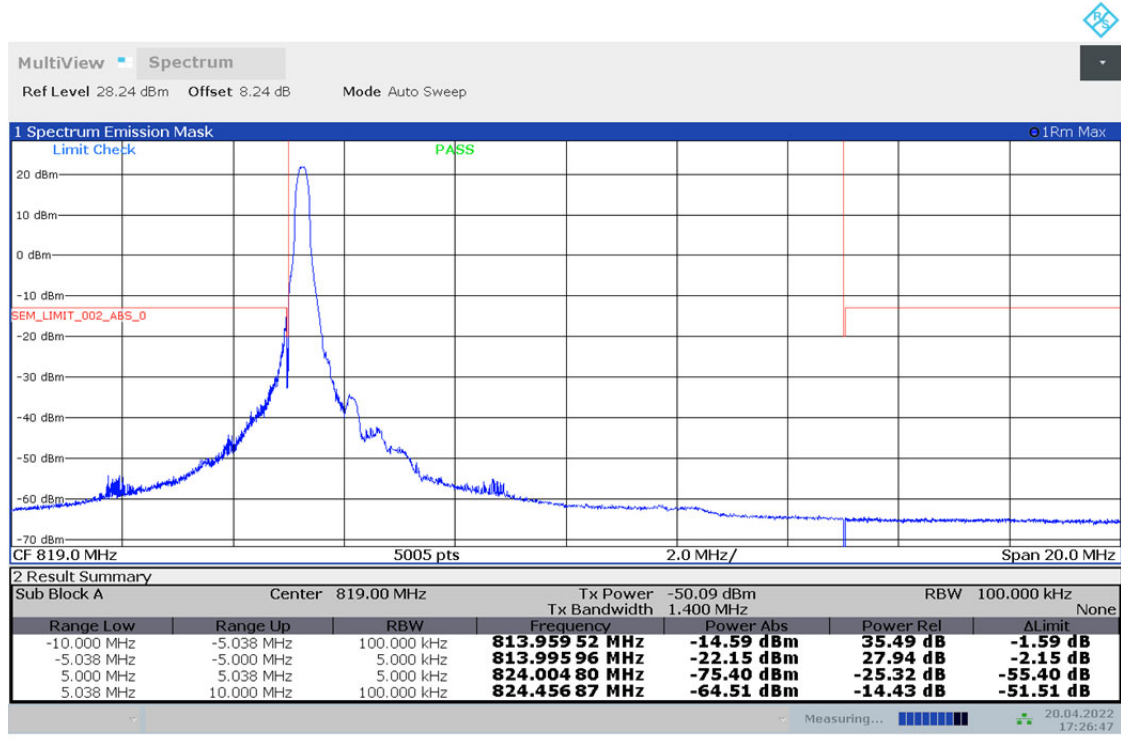
## 5.2.18 TM1\_10MHz\_HCH\_RB1#49



## 5.2.19 TM2\_1.4MHz\_LCH\_RB6#0

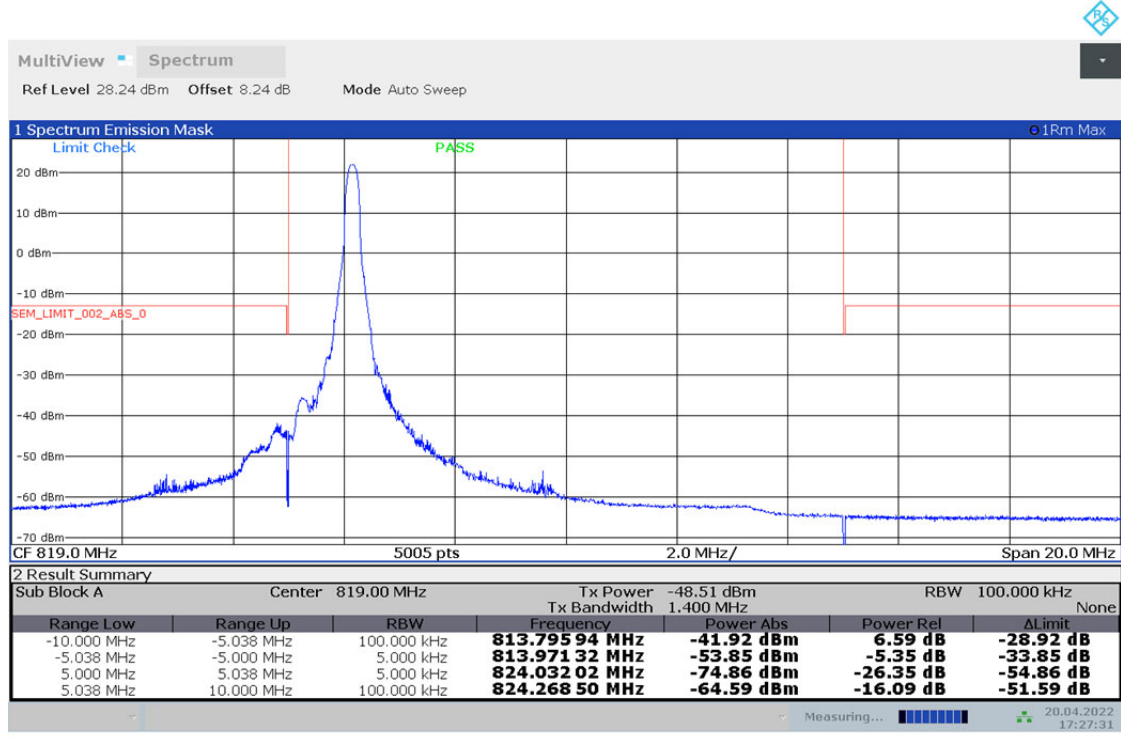


## 5.2.20 TM2\_1.4MHz\_LCH\_RB1#0



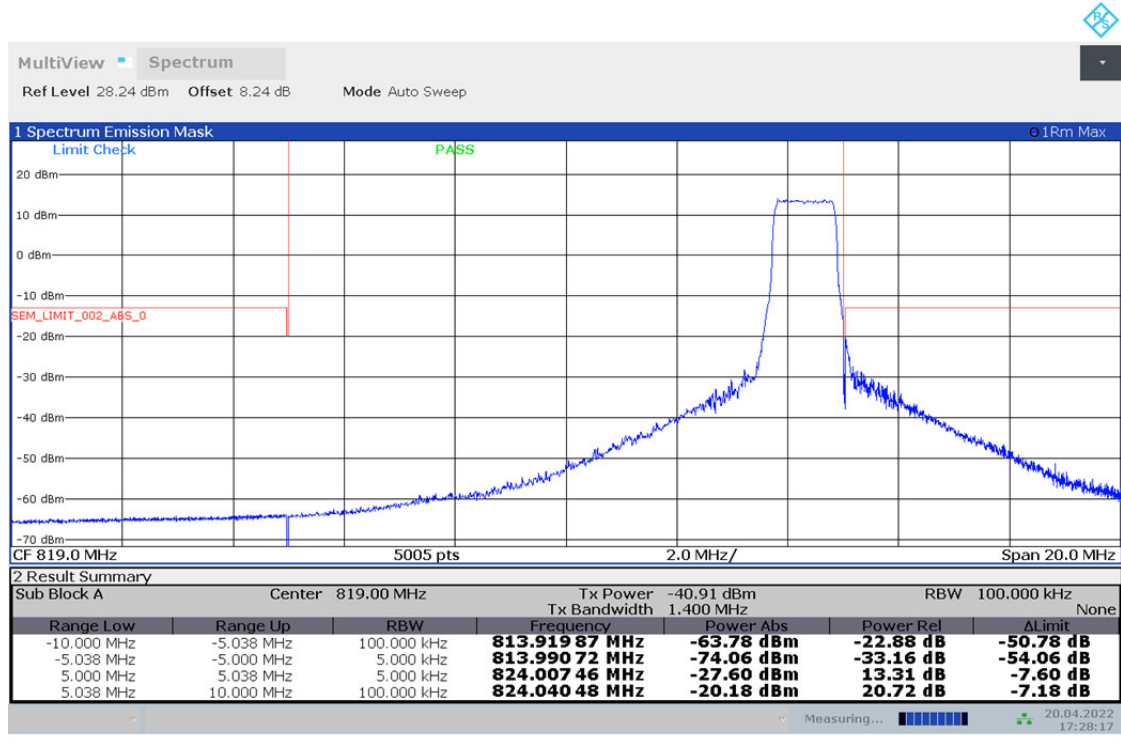
17:26:48 20.04.2022

## 5.2.21 TM2\_1.4MHz\_LCH\_RB1#5



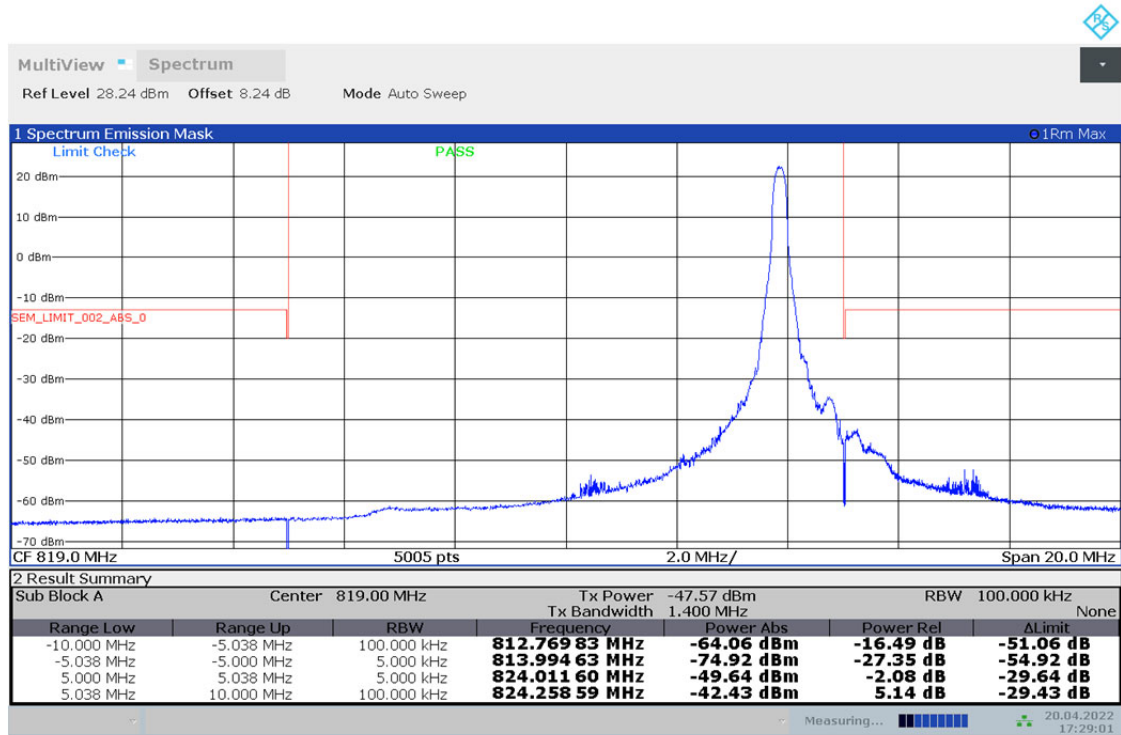
17:27:32 20.04.2022

## 5.2.22 TM2\_1.4MHz\_HCH\_RB6#0



17:28:18 20.04.2022

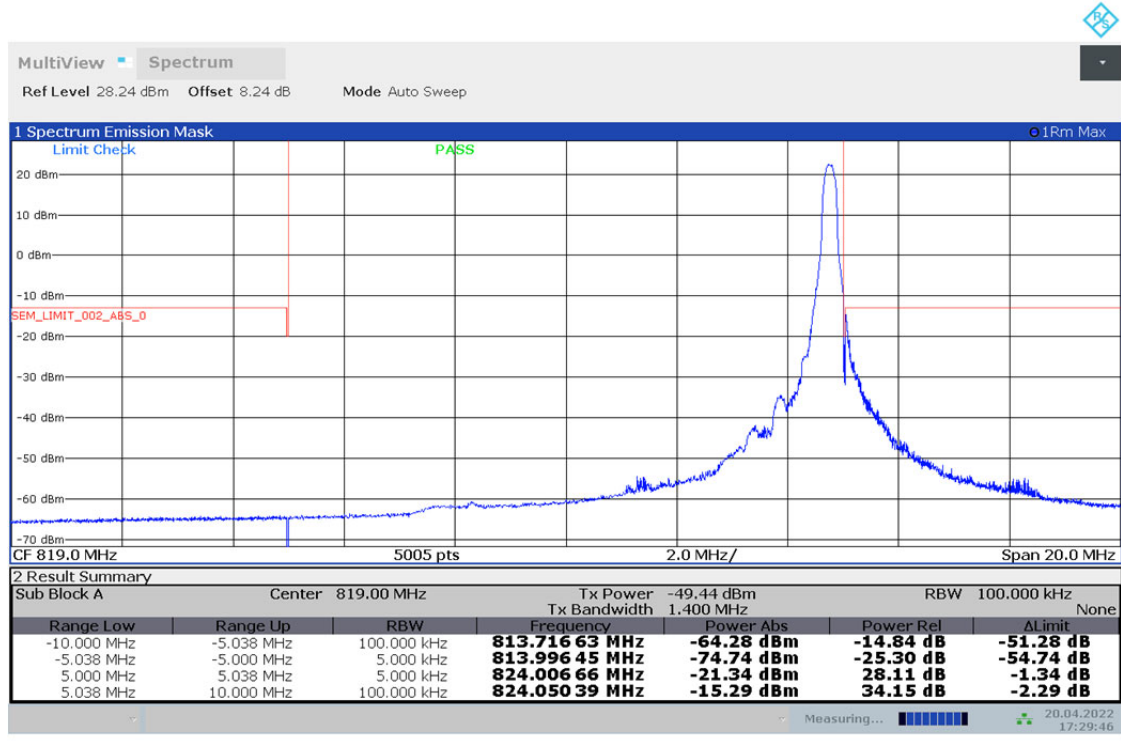
## 5.2.23 TM2\_1.4MHz\_HCH\_RB1#0



17:29:02 20.04.2022

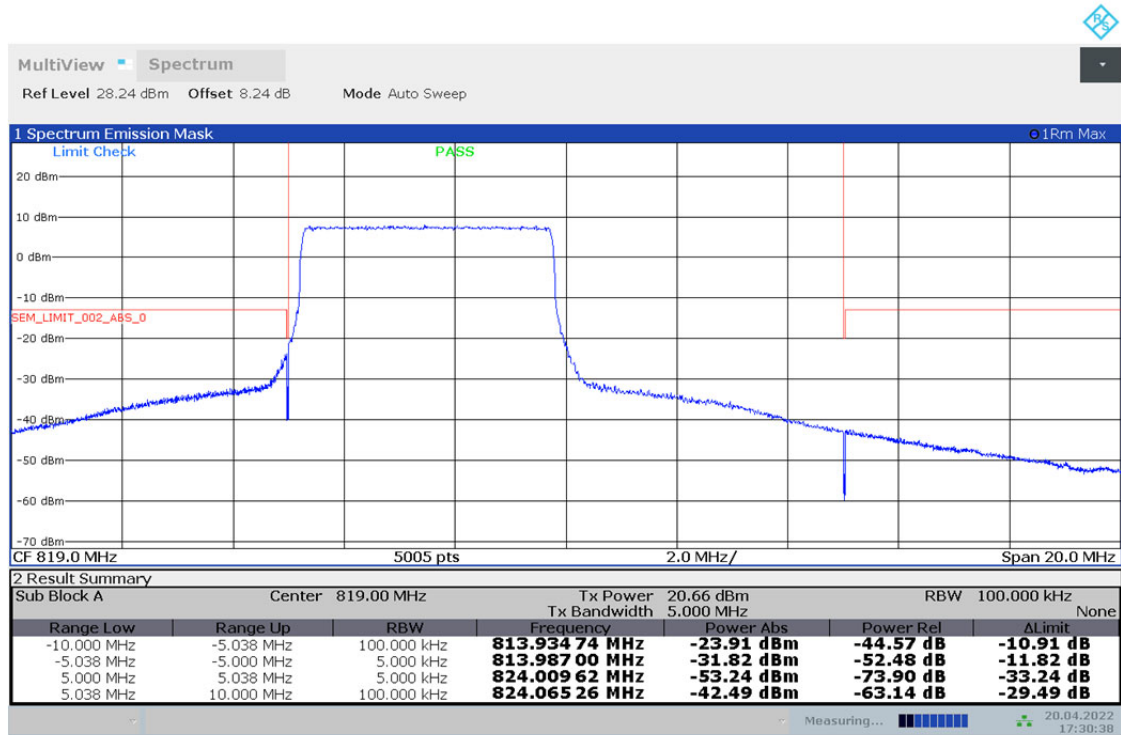


## 5.2.24 TM2\_1.4MHz\_HCH\_RB1#5



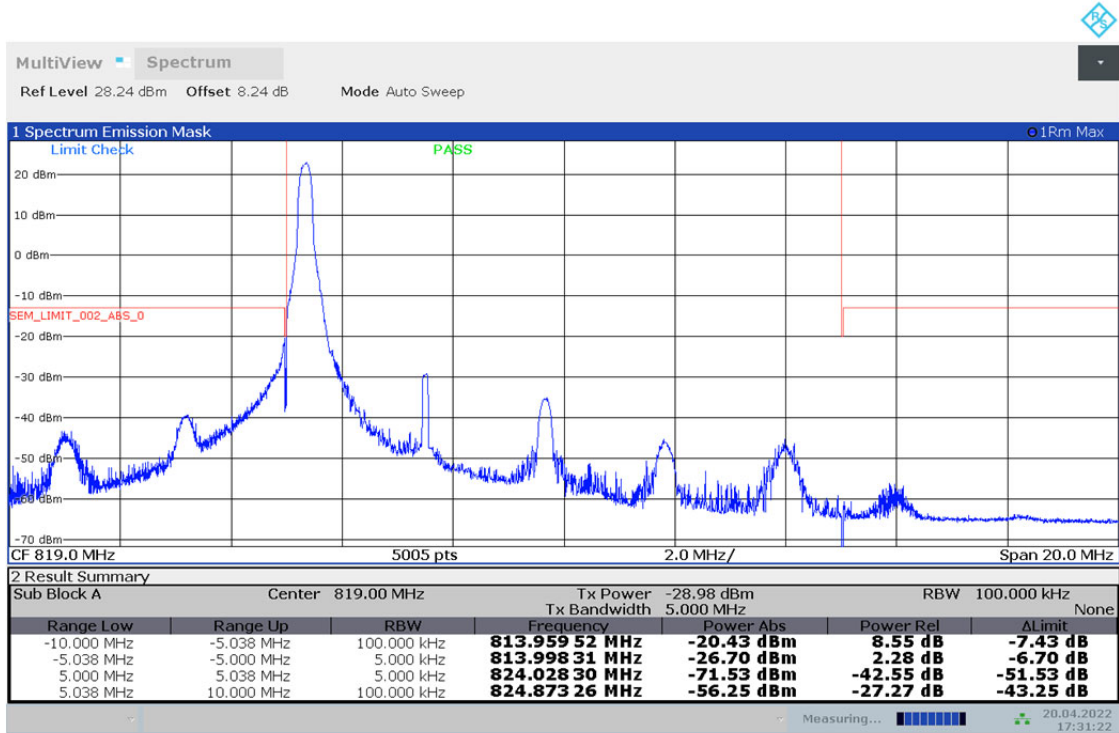
17:29:46 20.04.2022

## 5.2.25 TM2\_5MHz\_LCH\_RB25#0



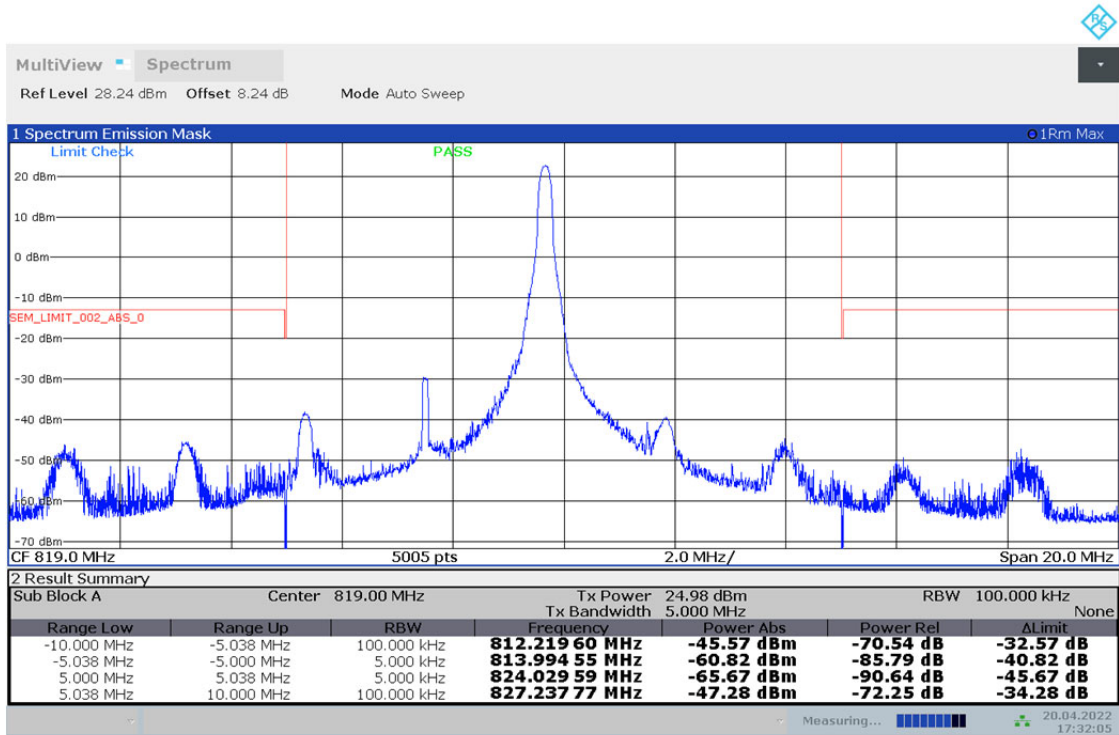
17:30:38 20.04.2022

## 5.2.26 TM2\_5MHz\_LCH\_RB1#0



17:31:22 20.04.2022

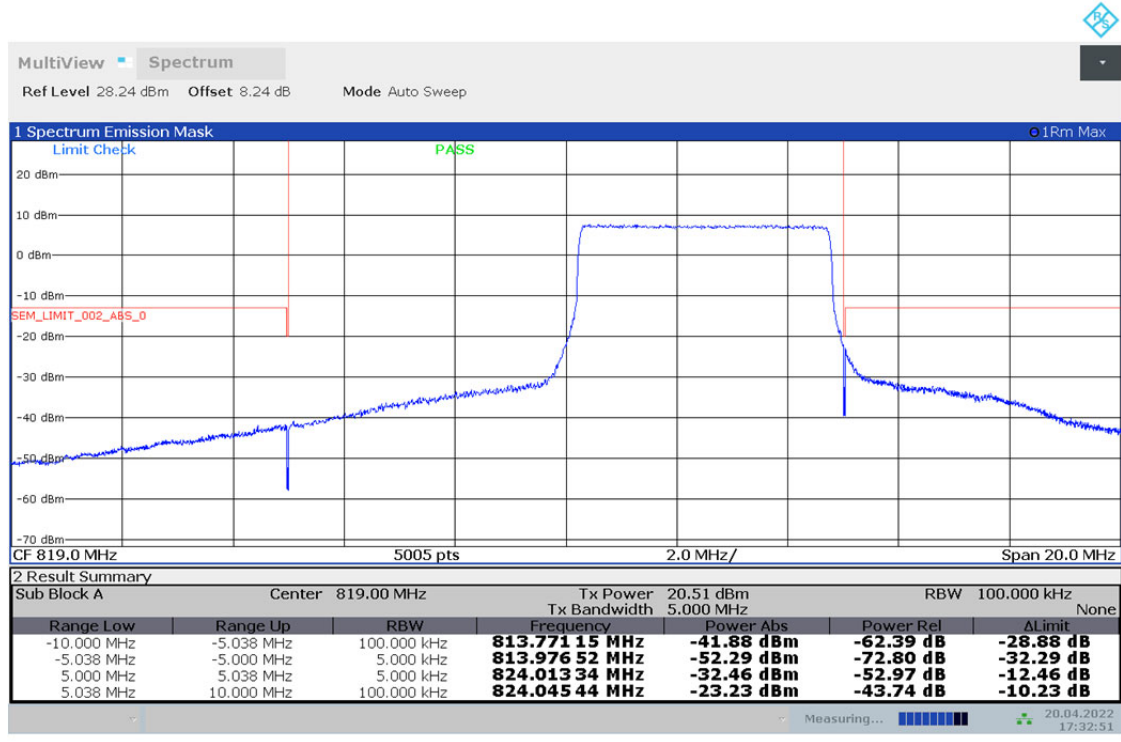
## 5.2.27 TM2\_5MHz\_LCH\_RB1#24



17:32:06 20.04.2022

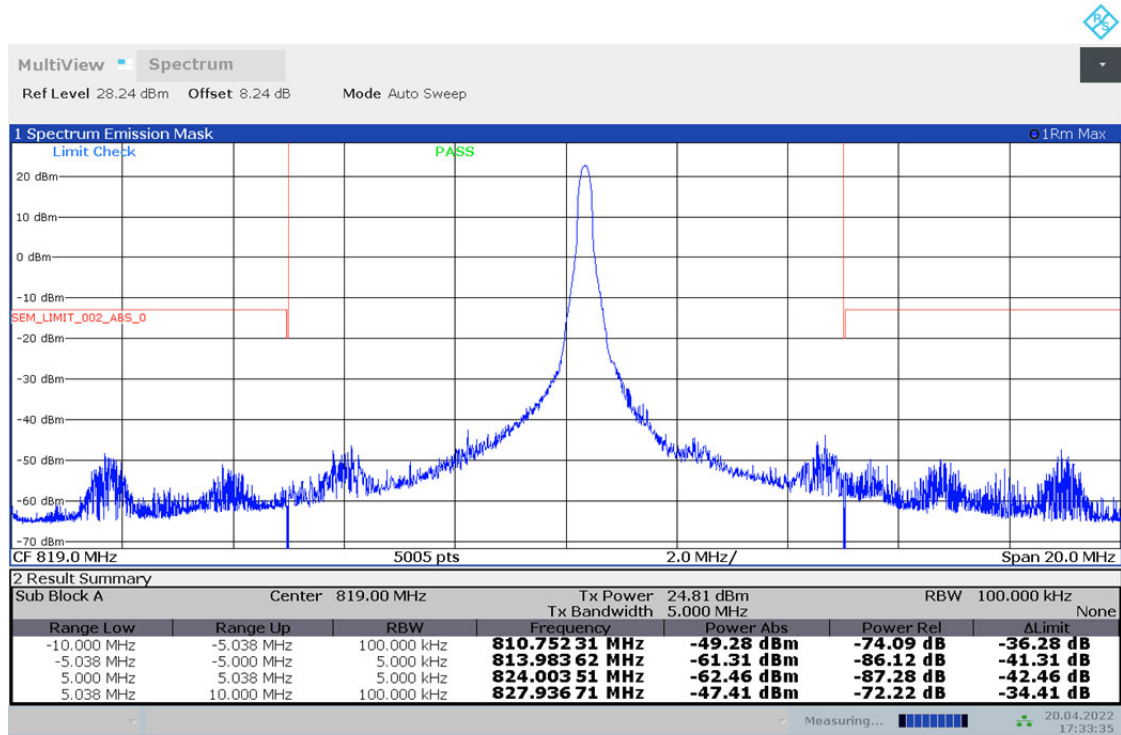


## 5.2.28 TM2\_5MHz\_HCH\_RB25#0



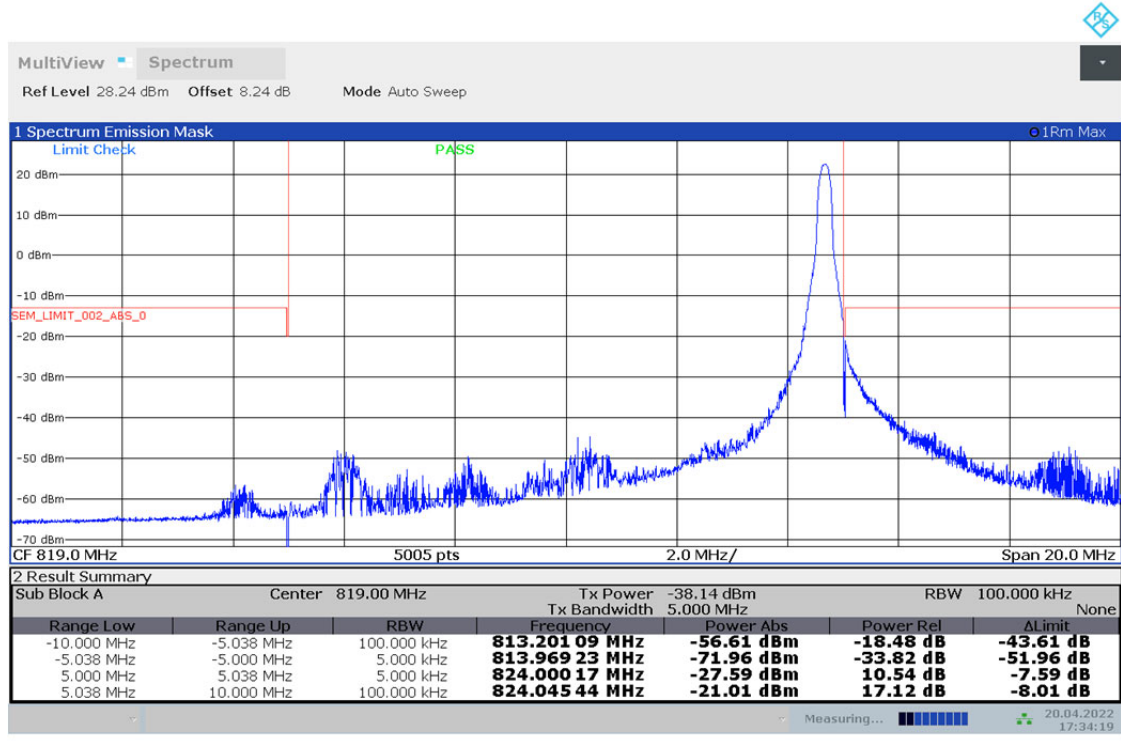
17:32:52 20.04.2022

## 5.2.29 TM2\_5MHz\_HCH\_RB1#0



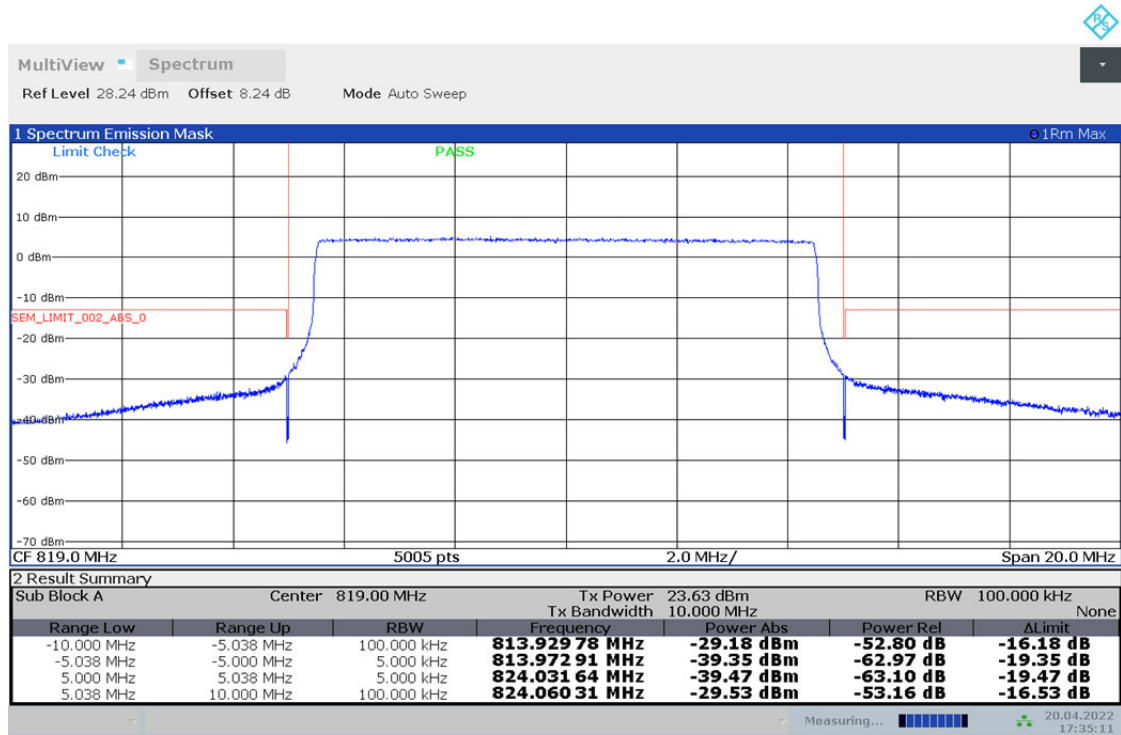
17:33:35 20.04.2022

### 5.2.30 TM2\_5MHz\_HCH\_RB1#24



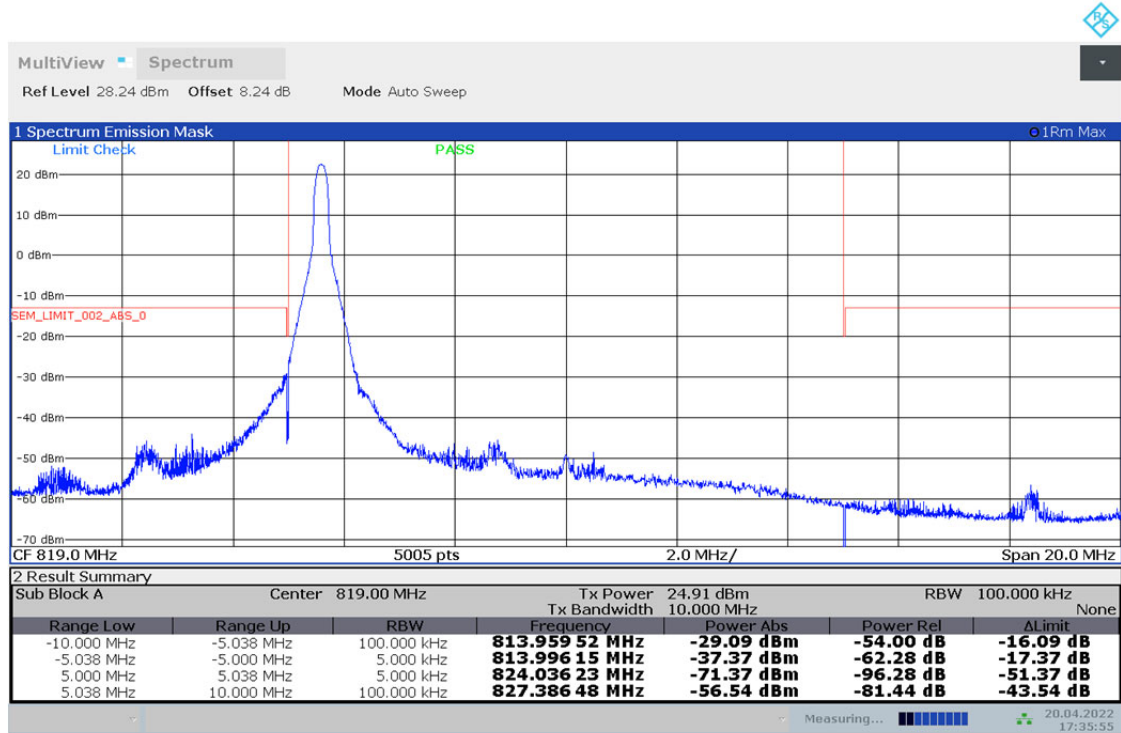
17:34:19 20.04.2022

### 5.2.31 TM2\_10MHz\_LCH\_RB50#0

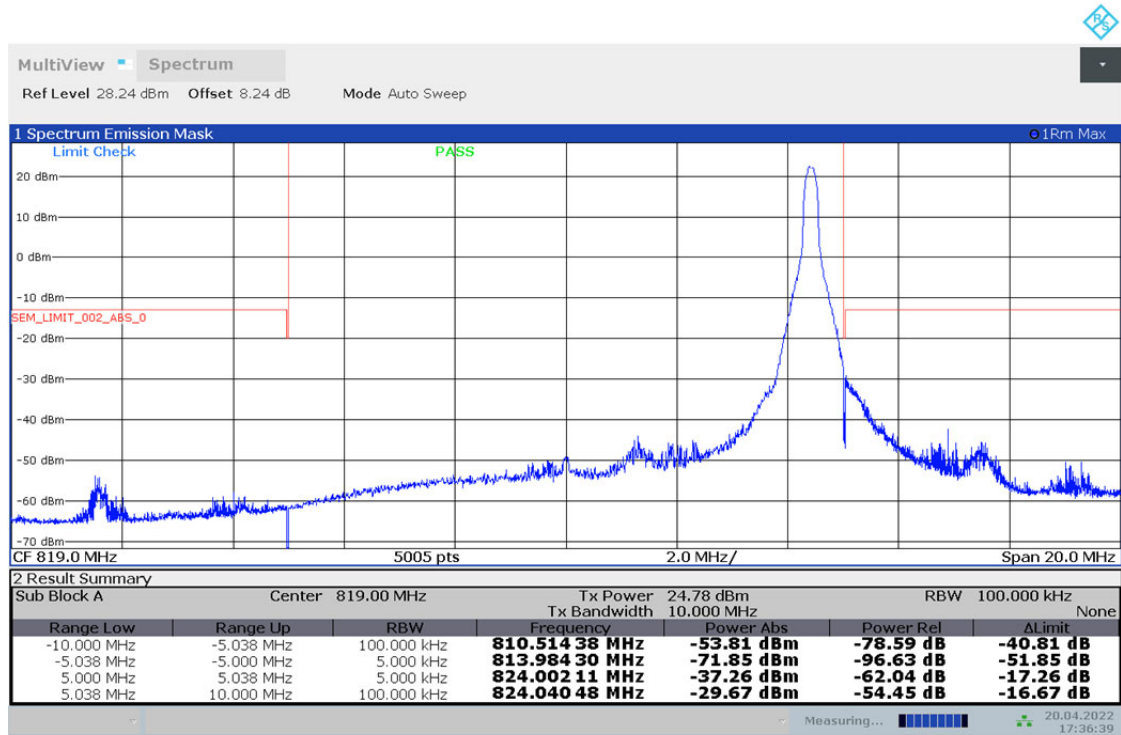


17:35:12 20.04.2022

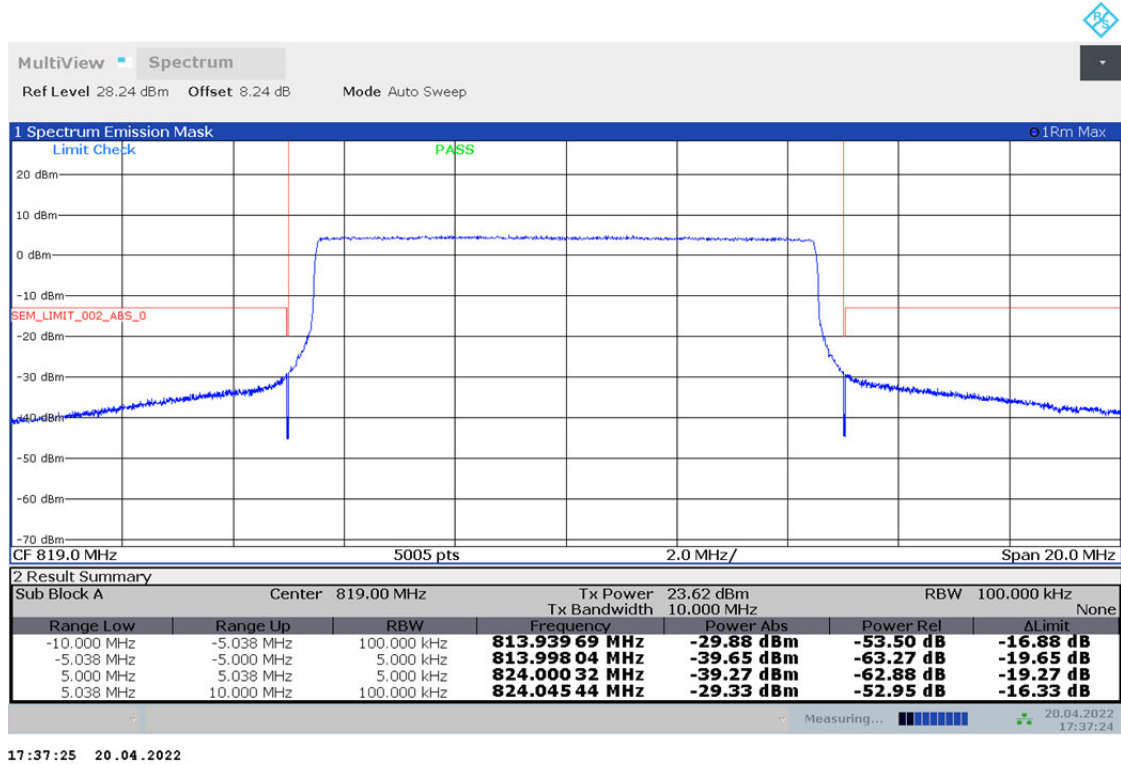
## 5.2.32 TM2\_10MHz\_LCH\_RB1#0



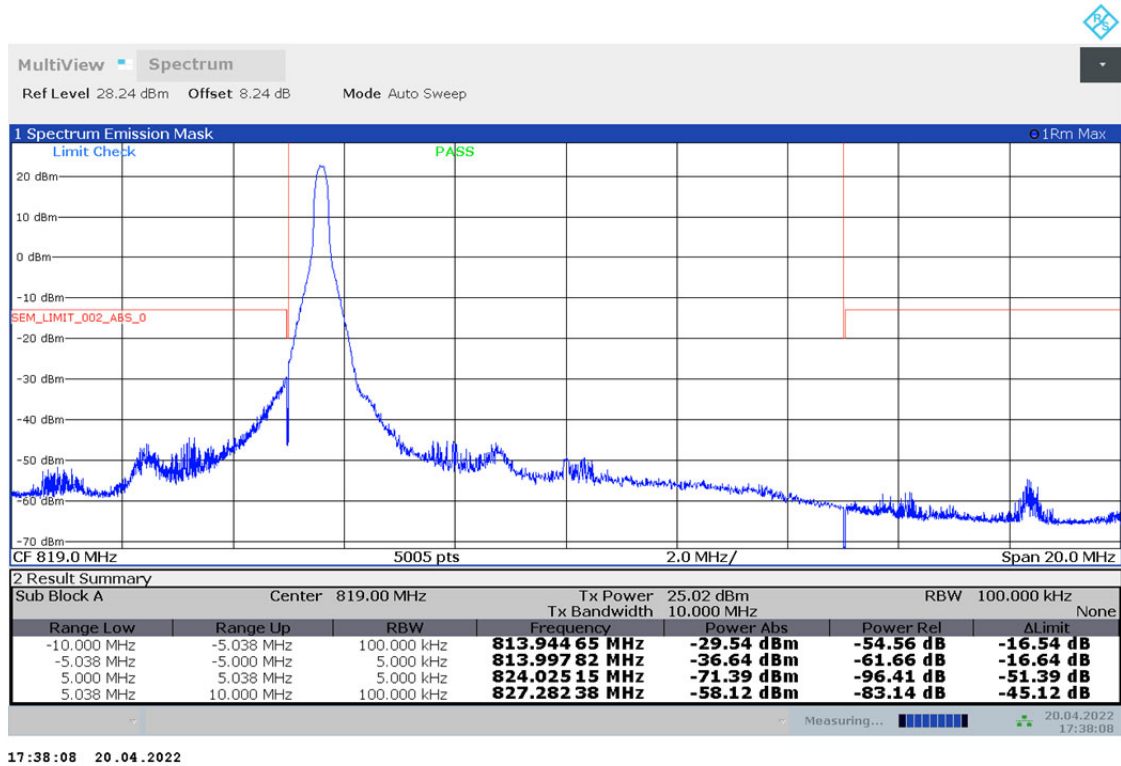
## 5.2.33 TM2\_10MHz\_LCH\_RB1#49



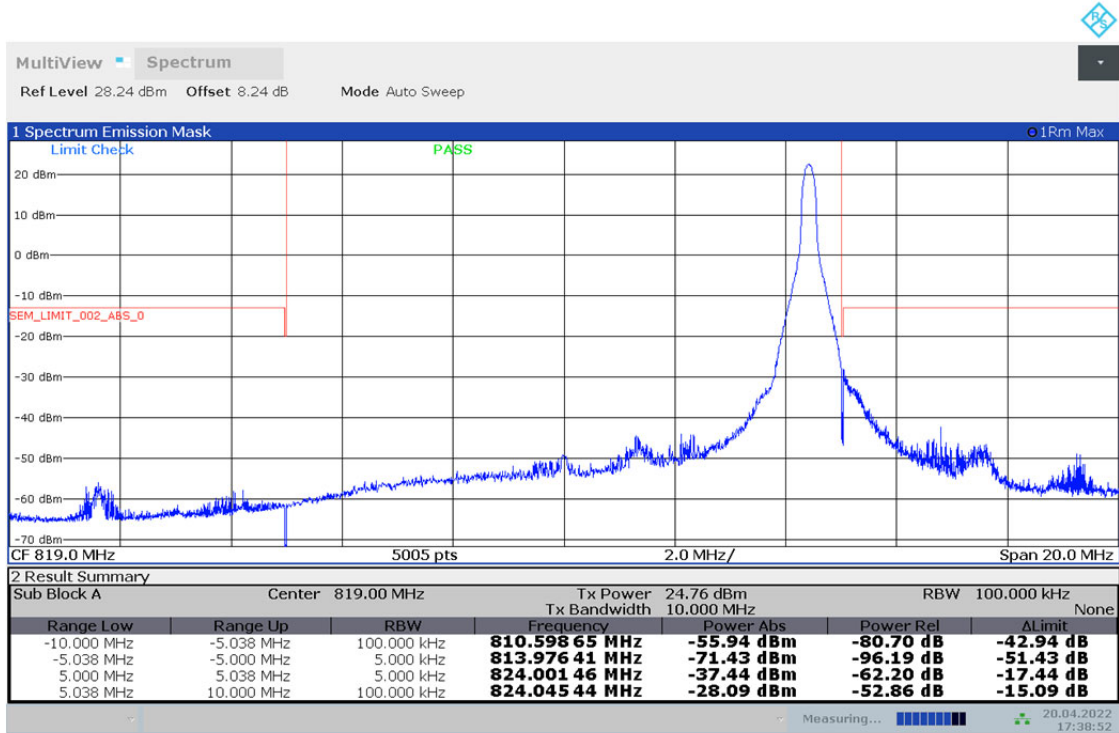
## 5.2.34 TM2\_10MHz\_HCH\_RB50#0



## 5.2.35 TM2\_10MHz\_HCH\_RB1#0

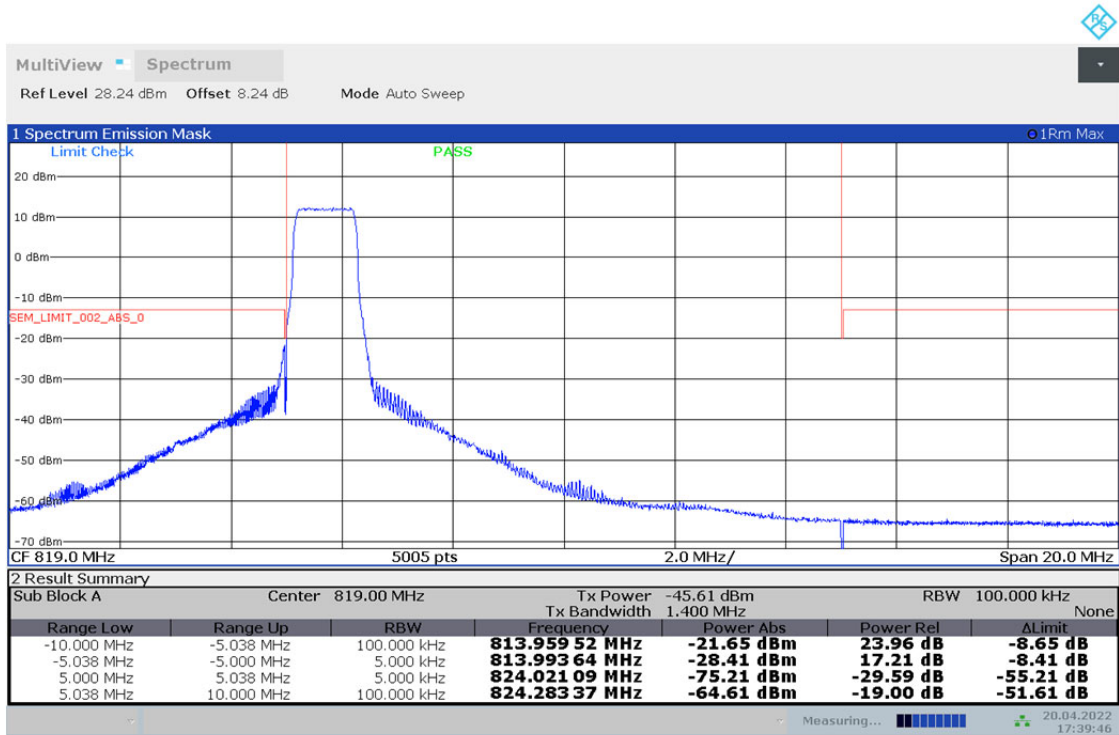


### 5.2.36 TM2\_10MHz\_HCH\_RB1#49



17:38:52 20.04.2022

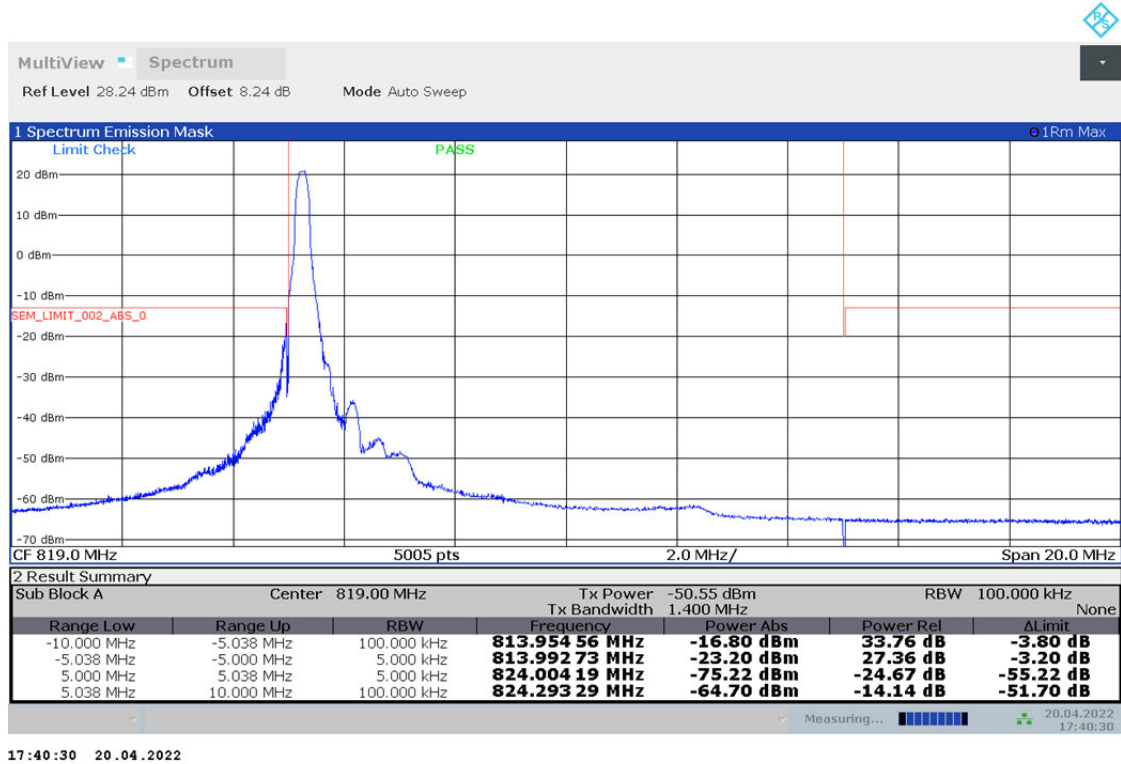
### 5.2.37 TM3\_1.4MHz\_LCH\_RB6#0



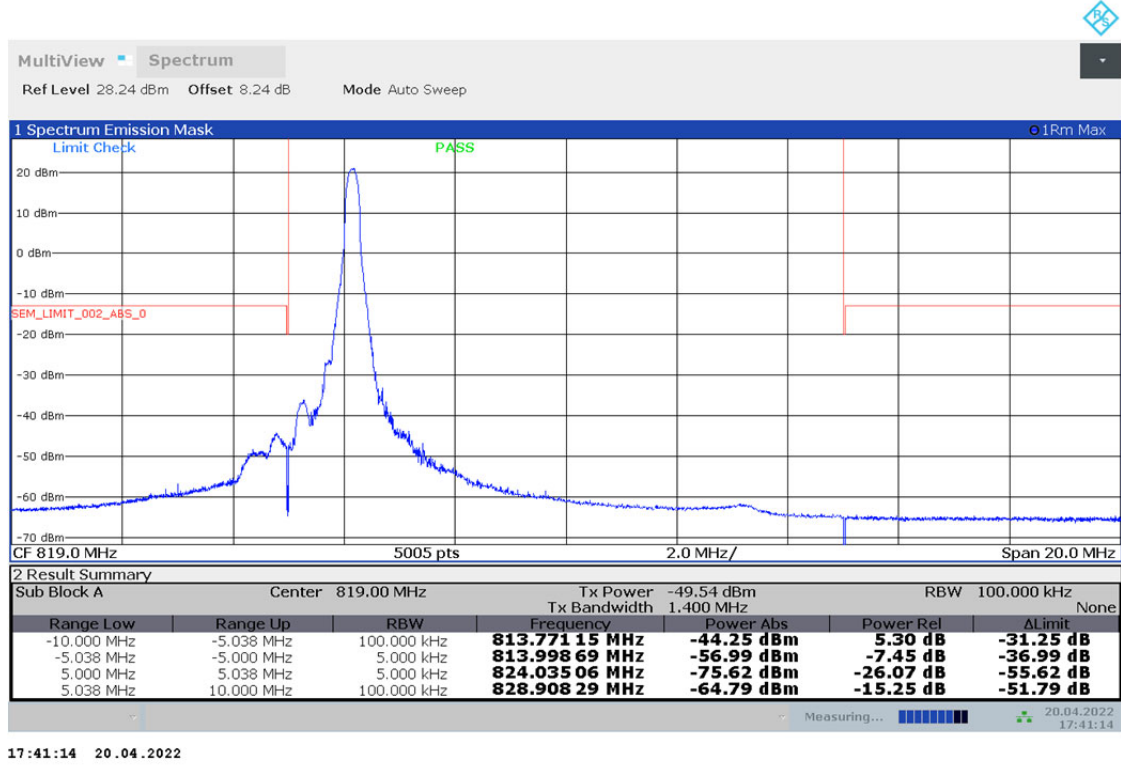
17:39:46 20.04.2022



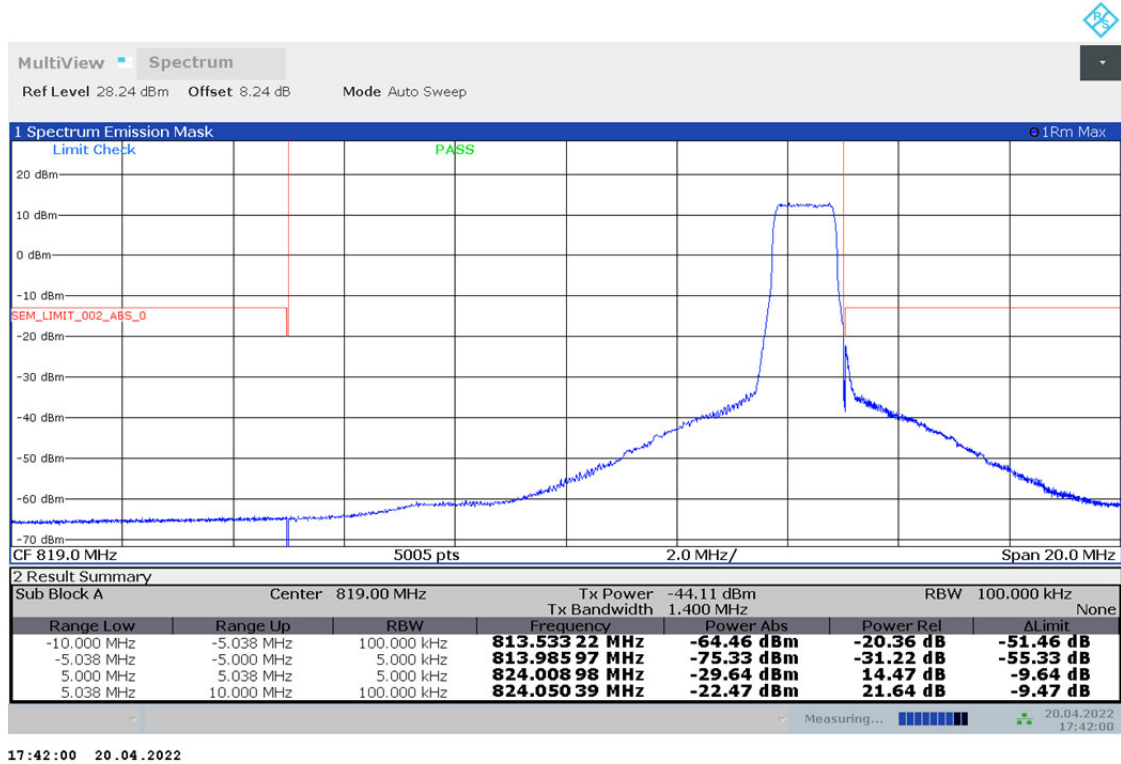
## 5.2.38 TM3\_1.4MHz\_LCH\_RB1#0



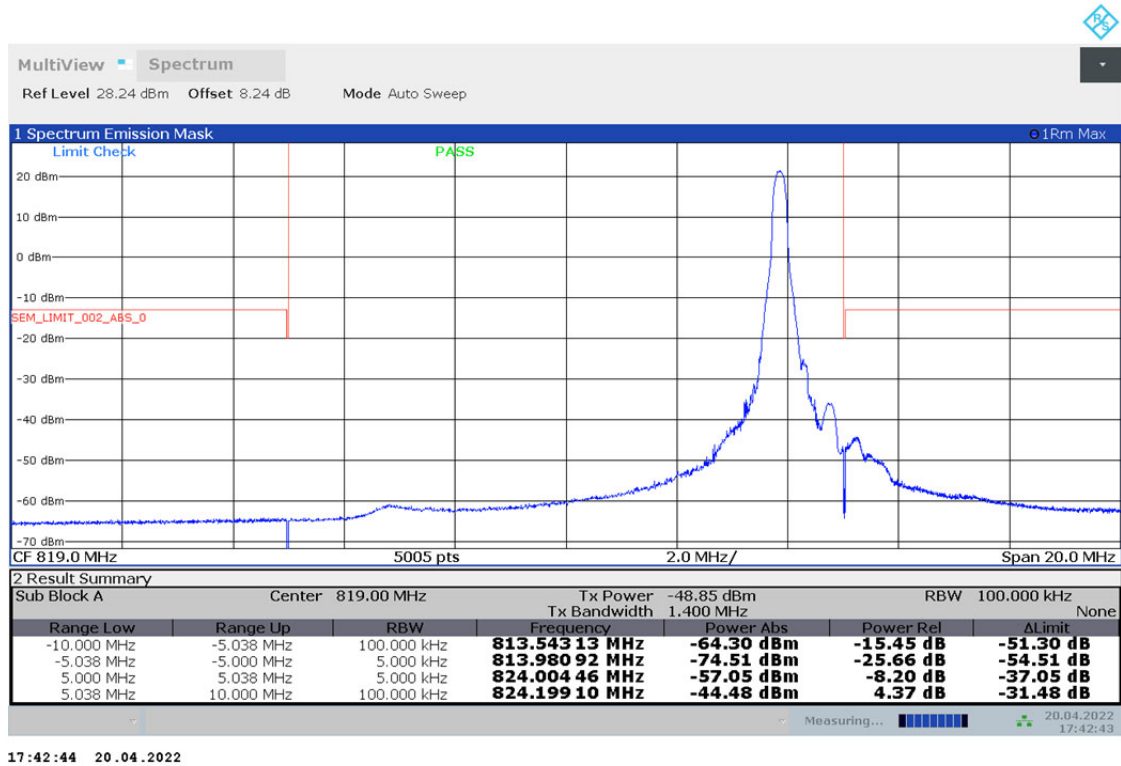
## 5.2.39 TM3\_1.4MHz\_LCH\_RB1#5



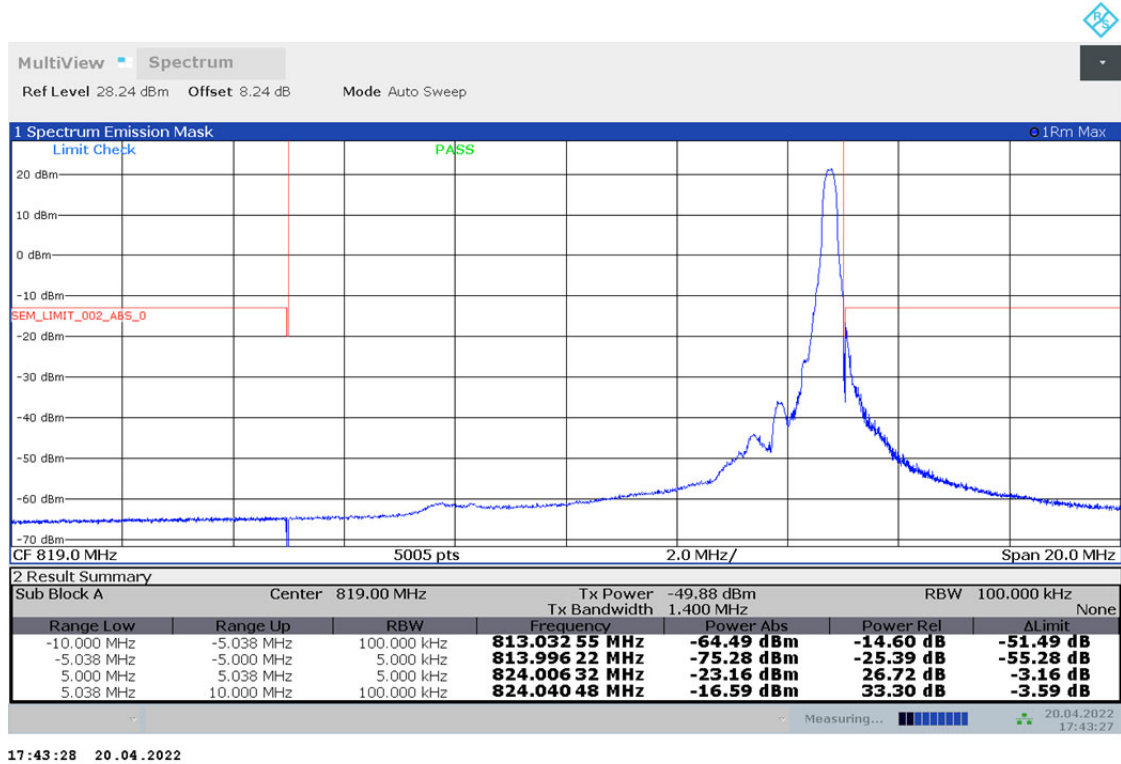
## 5.2.40 TM3\_1.4MHz\_HCH\_RB6#0



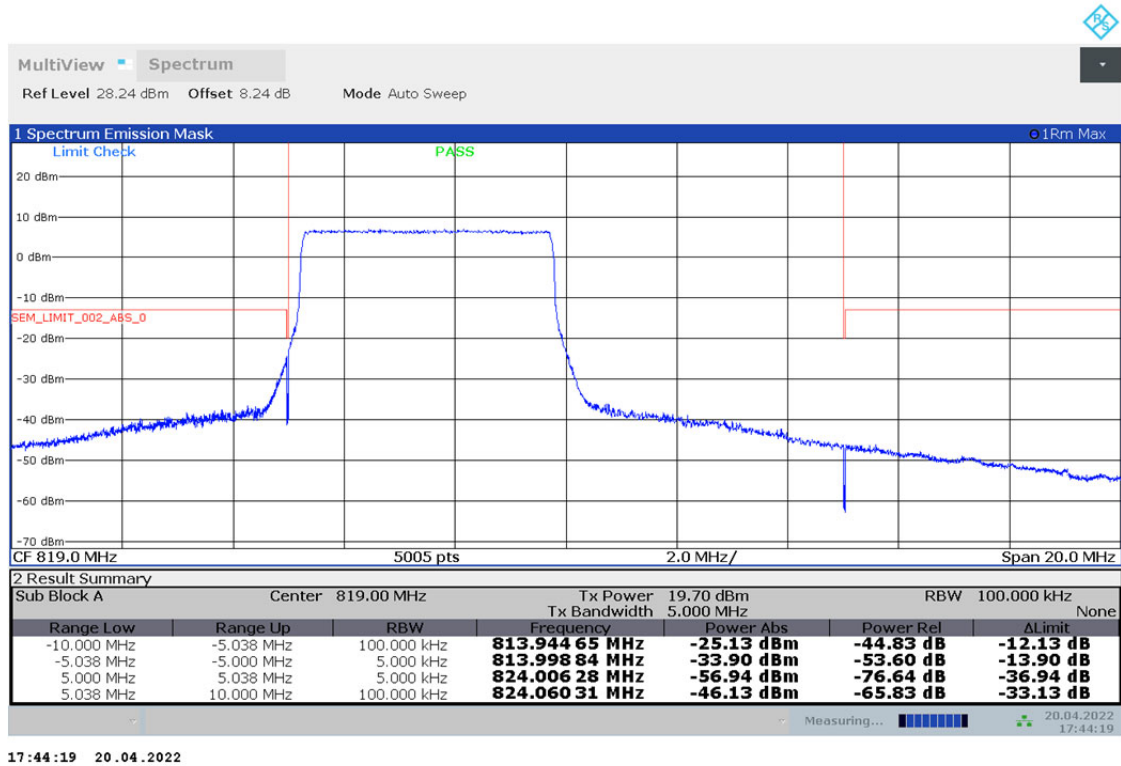
## 5.2.41 TM3\_1.4MHz\_HCH\_RB1#0



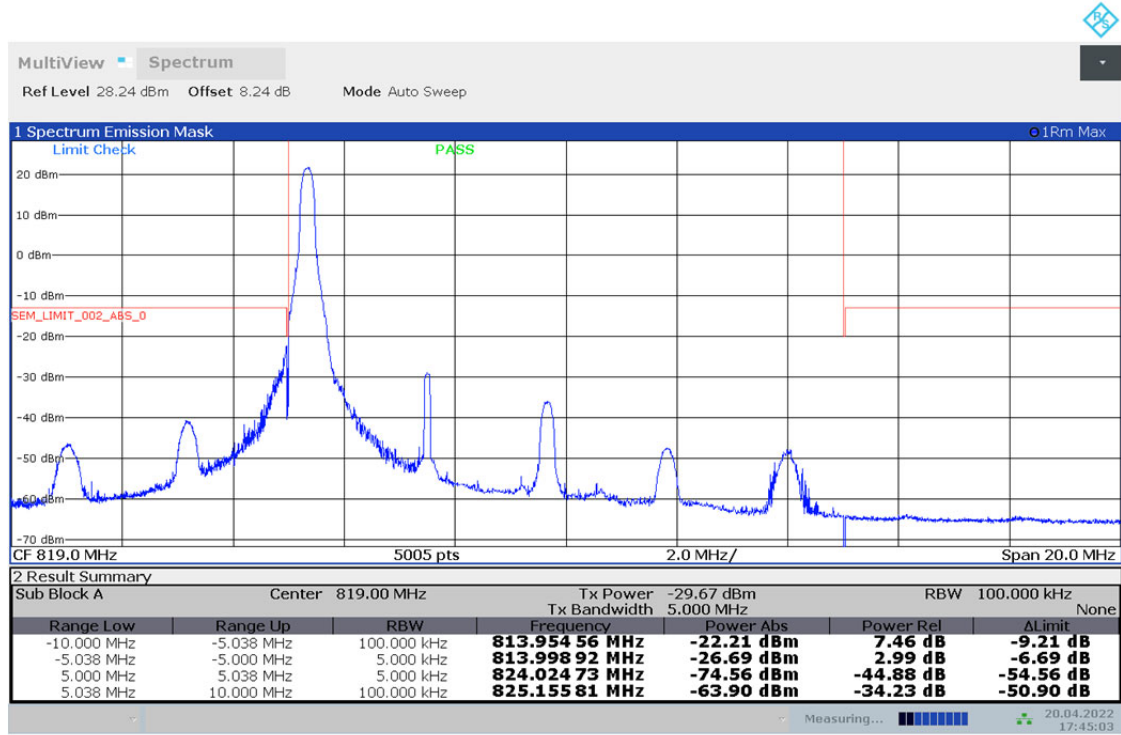
## 5.2.42 TM3\_1.4MHz\_HCH\_RB1#5



## 5.2.43 TM3\_5MHz\_LCH\_RB25#0

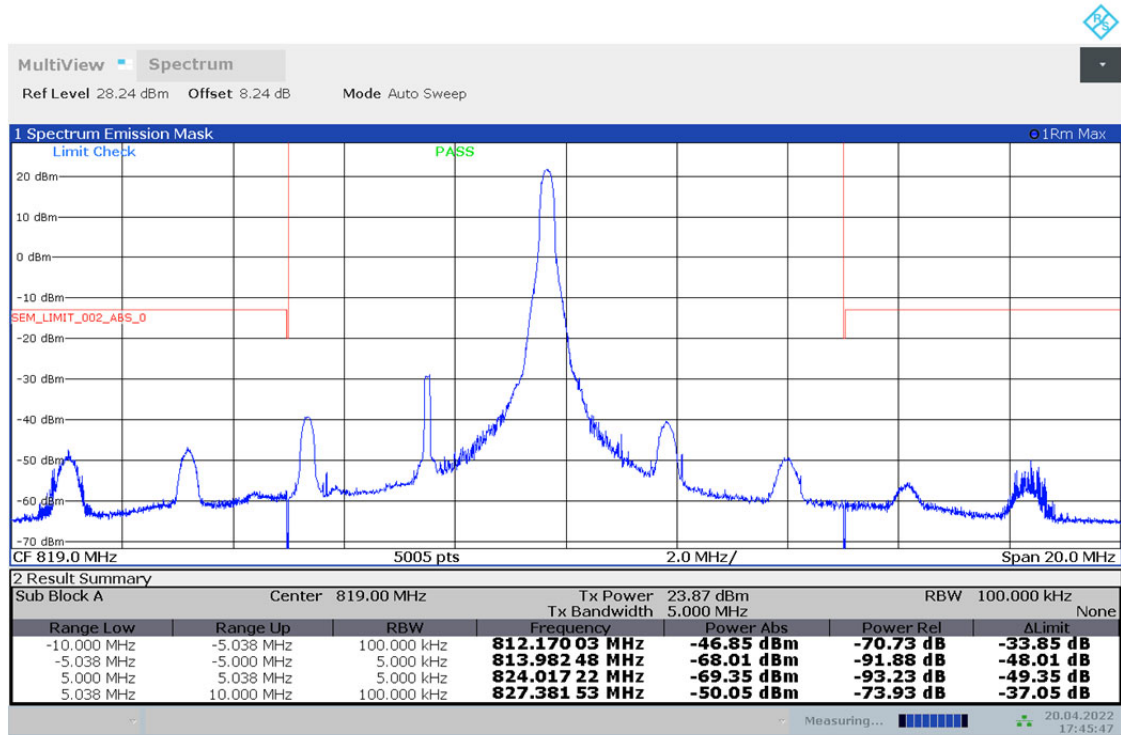


## 5.2.44 TM3\_5MHz\_LCH\_RB1#0



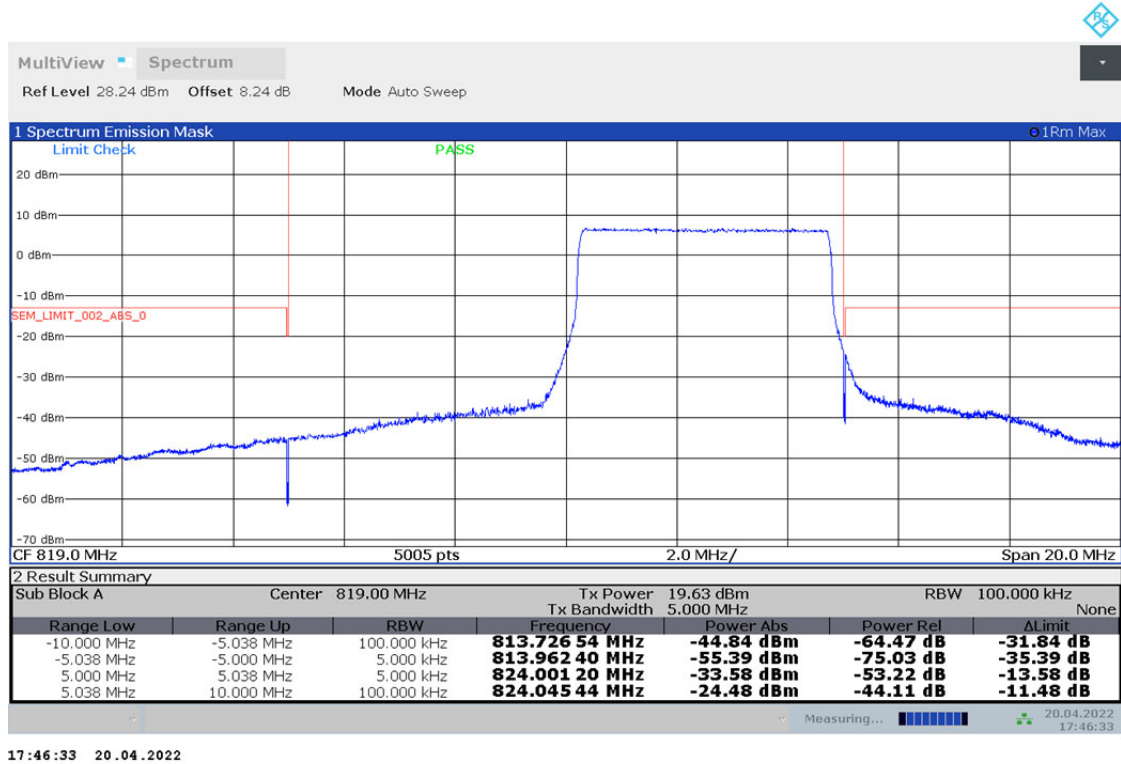
17:45:03 20.04.2022

## 5.2.45 TM3\_5MHz\_LCH\_RB1#24

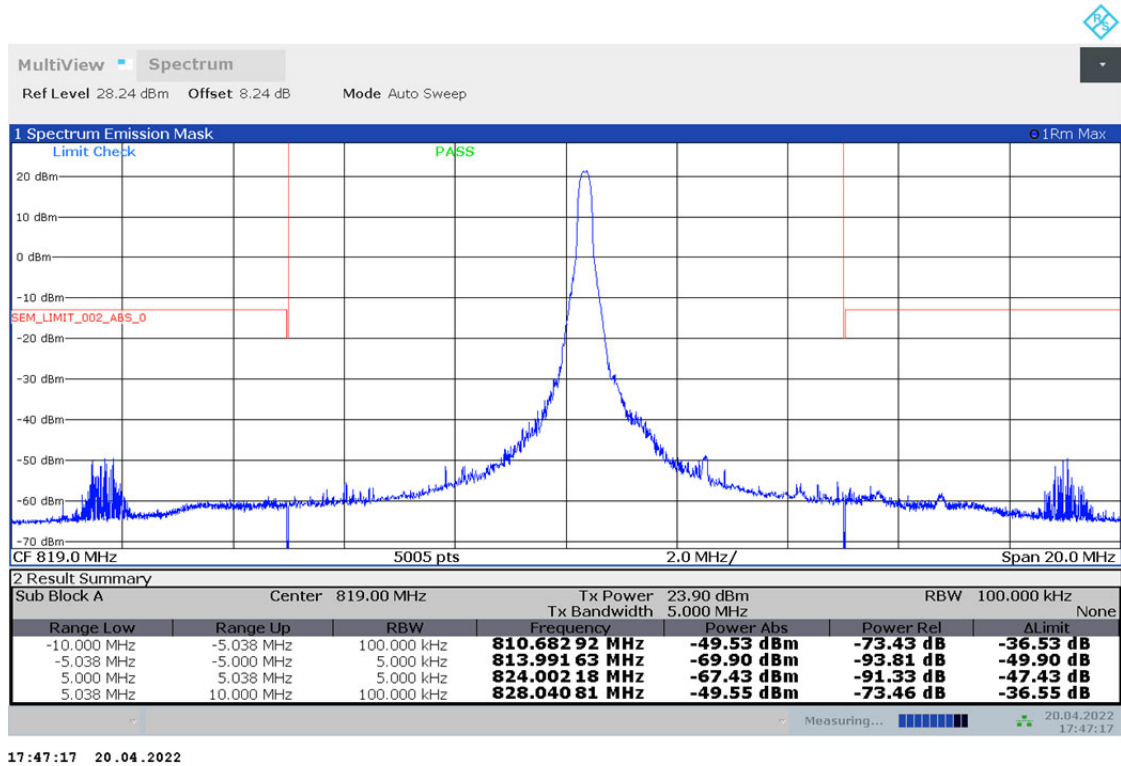


17:45:47 20.04.2022

## 5.2.46 TM3\_5MHz\_HCH\_RB25#0

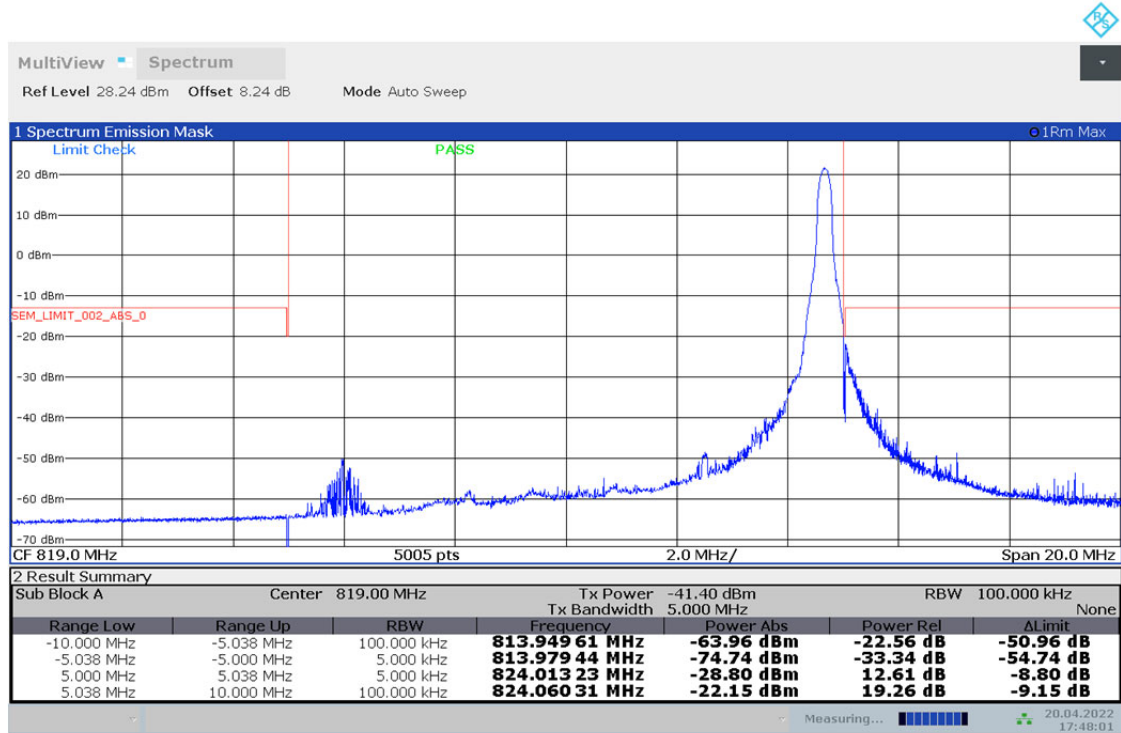


## 5.2.47 TM3\_5MHz\_HCH\_RB1#0



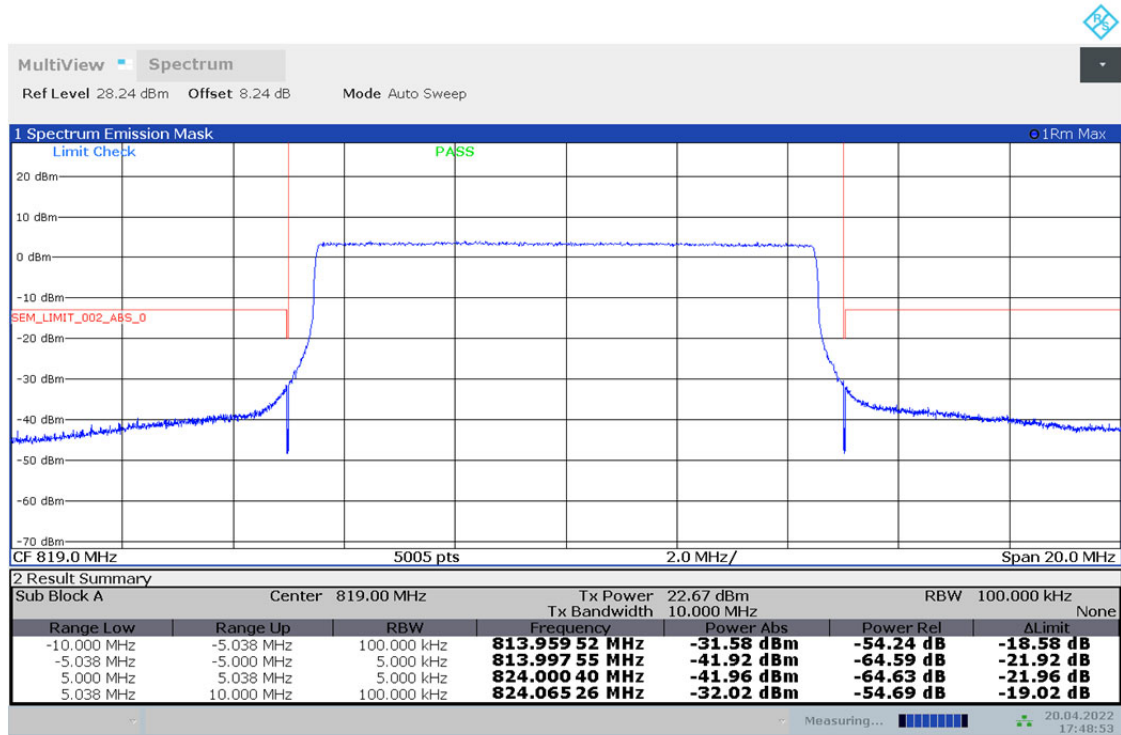


## 5.2.48 TM3\_5MHz\_HCH\_RB1#24



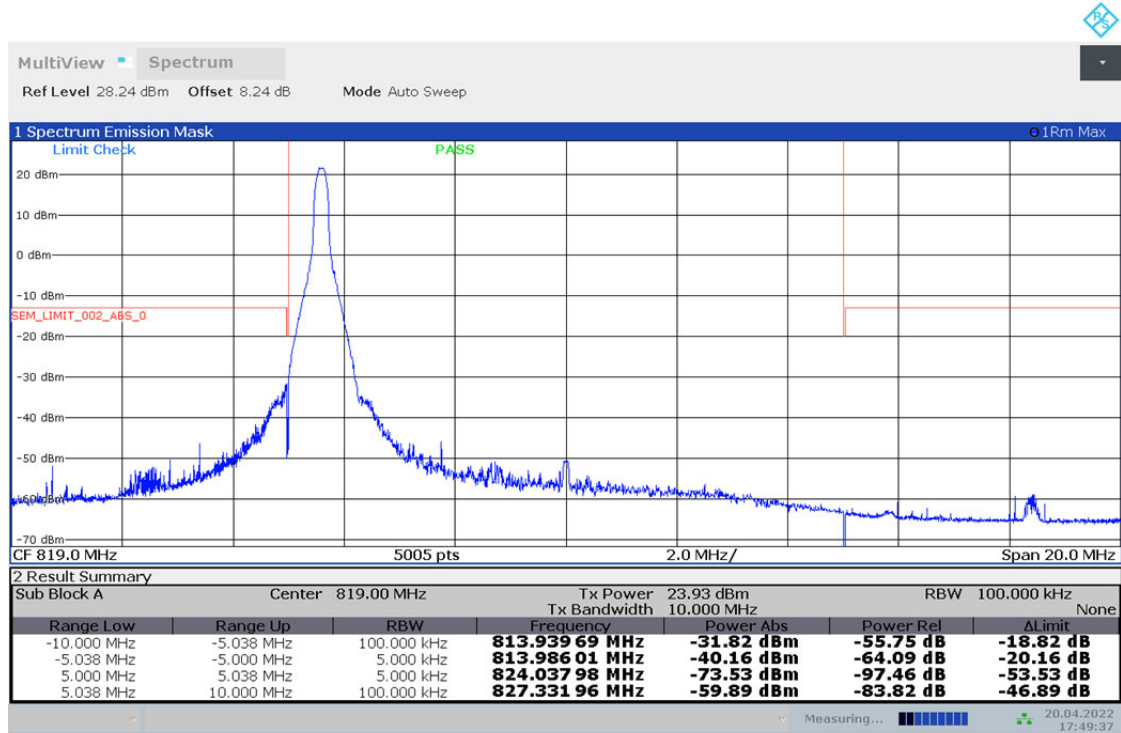
17:48:01 20.04.2022

## 5.2.49 TM3\_10MHz\_LCH\_RB50#0



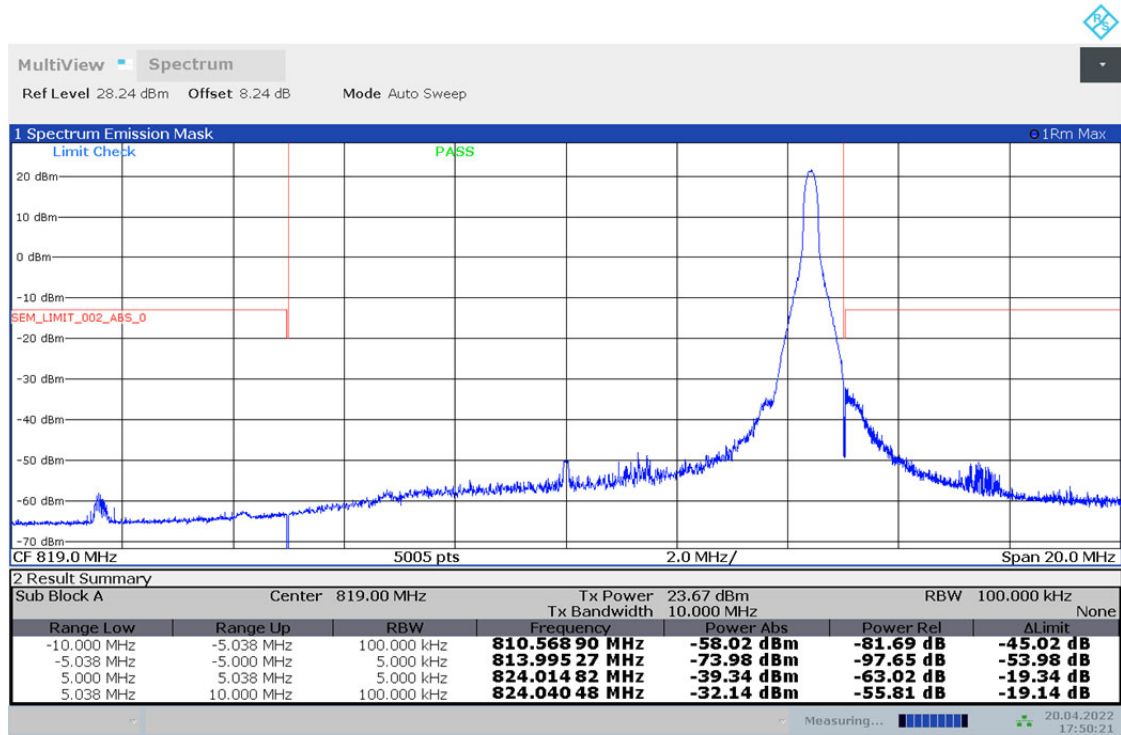
17:48:53 20.04.2022

## 5.2.50 TM3\_10MHz\_LCH\_RB1#0



17:49:37 20.04.2022

## 5.2.51 TM3\_10MHz\_LCH\_RB1#49



17:50:21 20.04.2022