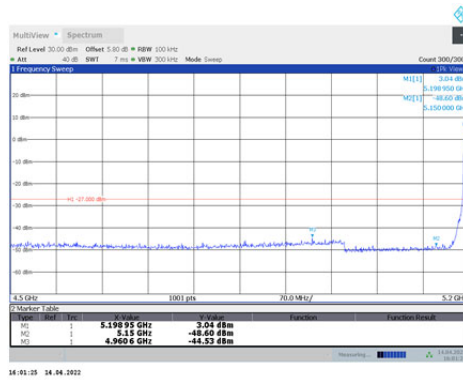
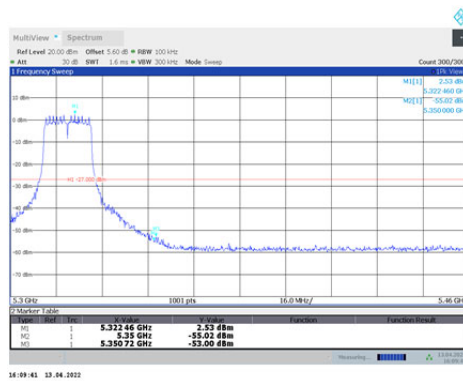


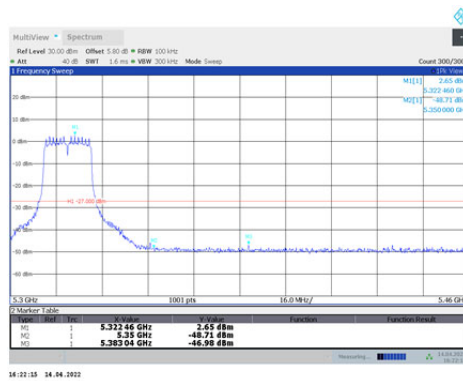
11A_Ant2_Low_5200



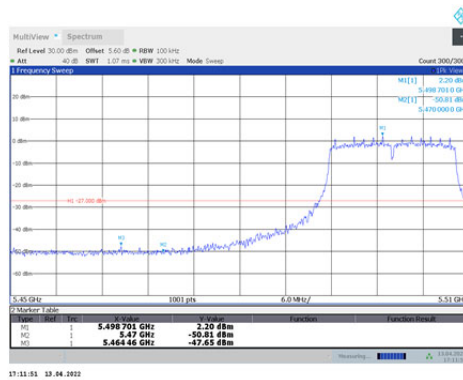
11A_Ant1_High_5320



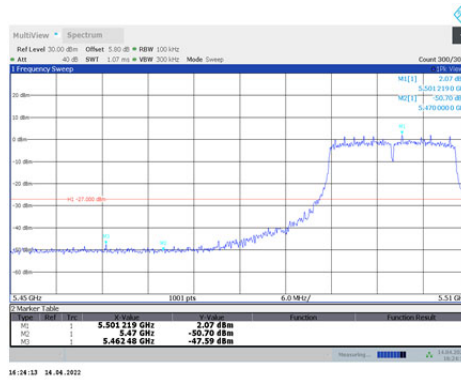
11A_Ant2_High_5320



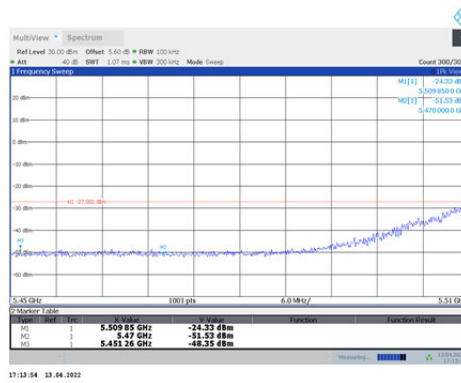
11A_Ant1_Low_5500



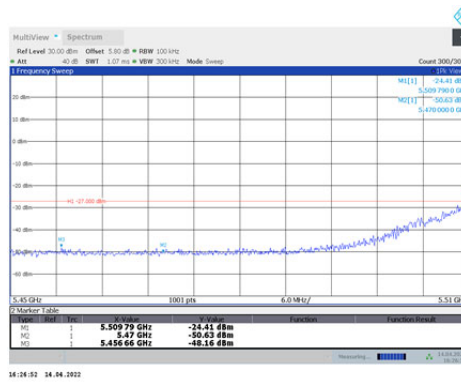
11A_Ant2_Low_5500



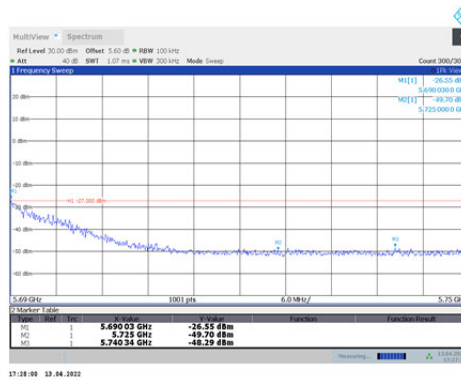
11A_Ant1_Low_5520



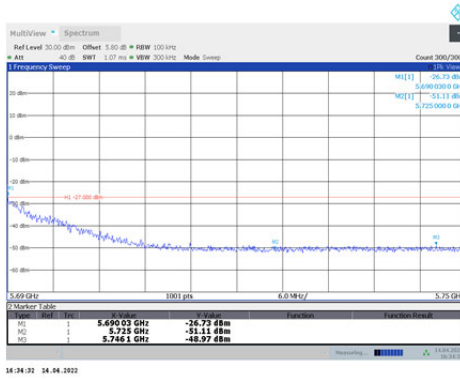
11A_Ant2_Low_5520



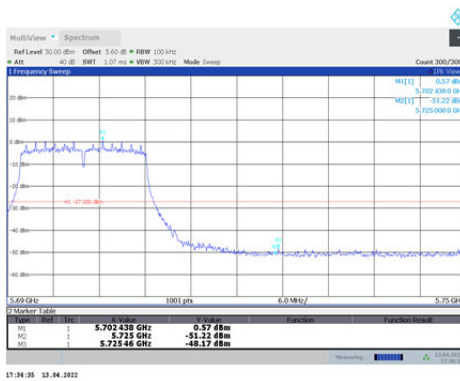
11A_Ant1_High_5680



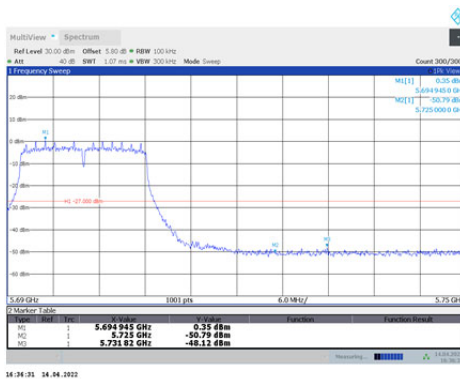
11A_Ant2_High_5680



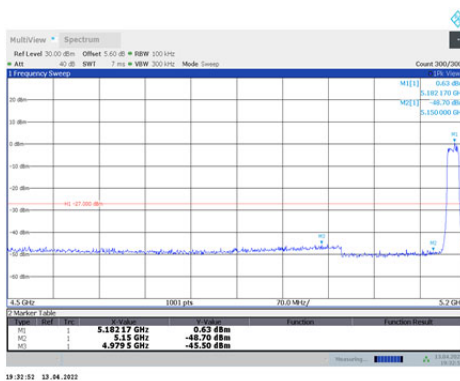
11A_Ant1_High_5700



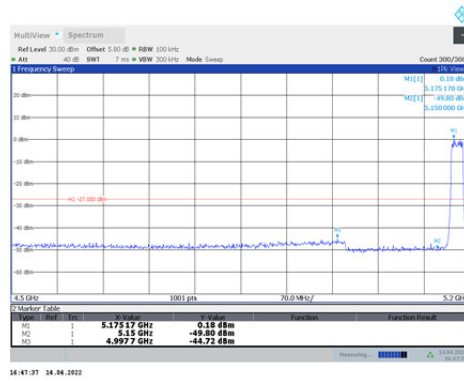
11A_Ant2_High_5700



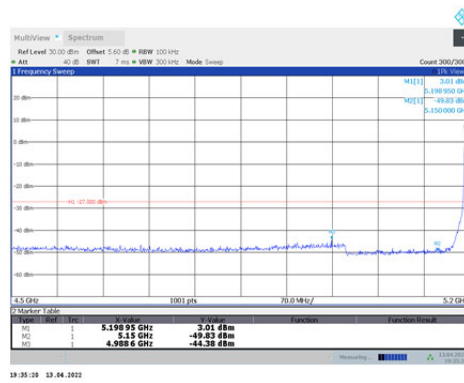
11N20SISO_Ant1_Low_5180



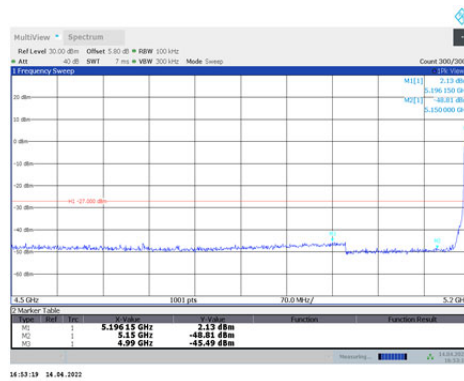
11N20SISO_Ant2_Low_5180



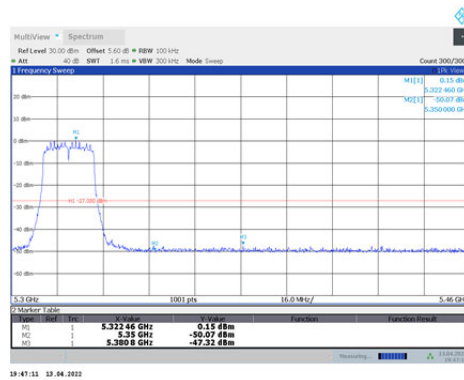
11N20SISO_Ant1_Low_5200



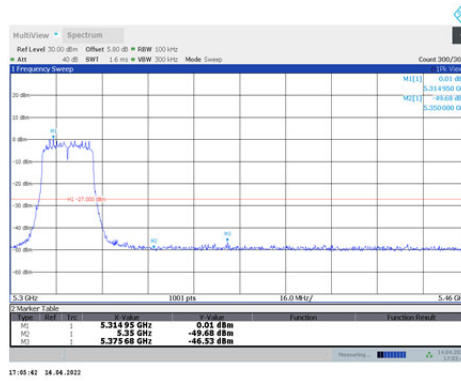
11N20SISO_Ant2_Low_5200



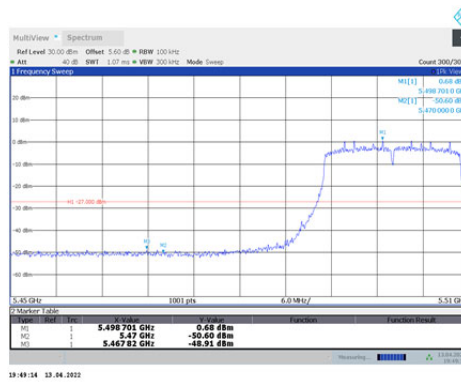
11N20SISO_Ant1_High_5320



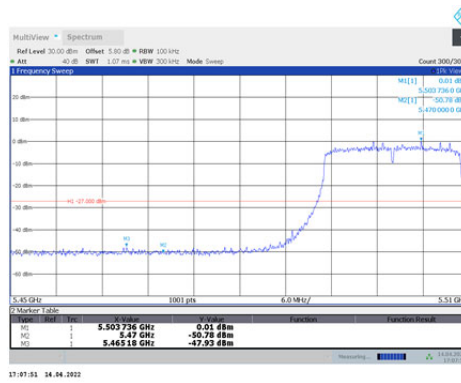
11N20SISO_Ant2_High_5320



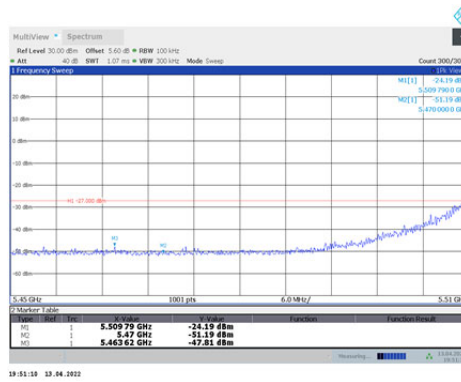
11N20SISO_Ant1_Low_5500



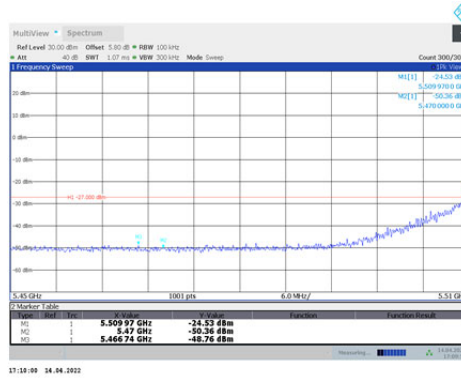
11N20SISO_Ant2_Low_5500



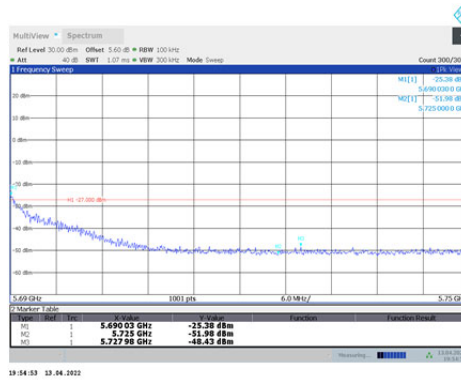
11N20SISO_Ant1_Low_5520



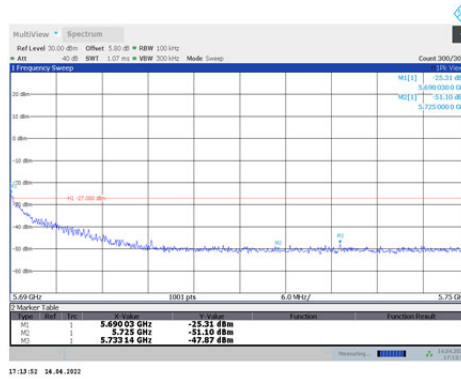
11N20SISO_Ant2_Low_5520



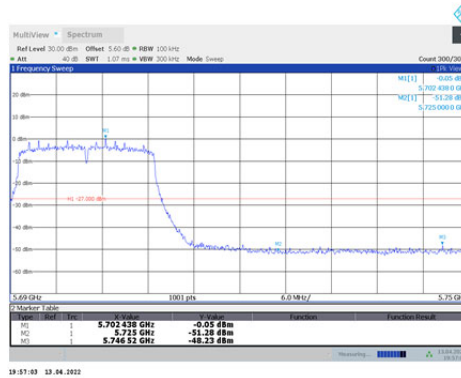
11N20SISO_Ant1_High_5680



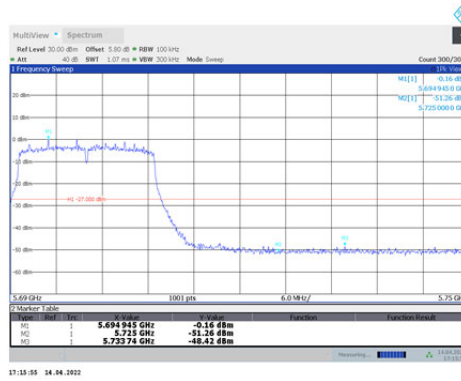
11N20SISO_Ant2_High_5680



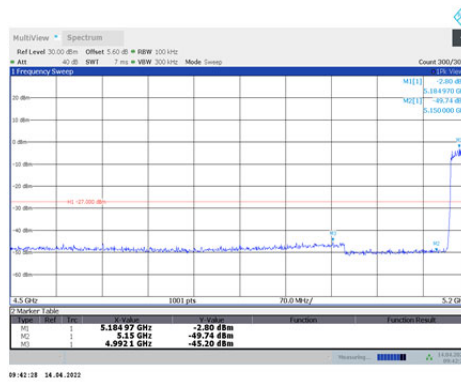
11N20SISO_Ant1_High_5700



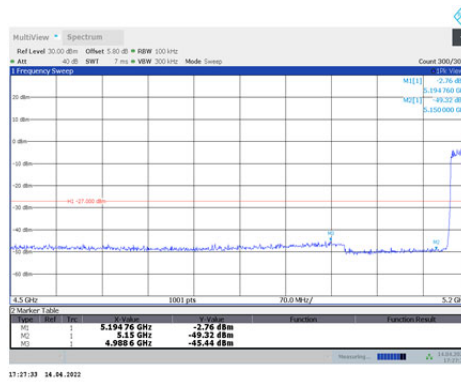
11N20SISO_Ant2_High_5700



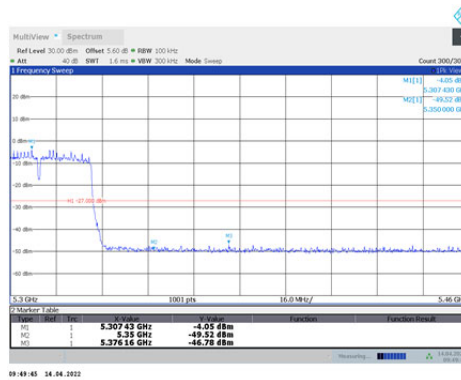
11N40SISO_Ant1_Low_5190



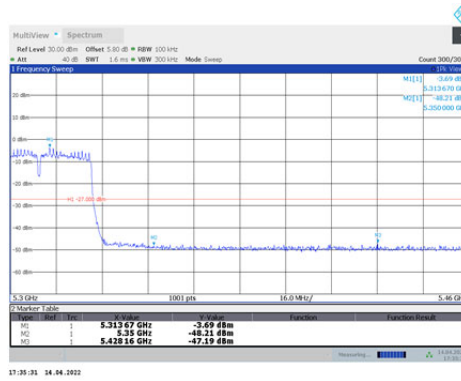
11N40SISO_Ant2_Low_5190



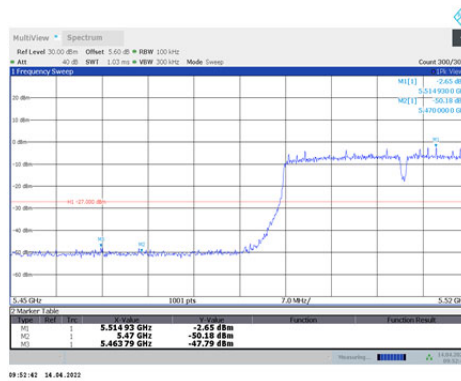
11N40SISO_Ant1_High_5310



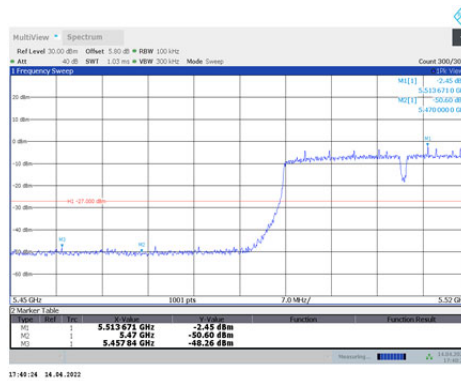
11N40SISO_Ant2_High_5310



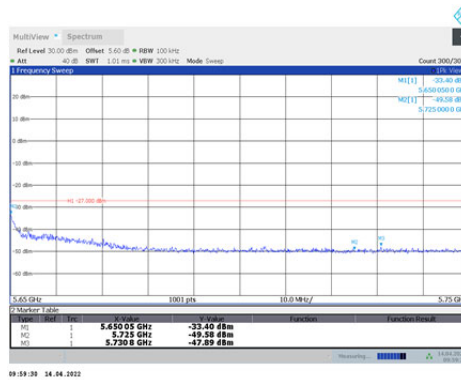
11N40SISO_Ant1_Low_5510



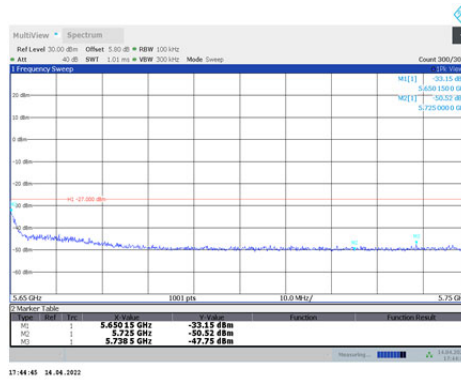
11N40SISO_Ant2_Low_5510



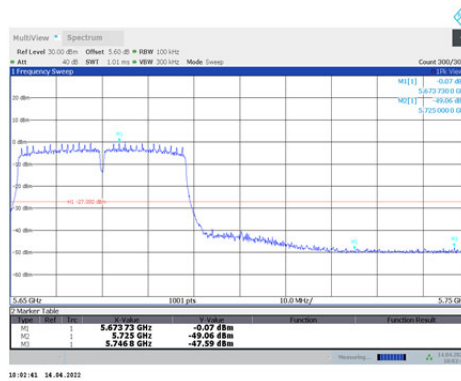
11N40SISO_Ant1_High_5630



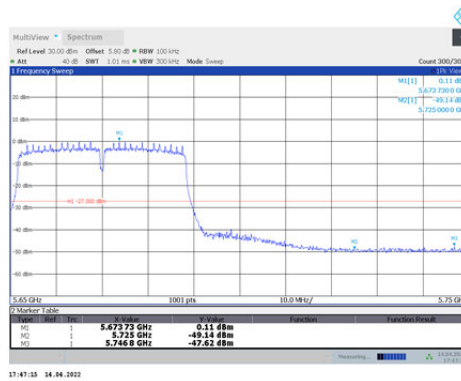
11N40SISO_Ant2_High_5630



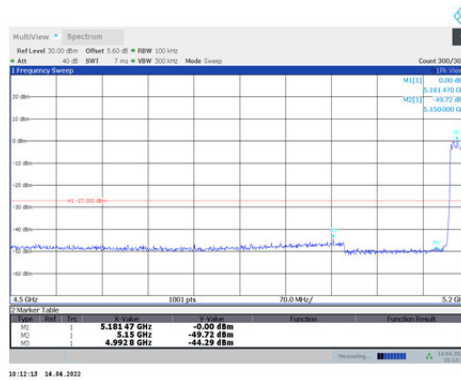
11N40SISO_Ant1_High_5670



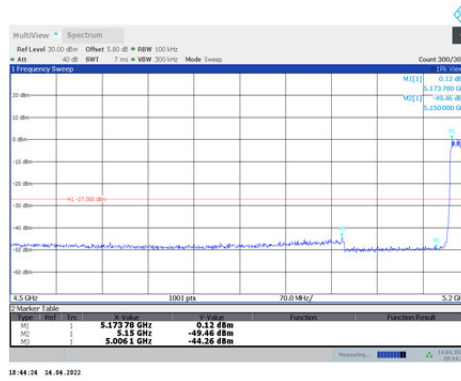
11N40SISO_Ant2_High_5670



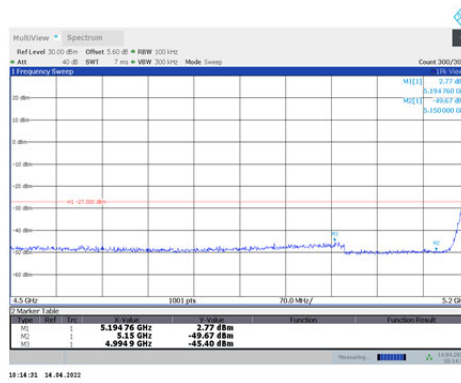
11AC20SISO_Ant1_Low_5180



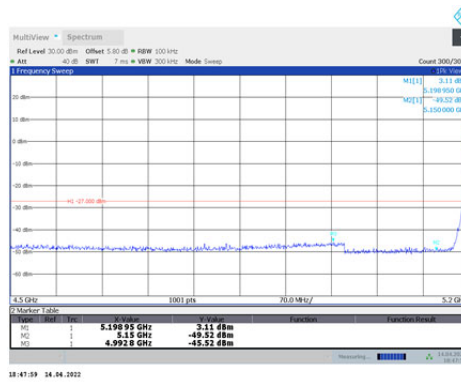
11AC20SISO_Ant2_Low_5180



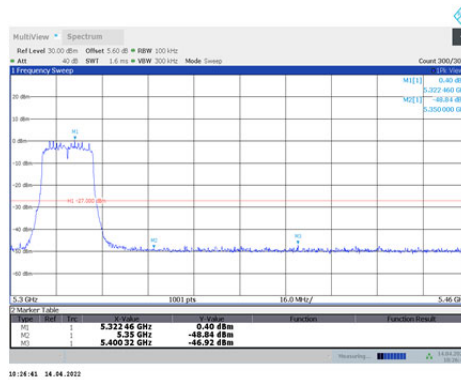
11AC20SISO_Ant1_Low_5200



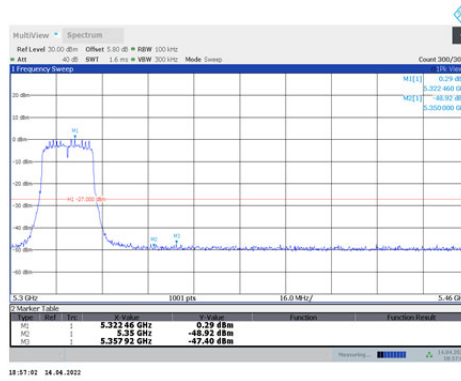
11AC20SISO_Ant2_Low_5200



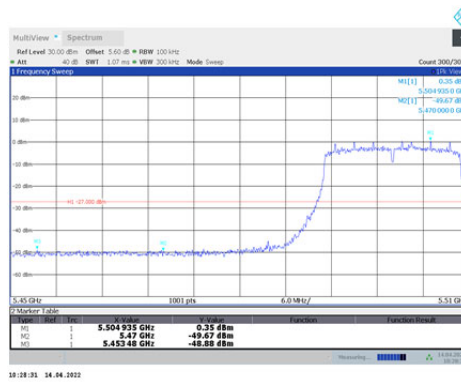
11AC20SISO_Ant1_High_5320



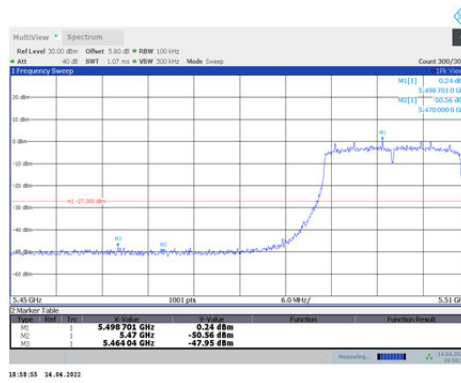
11AC20SISO_Ant2_High_5320



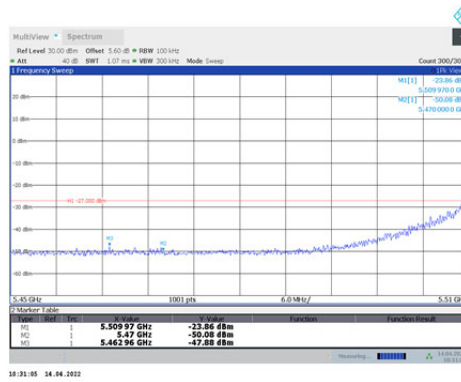
11AC20SISO_Ant1_Low_5500



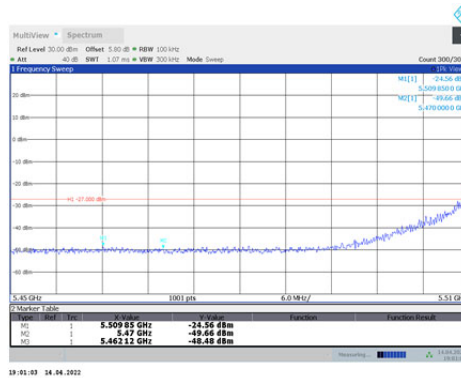
11AC20SISO_Ant2_Low_5500



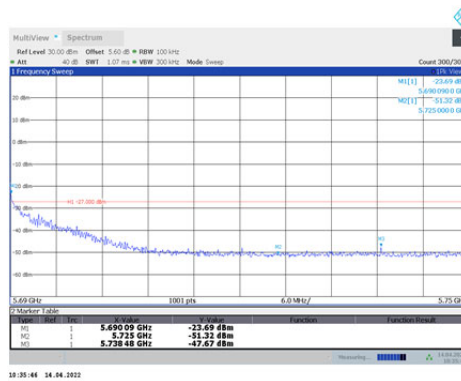
11AC20SISO_Ant1_Low_5520



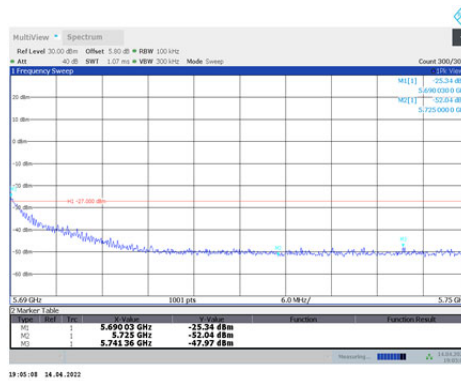
11AC20SISO_Ant2_Low_5520



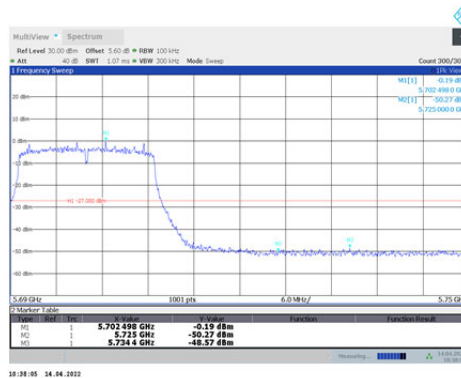
11AC20SISO_Ant1_High_5680



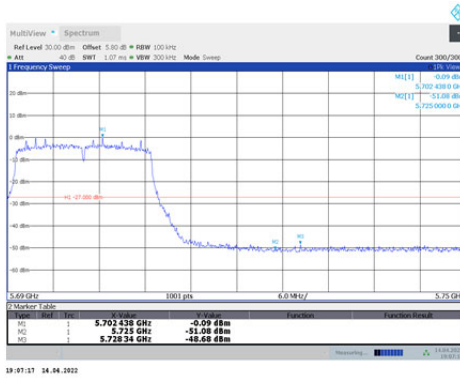
11AC20SISO_Ant2_High_5680



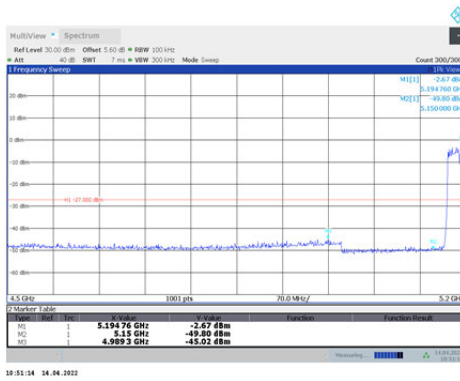
11AC20SISO_Ant1_High_5700



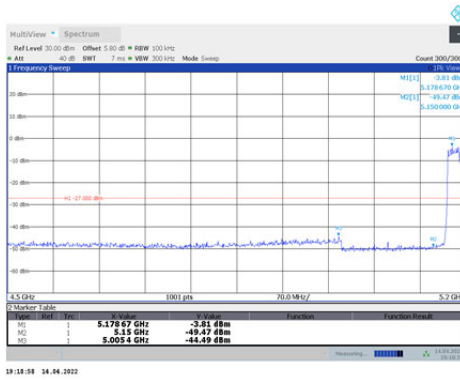
11AC20SISO_Ant2_High_5700



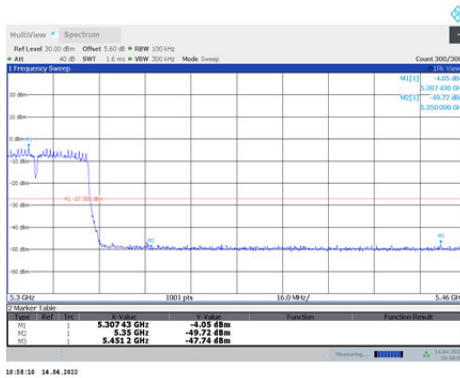
11AC40SISO_Ant1_Low_5190



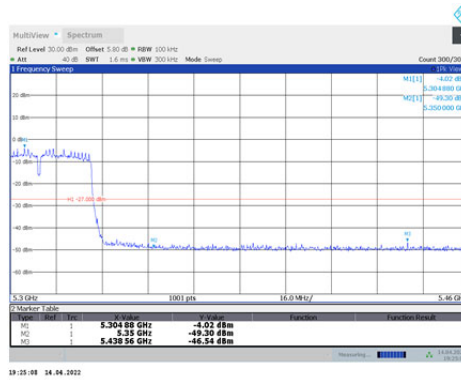
11AC40SISO_Ant2_Low_5190



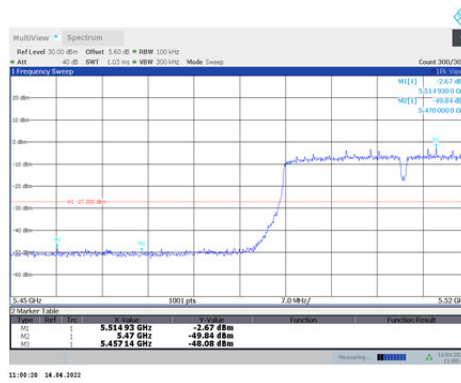
11AC40SISO_Ant1_High_5310



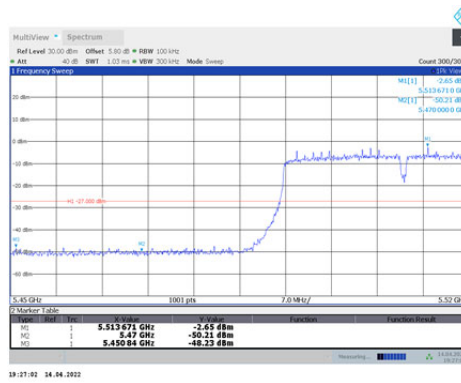
11AC40SISO_Ant2_High_5310



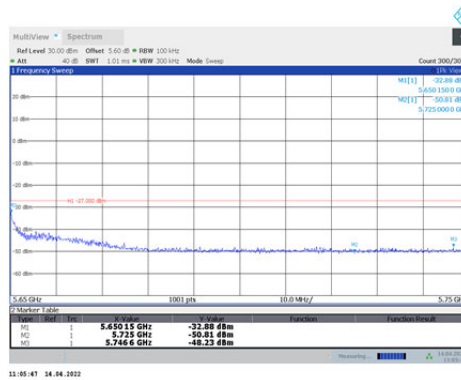
11AC40SISO_Ant1_Low_5510



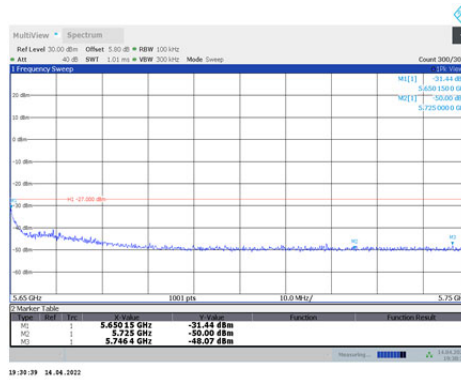
11AC40SISO_Ant2_Low_5510



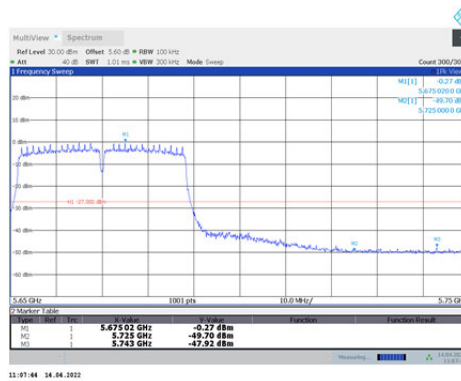
11AC40SISO_Ant1_High_5630



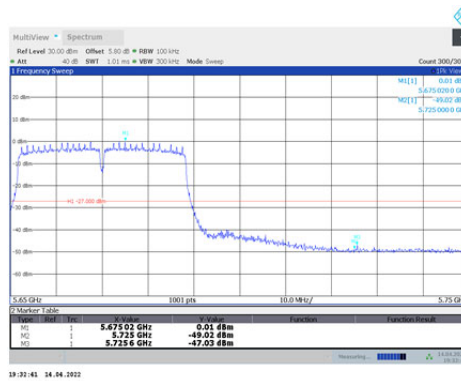
11AC40SISO_Ant2_High_5630



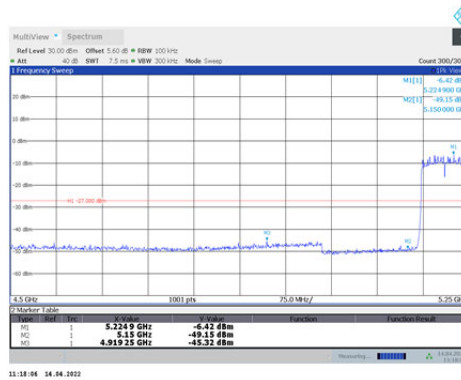
11AC40SISO_Ant1_High_5670



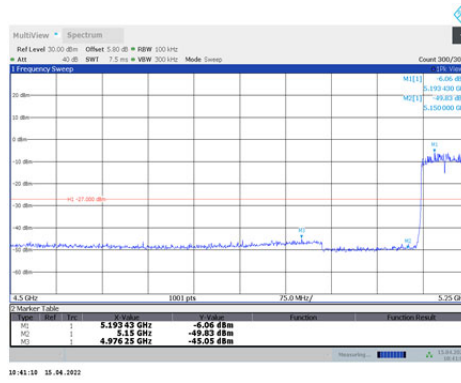
11AC40SISO_Ant2_High_5670



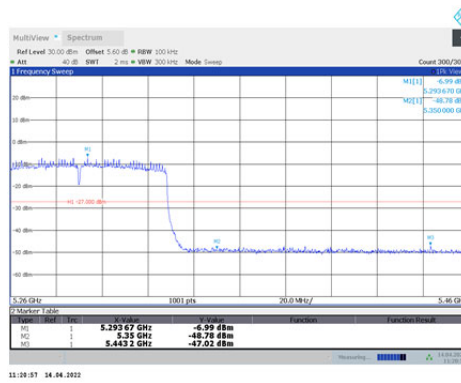
11AC80SISO_Ant1_Low_5210



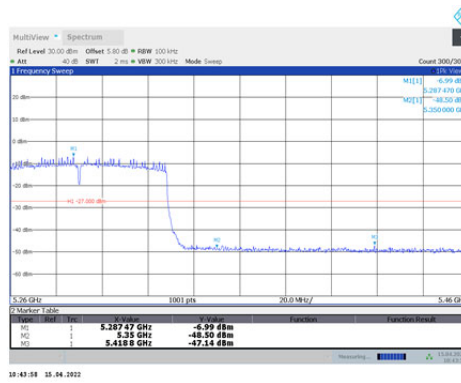
11AC80SISO_Ant2_Low_5210



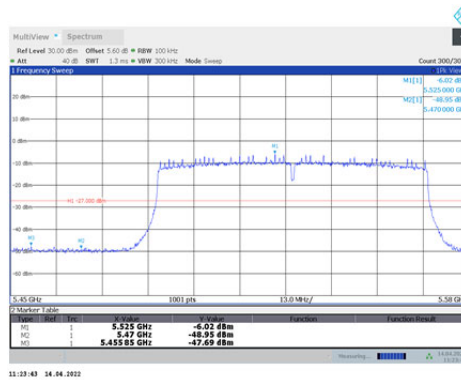
11AC80SISO_Ant1_High_5290



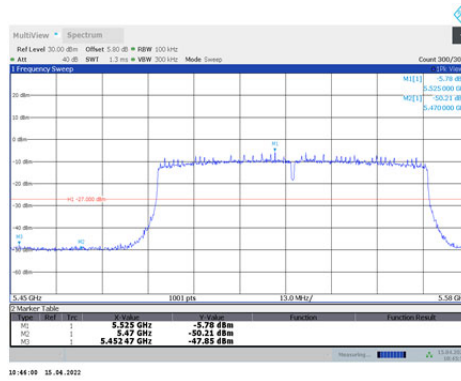
11AC80SISO_Ant2_High_5290



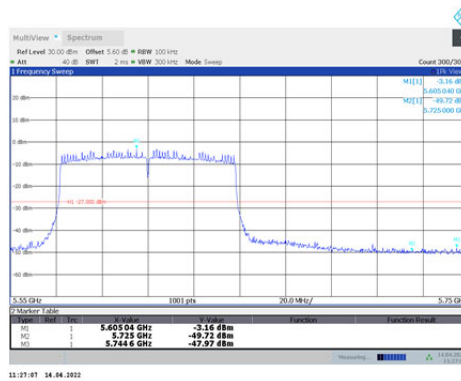
11AC80SISO_Ant1_Low_5530



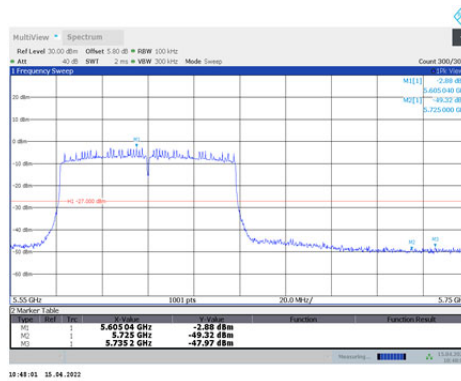
11AC80SISO_Ant2_Low_5530



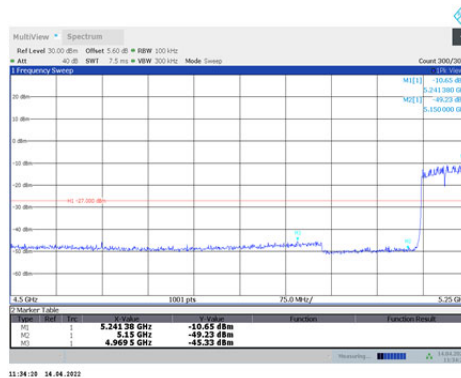
11AC80SISO_Ant1_High_5610



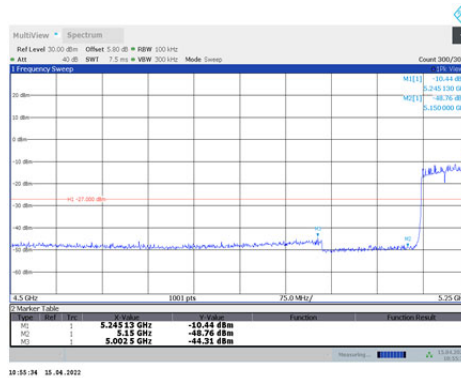
11AC80SISO_Ant2_High_5610



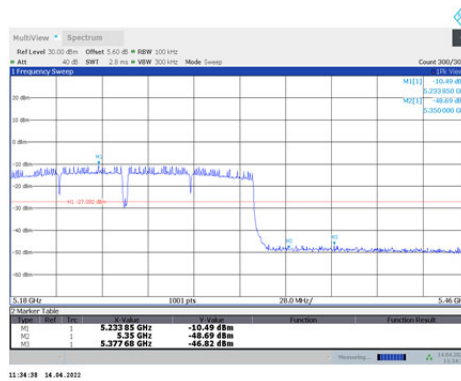
11AC160SISO_Ant1_Low_5250



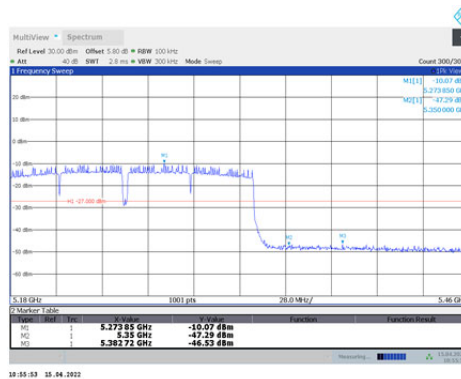
11AC160SISO_Ant2_Low_5250



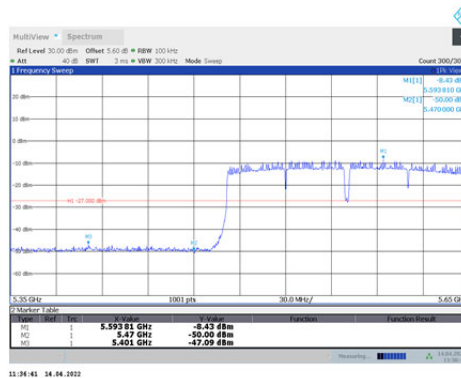
11AC160SISO_Ant1_High_5250



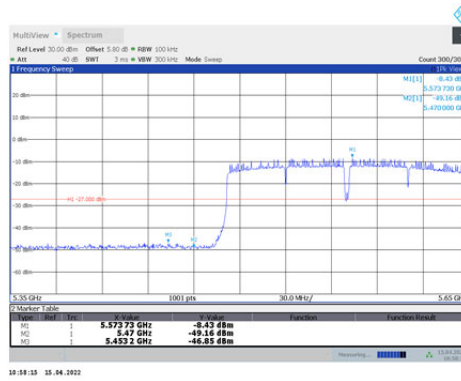
11AC160SISO_Ant2_High_5250



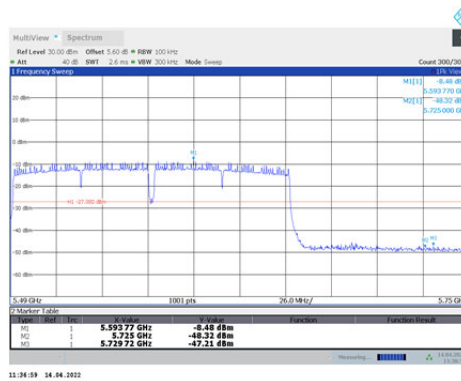
11AC160SISO_Ant1_Low_5570



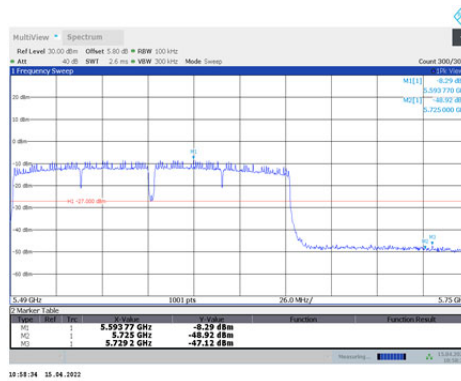
11AC160SISO_Ant2_Low_5570



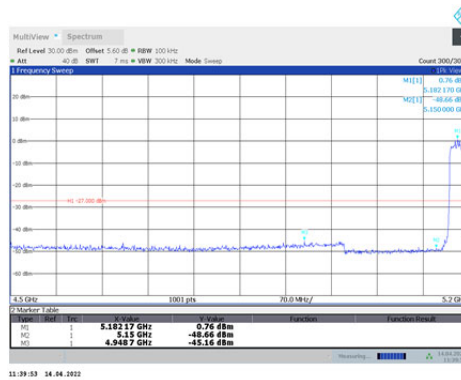
11AC160SISO_Ant1_High_5570



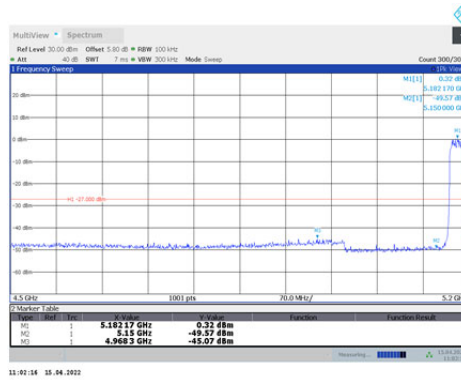
11AC160SISO_Ant2_High_5570



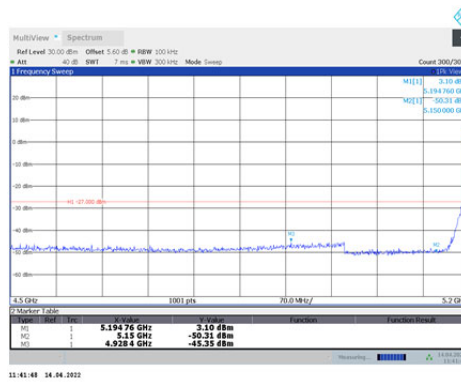
11AX20SISO_Ant1_Low_5180



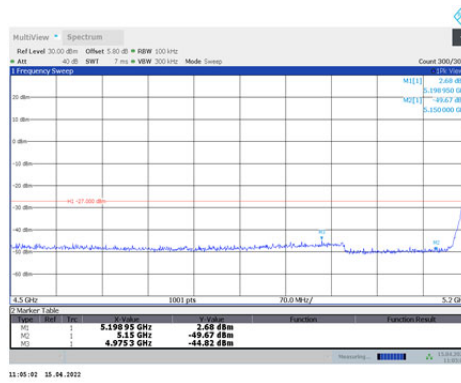
11AX20SISO_Ant2_Low_5180



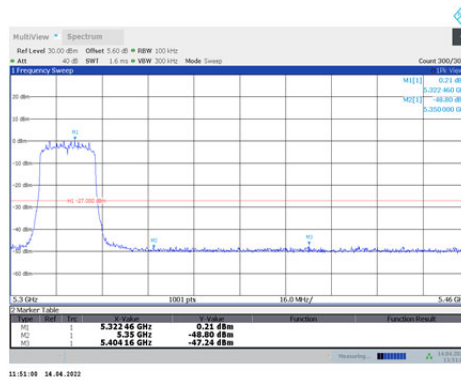
11AX20SISO_Ant1_Low_5200



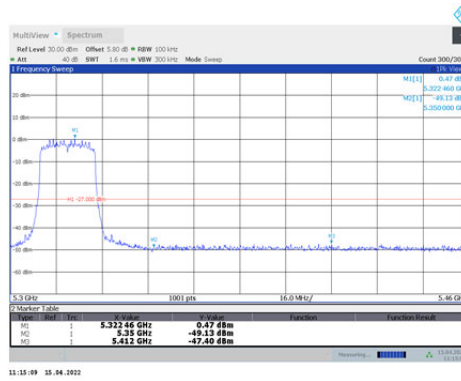
11AX20SISO_Ant2_Low_5200



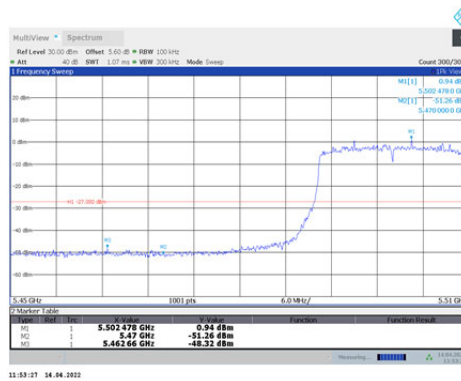
11AX20SISO_Ant1_High_5320



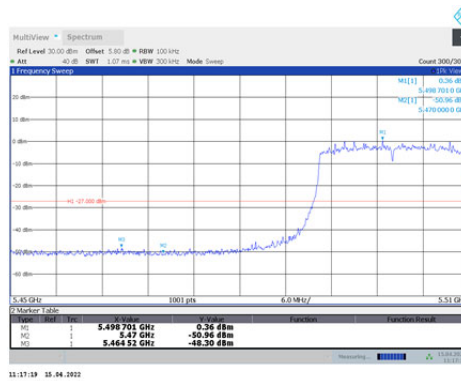
11AX20SISO_Ant2_High_5320



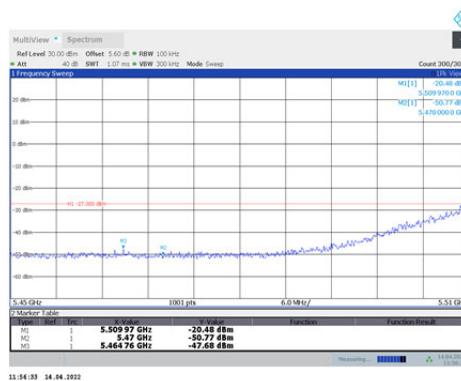
11AX20SISO_Ant1_Low_5500



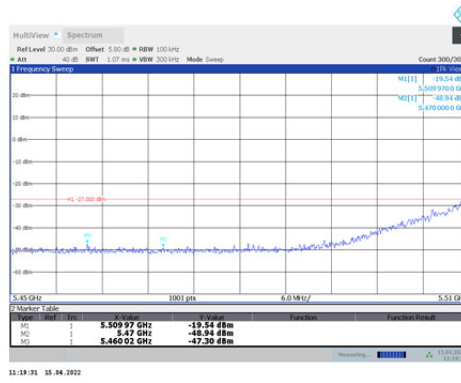
11AX20SISO_Ant2_Low_5500



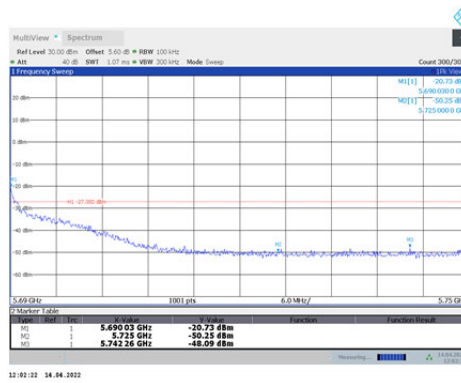
11AX20SISO_Ant1_Low_5520



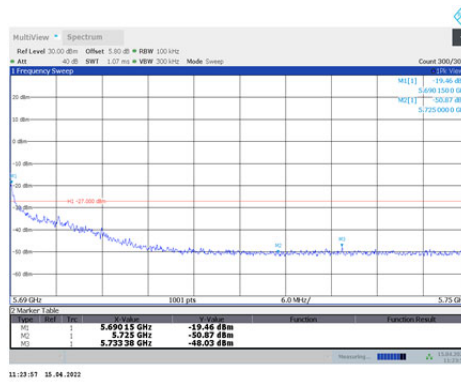
11AX20SISO_Ant2_Low_5520



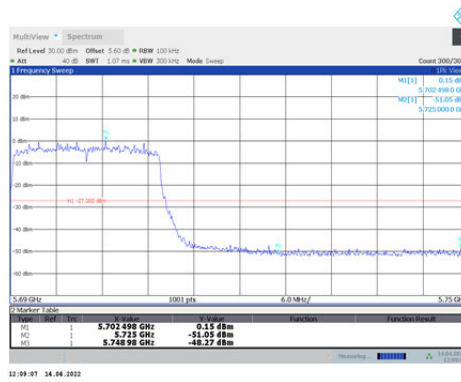
11AX20SISO_Ant1_High_5680



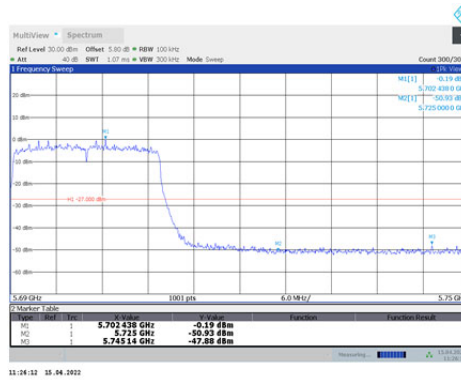
11AX20SISO_Ant2_High_5680



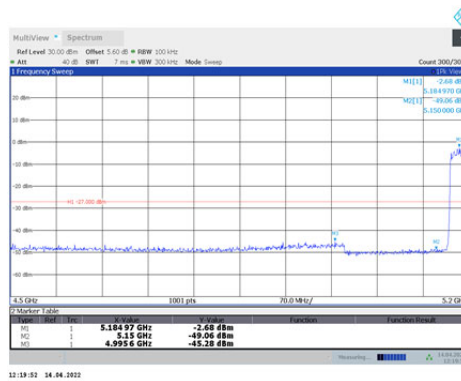
11AX20SISO_Ant1_High_5700



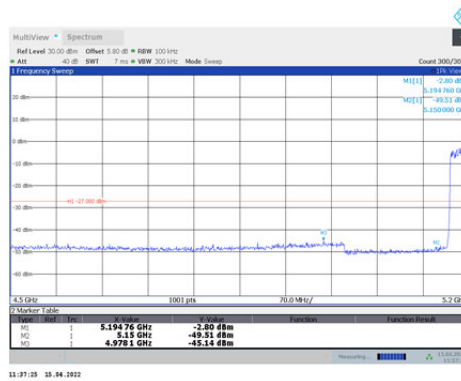
11AX20SISO_Ant2_High_5700



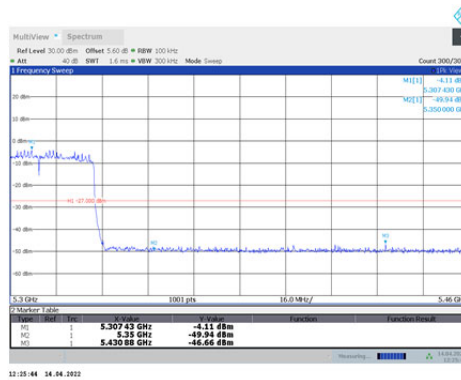
11AX40SISO_Ant1_Low_5190



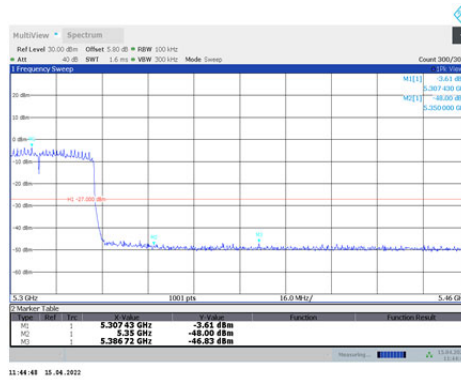
11AX40SISO_Ant2_Low_5190



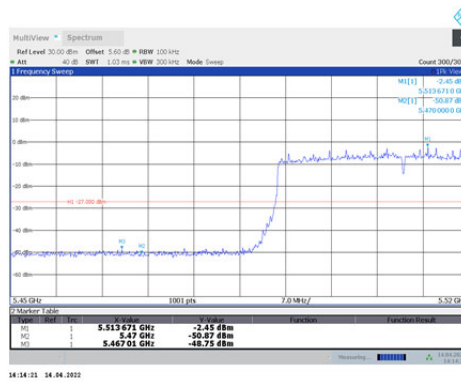
11AX40SISO_Ant1_High_5310



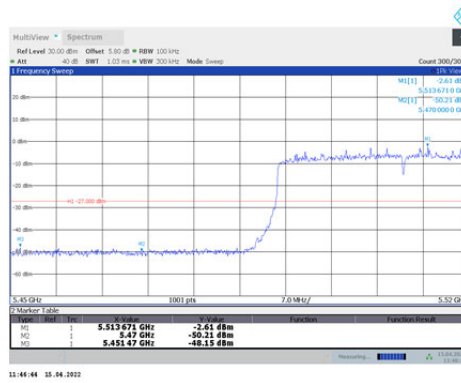
11AX40SISO_Ant2_High_5310



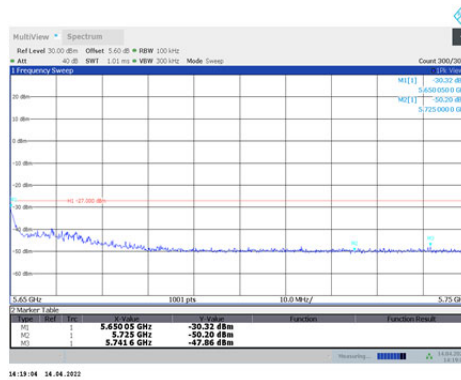
11AX40SISO_Ant1_Low_5510



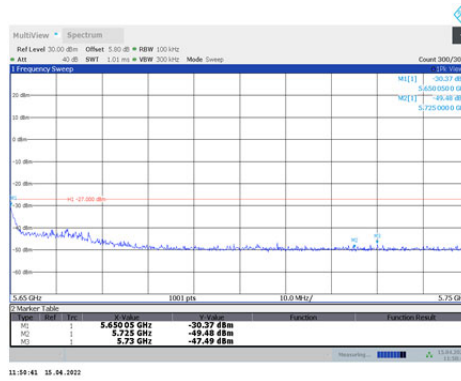
11AX40SISO_Ant2_Low_5510



11AX40SISO_Ant1_High_5630



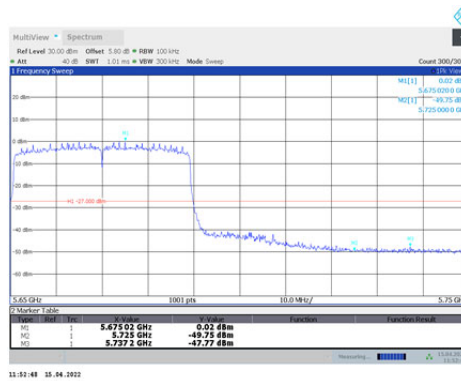
11AX40SISO_Ant2_High_5630



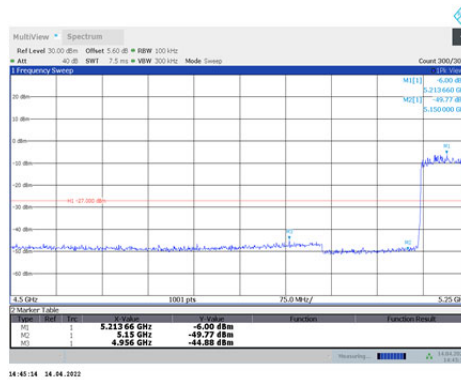
11AX40SISO_Ant1_High_5670



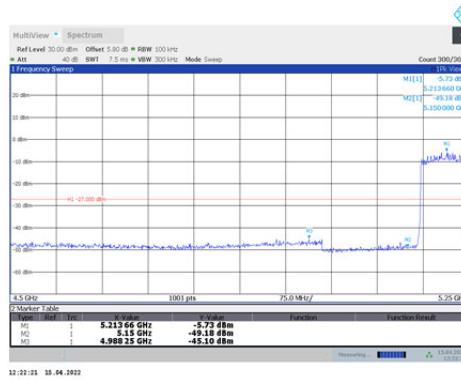
11AX40SISO_Ant2_High_5670



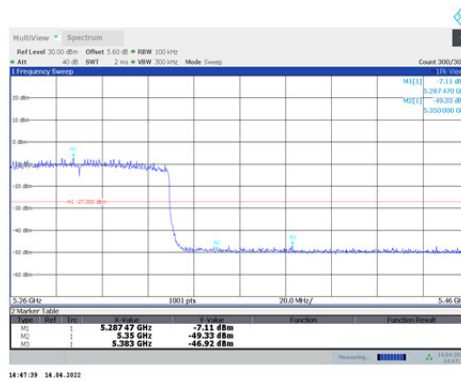
11AX80SISO_Ant1_Low_5210



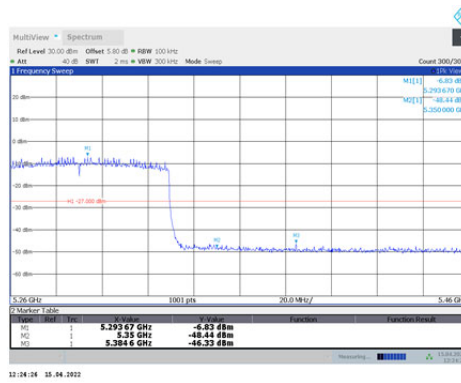
11AX80SISO_Ant2_Low_5210



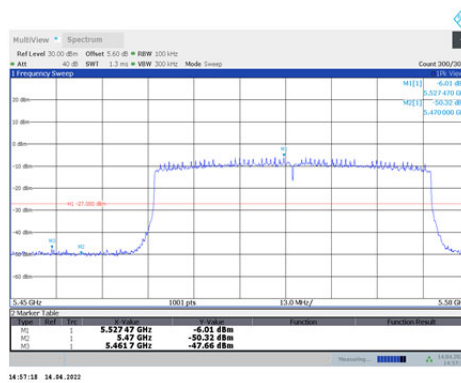
11AX80SISO_Ant1_High_5290



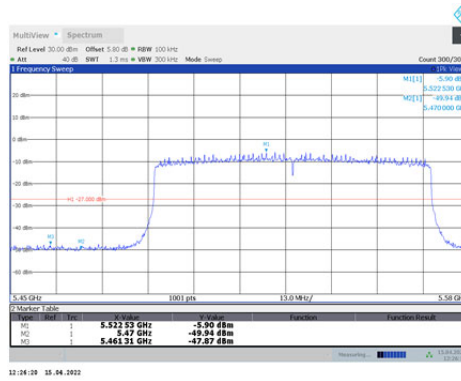
11AX80SISO_Ant2_High_5290



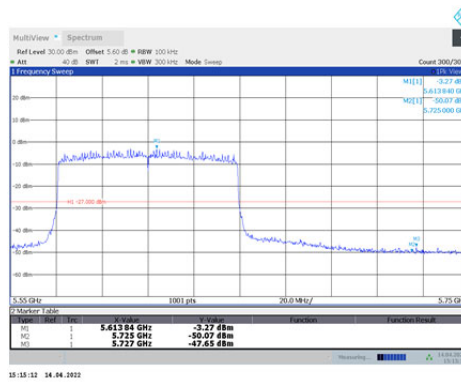
11AX80SISO_Ant1_Low_5530



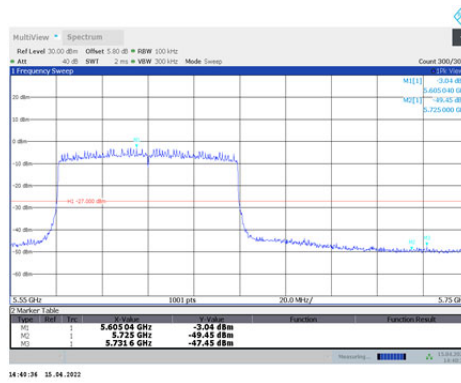
11AX80SISO_Ant2_Low_5530



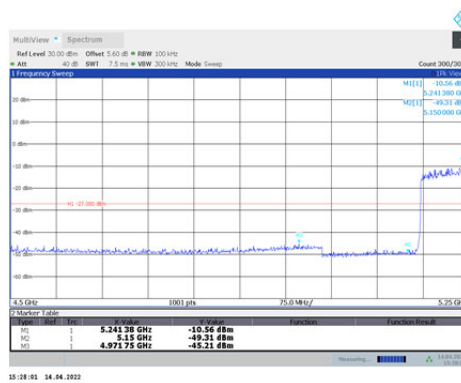
11AX80SISO_Ant1_High_5610



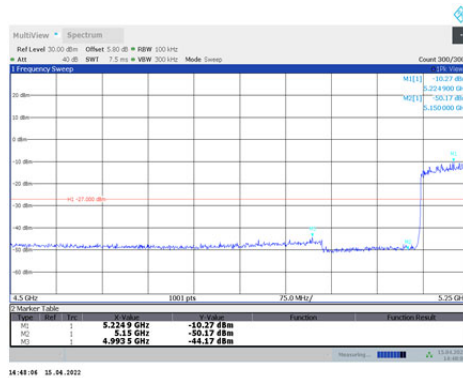
11AX80SISO_Ant2_High_5610



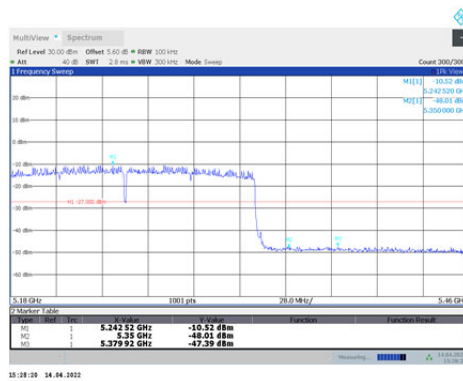
11AX160SISO_Ant1_Low_5250



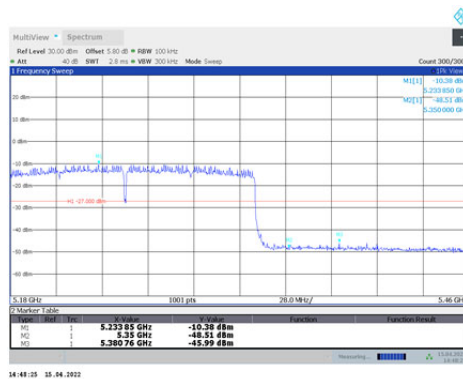
11AX160SISO_Ant2_Low_5250



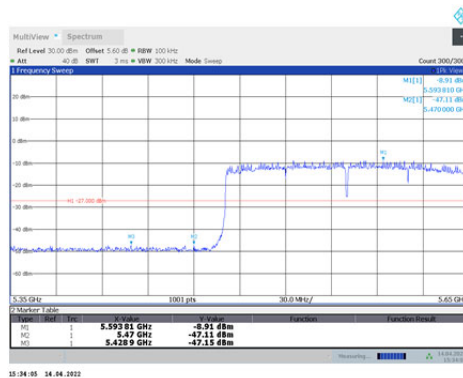
11AX160SISO_Ant1_High_5250



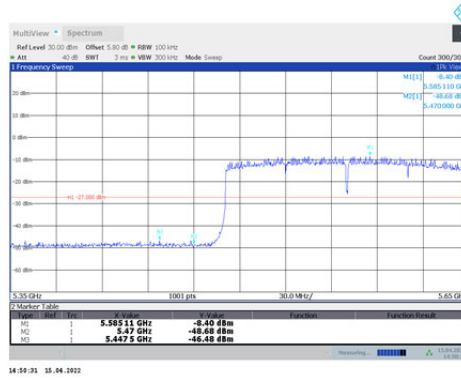
11AX160SISO_Ant2_High_5250



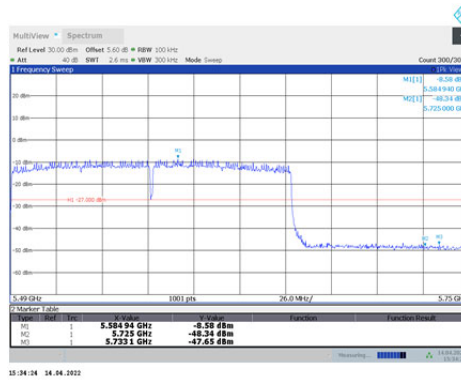
11AX160SISO_Ant1_Low_5570



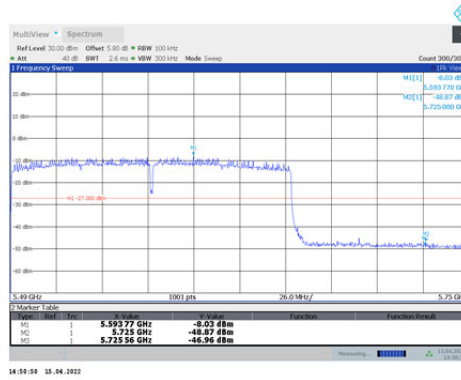
11AX160SISO_Ant2_Low_5570



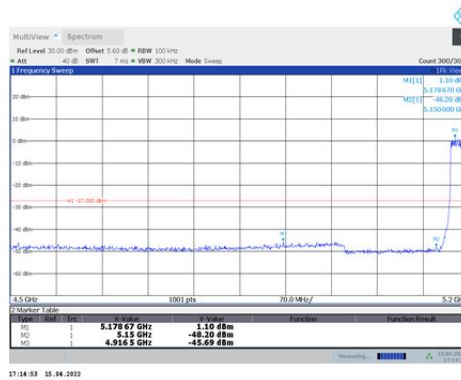
11AX160SISO_Ant1_High_5570



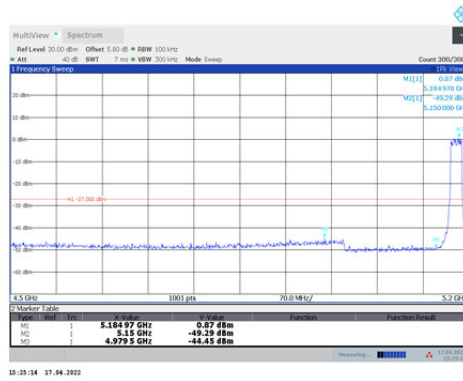
11AX160SISO_Ant2_High_5570



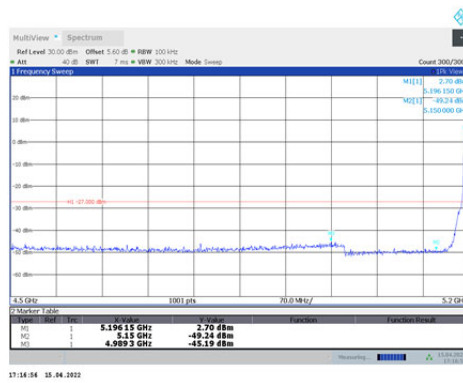
11A-CDD_Ant1_Low_5180



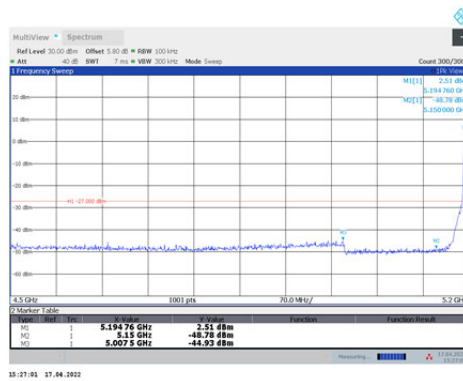
11A-CDD_Ant2_Low_5180



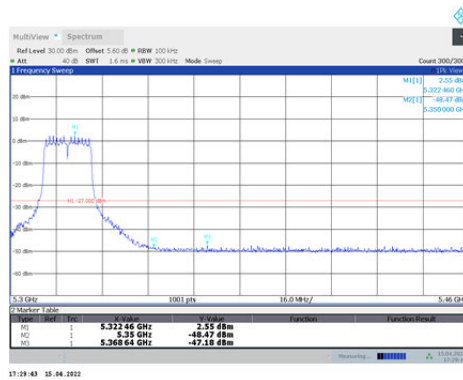
11A-CDD_Ant1_Low_5200



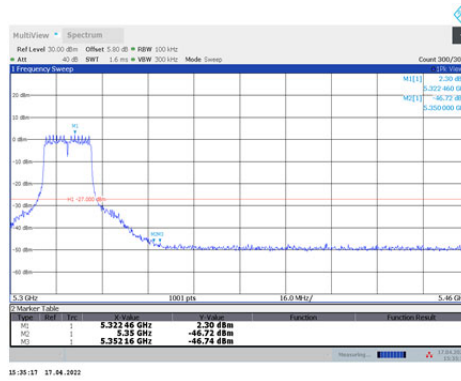
11A-CDD_Ant2_Low_5200



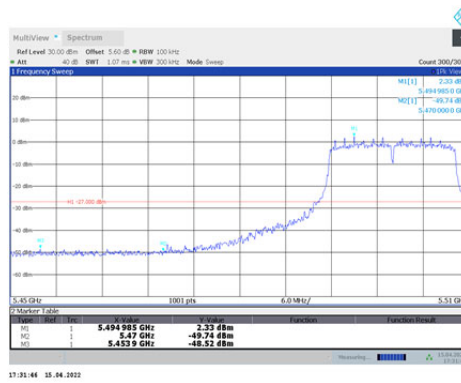
11A-CDD_Ant1_High_5320



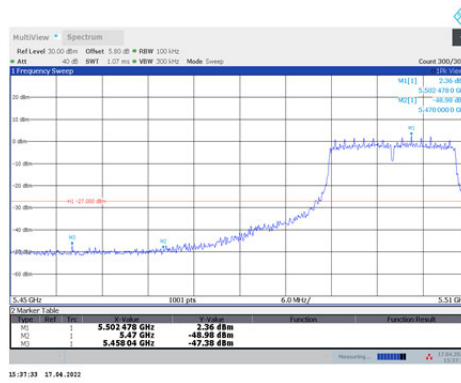
11A-CDD_Ant2_High_5320



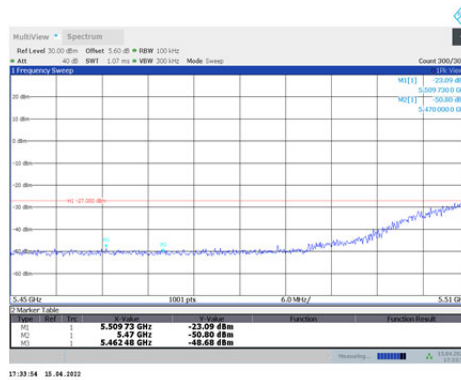
11A-CDD_Ant1_Low_5500



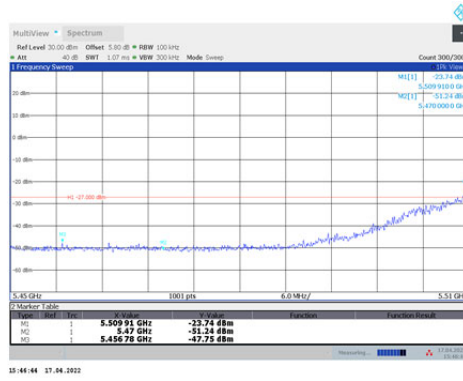
11A-CDD_Ant2_Low_5500



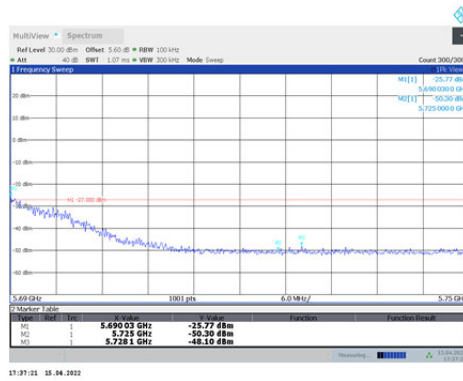
11A-CDD_Ant1_Low_5520



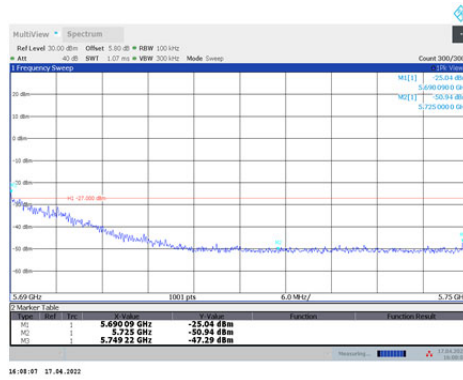
11A-CDD_Ant2_Low_5520



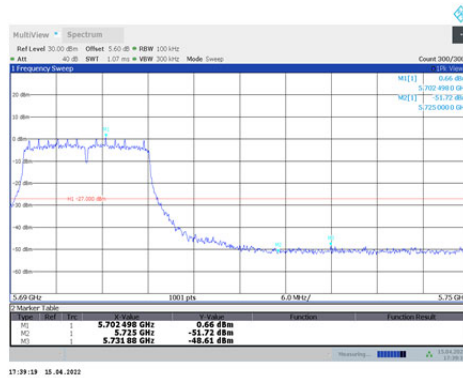
11A-CDD_Ant1_High_5680



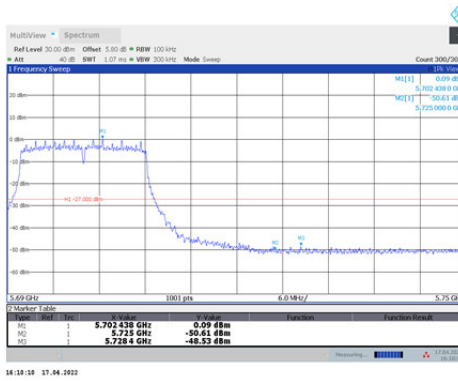
11A-CDD_Ant2_High_5680



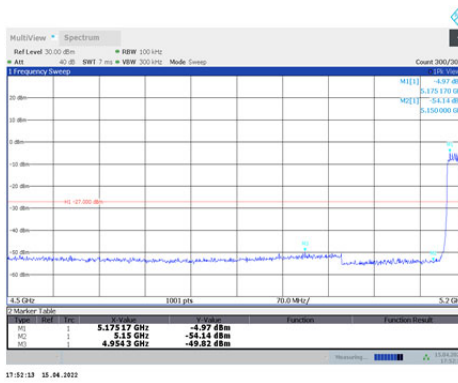
11A-CDD_Ant1_High_5700



11A-CDD_Ant2_High_5700



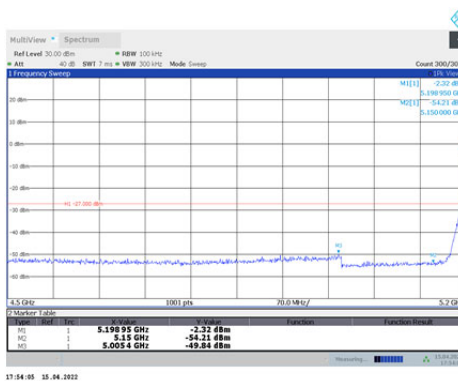
11N20MIMO_Ant1_Low_5180



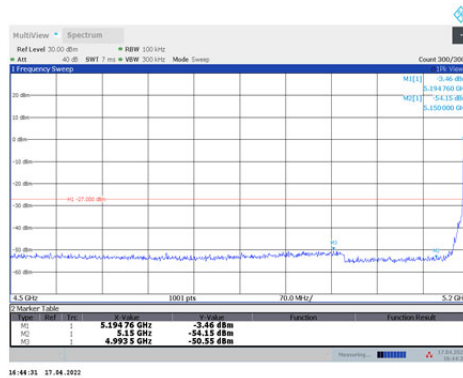
11N20MIMO_Ant2_Low_5180



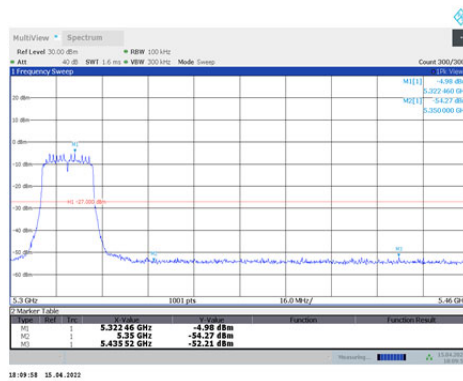
11N20MIMO_Ant1_Low_5200



11N20MIMO_Ant2_Low_5200



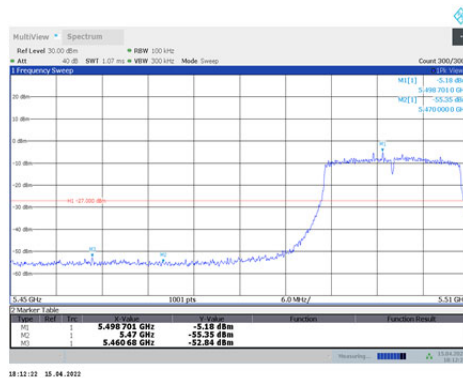
11N20MIMO_Ant1_High_5320



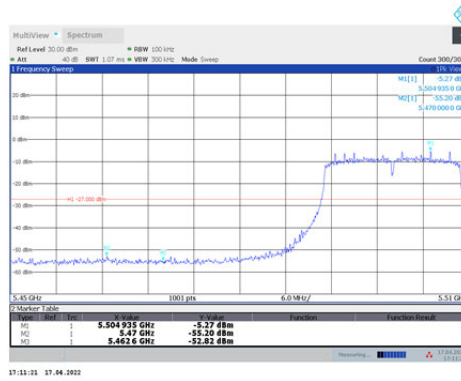
11N20MIMO_Ant2_High_5320



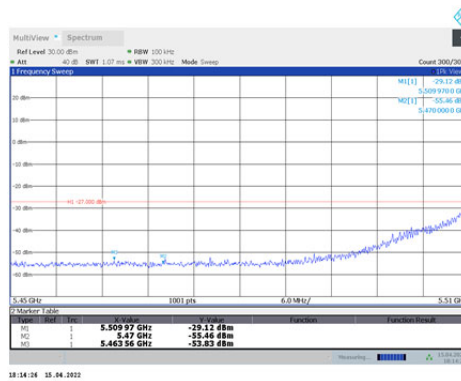
11N20MIMO_Ant1_Low_5500



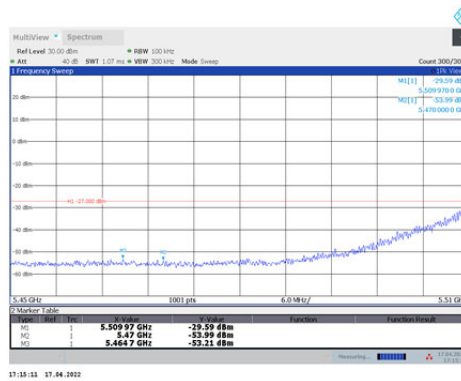
11N20MIMO_Ant2_Low_5500



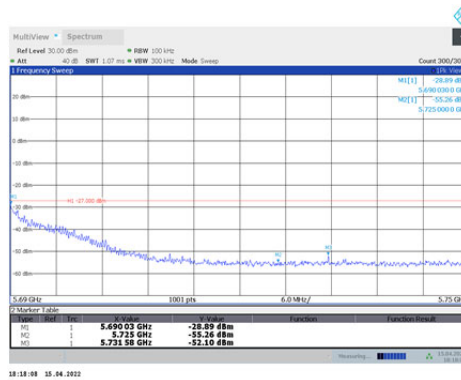
11N20MIMO_Ant1_Low_5520



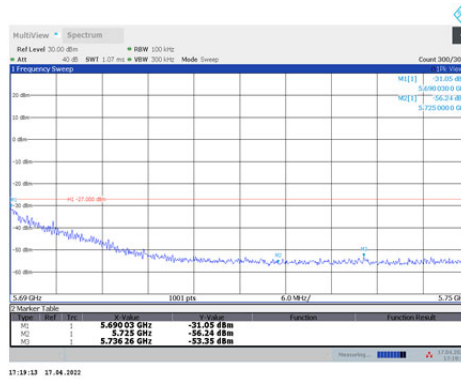
11N20MIMO_Ant2_Low_5520



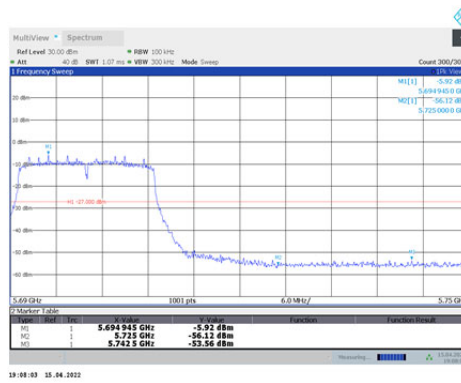
11N20MIMO_Ant1_High_5680



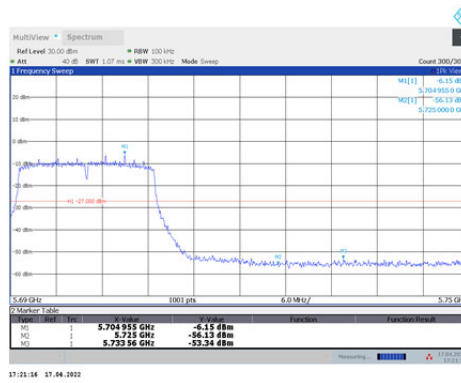
11N20MIMO_Ant2_High_5680



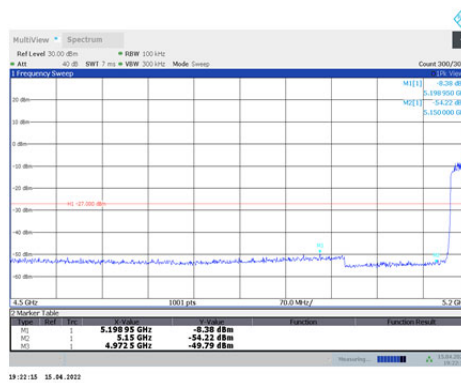
11N20MIMO_Ant1_High_5700



11N20MIMO_Ant2_High_5700



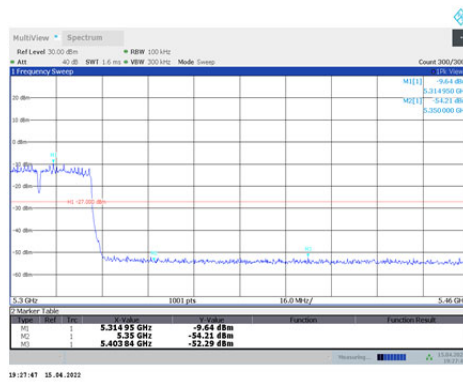
11N40MIMO_Ant1_Low_5190



11N40MIMO_Ant2_Low_5190



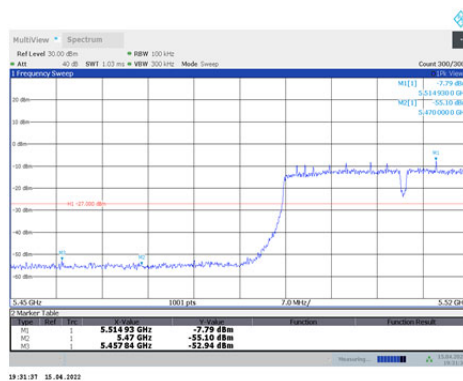
11N40MIMO_Ant1_High_5310



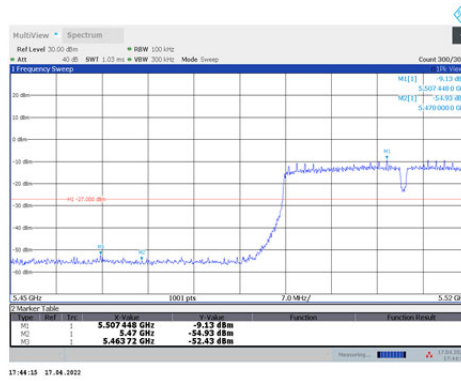
11N40MIMO_Ant2_High_5310



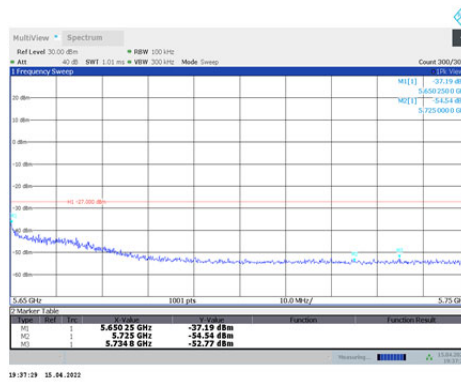
11N40MIMO_Ant1_Low_5510



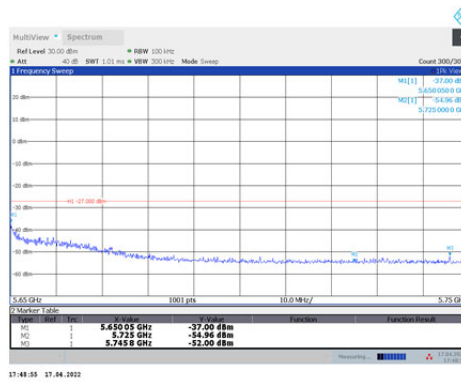
11N40MIMO_Ant2_Low_5510



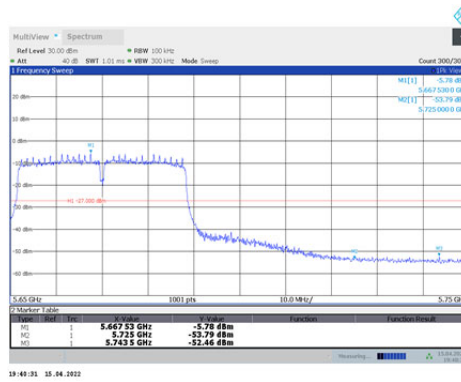
11N40MIMO_Ant1_High_5630



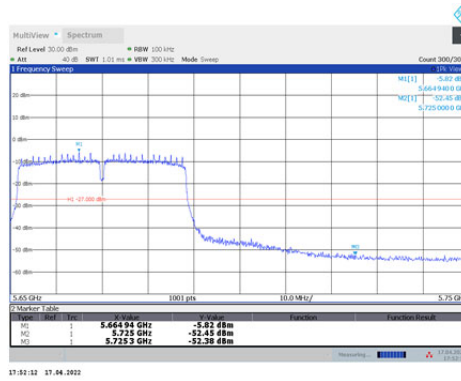
11N40MIMO_Ant2_High_5630



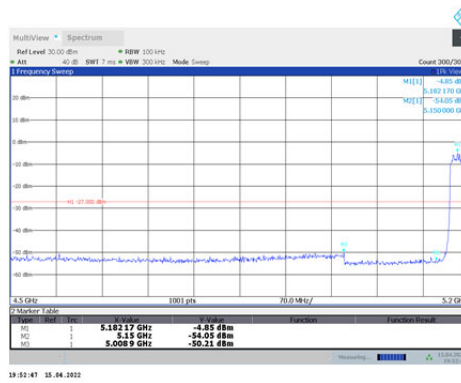
11N40MIMO_Ant1_High_5670



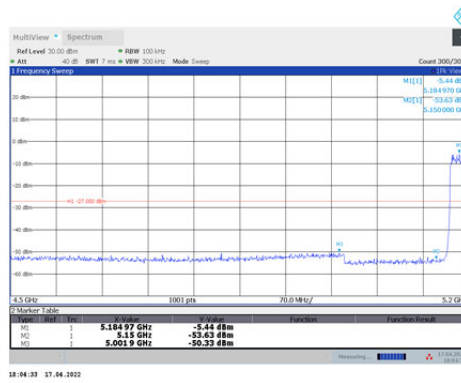
11N40MIMO_Ant2_High_5670



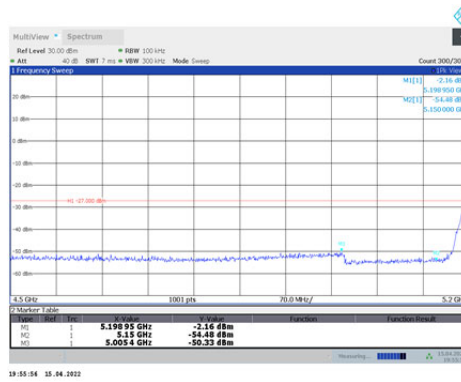
11AC20MIMO_Ant1_Low_5180



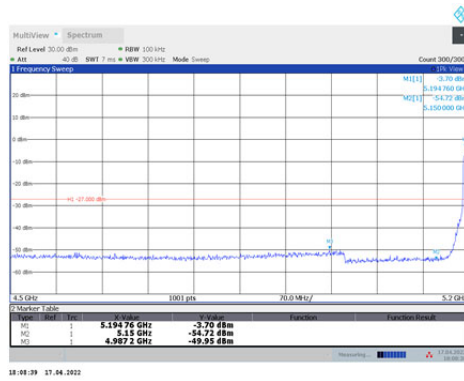
11AC20MIMO_Ant2_Low_5180



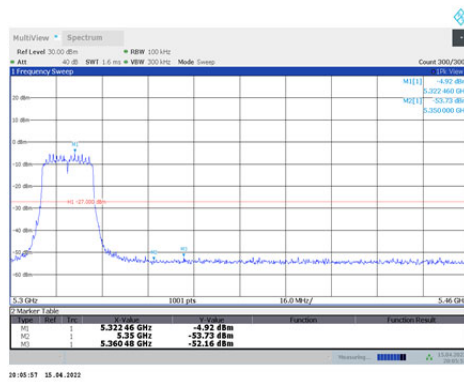
11AC20MIMO_Ant1_Low_5200



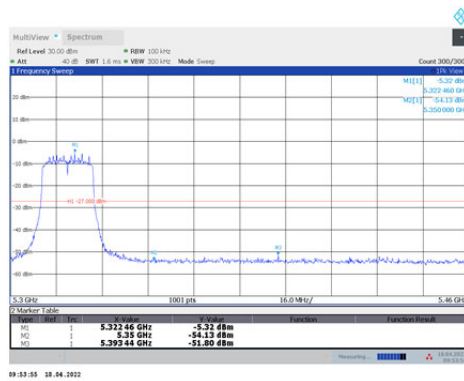
11AC20MIMO_Ant2_Low_5200



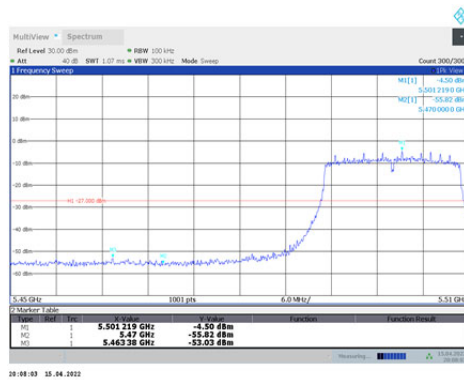
11AC20MIMO_Ant1_High_5320



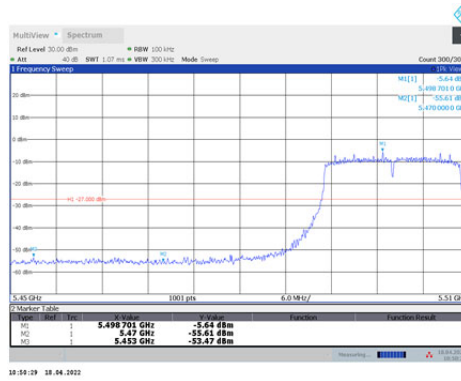
11AC20MIMO_Ant2_High_5320



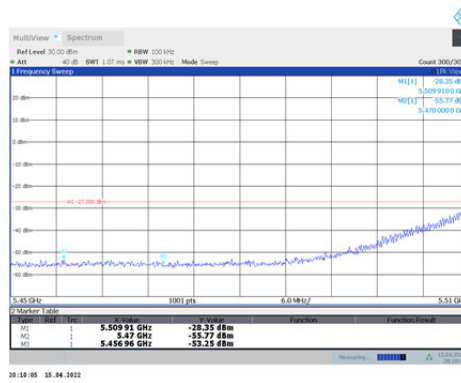
11AC20MIMO_Ant1_Low_5500



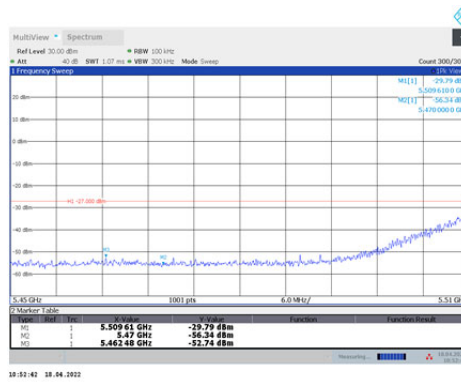
11AC20MIMO_Ant2_Low_5500



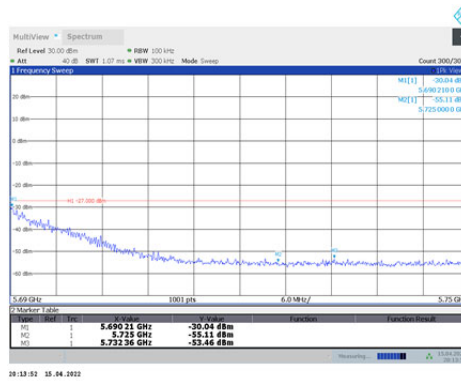
11AC20MIMO_Ant1_Low_5520



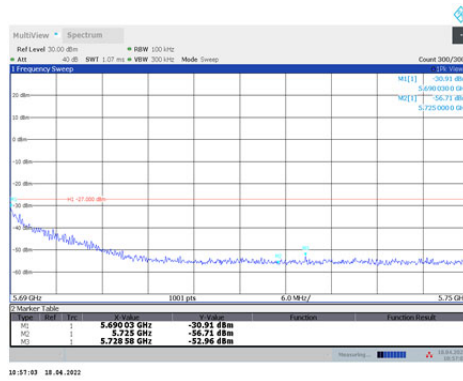
11AC20MIMO_Ant2_Low_5520



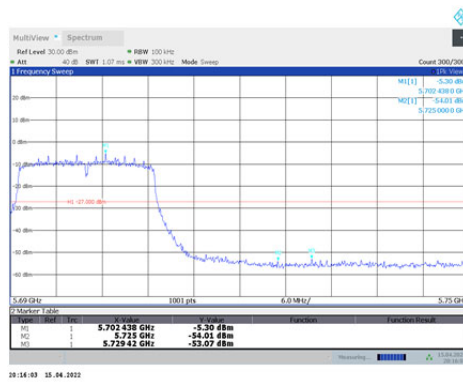
11AC20MIMO_Ant1_High_5680



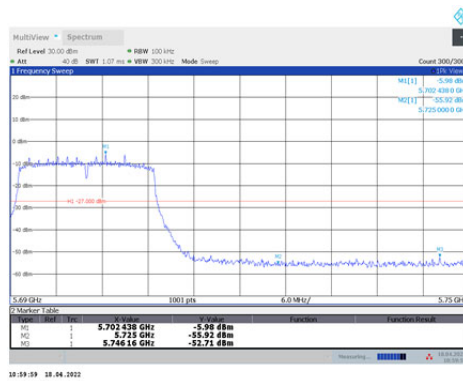
11AC20MIMO_Ant2_High_5680



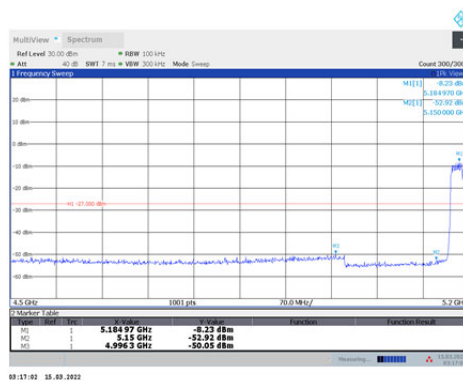
11AC20MIMO_Ant1_High_5700



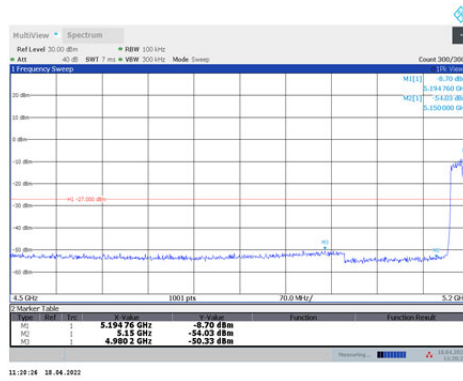
11AC20MIMO_Ant2_High_5700



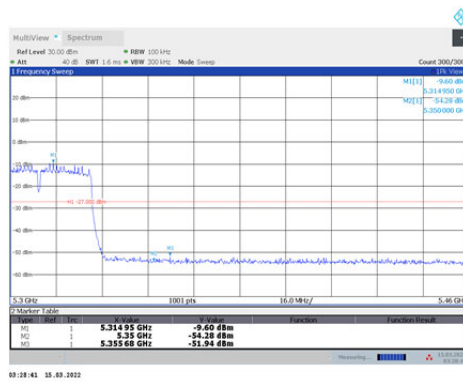
11AC40MIMO_Ant1_Low_5190



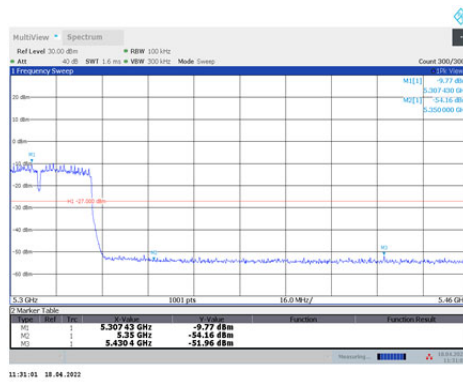
11AC40MIMO_Ant2_Low_5190



11AC40MIMO_Ant1_High_5310



11AC40MIMO_Ant2_High_5310



11AC40MIMO_Ant1_Low_5510

