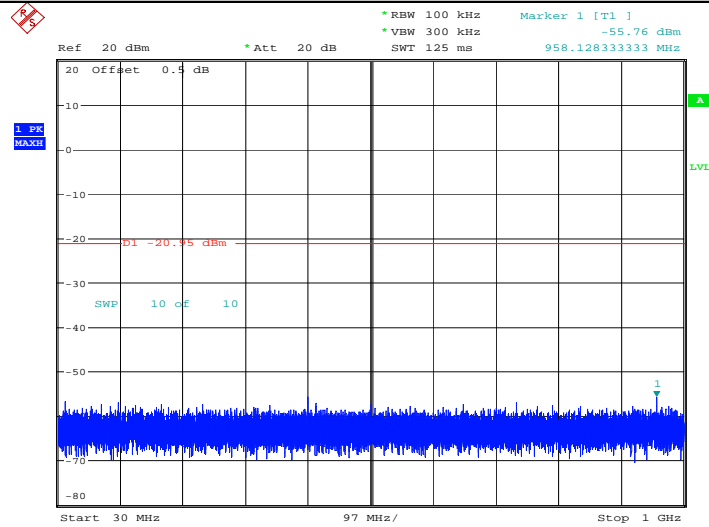


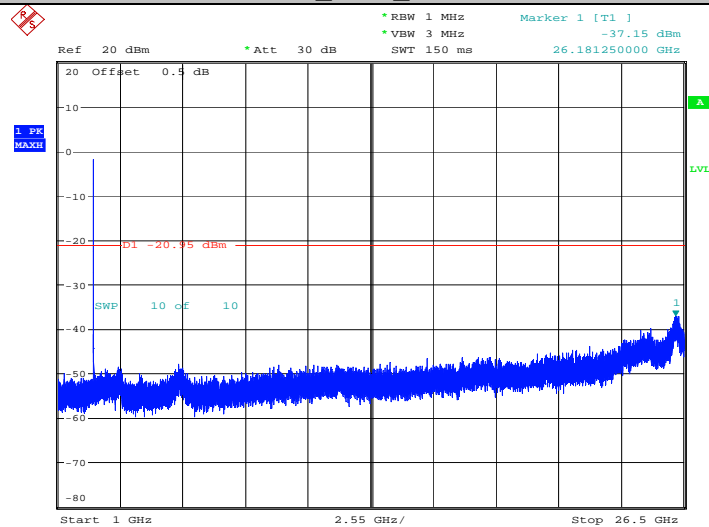
Date: 5.DEC.2020 10:04:13

/4-DQPSK_2441_30~1000



Date: 5.DEC.2020 10:04:24

/4-DQPSK_2441_1000~26500



Date: 5.DEC.2020 10:04:59

/4-DQPSK_2480_0~Reference

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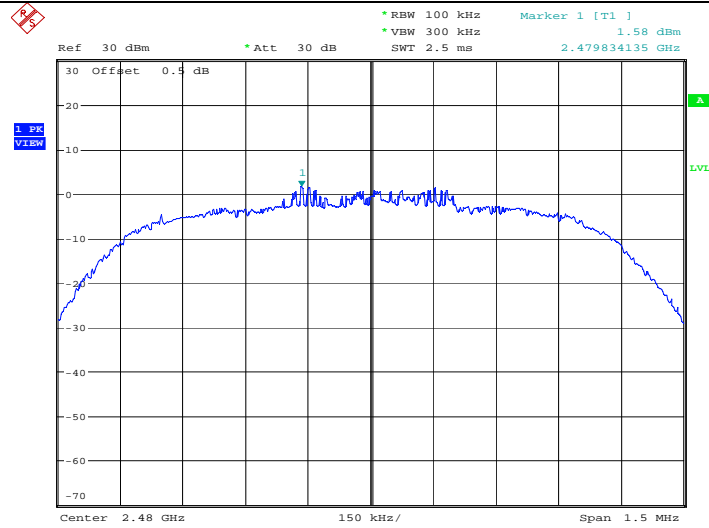
Tel.: (86)755-27521059

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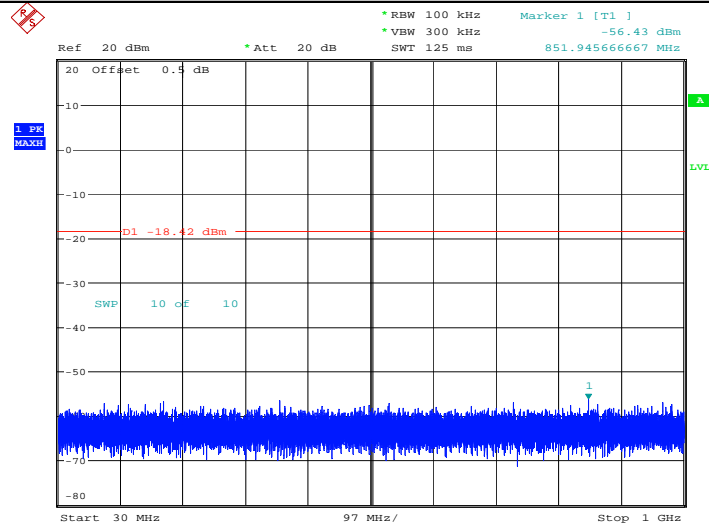


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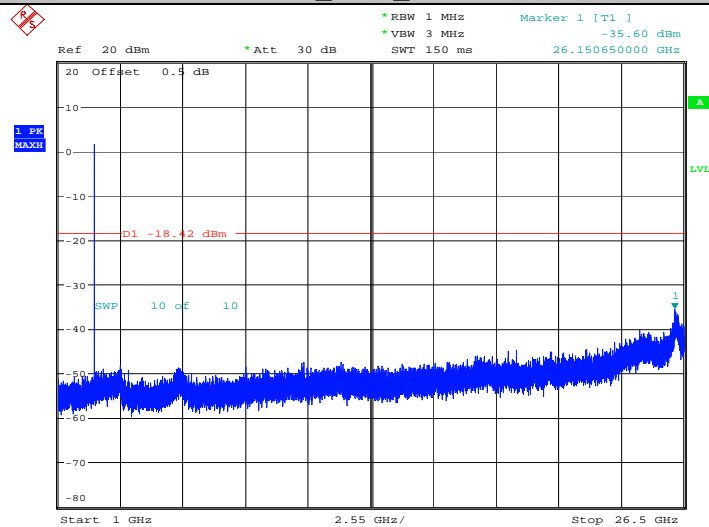
Date: 5.DEC.2020 10:08:50

/4-DQPSK_2480_30~1000



Date: 5.DEC.2020 10:09:01

/4-DQPSK_2480_1000~26500



Date: 5.DEC.2020 10:09:36

8-DPSK_2402_0~Reference

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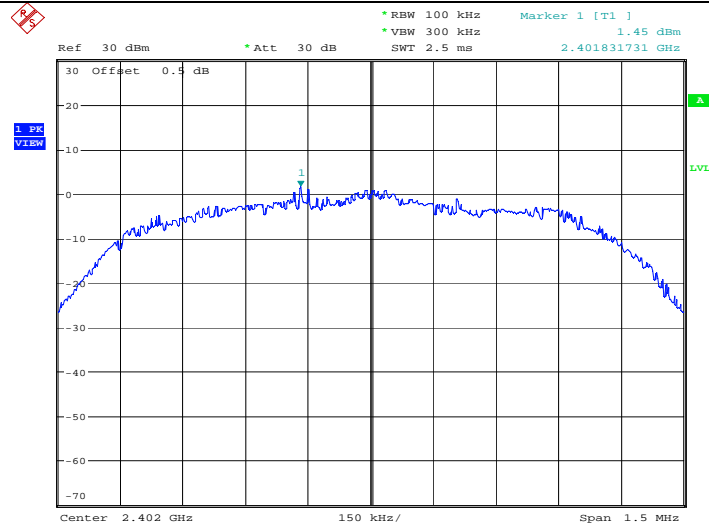
Tel.: (86)755-27521059

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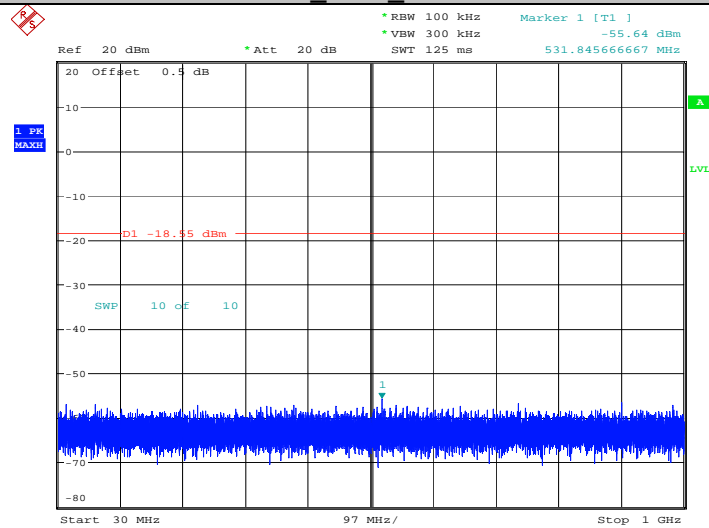


For anti-fake verification, please visit the official website of Certification and Accreditation Administration of the People's Republic of China : yz.cnca.cn



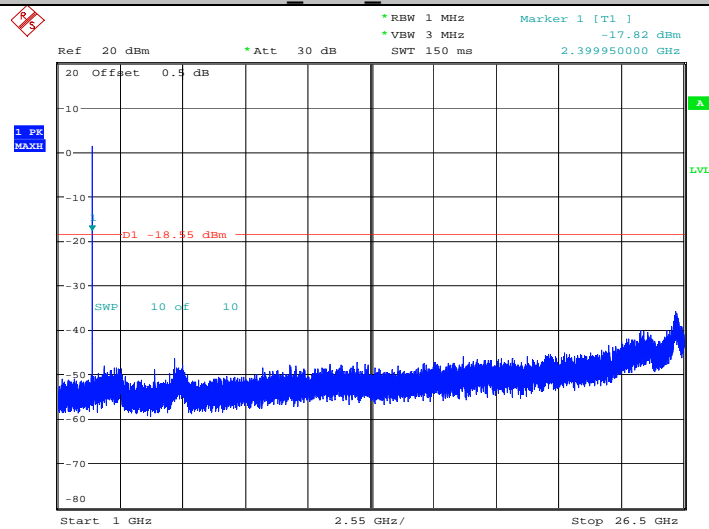
Date: 5.DEC.2020 10:14:45

8-DPSK_2402_30~1000



Date: 5.DEC.2020 10:14:55

8-DPSK_2402_1000~26500



Date: 5.DEC.2020 10:15:30

8-DPSK_2441_0~Reference

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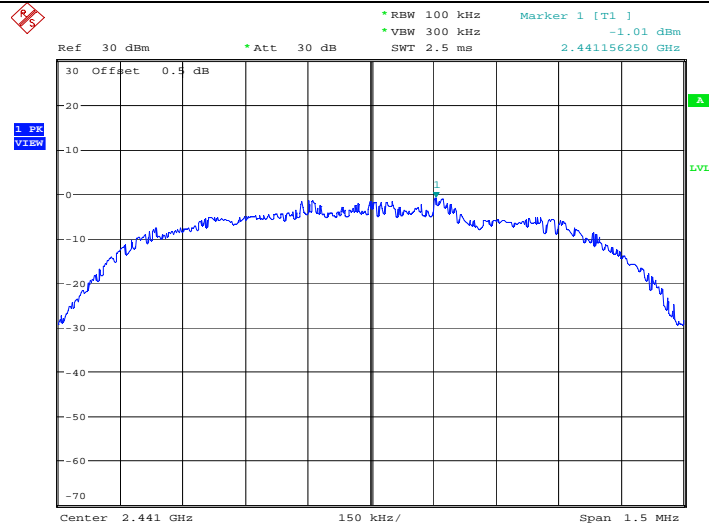
Tel.: (86)755-27521059

Fax: (86)755-27521011

Http://www.sz-ctc.org.cn

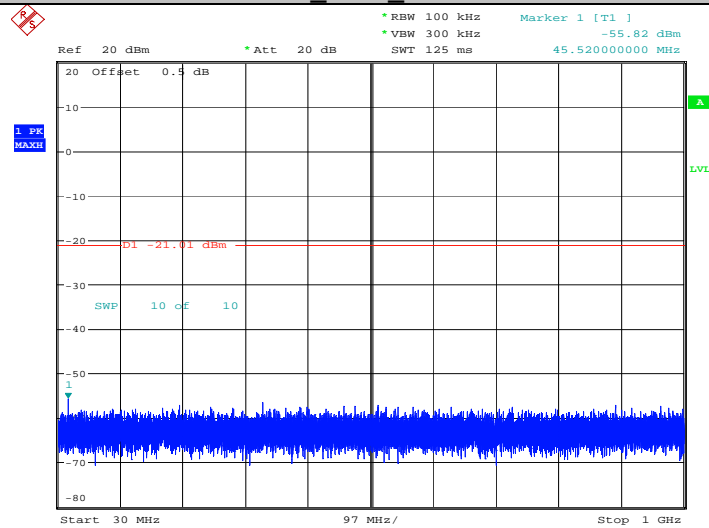


For anti-fake verification, please visit the official website of Certification and Accreditation Administration of the People's Republic of China : yz.cnca.cn



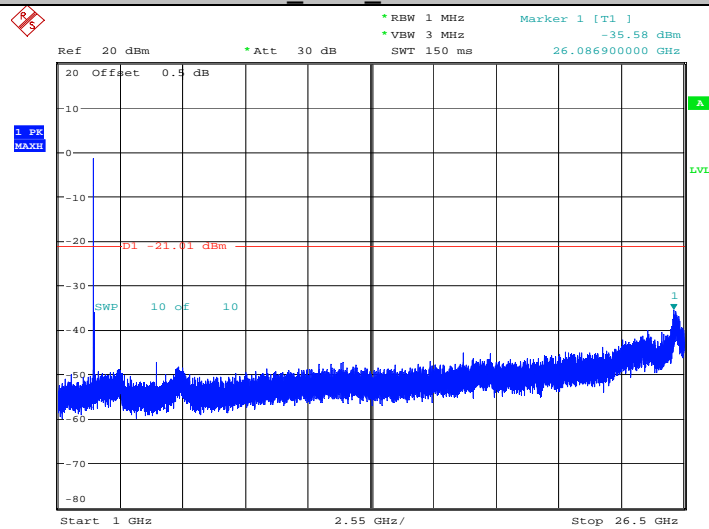
Date: 5.DEC.2020 10:18:10

8-DPSK_2441_30~1000



Date: 5.DEC.2020 10:18:21

8-DPSK_2441_1000~26500



Date: 5.DEC.2020 10:18:56

8-DPSK_2480_0~Reference

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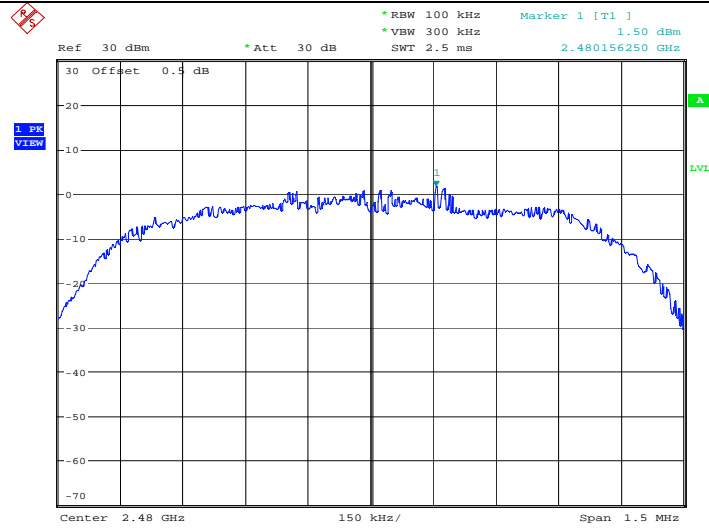
Tel.: (86)755-27521059

Fax: (86)755-27521011

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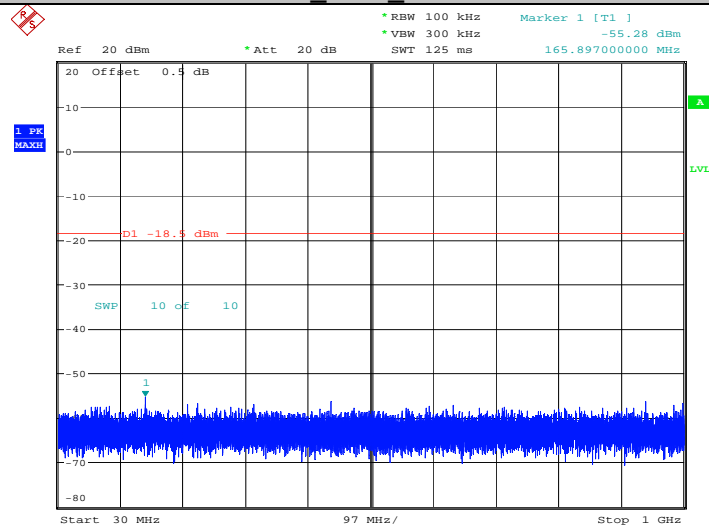


For anti-fake verification, please visit the official website of Certification and Accreditation Administration of the People's Republic of China : yz.cnca.cn



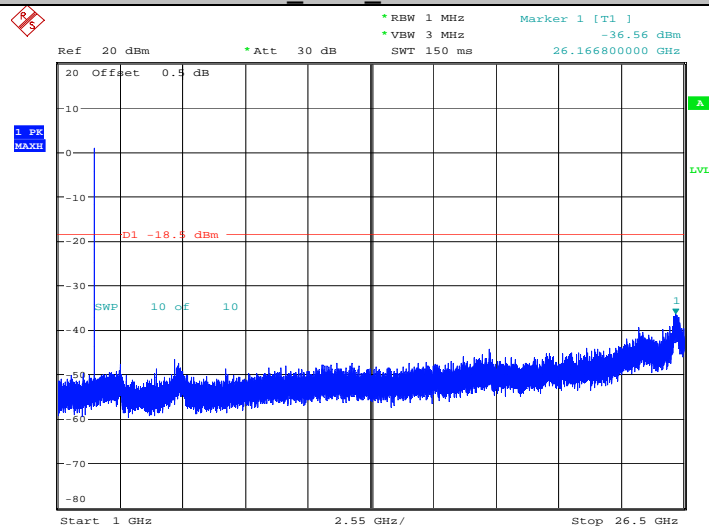
Date: 5.DEC.2020 10:22:43

8-DPSK_2480_30~1000



Date: 5.DEC.2020 10:22:54

8-DPSK_2480_1000~26500



Date: 5.DEC.2020 10:23:29

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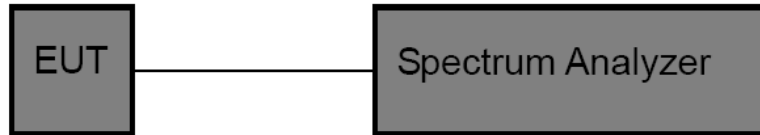


3.5. 20DB Bandwidth

Limit

N/A

Test Configuration



Test Procedure

5. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
6. OCB and 20dB Spectrum Setting:
 - (1) Set RBW = 1% ~ 5% occupied bandwidth.
 - (2) Set the video bandwidth (VBW) ≥ 3 RBW.
 - (3) Detector = Peak.
 - (4) Trace mode = Max hold.
 - (5) Sweep = Auto couple.

Note: The EUT was set to continuously transmitting in each mode and low, Middle and high channel for the test.

Test Mode

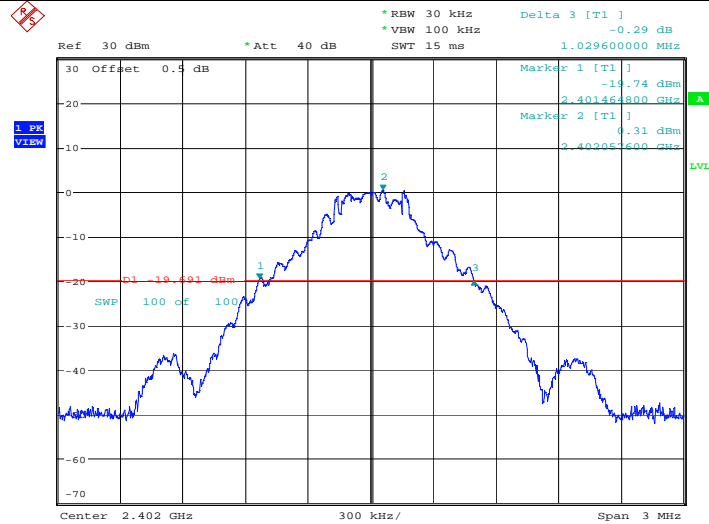
Please refer to the clause 2.4.

Test Results

Test Mode	Channel	20db EBW[MHz]	FL[MHz]	FH[MHz]	20dB Bandwidth *2/3 (kHz)	Verdict
GFSK	2402	1.030	2401.465	2402.494	686.667	PASS
	2441	1.022	2440.467	2441.490	681.333	PASS
	2480	1.027	2479.462	2480.490	684.667	PASS
/4-DQPSK	2402	1.306	2401.347	2402.653	870.667	PASS
	2441	1.303	2440.347	2441.650	868.667	PASS
	2480	1.301	2479.350	2480.650	867.333	PASS
8-DPSK	2402	1.294	2401.347	2402.641	862.667	PASS
	2441	1.306	2440.342	2441.648	870.667	PASS
	2480	1.296	2479.345	2480.641	864.000	PASS

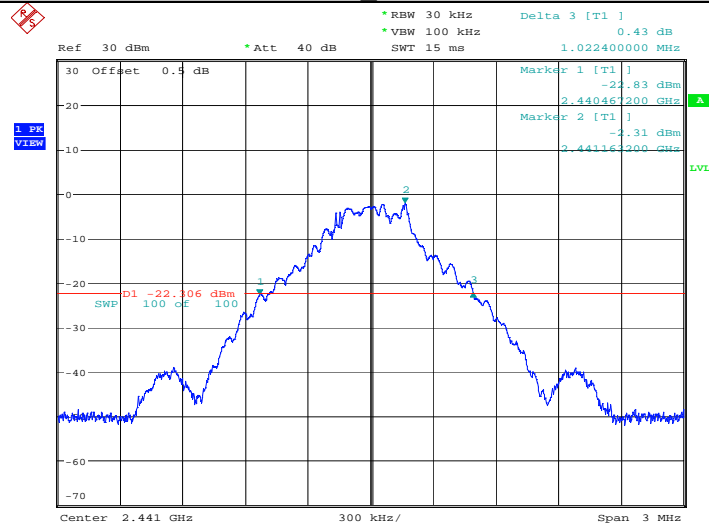


GFSK 2402



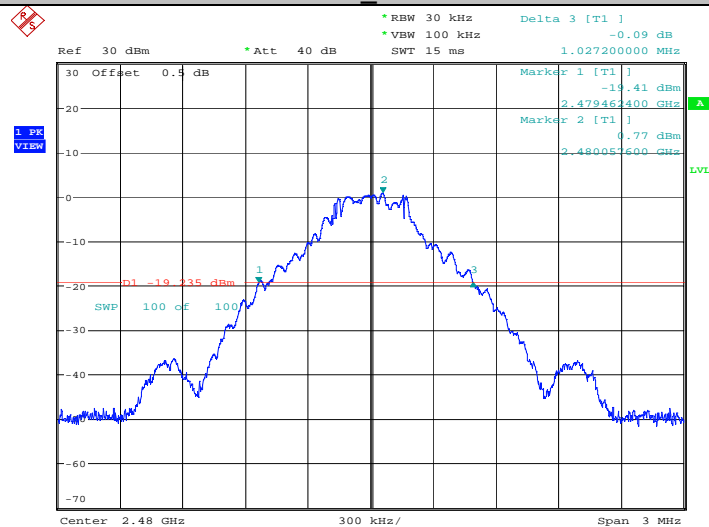
Date: 4.DEC.2020 14:22:53

GFSK 2441



Date: 4.DEC.2020 14:54:50

GFSK 2480



Date: 4.DEC.2020 14:57:56

/4-DQPSK_2402

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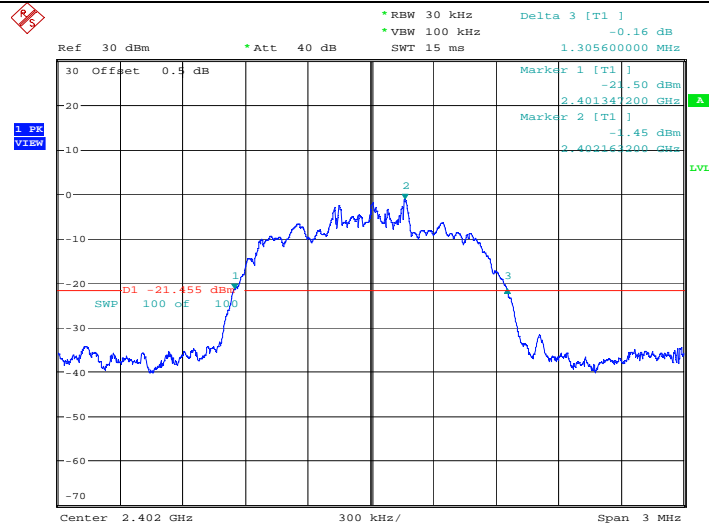
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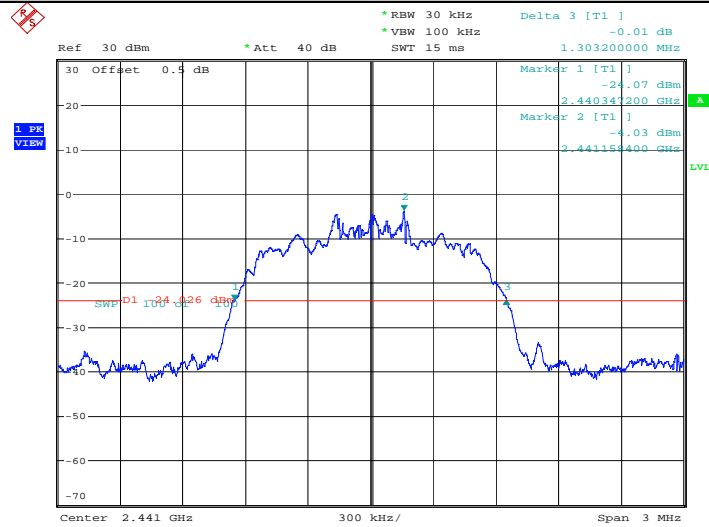


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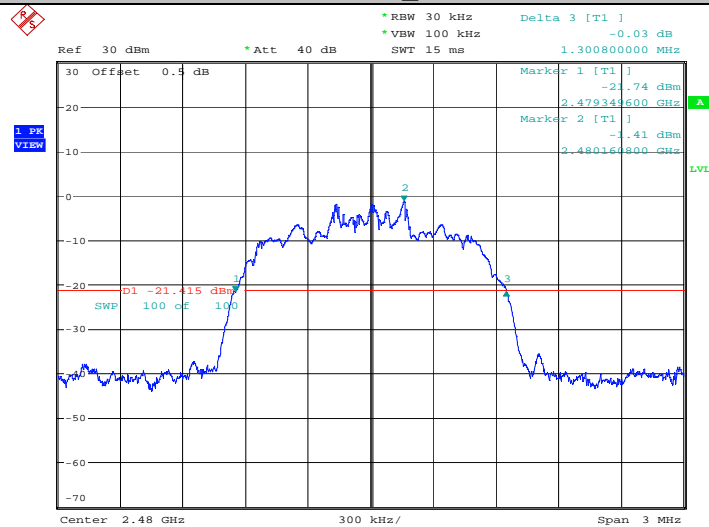
Date: 5.DEC.2020 09:58:01

1/4-DQPSK_2441



Date: 5.DEC.2020 10:03:04

1/4-DQPSK_2480



Date: 5.DEC.2020 10:06:10

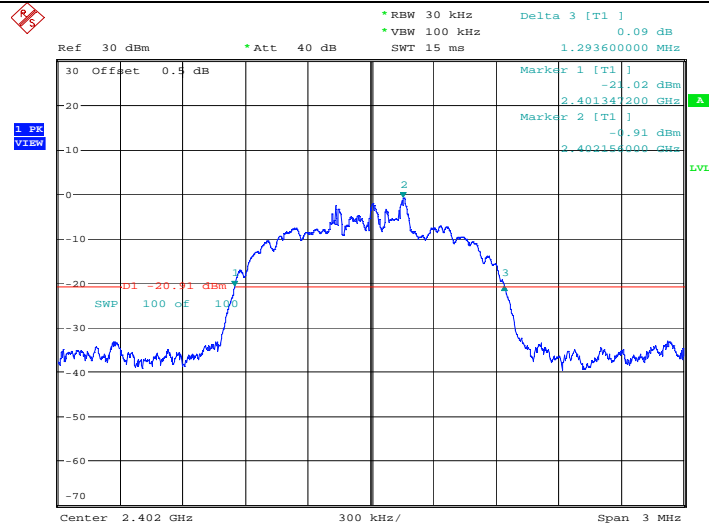
8-DPSK_2402

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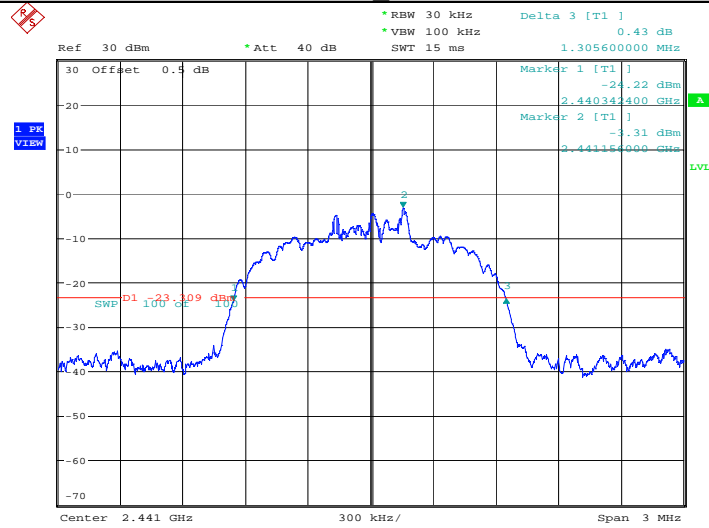


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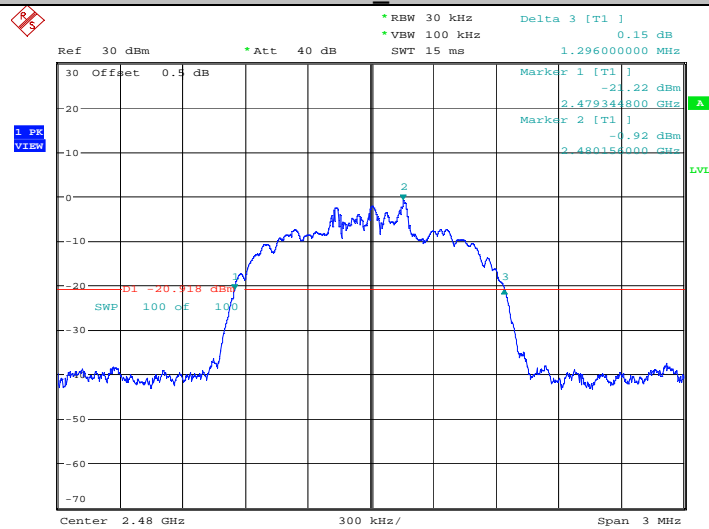
Date: 5.DEC.2020 10:11:54

8-DPSK_2441



Date: 5.DEC.2020 10:17:01

8-DPSK_2480



Date: 5.DEC.2020 10:20:04

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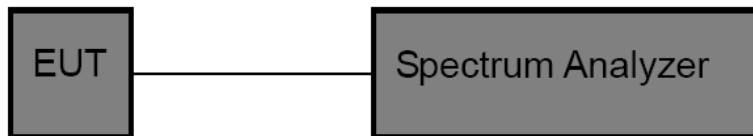
3.6. Channel Separation

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(1)/ RSS-247 5.1 b :

Test Item	Limit	Frequency Range(MHz)
Channel Separation	>25KHz or >two-thirds of the 20 dB bandwidth Which is greater	2400~2483.5

Test Configuration



Test Procedure

7. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
8. Spectrum Setting:
 - (1) Set RBW = 100 kHz.
 - (2) Set the video bandwidth (VBW) ≥ 3 RBW.
 - (3) Detector = Peak.
 - (4) Trace mode = Max hold.
 - (5) Sweep = Auto couple.

Test Mode

Please refer to the clause 2.4.

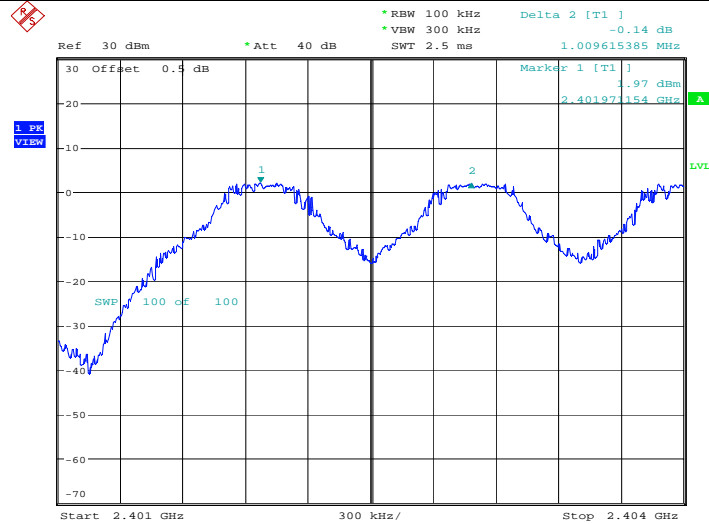


Test Results

Test Mode	Channel	Result[MHz]	Limit[MHz]	Verdict
GFSK	Hop_2402	1.010	>686.667	PASS
	Hop_2441	0.986	>681.333	PASS
	Hop_2480	1.034	>684.667	PASS
/4-DQPSK	Hop_2402	1.024	>870.667	PASS
	Hop_2441	0.995	>868.667	PASS
	Hop_2480	0.978	>867.333	PASS
8-DPSK	Hop_2402	1.005	>862.667	PASS
	Hop_2441	0.981	>870.667	PASS
	Hop_2480	1.018	>864.000	PASS

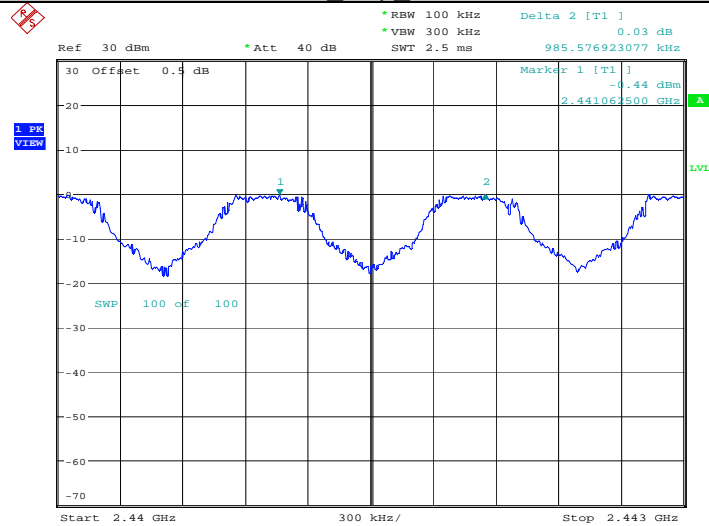


GFSK Hop 2402



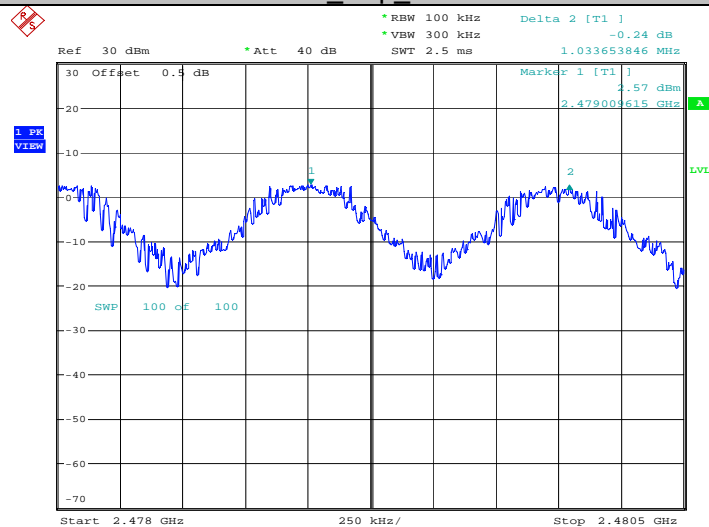
Date: 5.DEC.2020 10:30:46

GFSK Hop 2441



Date: 5.DEC.2020 10:33:32

GFSK Hop 2480



Date: 5.DEC.2020 10:49:01

/4-DQPSK_Hop_2402

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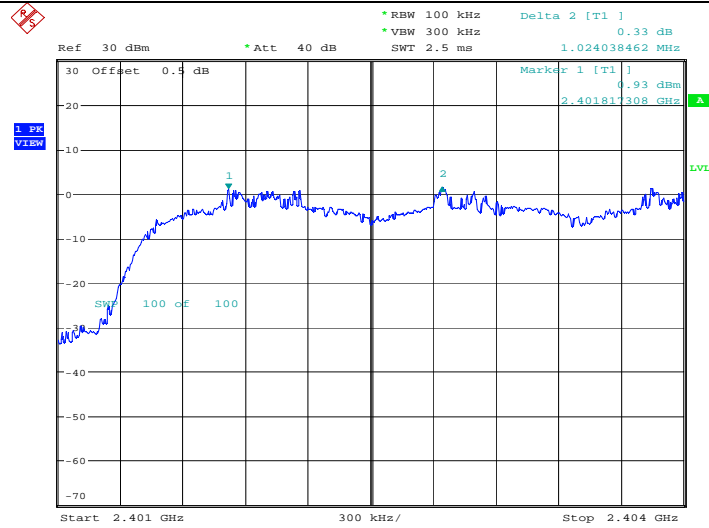
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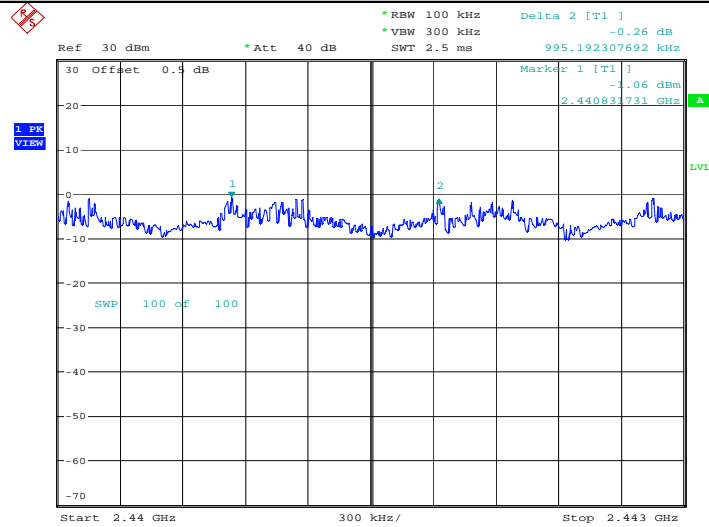


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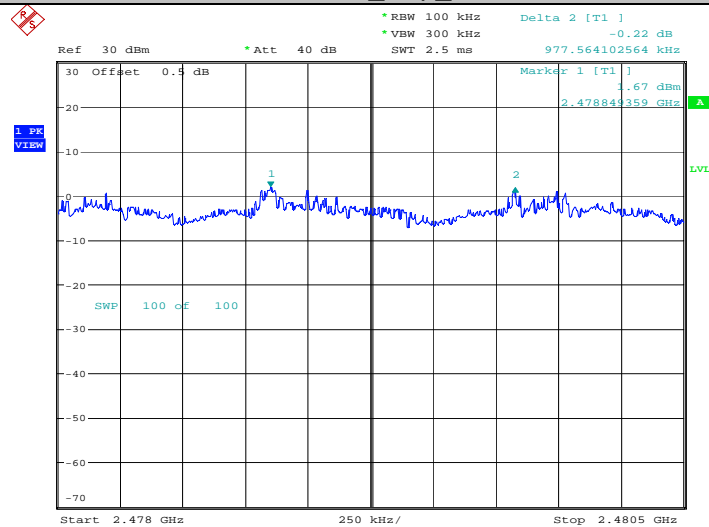
Date: 5.DEC.2020 11:07:45

1/4-DQPSK_Hop_2441



Date: 5.DEC.2020 11:09:37

1/4-DQPSK_Hop_2480



Date: 5.DEC.2020 11:12:21

8-DPSK_Hop_2402

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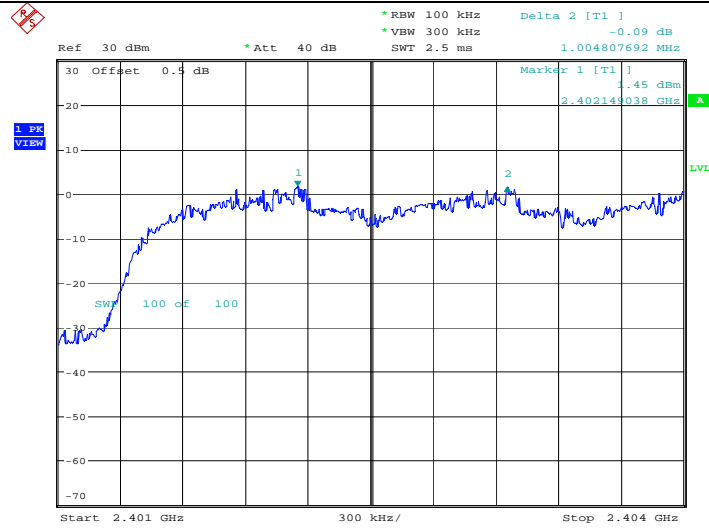
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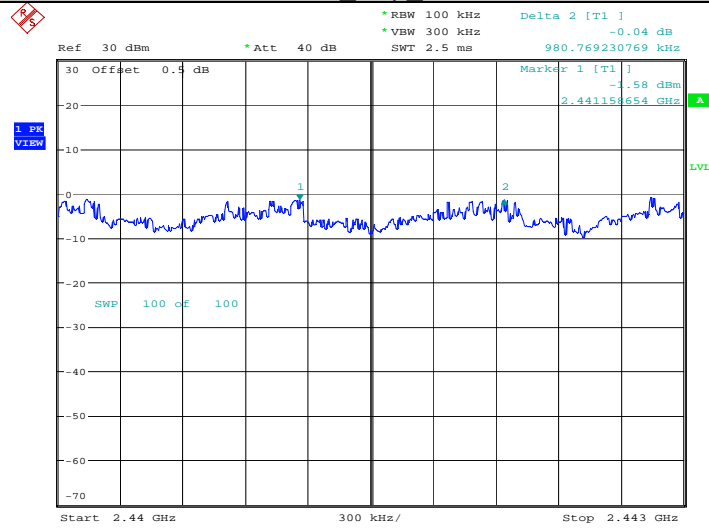


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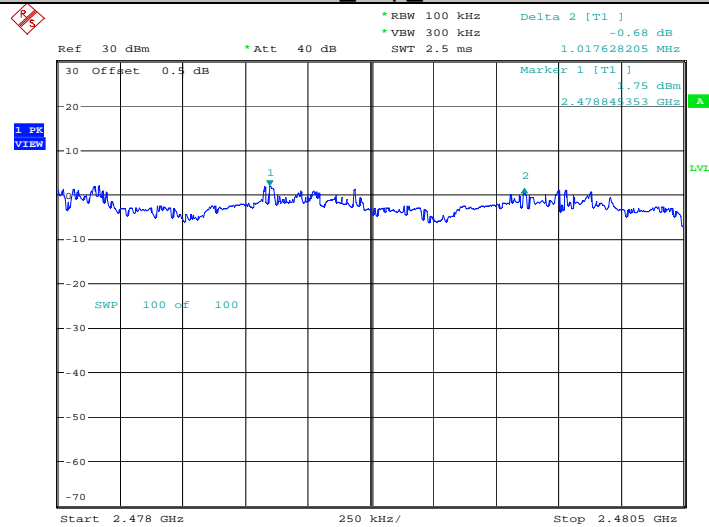
Date: 5.DEC.2020 13:06:26

8-DPSK_Hop_2441



Date: 5.DEC.2020 13:20:53

8-DPSK_Hop_2480



Date: 5.DEC.2020 13:27:10

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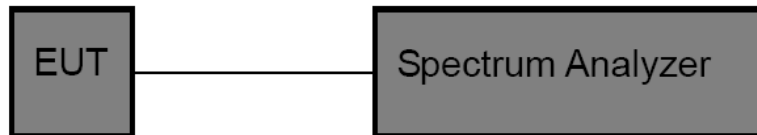
3.7. Number of Hopping Channel

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(iii)/ RSS-247 5.1 d:

Section	Test Item	Limit
15.247 (a)(iii)/ RSS-247 5.1 d:	Number of Hopping Channel	>15

Test Configuration



Test Procedure

- The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
- Spectrum Setting:
 - Peak Detector: RBW=100 kHz, VBW RBW, Sweep time= Auto.

Test Mode

Please refer to the clause 2.4.

Test Result

Modulation type	Channel number	Limit	Result
GFSK	79	≥15.00	Pass
/4-DQPSK	79		
8DPSK	79		



<p style="text-align: center;">GFSK</p>	<p>Ref 30 dBm Offset 0.5 dB Att 40 dB RBW 100 kHz VBW 300 kHz SWT 10 ms Marker 1 [T1] 2.401873397 GHz 1.02 dBm</p> <p>Delta 1 [T1] 1.18 dB 78.281250000 MHz</p> <p>Center: 2.44175 GHz 8.35 MHz/ Span 83.5 MHz</p>
<p style="text-align: center;">/4-DQPSK</p>	<p>Date: 5.DEC.2020 10:50:08</p> <p>Ref 30 dBm Offset 0.5 dB Att 40 dB RBW 100 kHz VBW 300 kHz SWT 10 ms Delta 1 [T1] -1.10 dBm</p> <p>Marker 1 [T1] 0.69 dBm 2.402007212 GHz</p> <p>Center: 2.44175 GHz 8.35 MHz/ Span 83.5 MHz</p>
<p style="text-align: center;">8-DPSK</p>	<p>Date: 5.DEC.2020 11:16:44</p> <p>Ref 30 dBm Offset 0.5 dB Att 40 dB RBW 100 kHz VBW 300 kHz SWT 10 ms Marker 1 [T1] -0.11 dBm</p> <p>Delta 1 [T1] 1.61 dB 78.013621795 MHz</p> <p>Center: 2.44175 GHz 8.35 MHz/ Span 83.5 MHz</p> <p>Date: 5.DEC.2020 13:36:08</p>

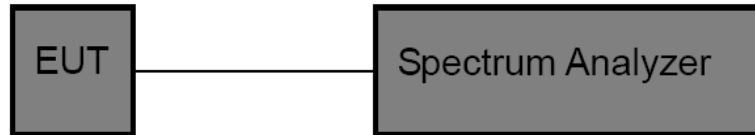


3.8. Dwell Time

Limit

Section	Test Item	Limit
15.247(a)(iii)/ RSS-247 5.1 d	Average Time of Occupancy	0.4 sec

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
 - (1) Spectrum Setting: RBW=1MHz, VBW RBW.
 - (2) Use video trigger with the trigger level set to enable triggering only on full pulses.
 - (3) Sweep Time is more than once pulse time.
 - (4) Set the center frequency on any frequency would be measure and set the frequency span to zero.
 - (5) Measure the maximum time duration of one single pulse.
 - (6) Set the EUT for packet transmitting.

Test Mode

Please refer to the clause 2.4.



Test Result

Modulation type	Channel	Channel (MHz)	Pulse Time (ms)	Total of Dwell (ms)	Period Time (ms)	Limit (Second)	Result
GFSK	DH1	2441	0.38	121.60	31.60	≤ 0.40	Pass
	DH3	2441	1.63	260.80	31.60		
	DH5	2441	2.88	307.20	31.60		
/4-DQPSK	2DH1	2441	0.38	121.60	31.60	≤ 0.40	Pass
	2DH3	2441	1.63	260.80	31.60		
	2DH5	2441	2.88	307.20	31.60		
8-DPSK	3DH1	2441	0.38	121.60	31.60	≤ 0.40	Pass
	3DH3	2441	1.63	260.80	31.60		
	3DH5	2441	2.88	307.20	31.60		

Note: 1DH1/2DH1/3DH1 Total of Dwell= Pulse Time*(1600/2)*31.6/79
1DH3/2DH3/3DH3 Total of Dwell= Pulse Time*(1600/4)*31.6/79
1DH5/2DH5/3DH5 Total of Dwell= Pulse Time*(1600/6)*31.6/79



Modulation Type:		GFSK
DH1	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] 16.97 dB *VBW 3 MHz SWT 10 ms Marker 1 [T1] -17.44 dBm -6.000000 us</p> <p>Center 2.441 GHz 1 ms/</p> <p>Date: 7.DEC.2020 16:44:23</p>	
DH3	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] 23.13 dB *VBW 3 MHz SWT 20 ms Marker 1 [T1] -23.74 dBm -8.000000 us</p> <p>Center 2.441 GHz 2 ms/</p> <p>Date: 7.DEC.2020 16:45:19</p>	
DH5	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] 0.93 dB *VBW 3 MHz SWT 30 ms Marker 1 [T1] -1.32 dBm -8.000000 us</p> <p>Center 2.441 GHz 3 ms/</p> <p>Date: 5.DEC.2020 10:57:09</p>	

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Modulation Type:		/4-DQPSK
2DH1		<p>RBW 1 MHz Delta 2 [T1] 5.98 dB * VBW 3 MHz 384.000000 us * Att 30 dB * SWT 10 ms</p> <p>Ref 20 dBm</p> <p>Marker 1 [T1] -4.49 dBm -4.000000 us</p> <p>Center 2.441 GHz 1 ms/</p>
2DH3		<p>RBW 1 MHz Delta 2 [T1] 6.07 dB * VBW 3 MHz 1.634000 ms * Att 30 dB * SWT 20 ms</p> <p>Ref 20 dBm</p> <p>Marker 1 [T1] -8.54 dBm -10.000000 us</p> <p>Center 2.441 GHz 2 ms/</p>
2DH5		<p>RBW 1 MHz Delta 2 [T1] 4.72 dB * VBW 3 MHz 2.880000 ms * Att 30 dB * SWT 30 ms</p> <p>Ref 20 dBm</p> <p>Marker 1 [T1] -8.49 dBm -8.000000 us</p> <p>Center 2.441 GHz 3 ms/</p>

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Modulation Type:	8-DPSK
3DH1	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] 9.83 dB *VBW 3 MHz SWT 10 ms Marker 1 [T1] -1.87 dBm -4.000000 us</p> <p>Center 2.441 GHz 1 ms/</p> <p>Date: 7.DEC.2020 16:57:04</p>
3DH3	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] 2.29 dB *VBW 3 MHz SWT 20 ms Marker 1 [T1] -4.60 dBm -4.000000 us</p> <p>Center 2.441 GHz 2 ms/</p> <p>Date: 7.DEC.2020 16:58:15</p>
3DH5	<p>Ref 20 dBm *Att 30 dB RBW 1 MHz Delta 2 [T1] -0.52 dB *VBW 3 MHz SWT 30 ms Marker 1 [T1] -1.72 dBm -188.000000 us</p> <p>Center 2.441 GHz 3 ms/</p> <p>Date: 7.DEC.2020 16:40:35</p>

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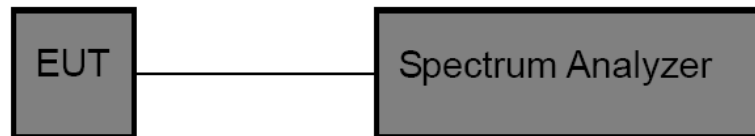
3.9. Peak Output Power

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (b)(1) / RSS-247 5.4 b:

Test Item	Limit	Frequency Range(MHz)
Peak Output Power	Hopping Channels>75 Power<1W(30dBm) Other <125mW(21dBm)	2400~2483.5

Test Configuration



Test Procedure

- The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
- Spectrum Setting:
 - Set RBW> 20DB Bandwidth.
 - Set the video bandwidth (VBW) ≥ RBW.
 - Detector = Peak.
 - Trace mode = Max hold.
 - Sweep = Auto couple.

Test Mode

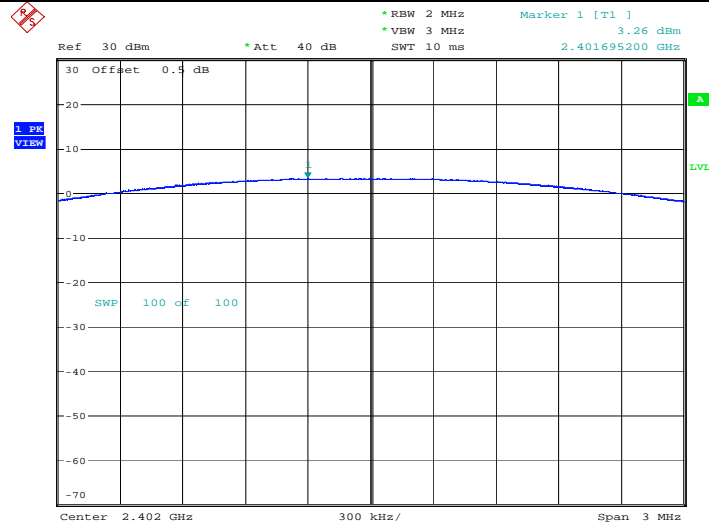
Please refer to the clause 2.4.

Test Result

Test Mode	Channel	Result[dBm]	Limit[dBm]	Verdict
GFSK	2402	3.26	<=30	PASS
	2441	0.50	<=30	PASS
	2480	3.71	<=30	PASS
/4-DQPSK	2402	2.10	<=30	PASS
	2441	-0.44	<=30	PASS
	2480	2.40	<=30	PASS
8-DPSK	2402	2.36	<=30	PASS
	2441	-0.29	<=30	PASS
	2480	2.56	<=30	PASS

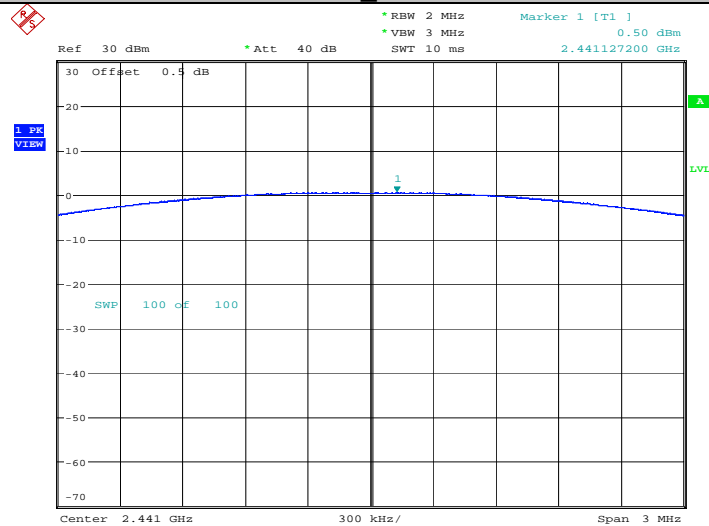


GFSK 2402



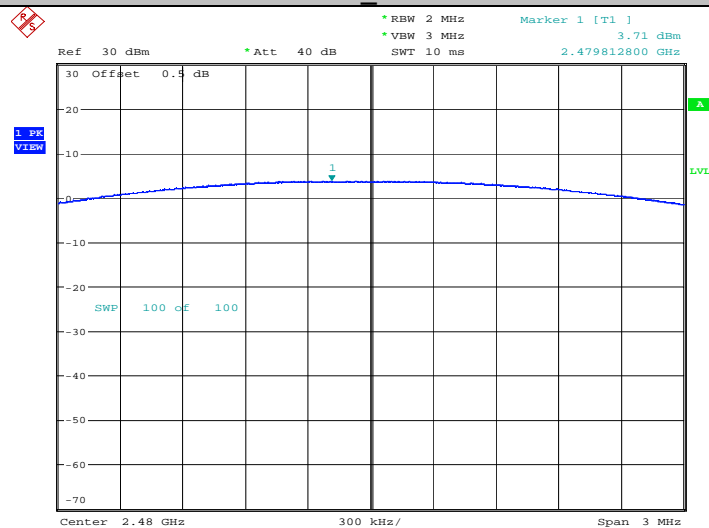
Date: 4 DEC 2020 14:26:47

GFSK 2441



Date: 4 DEC 2020 14:57:01

GFSK 2480



Date: 4 DEC 2020 15:01:38

/4-DQPSK_2402

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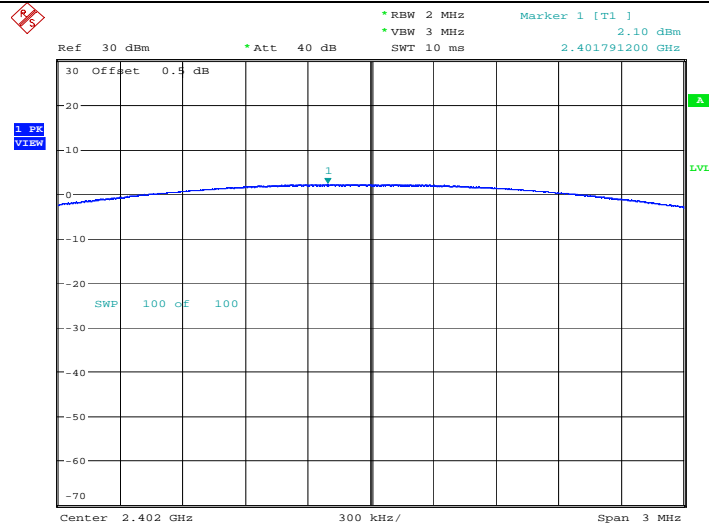
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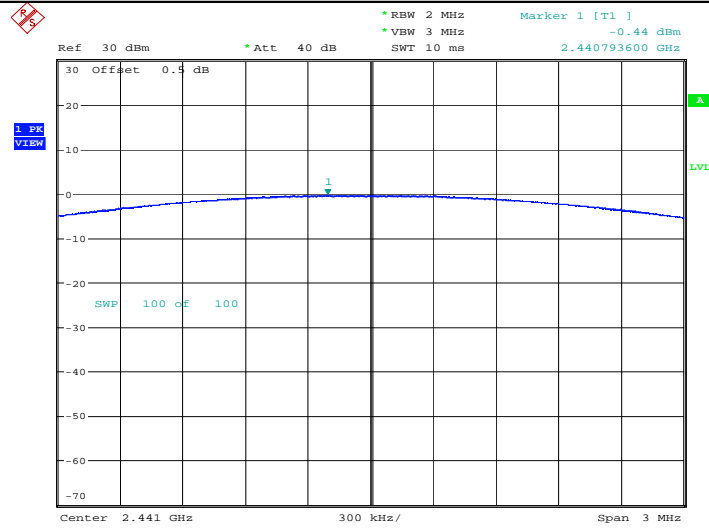


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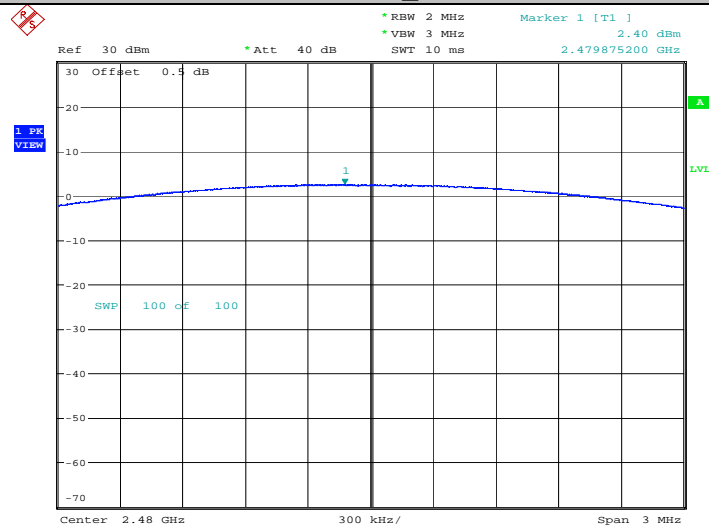
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1/4-DQPSK_2441



Date: 5.DEC.2020 10:05:15

1/4-DQPSK_2480



Date: 5.DEC.2020 10:09:52

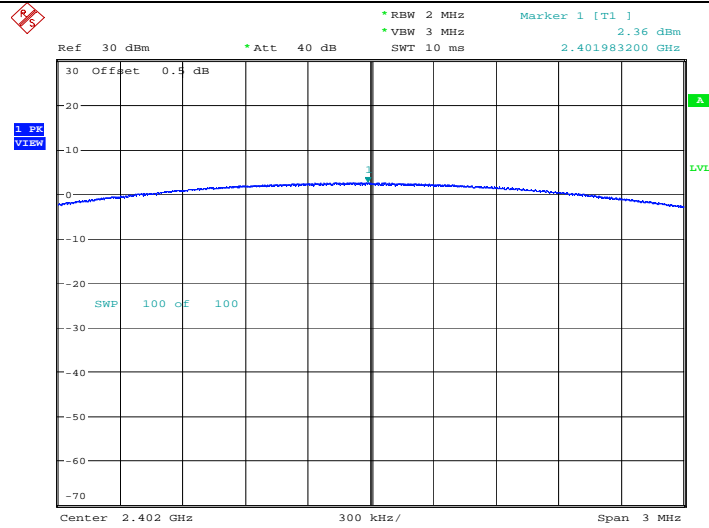
8-DPSK_2402

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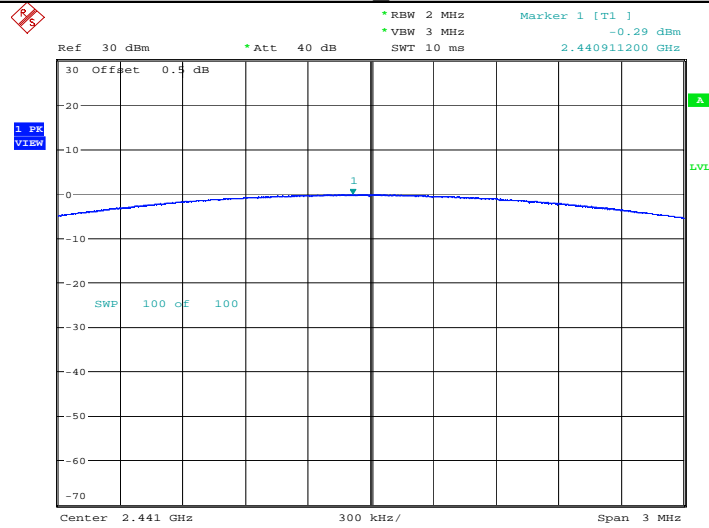


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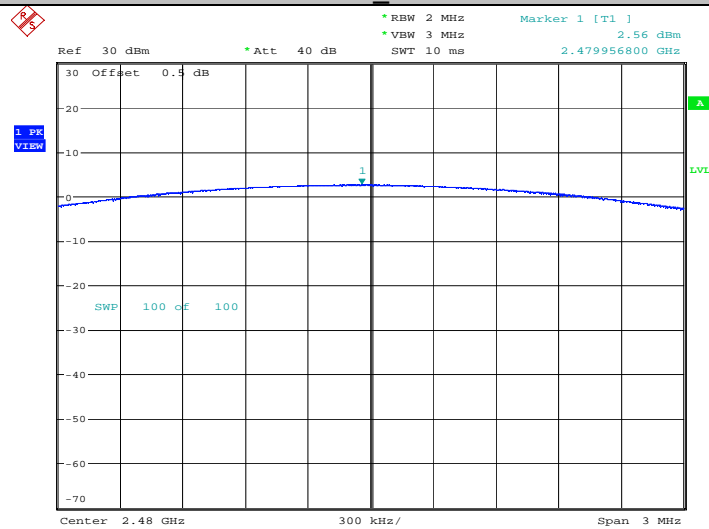
Date: 5.DEC.2020 10:15:46

8-DPSK_2441



Date: 5.DEC.2020 10:19:12

8-DPSK_2480



Date: 5.DEC.2020 10:23:45

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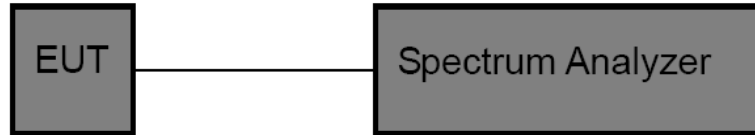


3.10. Duty Cycle

Limit

None, for report purposes only.

Test Configuration



Test Procedure

- The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
- The EUT was directly connected to the Spectrum Analyzer and antenna output port as show in the block diagram above. The measurement according to section 10.2 of KDB 558074 D01 DTS Meas Guidance v05r02.
- Spectrum Setting:
 Set analyzer center frequency to test channel center frequency.
 Set the span to 0Hz
 Set the RBW to 10MHz
 Set the VBW to 10MHz
 Detector: Peak
 Sweep time: Auto
 Allow trace to fully stabilize. Then use the peak marker function to determine the maximum amplitude level.

Test Mode

Please refer to the clause 2.4.

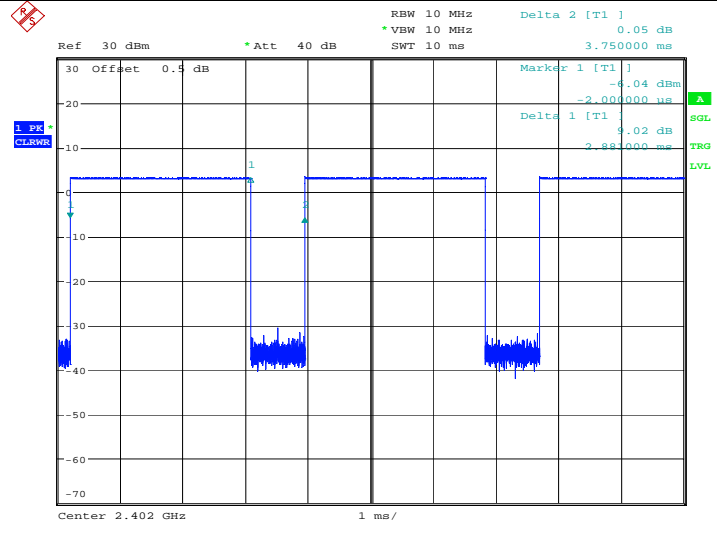
Test Result

Test Mode	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
GFSK	2402	2.88	3.75	76.83	0.35	1
	2441	2.88	3.75	76.83	0.35	1
	2480	2.88	3.75	76.83	0.35	1
/4-DQPSK	2402	2.88	3.75	76.91	0.35	1
	2441	2.89	3.75	76.93	0.35	1
	2480	2.89	3.75	76.93	0.35	1
8-DPSK	2402	2.89	3.75	76.96	0.35	1
	2441	2.89	3.75	76.99	0.35	1
	2480	2.89	3.75	76.96	0.35	1



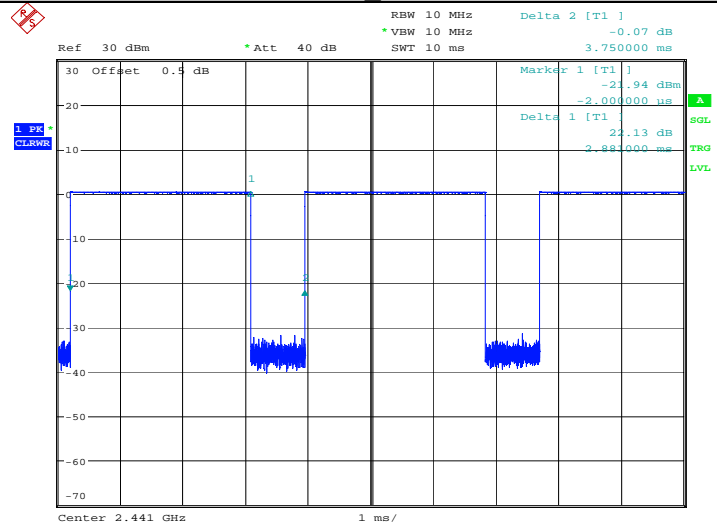


GFSK_2402



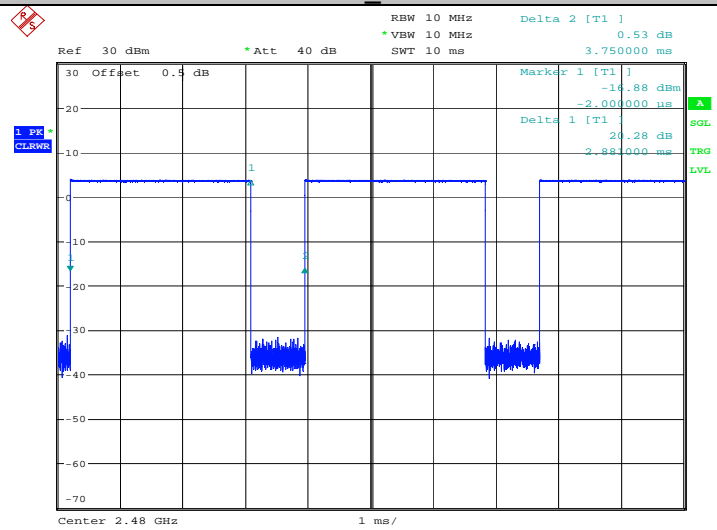
Date: 4.DEC.2020 14:22:27

GFSK_2441



Date: 4.DEC.2020 14:54:20

GFSK_2480



Date: 4.DEC.2020 14:57:30

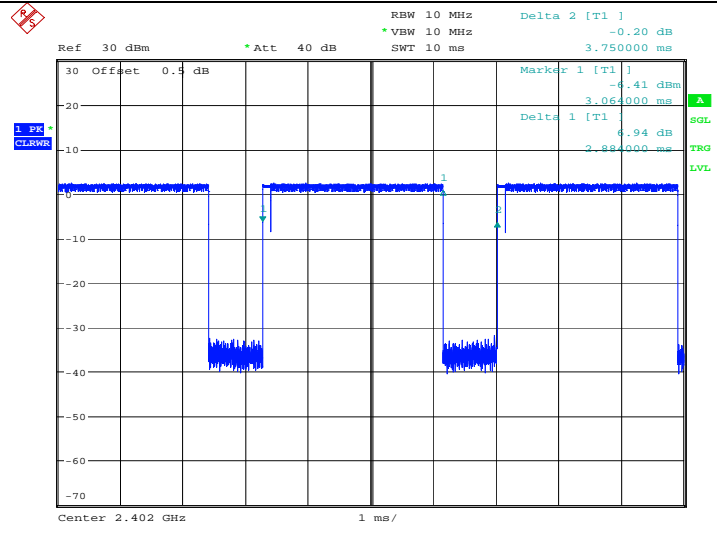
/4-DQPSK_2402

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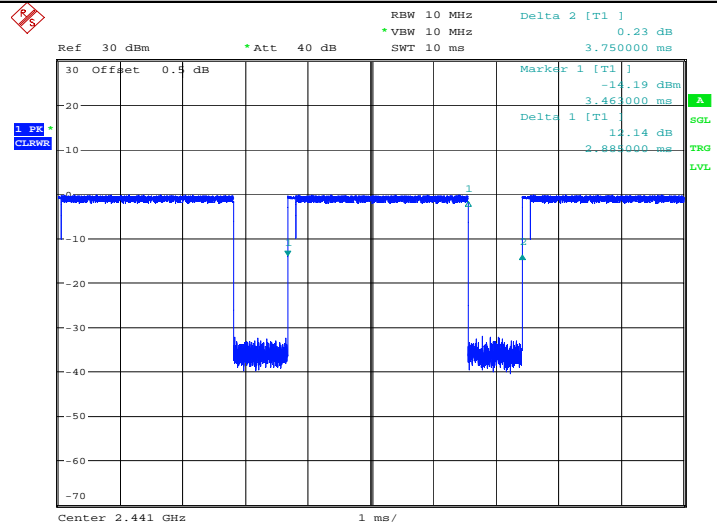


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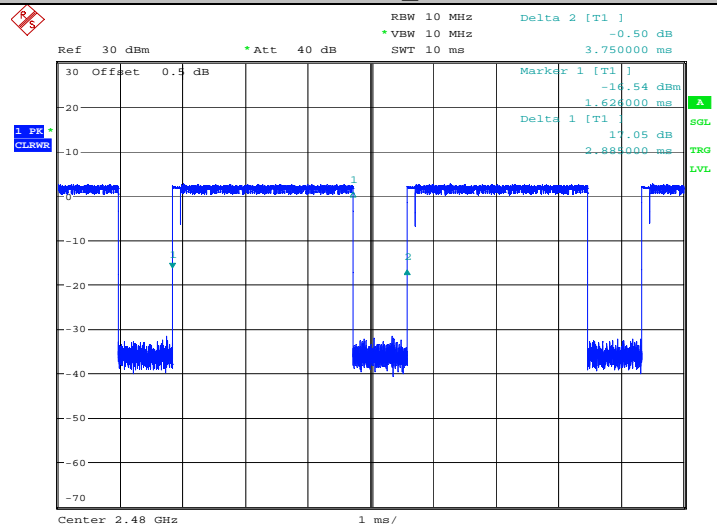
Date: 5.DEC.2020 09:57:34

/4-DQPSK_2441



Date: 5.DEC.2020 10:02:37

/4-DQPSK_2480



Date: 5.DEC.2020 10:05:43

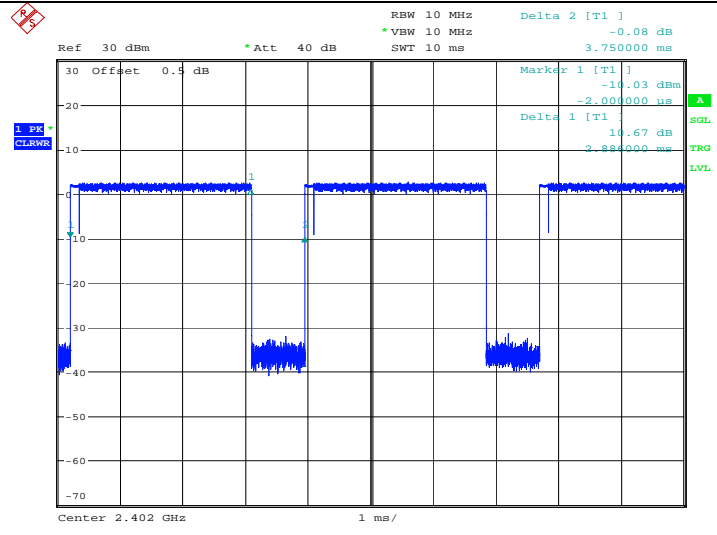
8-DPSK_2402

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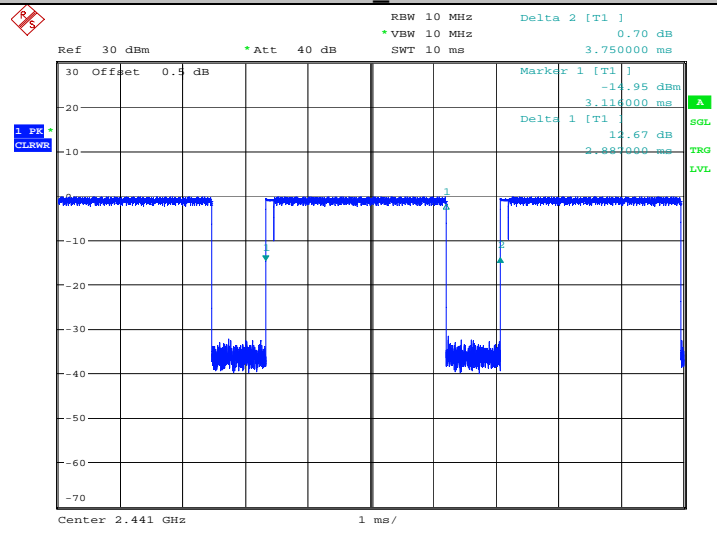


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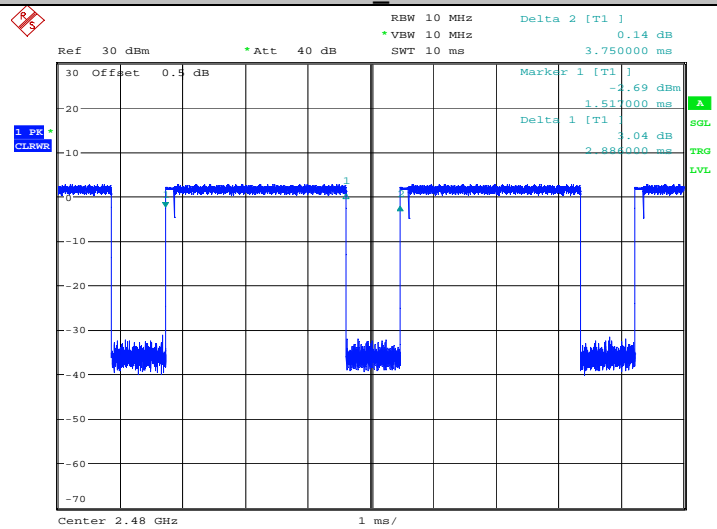
Date: 5.DEC.2020 10:11:27

8-DPSK_2441



Date: 5.DEC.2020 10:16:34

8-DPSK_2480



Date: 5.DEC.2020 10:19:37

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3.11. Antenna requirement

Requirement

FCC CFR Title 47 Part 15 Subpart C Section 15.203:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

FCC CFR Title 47 Part 15 Subpart C Section 15.247(c) (1)(i):

(i) Systems operating in the 2400~2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.

Test Result

The directional gain of the antenna less than 6dBi, please refer to the EUT internal photographs antenna photo.

*****THE END*****