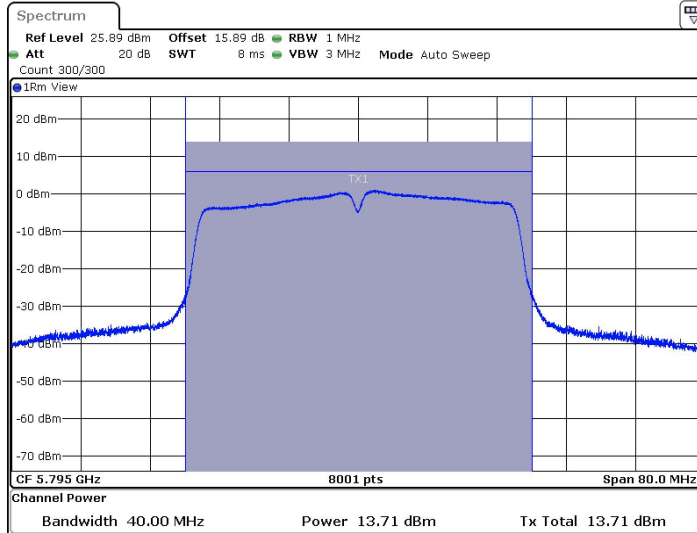
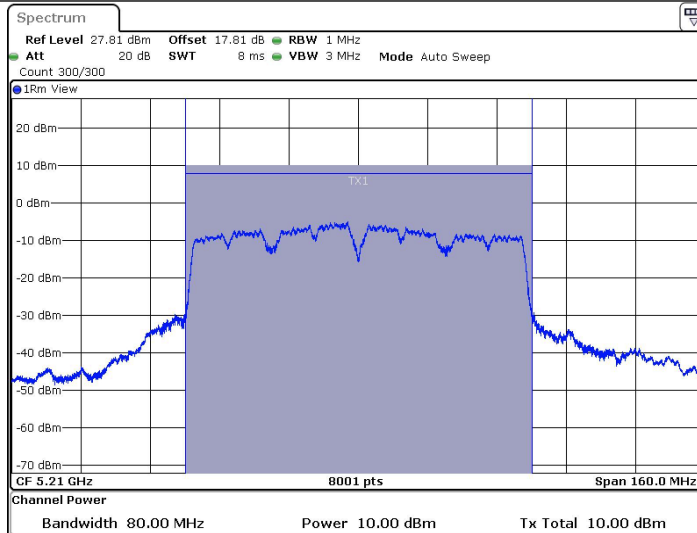


11AC40SISO_Ant2_5795



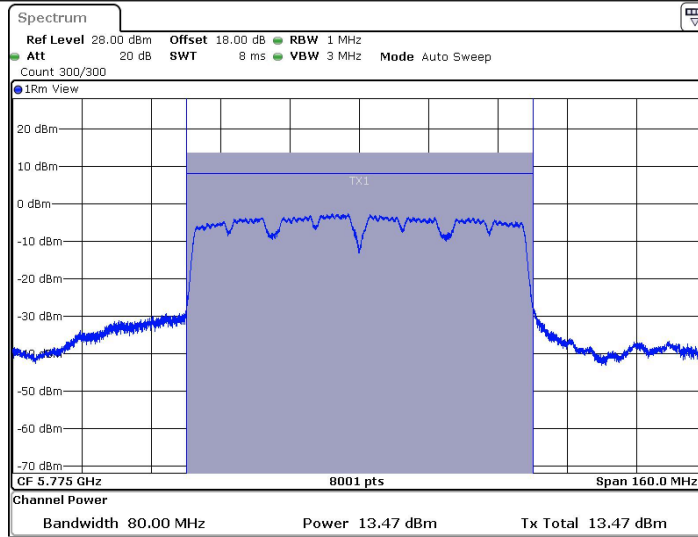
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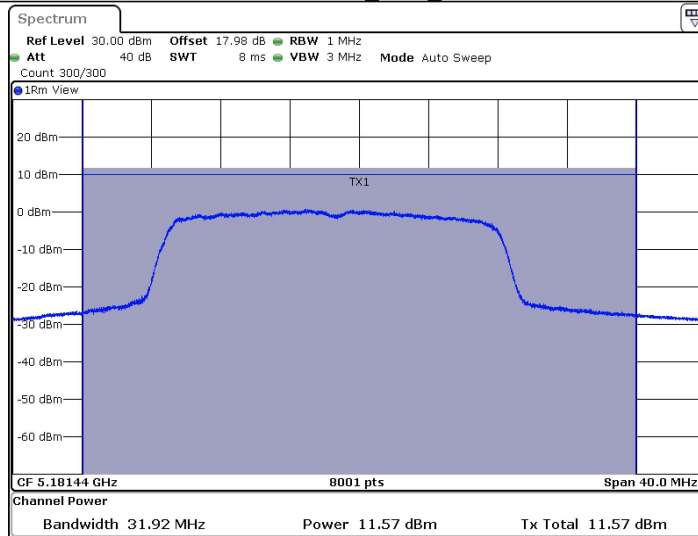
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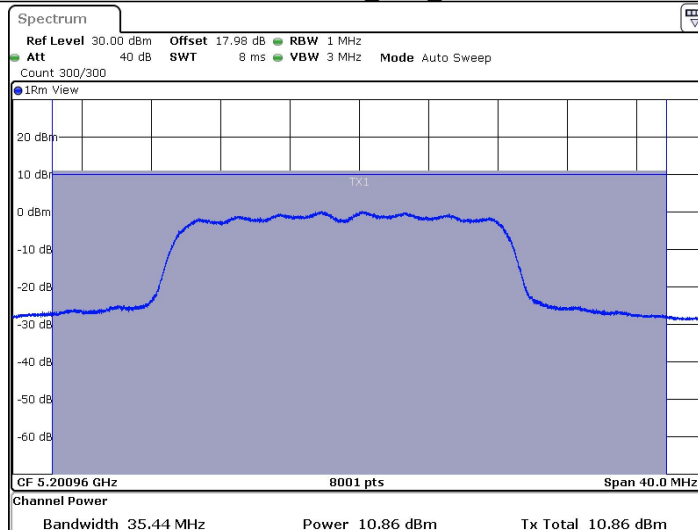
Date: 6 MAY 2024 19:40:46

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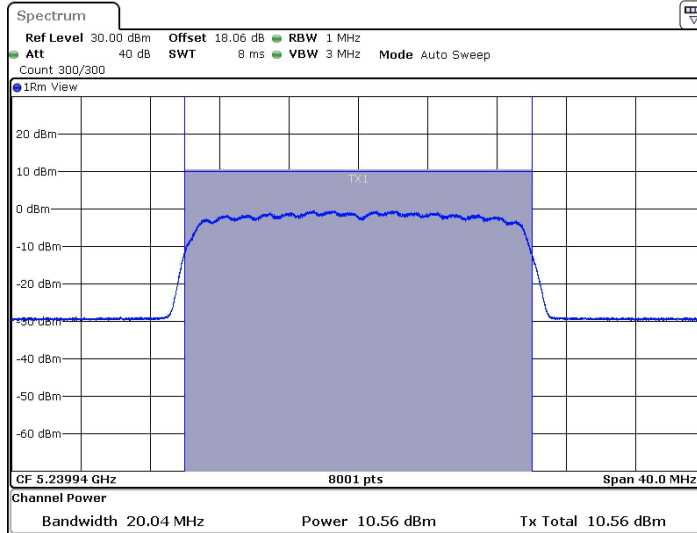
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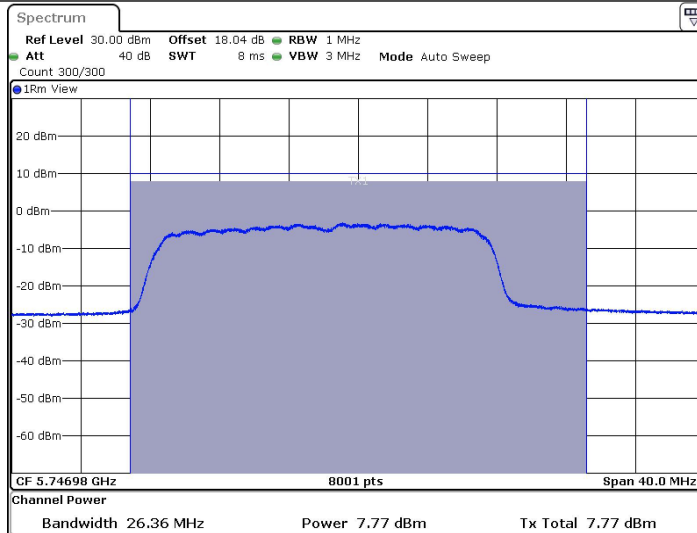
Date: 10 MAY 2024 17:05:06

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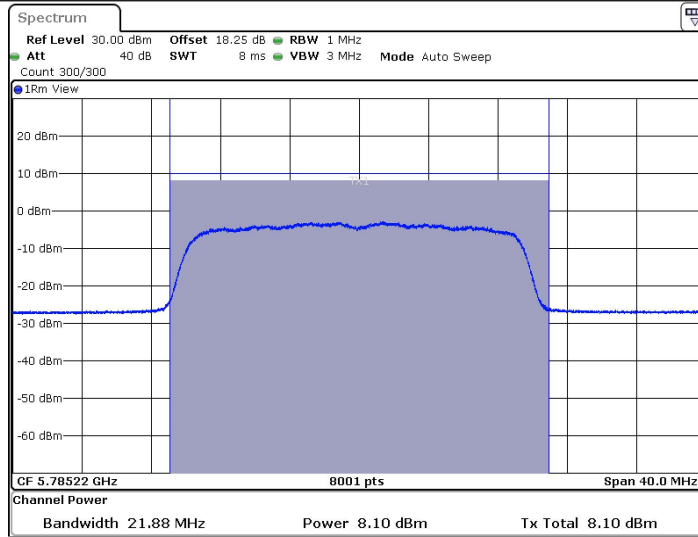
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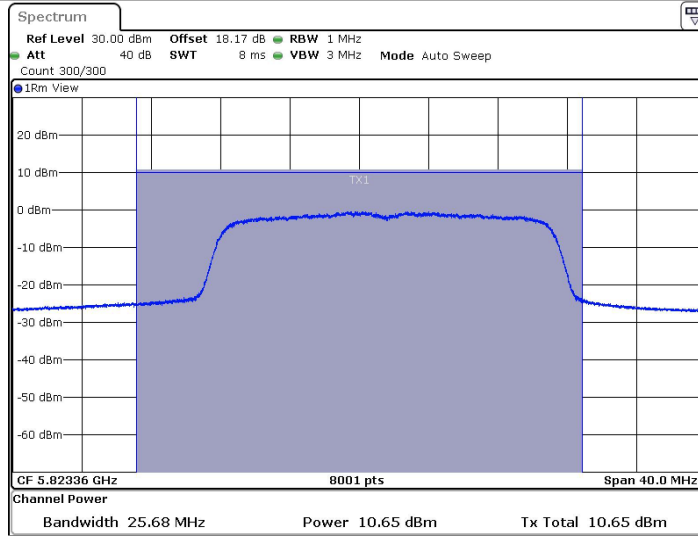


Date: 10.MAY.2024 17:11:01

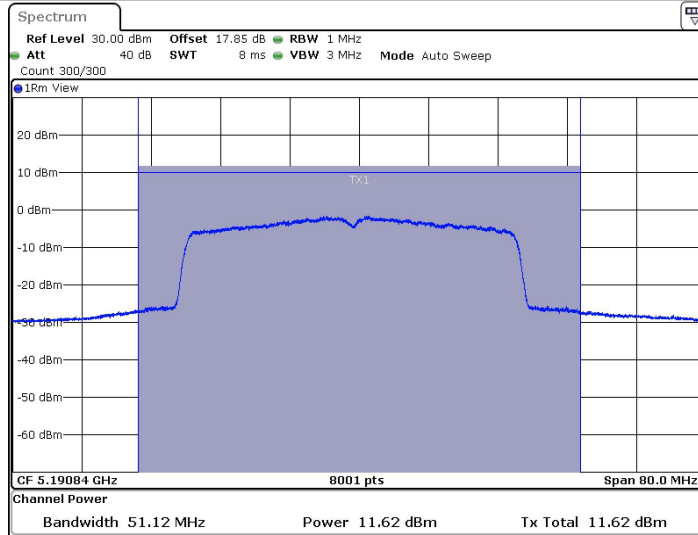
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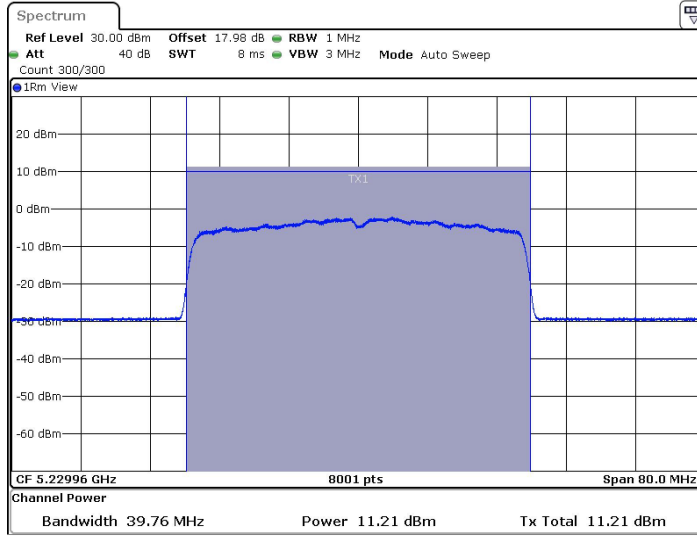
11AX20SISO_Ant2_5825



11AX40SISO_Ant2_5190

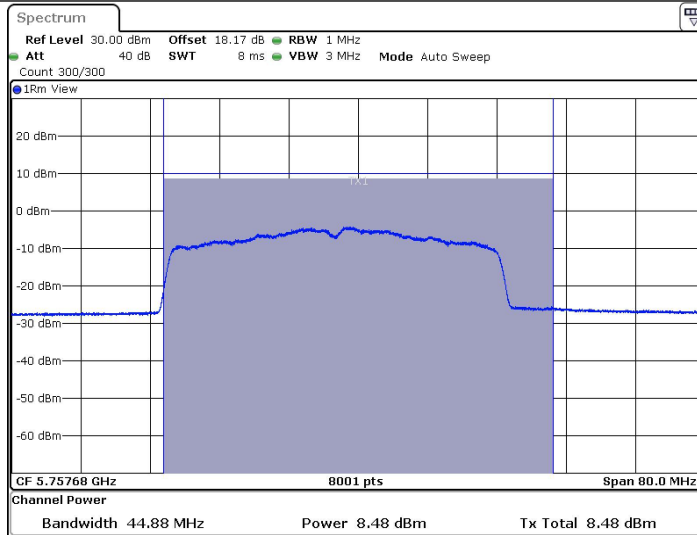


11AX40SISO_Ant2_5230



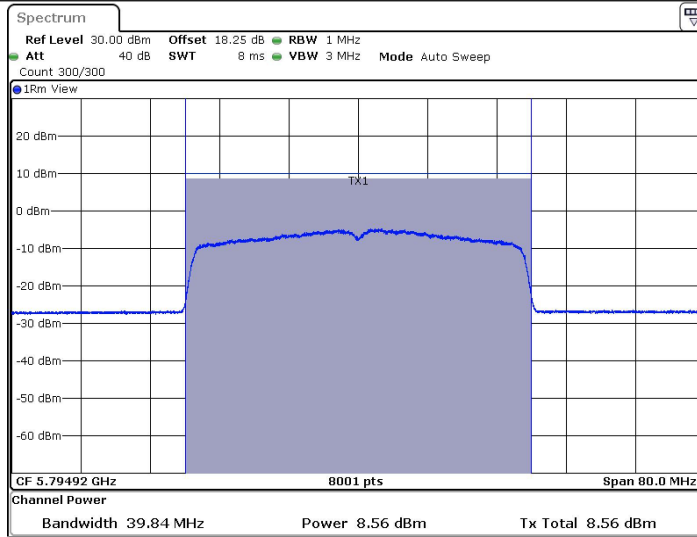
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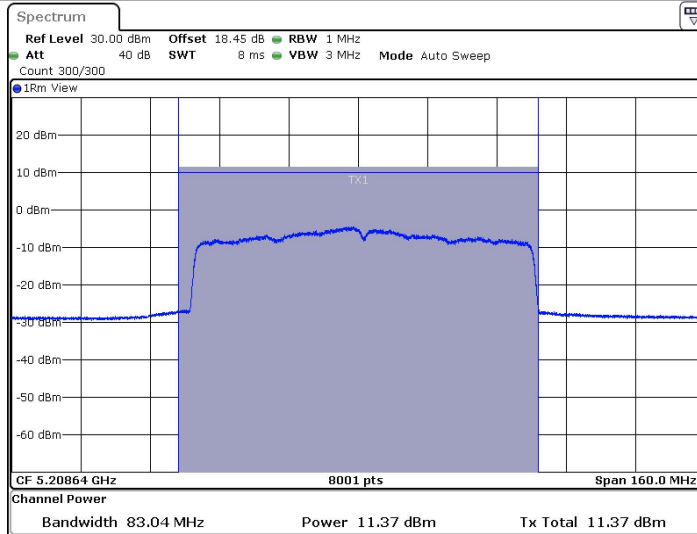
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11AX40SISO_Ant2_5795



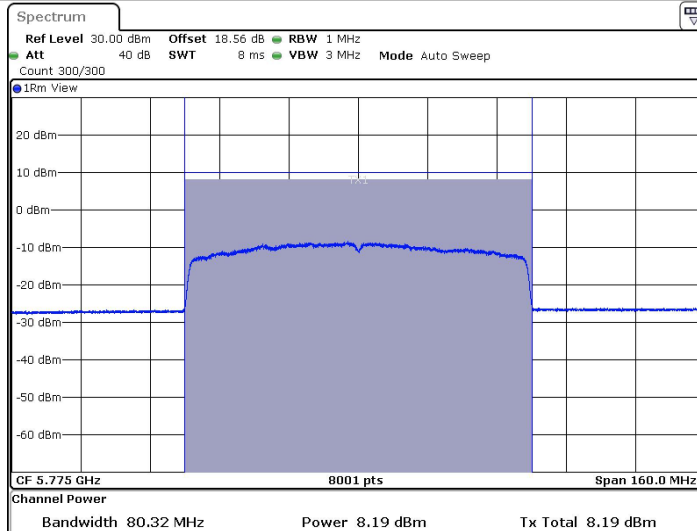
Date: 10.MAY.2024 17:29:46

11AX80SISO_Ant2_5210



Date: 10.MAY.2024 17:32:29

11AX80SISO_Ant2_5775



Date: 10.MAY.2024 17:36:53

Remark:

$Av.Power = Meas.Level + 10 \log(1/duty\ cycle)$

$E.i.r.p = Av.Power + G$,

G = antenna gain in dBi.

Appendix C): Maximum Power Spectral Density

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II F

Test Procedure:

For 5150-5725MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 1MHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

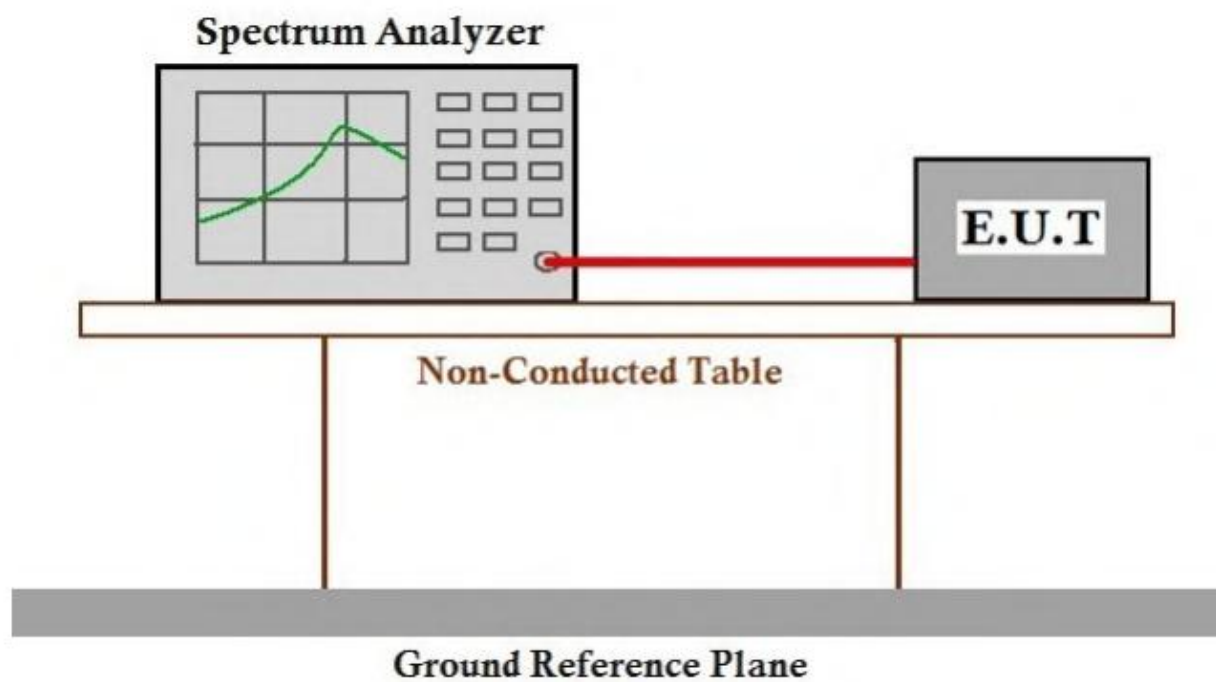
For 5725-5850MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 500KHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

Limit:

Frequency band(MHz)	Limit
5150-5250	≤ 17 dBm in 1MHz for master device
	≤ 11 dBm in 1MHz for client device
5250-5350	≤ 11 dBm in 1MHz for client device
5470-5725	≤ 11 dBm in 1MHz for client device
5725-5850	≤ 30 dBm in 500 kHz
Remark:	The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test.

Test Setup Diagram



Result Table

ANT1:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	1.82	≤11.00	PASS
	5200	-0.65	≤11.00	PASS
	5240	-0.21	≤11.00	PASS
	5745	0.81	≤30.00	PASS
	5785	0.1	≤30.00	PASS
	5825	-1.76	≤30.00	PASS
11N20SISO	5180	-0.37	≤11.00	PASS
	5200	-0.69	≤11.00	PASS
	5240	0.02	≤11.00	PASS
	5745	-4.73	≤30.00	PASS
	5785	-4.75	≤30.00	PASS
	5825	-2.31	≤30.00	PASS
11N40SISO	5190	-0.27	≤11.00	PASS
	5230	0.07	≤11.00	PASS
	5755	-3.84	≤30.00	PASS
	5795	-5.34	≤30.00	PASS
11AC20SISO	5180	1.83	≤11.00	PASS
	5200	1.28	≤11.00	PASS
	5240	1	≤11.00	PASS
	5745	-2.24	≤30.00	PASS
	5785	-2.94	≤30.00	PASS
	5825	-1.8	≤30.00	PASS
11AC40SISO	5190	-0.73	≤11.00	PASS
	5230	0.09	≤11.00	PASS
	5755	-4.59	≤30.00	PASS
	5795	-4.76	≤30.00	PASS
11AC80SISO	5210	-3.59	≤11.00	PASS
	5775	-6.39	≤30.00	PASS
11AX20SISO	5180	-0.14	≤11.00	PASS
	5200	-1.13	≤11.00	PASS
	5240	0.03	≤11.00	PASS
	5745	-3.72	≤30.00	PASS
	5785	-2.97	≤30.00	PASS
	5825	-3.09	≤30.00	PASS

11AX40SISO	5190	-2.07	≤ 11.00	PASS
	5230	-1.91	≤ 11.00	PASS
	5755	-4.95	≤ 30.00	PASS
	5795	-5.06	≤ 30.00	PASS
11AX80SISO	5210	-4.61	≤ 11.00	PASS
	5775	-6.26	≤ 30.00	PASS

ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	-0.56	≤11.00	PASS
	5200	0.19	≤11.00	PASS
	5240	-0.63	≤11.00	PASS
	5745	0.72	≤30.00	PASS
	5785	0.07	≤30.00	PASS
	5825	0.74	≤30.00	PASS
11N20SISO	5180	-1.49	≤11.00	PASS
	5200	-1.5	≤11.00	PASS
	5240	-1.96	≤11.00	PASS
	5745	-0.18	≤30.00	PASS
	5785	-0.86	≤30.00	PASS
	5825	-0.3	≤30.00	PASS
11N40SISO	5190	-2.76	≤11.00	PASS
	5230	-4.24	≤11.00	PASS
	5755	-2.41	≤30.00	PASS
	5795	-1.62	≤30.00	PASS
11AC20SISO	5180	-1.02	≤11.00	PASS
	5200	-1.19	≤11.00	PASS
	5240	-1.79	≤11.00	PASS
	5745	-0.15	≤30.00	PASS
	5785	-0.87	≤30.00	PASS
	5825	-0.56	≤30.00	PASS
11AC40SISO	5190	-2.56	≤11.00	PASS
	5230	-3.78	≤11.00	PASS
	5755	-1.72	≤30.00	PASS
	5795	-1.77	≤30.00	PASS
11AC80SISO	5210	-5.37	≤11.00	PASS
	5775	-3.85	≤30.00	PASS
11AX20SISO	5180	0.72	≤11.00	PASS
	5200	-0.66	≤11.00	PASS
	5240	-1.57	≤11.00	PASS
	5745	-6.42	≤30.00	PASS
	5785	-5.48	≤30.00	PASS
	5825	-3.11	≤30.00	PASS
11AX40SISO	5190	-2.47	≤11.00	PASS

	5230	-2.5	≤11.00	PASS
	5755	-6.37	≤30.00	PASS
	5795	-7.8	≤30.00	PASS
11AX80SISO	5210	-4.65	≤11.00	PASS
	5775	-10.69	≤30.00	PASS

ANT1+ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11N20MIMO	5180	2.12	≤11.00	PASS
	5200	1.93	≤11.00	PASS
	5240	2.15	≤11.00	PASS
	5745	1.13	≤29.8	PASS
	5785	0.63	≤29.8	PASS
	5825	1.82	≤29.8	PASS
11N40MIMO	5190	1.67	≤11.00	PASS
	5230	1.44	≤11.00	PASS
	5755	-0.06	≤29.8	PASS
	5795	-0.08	≤29.8	PASS
11AC20MIMO	5180	3.65	≤11.00	PASS
	5200	3.23	≤11.00	PASS
	5240	2.84	≤11.00	PASS
	5745	1.94	≤29.8	PASS
	5785	1.23	≤29.8	PASS
	5825	1.87	≤29.8	PASS
11AC40MIMO	5190	1.46	≤11.00	PASS
	5230	1.58	≤11.00	PASS
	5755	0.09	≤29.8	PASS
	5795	0.00	≤29.8	PASS
11AC80MIMO	5210	-1.38	≤11.00	PASS
	5775	-1.93	≤29.8	PASS
11AX20MIMO	5180	3.32	≤11.00	PASS
	5200	2.12	≤11.00	PASS
	5240	2.31	≤11.00	PASS
	5745	-1.85	≤29.8	PASS
	5785	-1.04	≤29.8	PASS
	5825	-0.09	≤29.8	PASS
11AX40MIMO	5190	0.74	≤11.00	PASS
	5230	0.82	≤11.00	PASS
	5755	-2.59	≤29.8	PASS
	5795	-3.21	≤29.8	PASS
11AX80MIMO	5210	-1.62	≤11.00	PASS
	5775	-4.92	≤29.8	PASS

Remark:

PSD MIMO limit=Conducted output PSD Limit-(directional gains-6dBi)

Directional gain:

6.2dBi@5GHz: Wi-Fi: U-NII-3

PSD = Meas PSD + Duty Cycle Factor

11A Ant1_5180



Date: 6 MAY 2024 20:10:51

11A Ant1_5200



Date: 6 MAY 2024 19:53:24

11A Ant1_5240



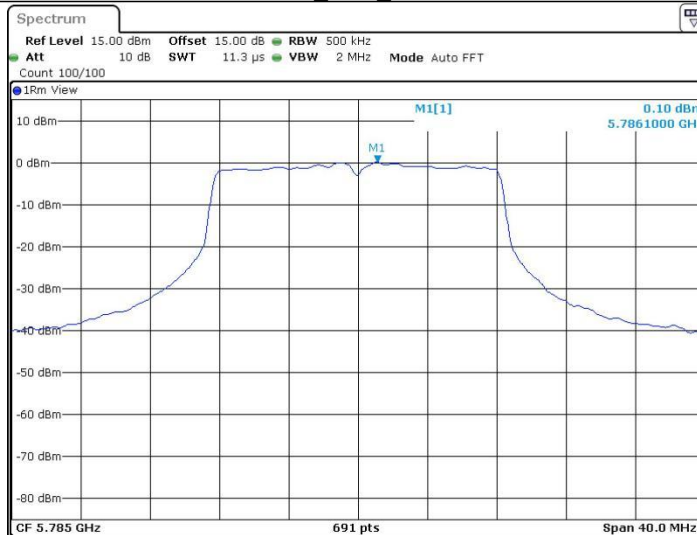
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11A Ant1 5745



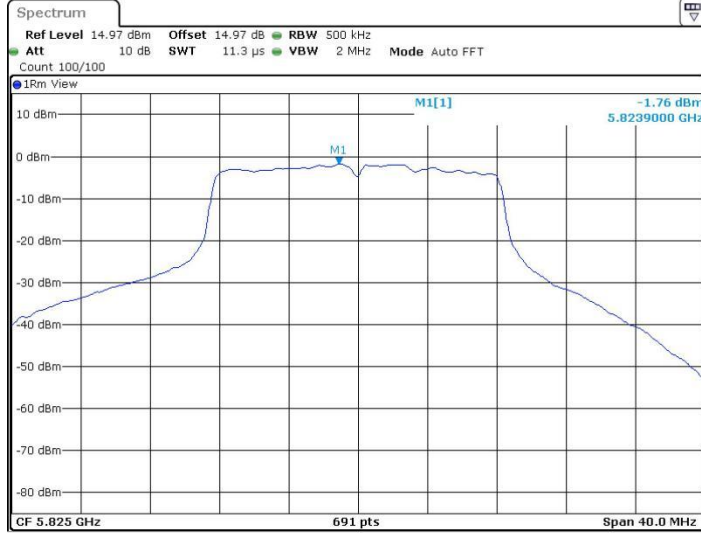
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11A Ant1 5785



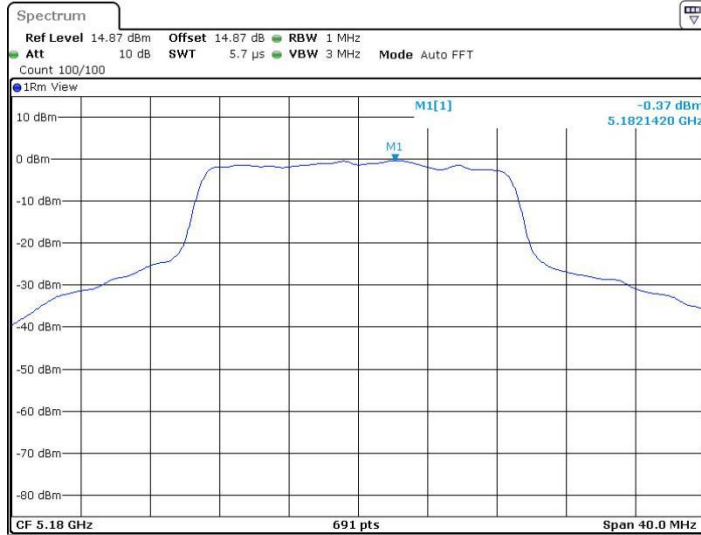
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11A_Ant1_5825



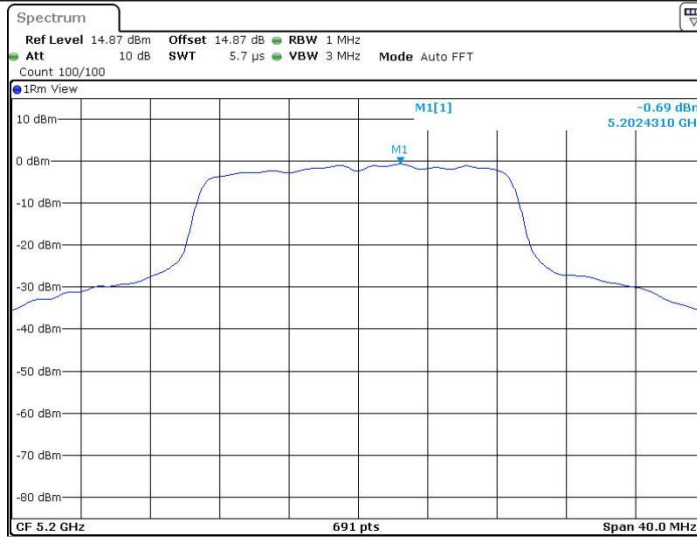
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11N20SISO_Ant1_5180



Date: 8 MAY 2024 14:01:50

11N20SISO_Ant1_5200



Date: 8 MAY 2024 14:06:35

11N20SISO_Ant1_5240



Date: 8 MAY 2024 14:08:36

11N20SISO_Ant1_5745



Date: 8 MAY 2024 14:11:32

11N20SISO_Ant1_5785



Date: 8 MAY 2024 14:14:01

11N20SISO_Ant1_5825



Date: 8 MAY 2024 16:19:34

11N40SISO_Ant1_5190



Date: 8 MAY 2024 16:28:05

11N40SISO_Ant1_5230



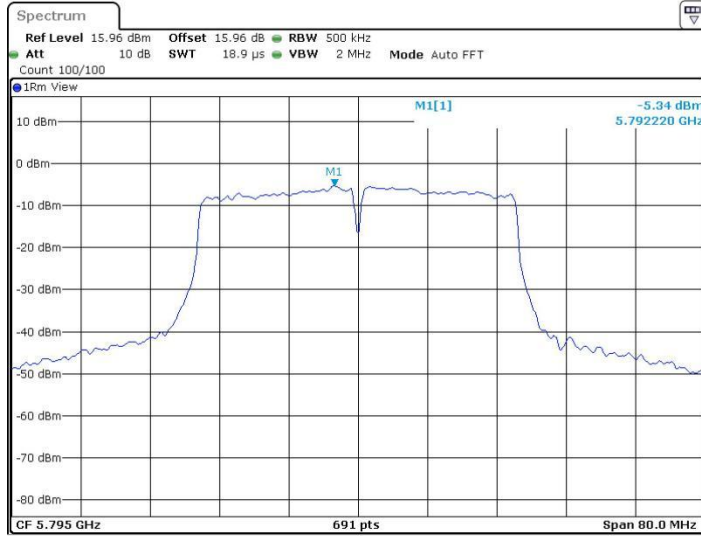
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11N40SISO_Ant1_5755



Date: 8 MAY 2024 16:33:33

11N40SISO_Ant1_5795



Date: 8 MAY 2024 16:36:28

11AC20SISO_Ant1_5180



Date: 8 MAY 2024 16:39:18

11AC20SISO_Ant1_5200



Date: 8 MAY 2024 16:42:03

11AC20SISO_Ant1_5240



Date: 8 MAY 2024 16:43:30

11AC20SISO_Ant1_5745



Date: 8 MAY 2024 16:46:15

11AC20SISO_Ant1_5785



Date: 8 MAY 2024 16:48:51

11AC20SISO_Ant1_5825



Date: 8 MAY 2024 16:50:33

11AC40SISO_Ant1_5190



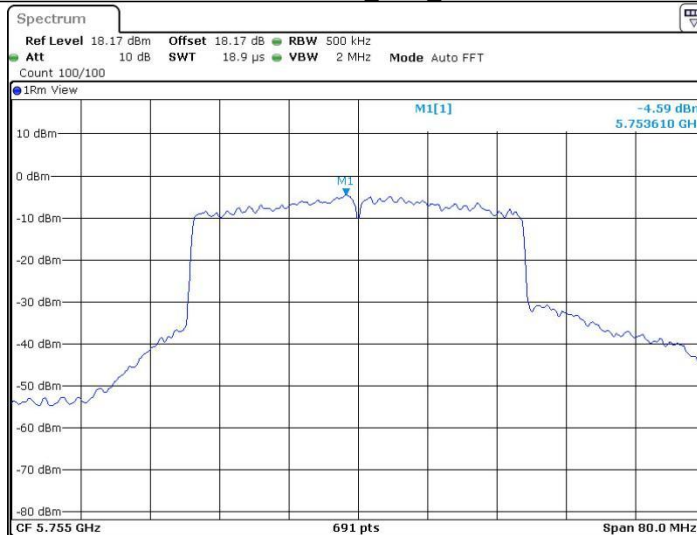
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11AC40SISO_Ant1_5230



Date: 8 MAY 2024 16:56:20

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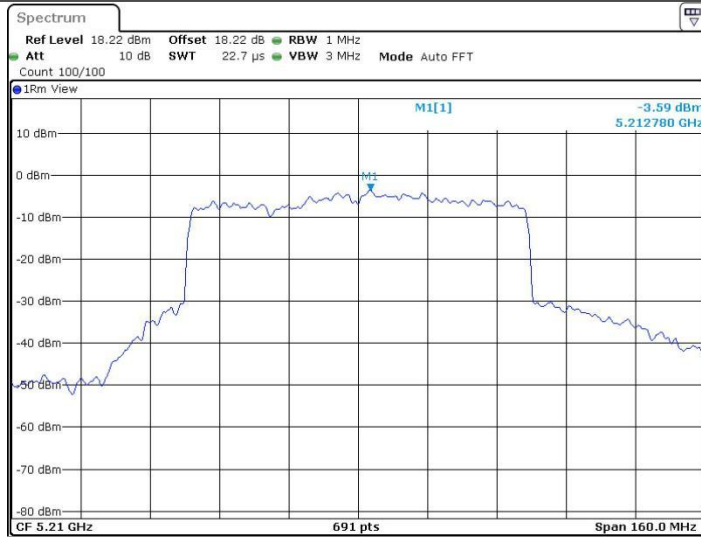
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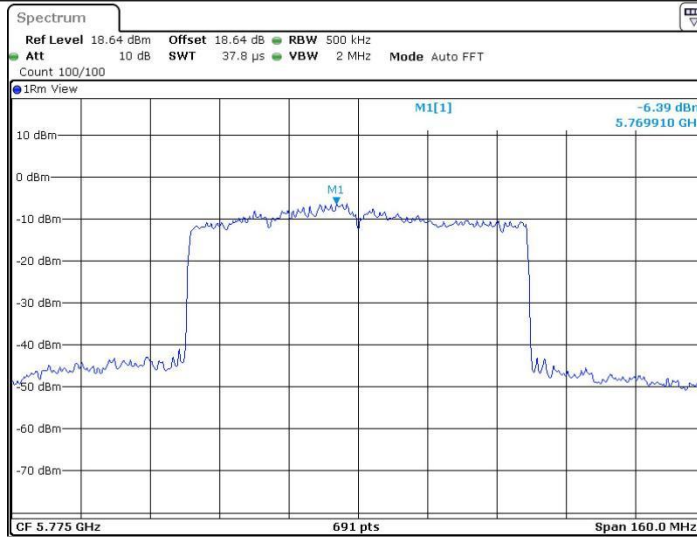
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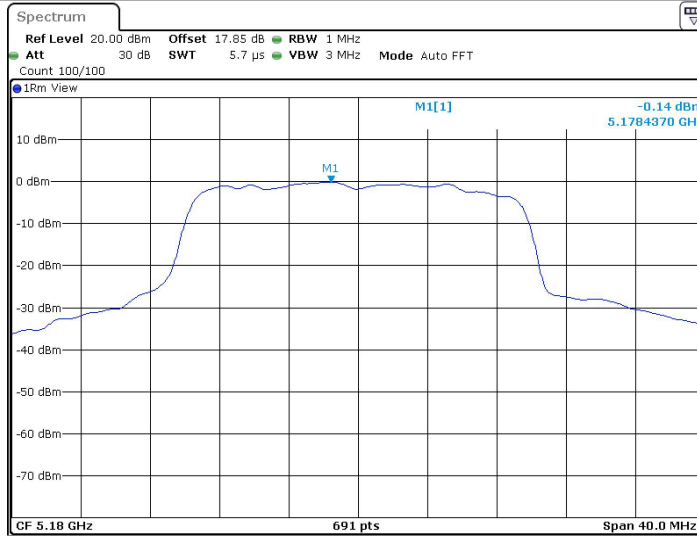
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11AC80SISO_Ant1_5775



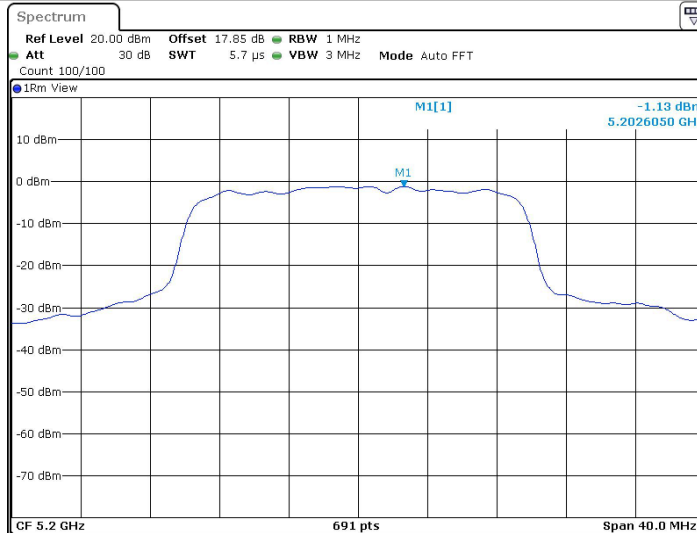
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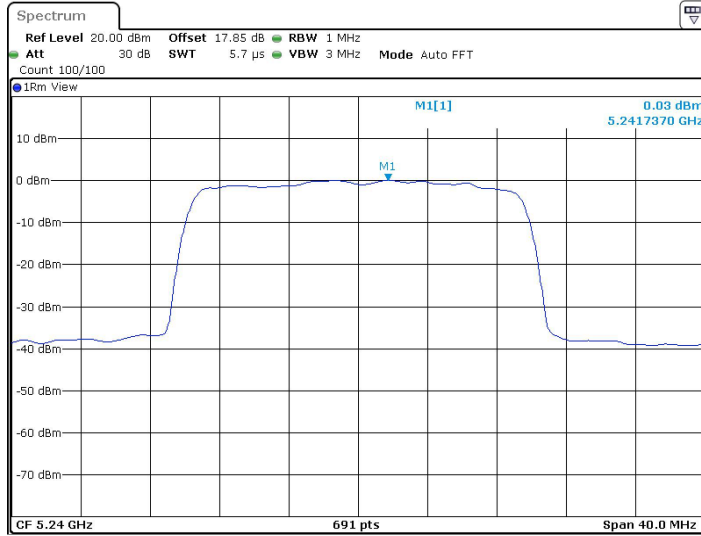
Date: 10 MAY 2024 16:17:54

11AX20SISO_Ant1_5200



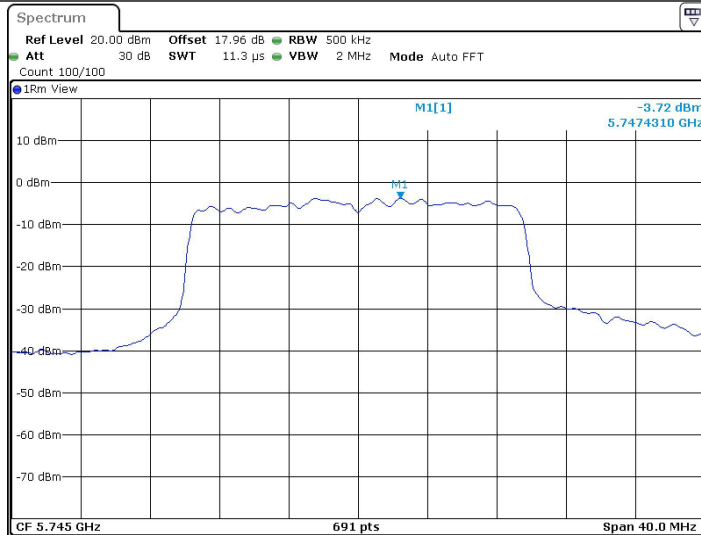
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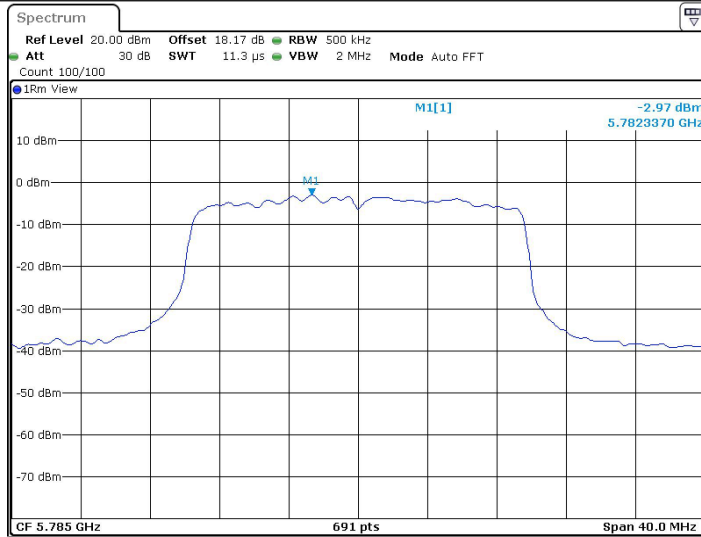
Date: 10.MAY.2024 16:22:23

11AX20SISO_Ant1_5745

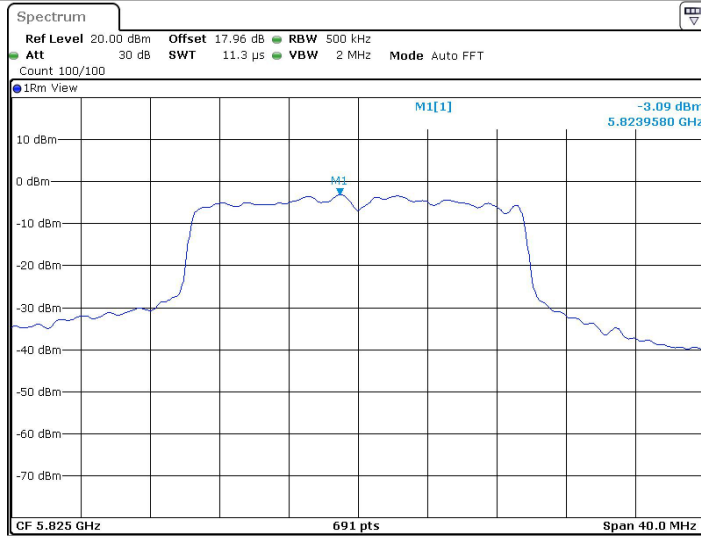


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11AX20SISO_Ant1_5785



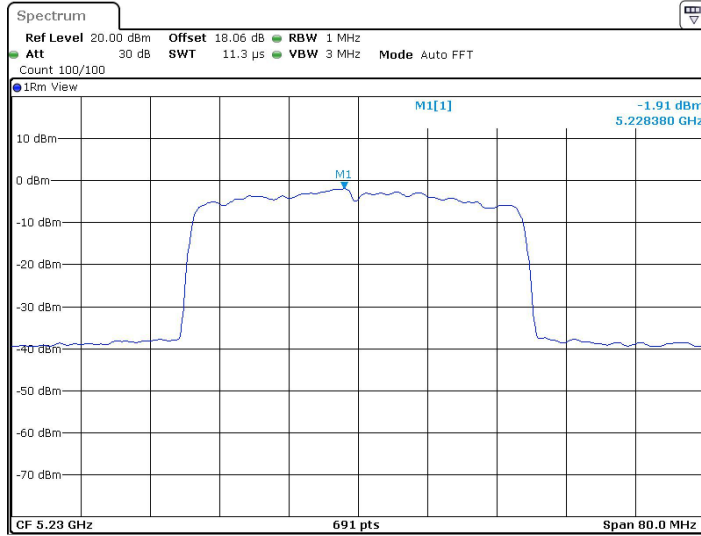
11AX20SISO_Ant1_5825



11AX40SISO_Ant1_5190

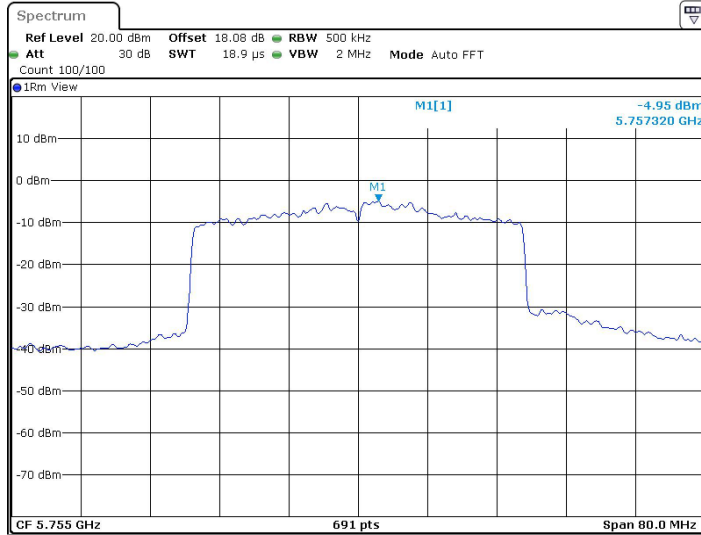


11AX40SISO_Ant1_5230



Date: 10.MAY.2024 16:36:49

11AX40SISO_Ant1_5755



Date: 10.MAY.2024 16:40:38

11AX40SISO_Ant1_5795