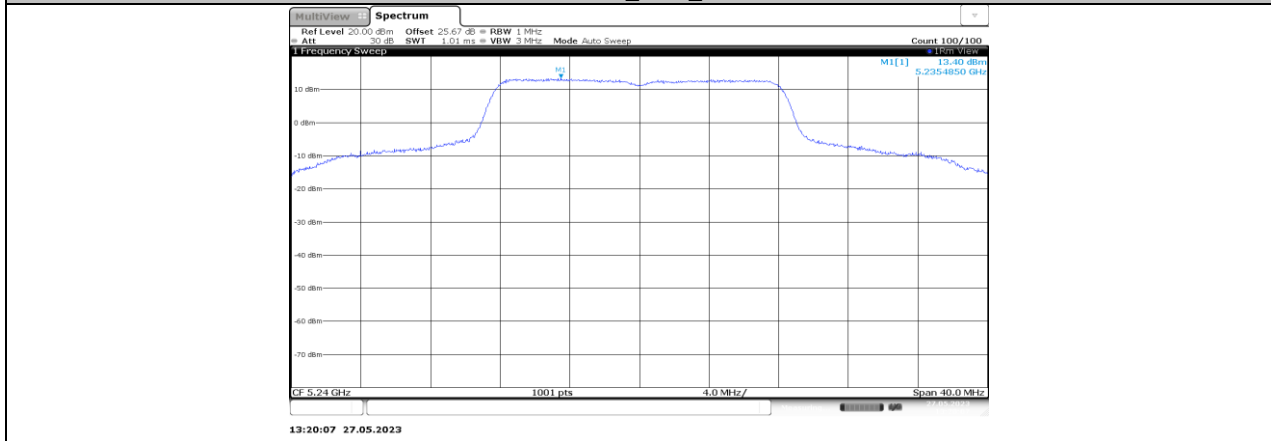
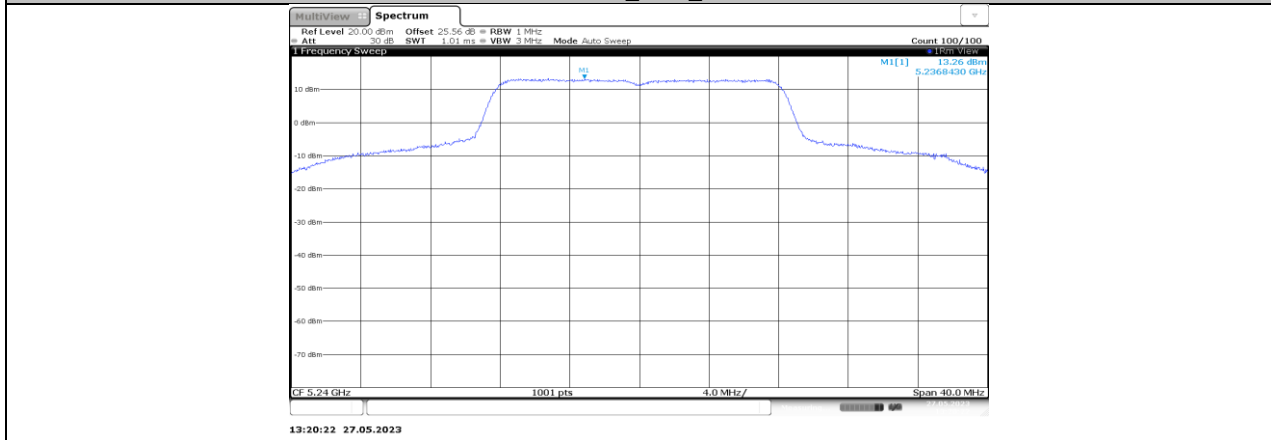


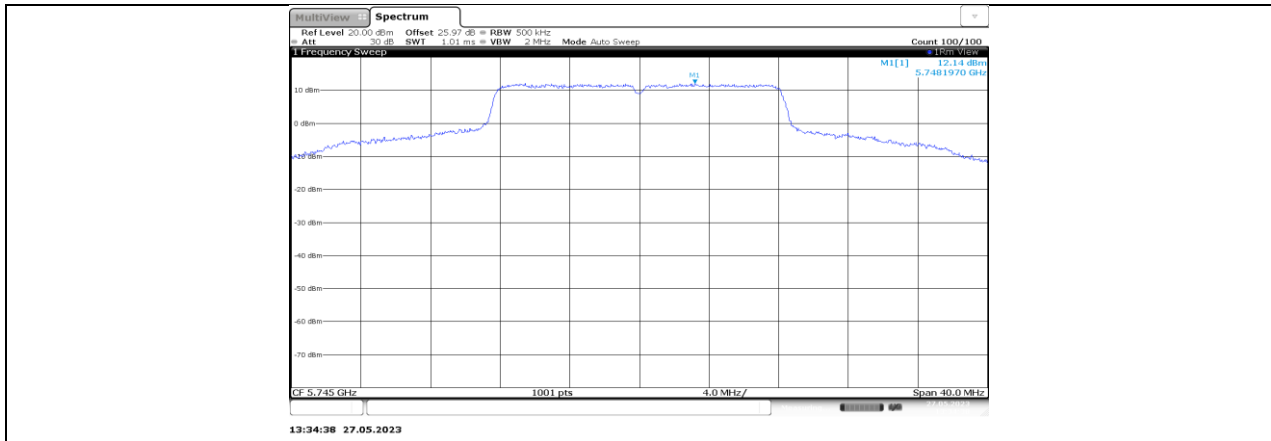
11A-CDD_Ant4_5200



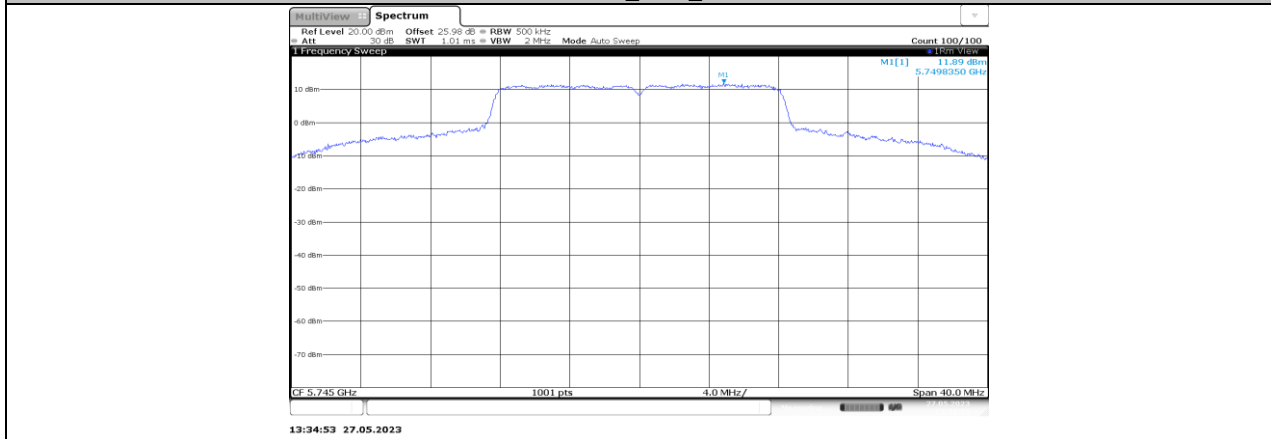
11A-CDD_Ant2_5240



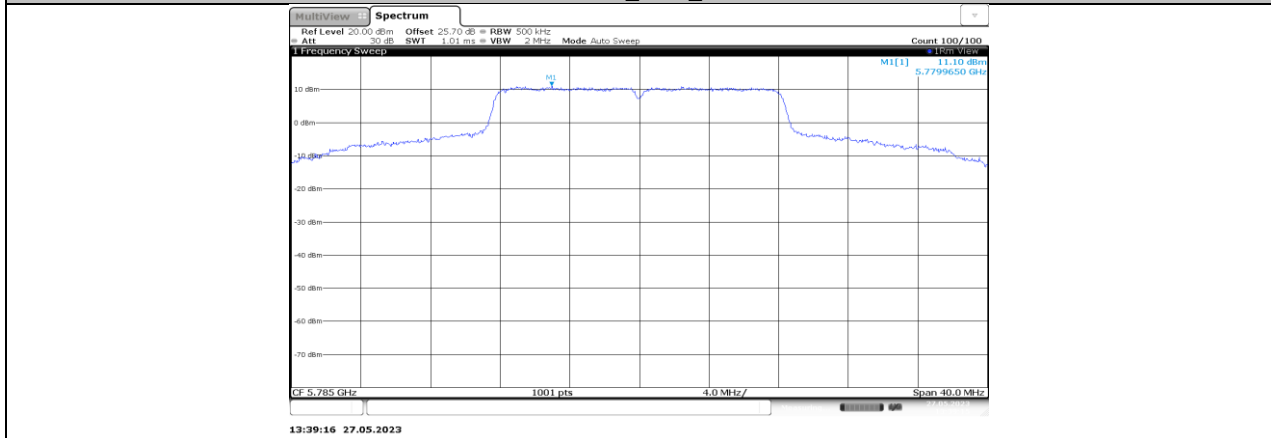
11A-CDD_Ant4_5240



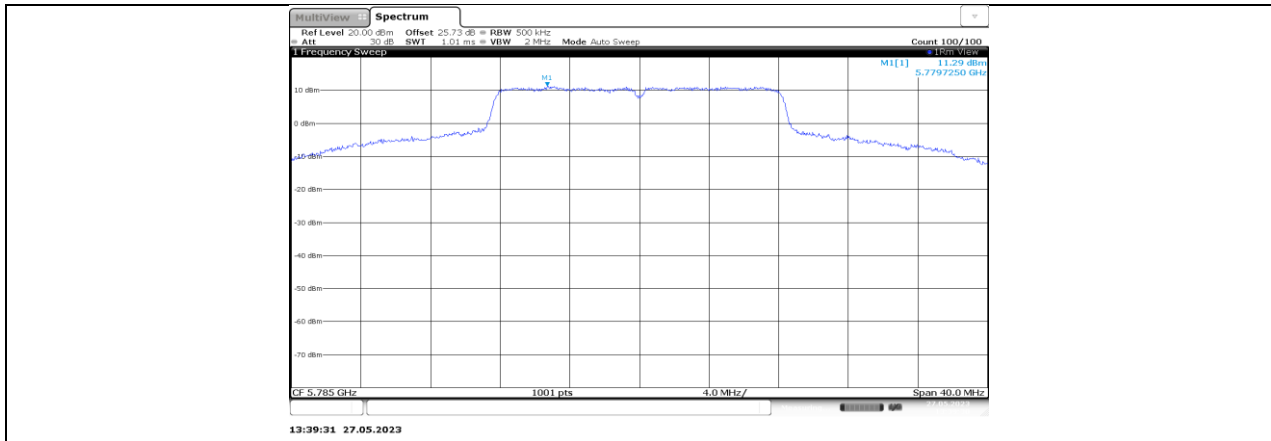
11A-CDD_Ant2_5745



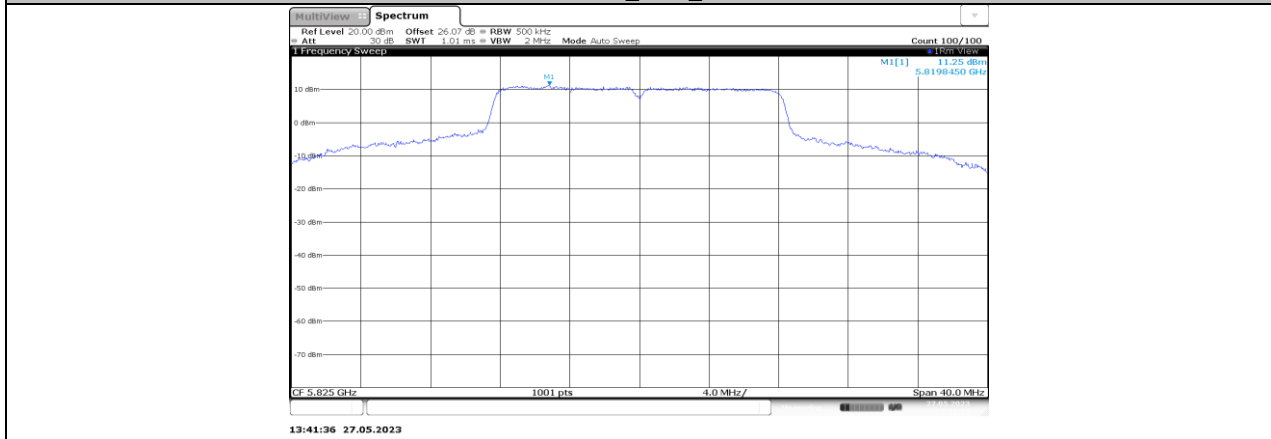
11A-CDD_Ant4_5745



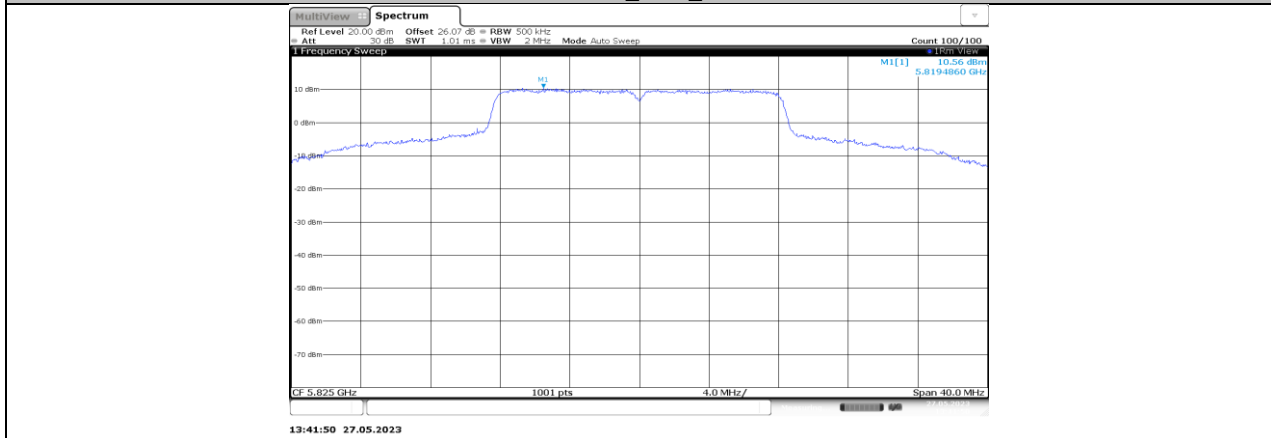
11A-CDD_Ant2_5785



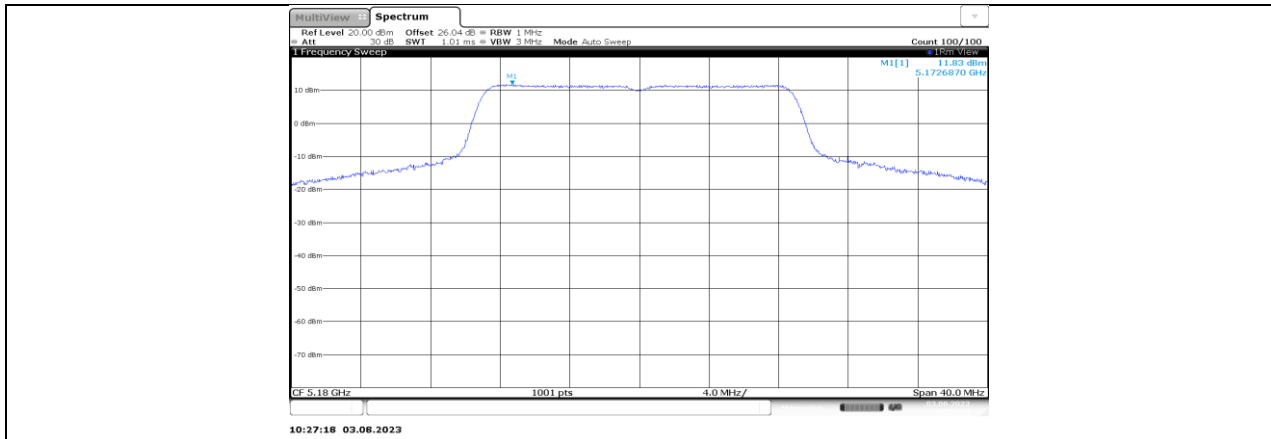
11A-CDD_Ant4_5785



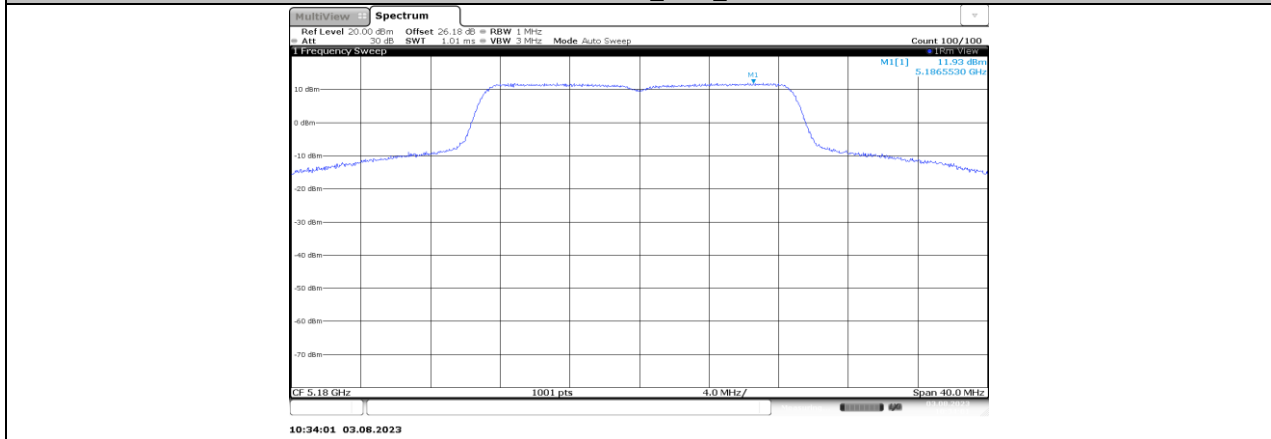
11A-CDD_Ant2_5825



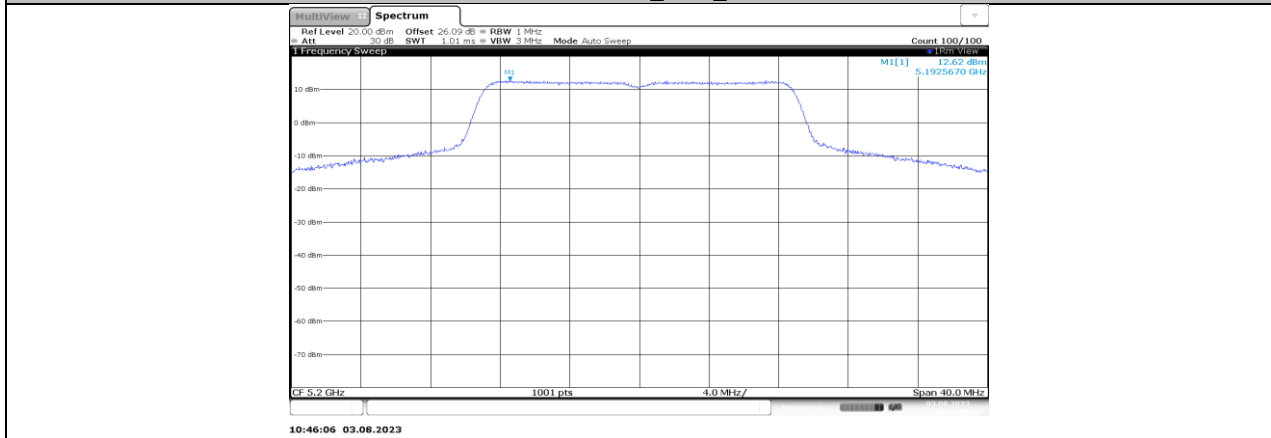
11A-CDD_Ant4_5825



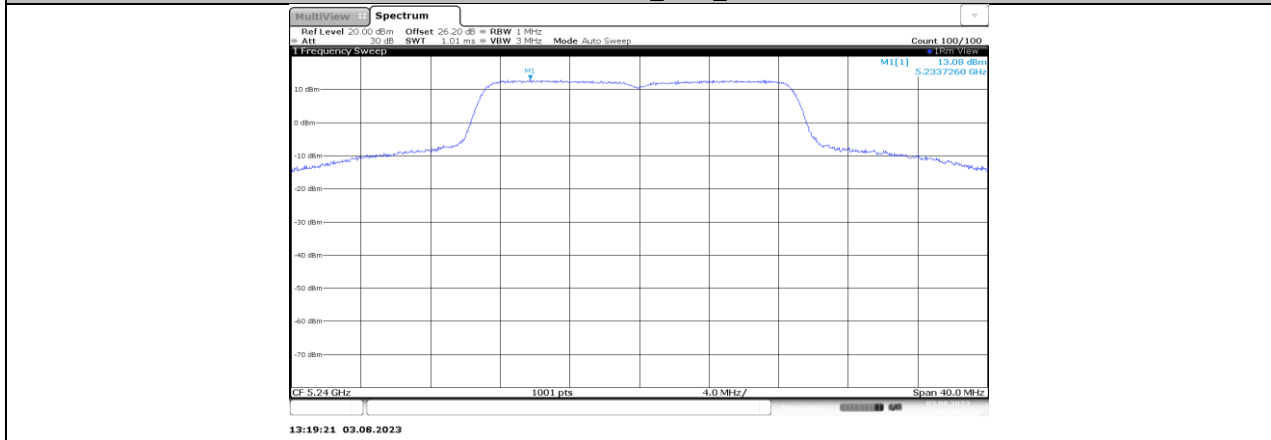
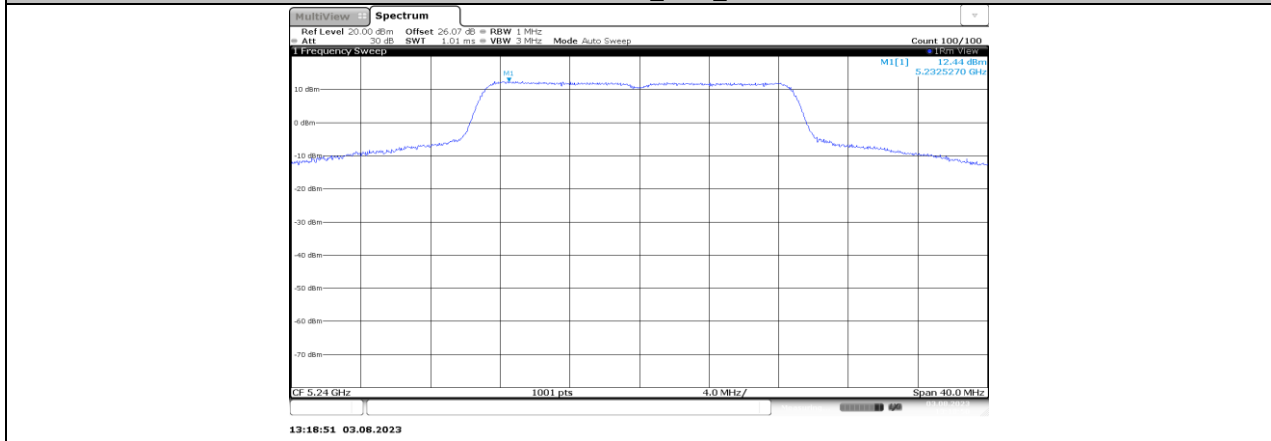
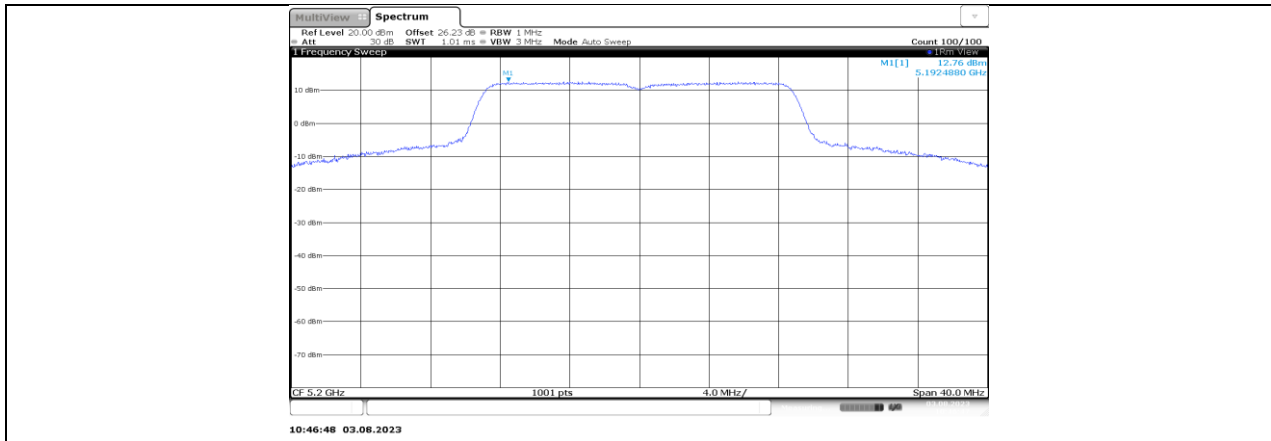
11AC20MIMO_Ant2_5180

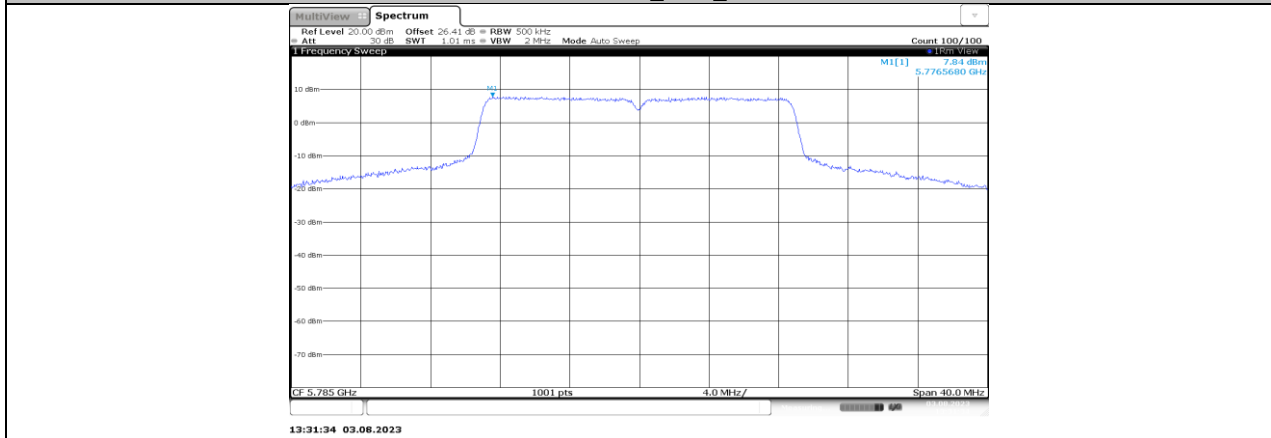
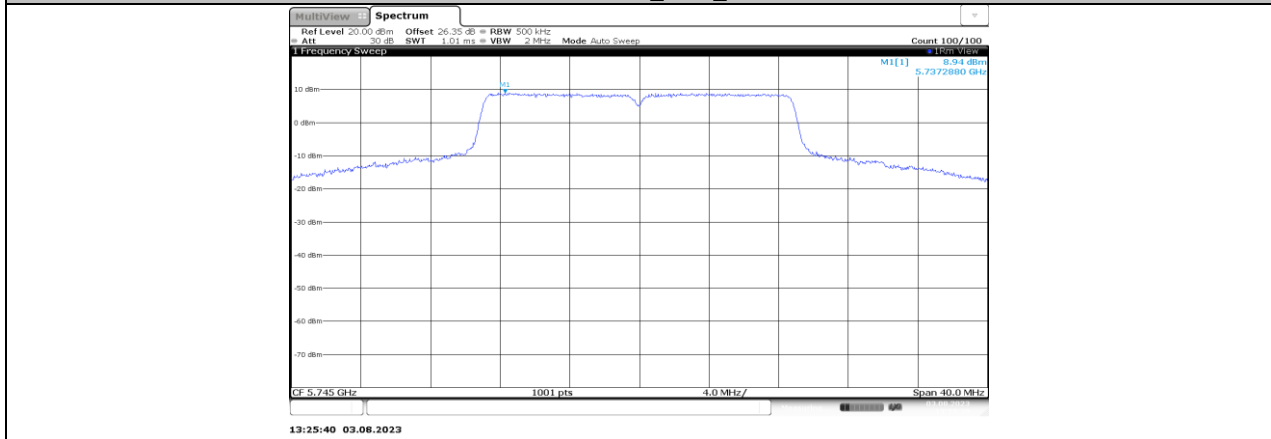
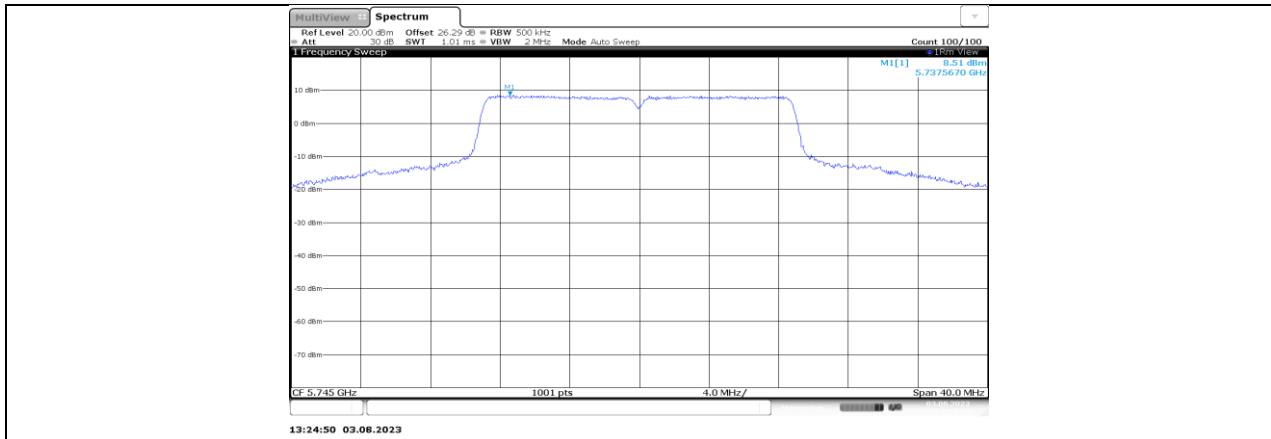


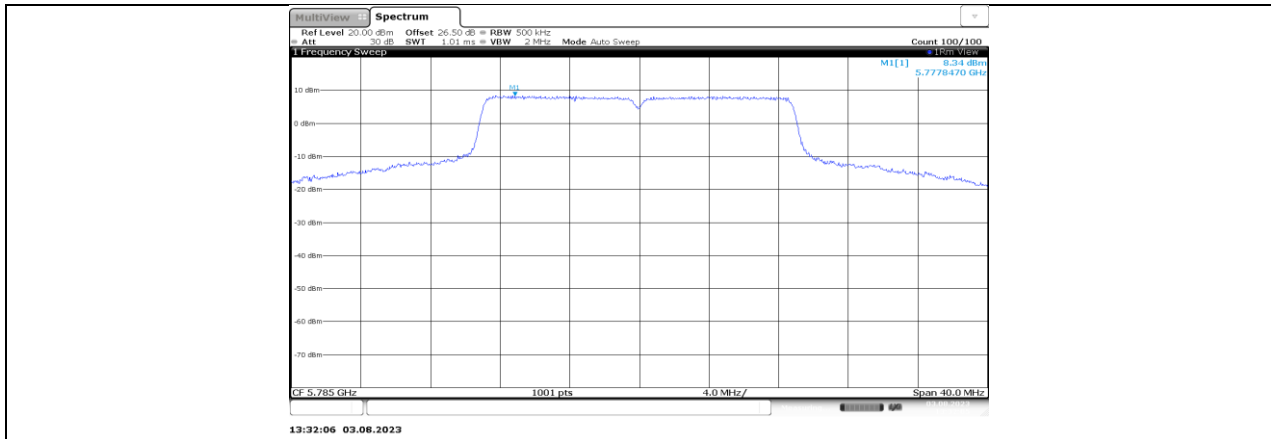
11AC20MIMO_Ant4_5180



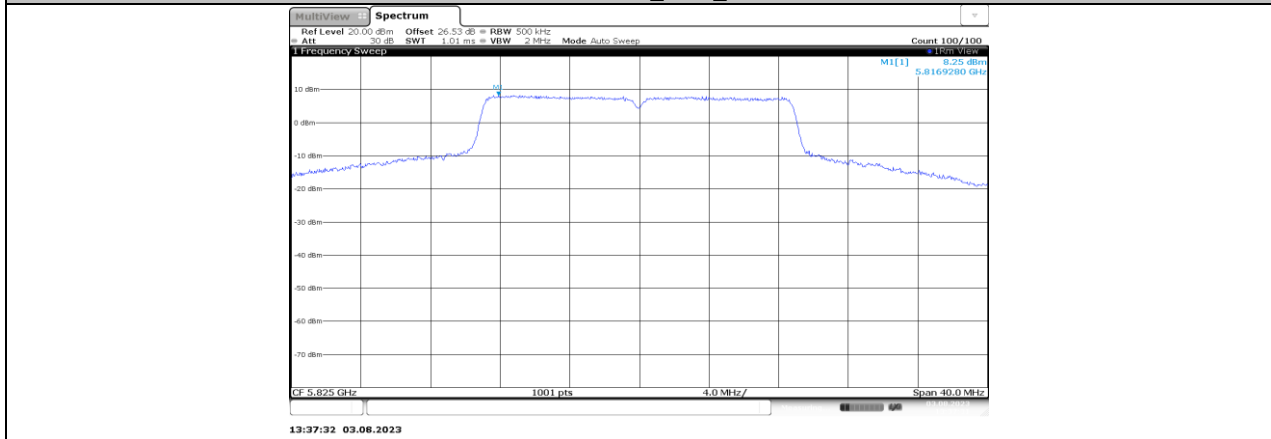
11AC20MIMO_Ant2_5200



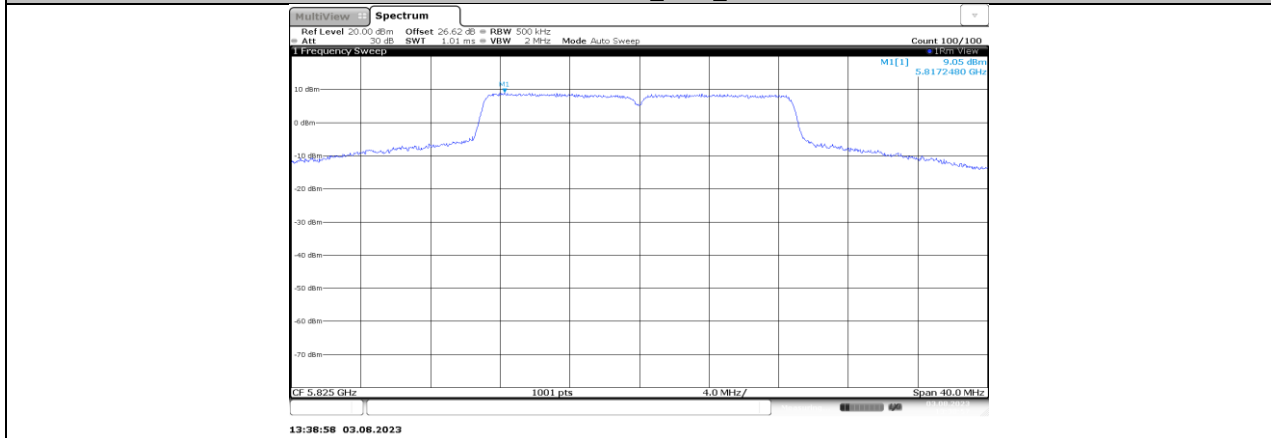




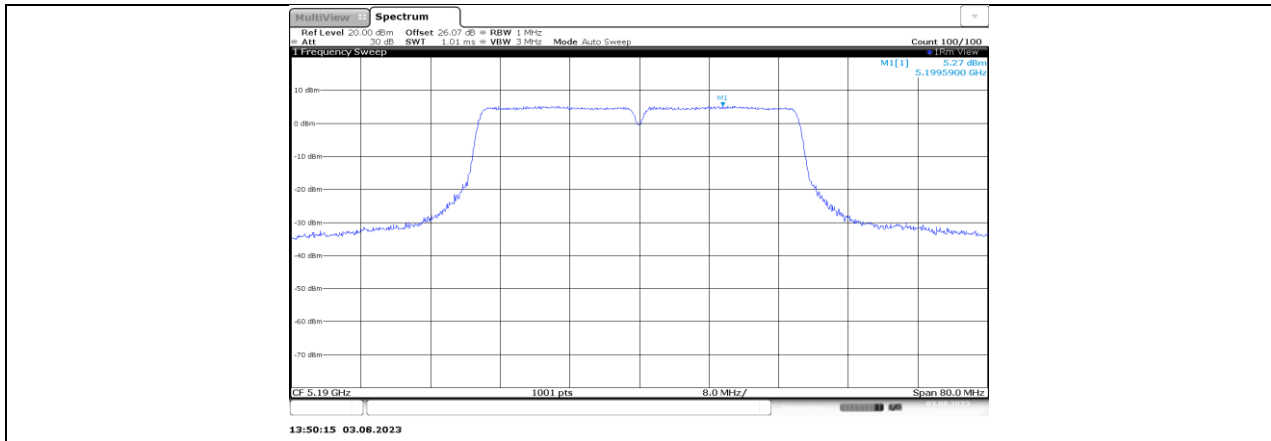
11AC20MIMO_Ant4_5785



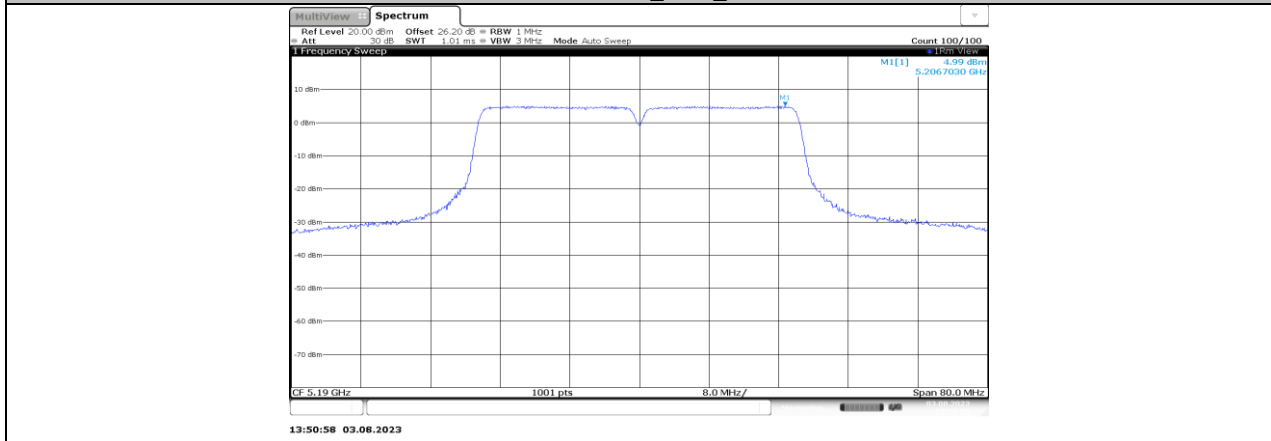
11AC20MIMO_Ant2_5825



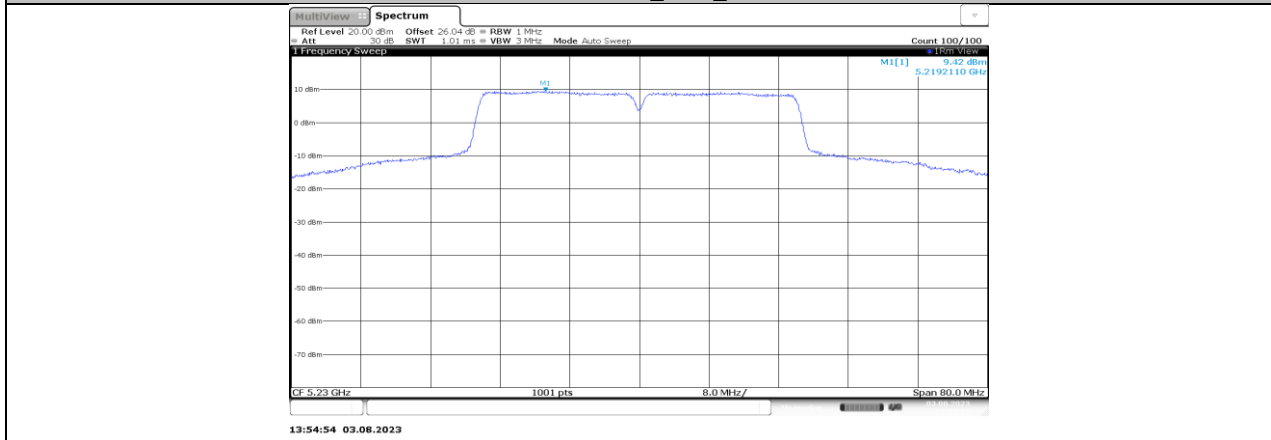
11AC20MIMO_Ant4_5825



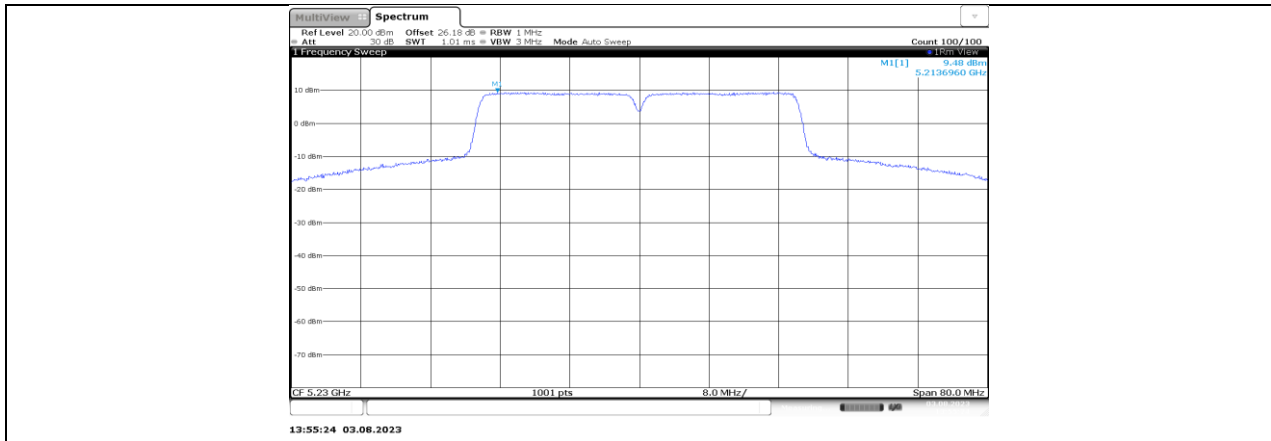
11AC40MIMO_Ant2_5190



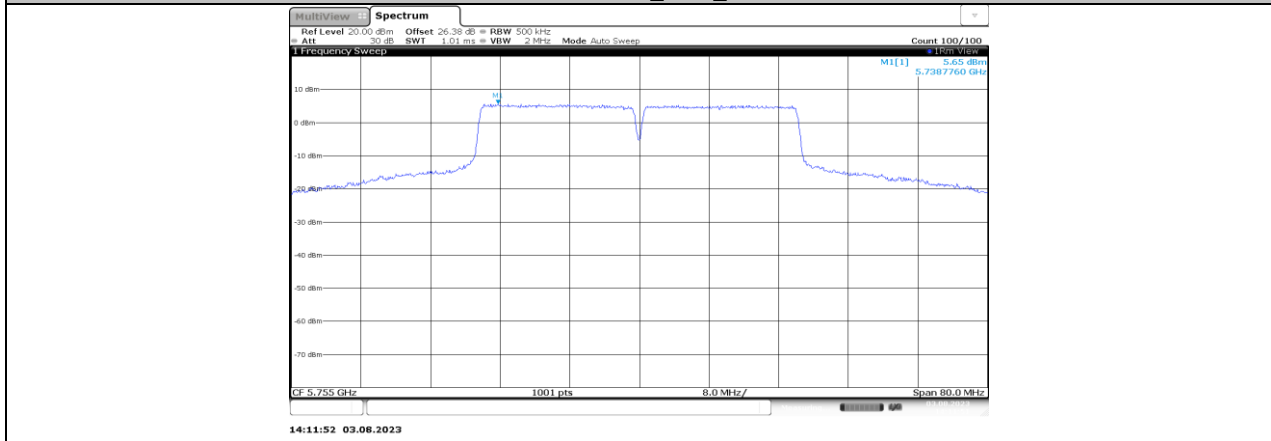
11AC40MIMO_Ant4_5190



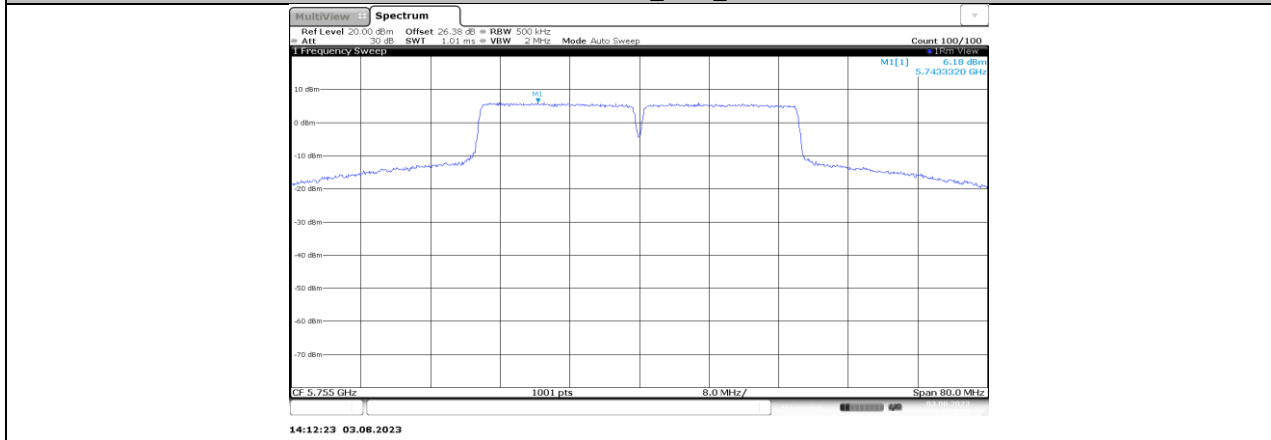
11AC40MIMO_Ant2_5230



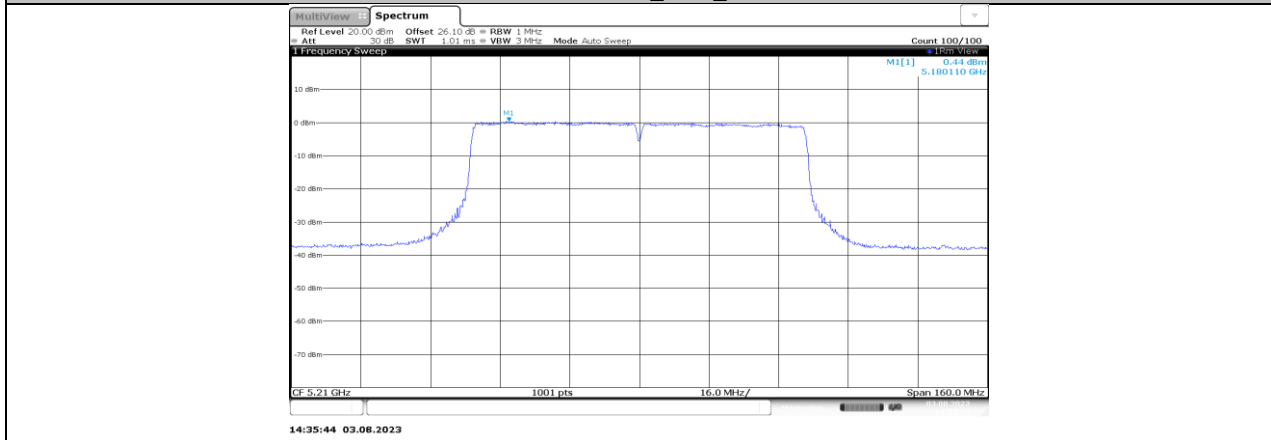
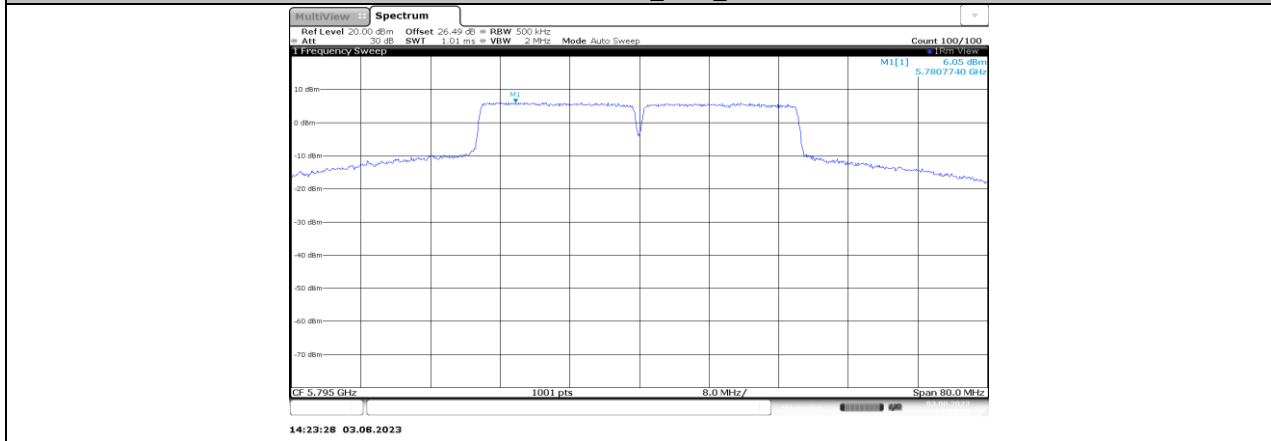
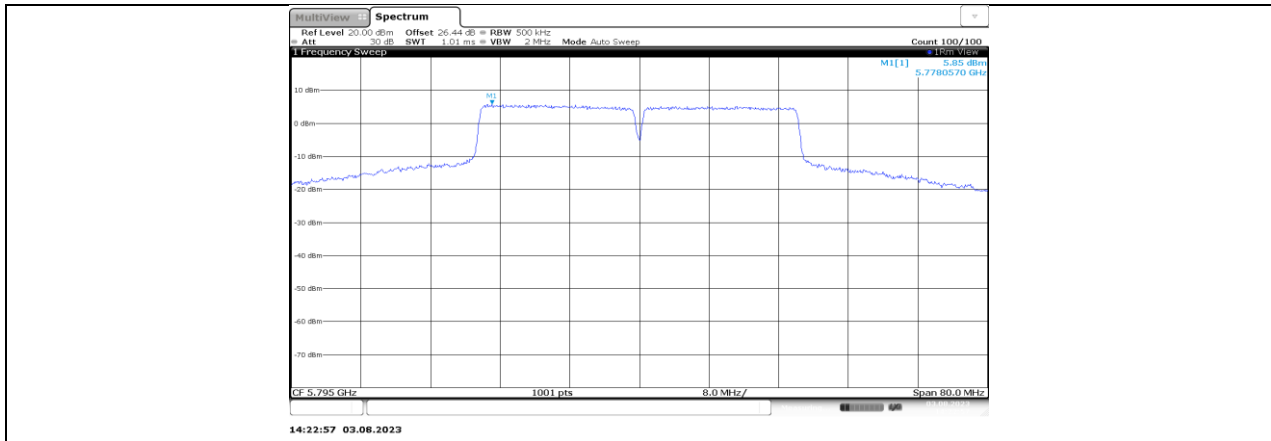
11AC40MIMO_Ant4_5230

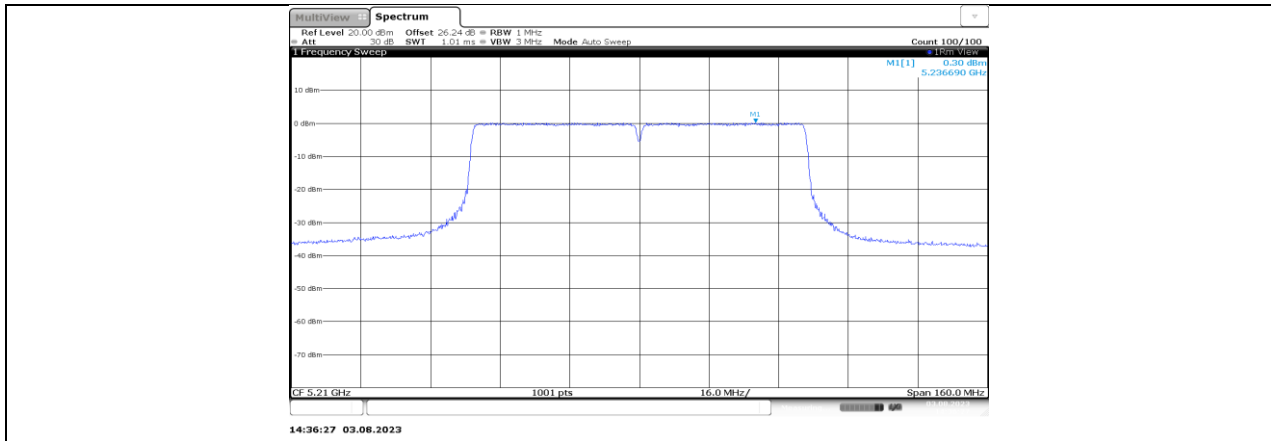


11AC40MIMO_Ant2_5755

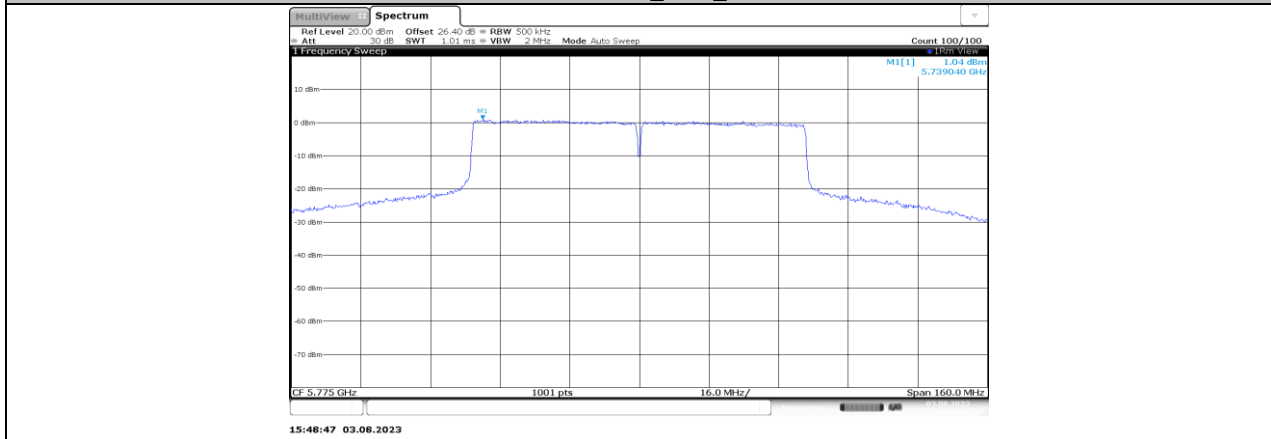


11AC40MIMO_Ant4_5755

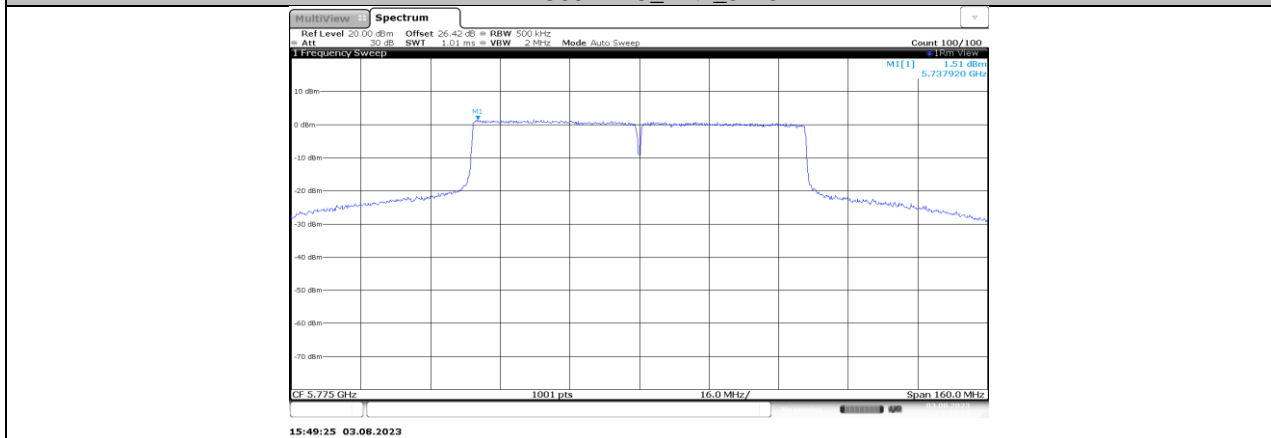




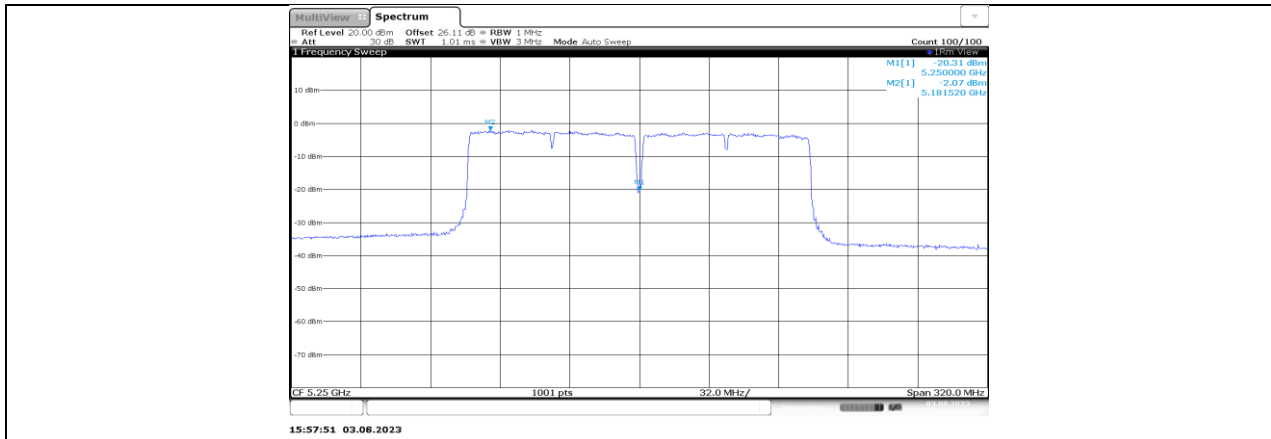
11AC80MIMO_Ant4_5210



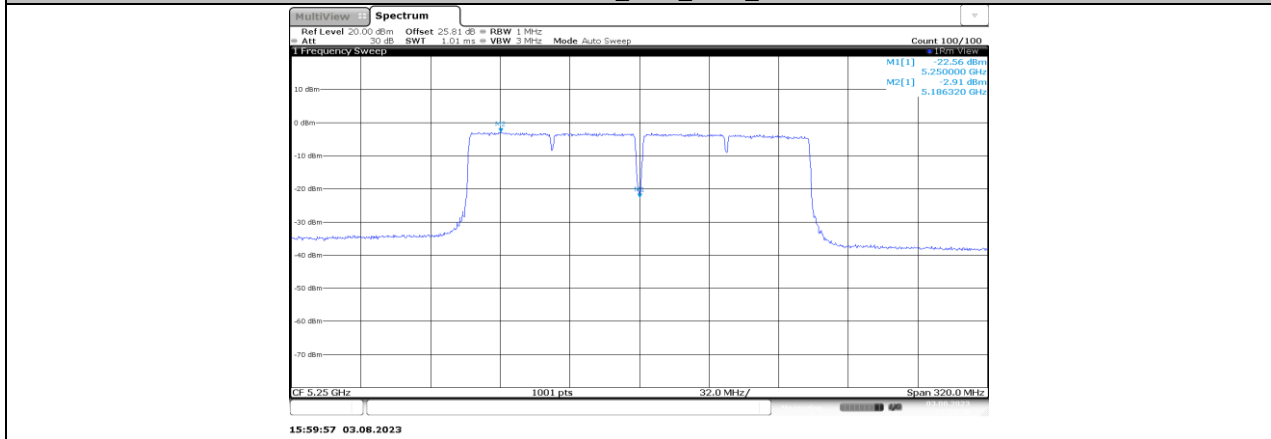
11AC80MIMO_Ant2_5775



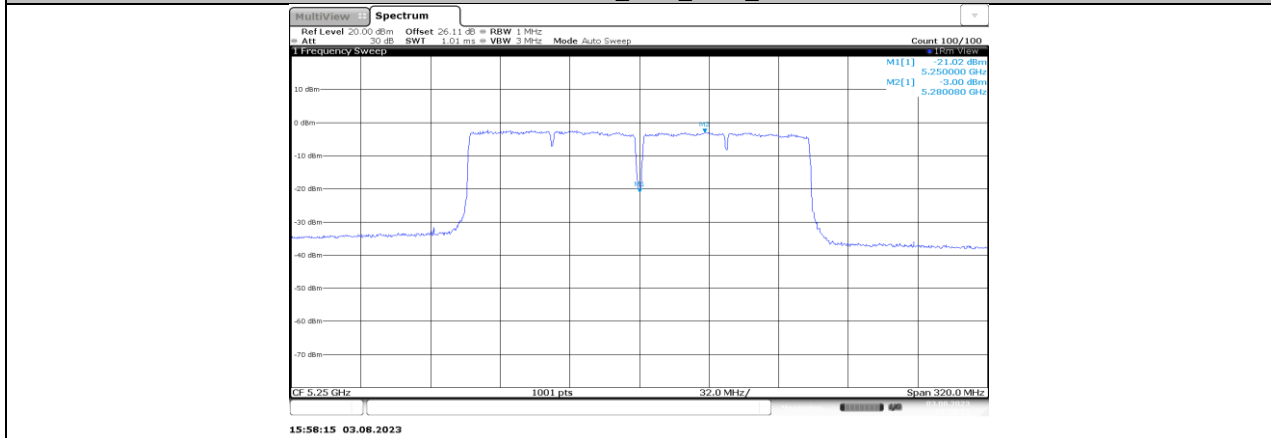
11AC80MIMO_Ant4_5775



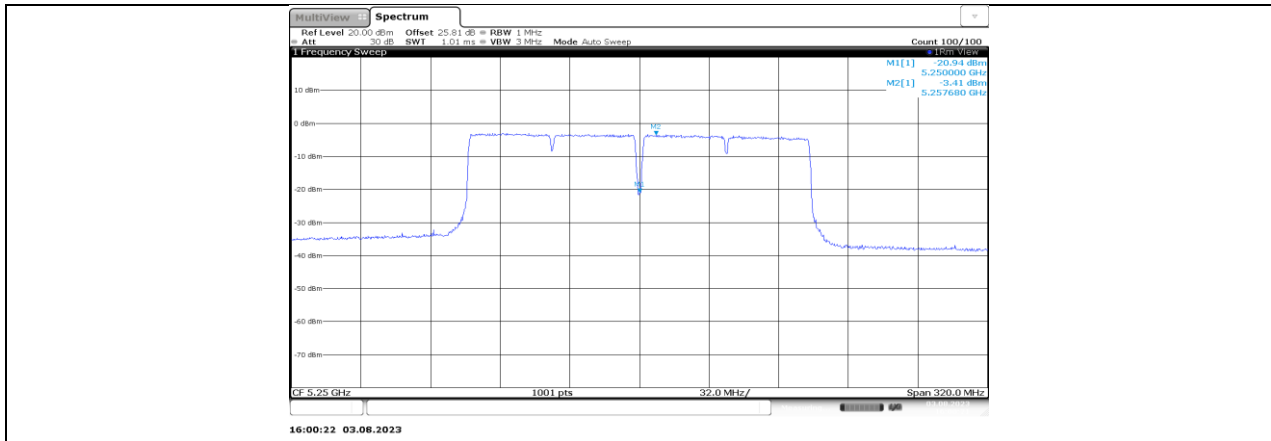
11AC160MIMO_Ant2_5250_UNII-1



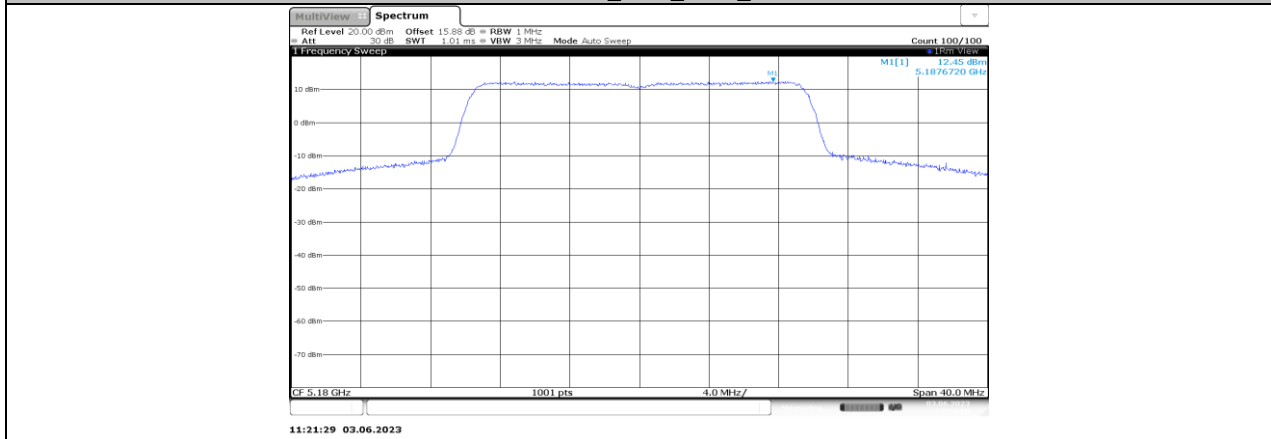
11AC160MIMO_Ant4_5250_UNII-1



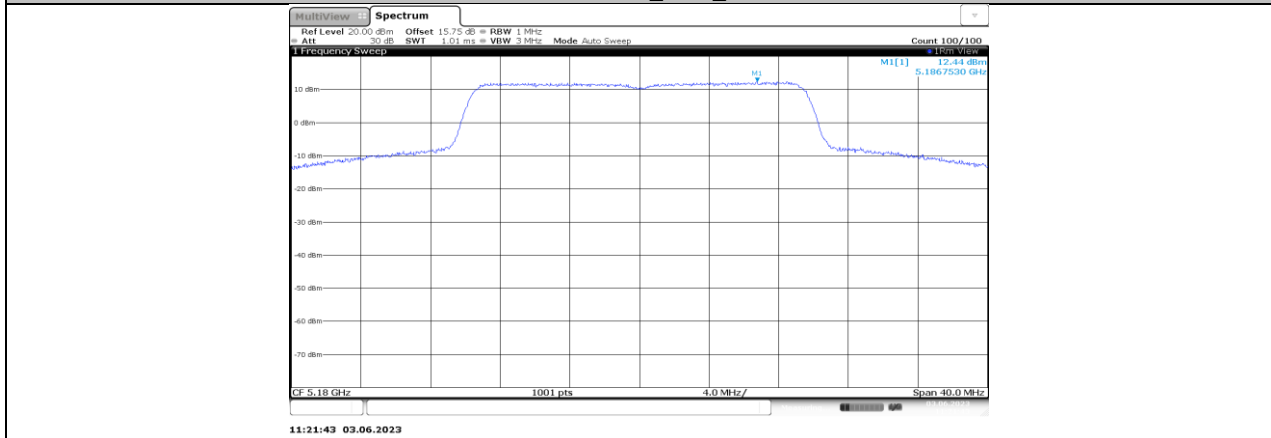
11AC160MIMO_Ant2_5250_UNII-2A



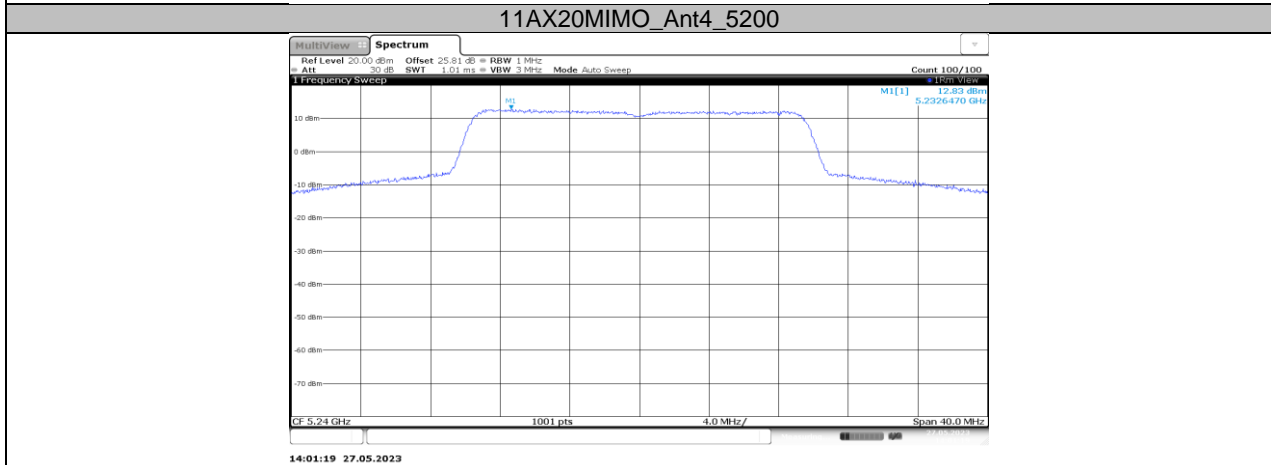
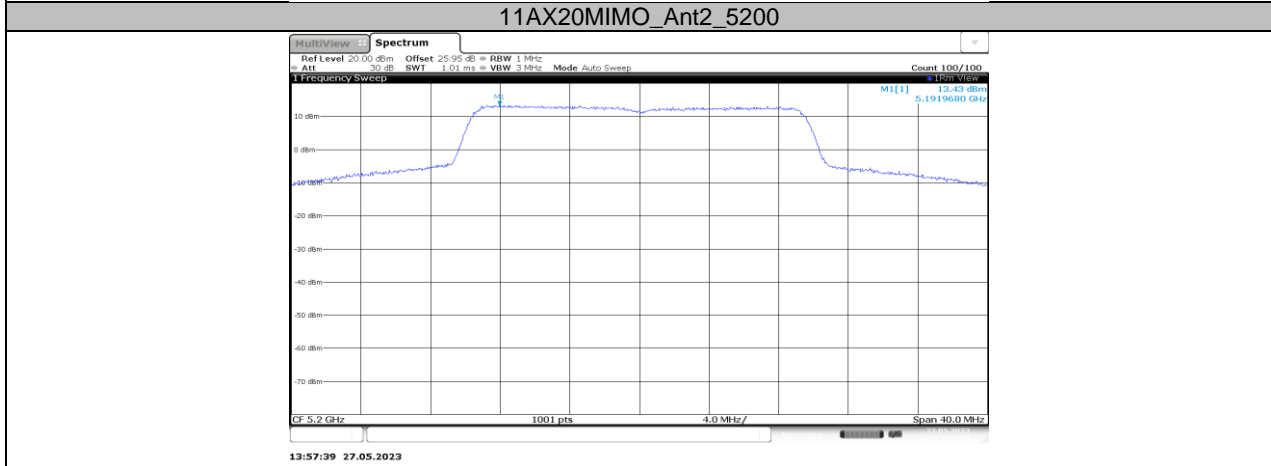
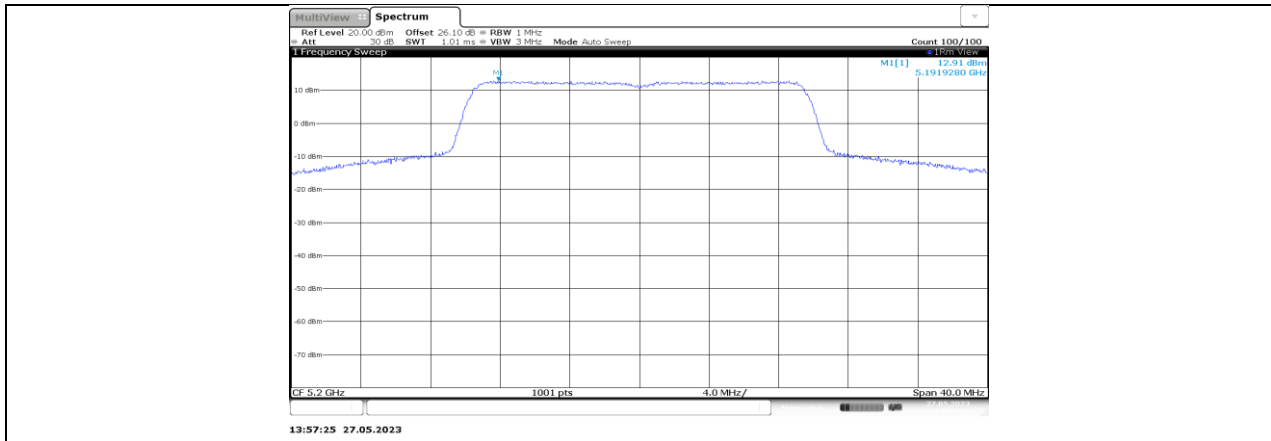
11AC160MIMO_Ant4_5250_UNII-2A

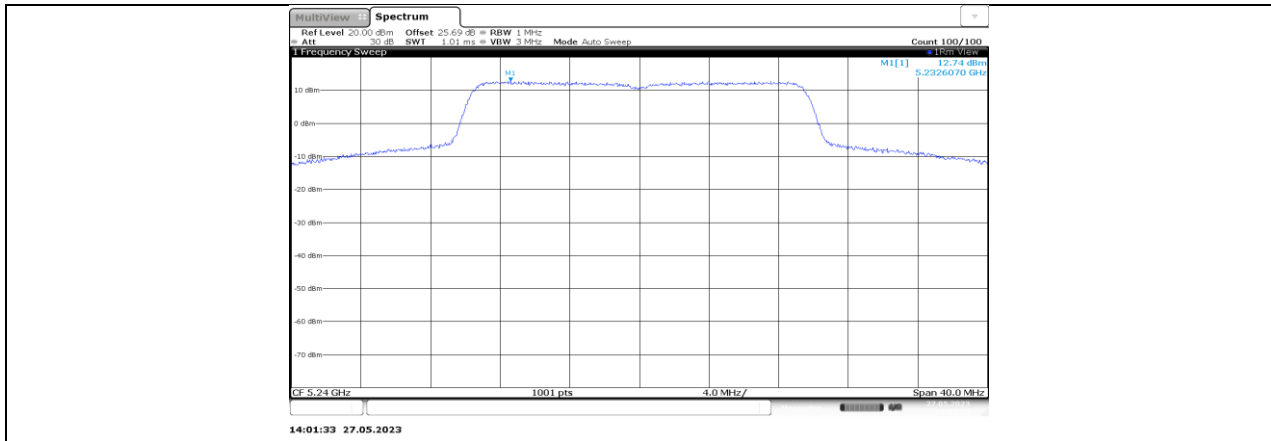


11AX20MIMO_Ant2_5180

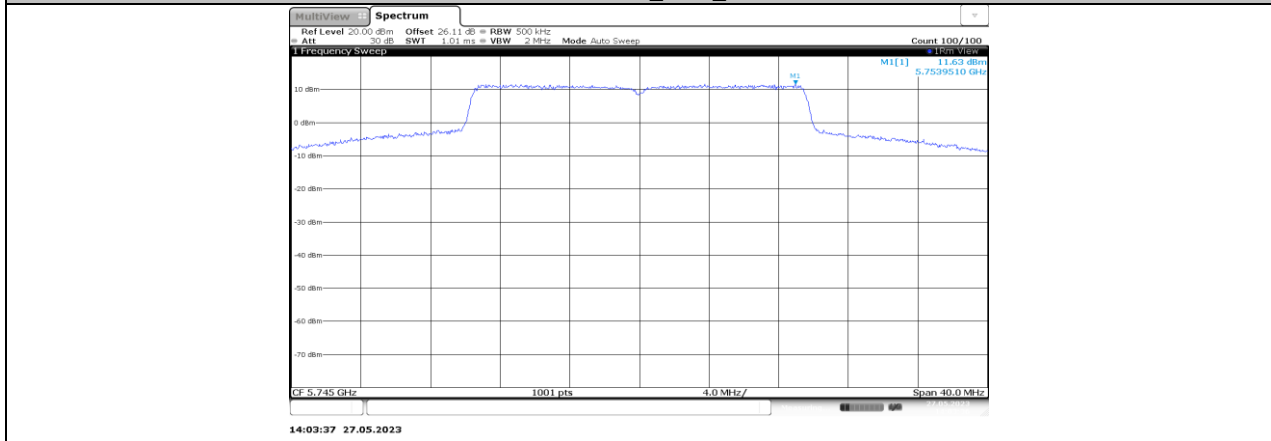


11AX20MIMO_Ant4_5180

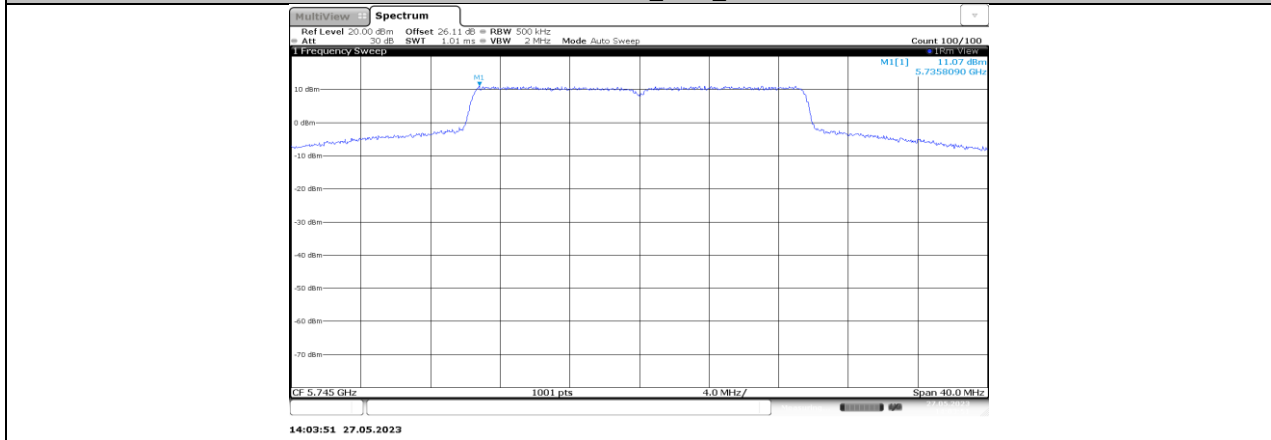




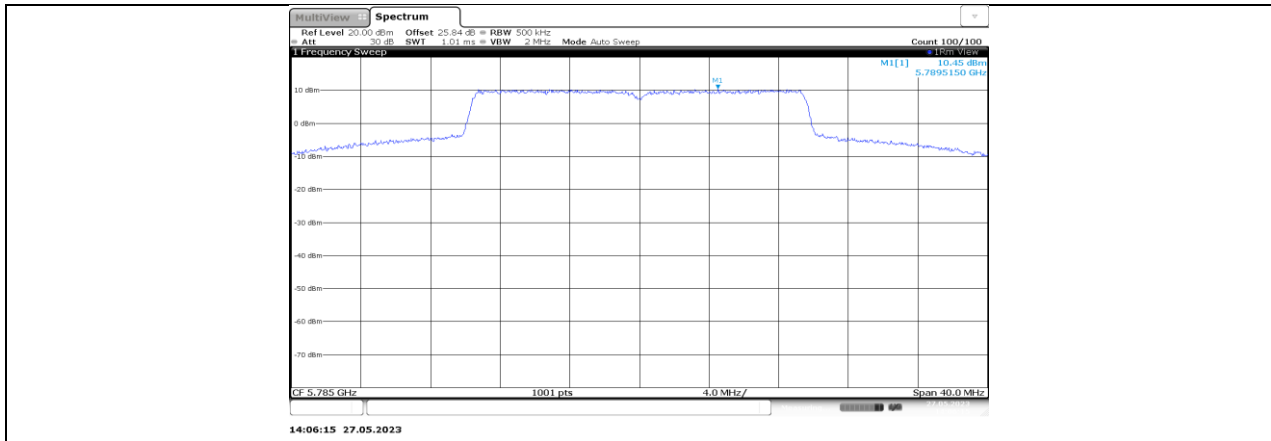
11AX20MIMO_Ant4_5240



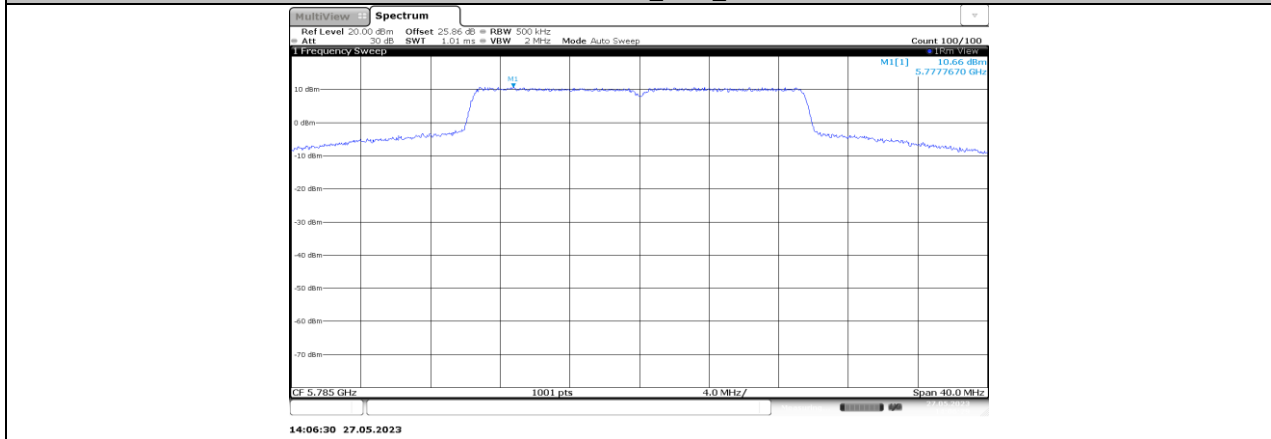
11AX20MIMO_Ant2_5745



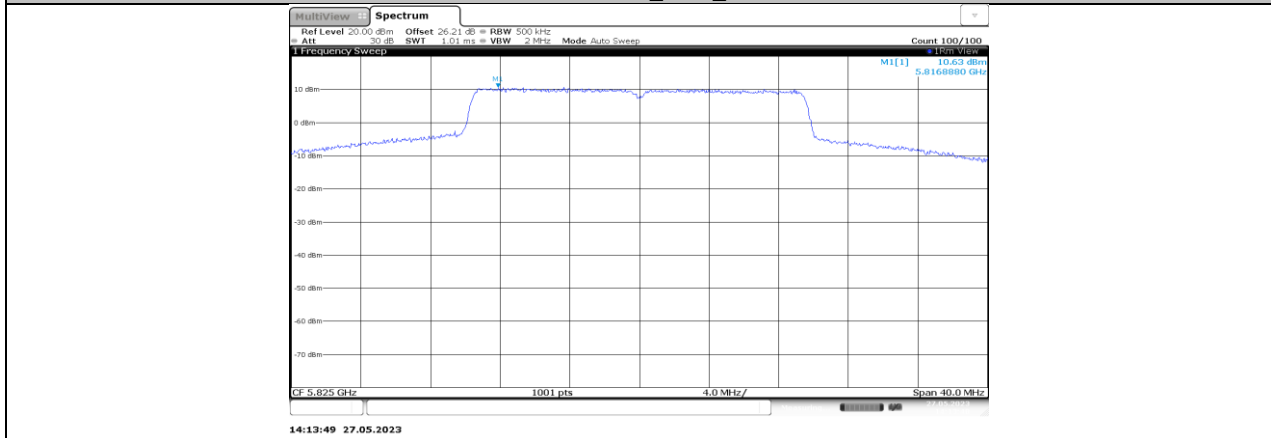
11AX20MIMO_Ant4_5745



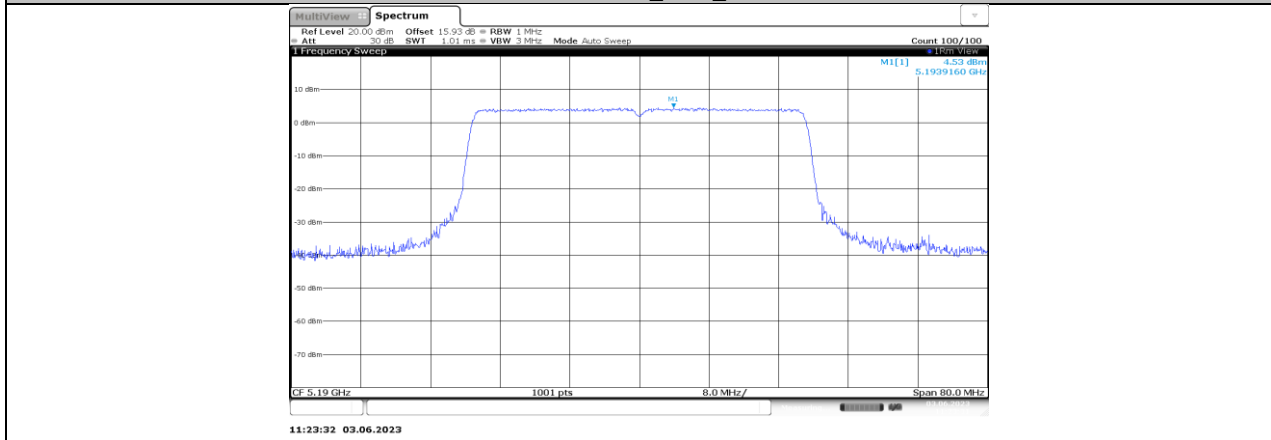
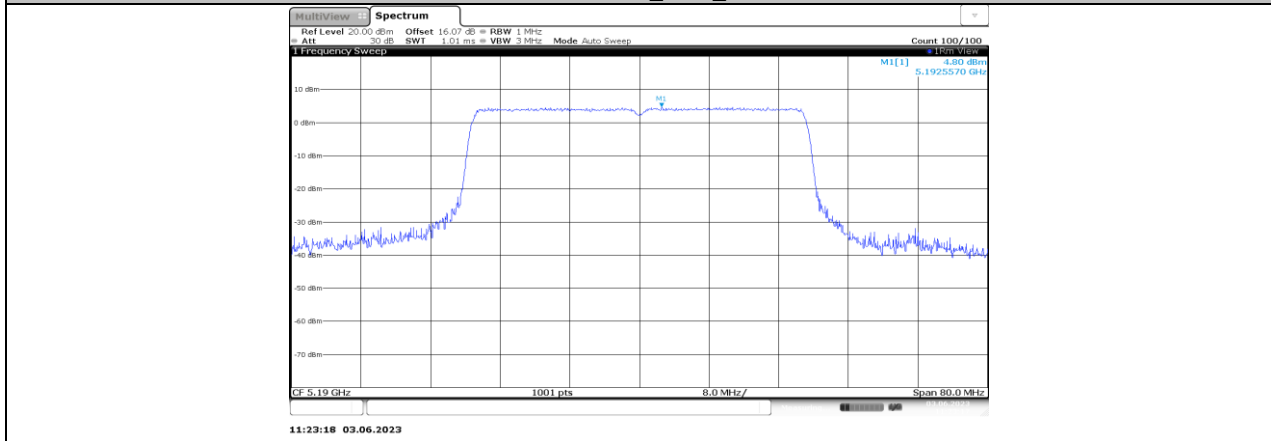
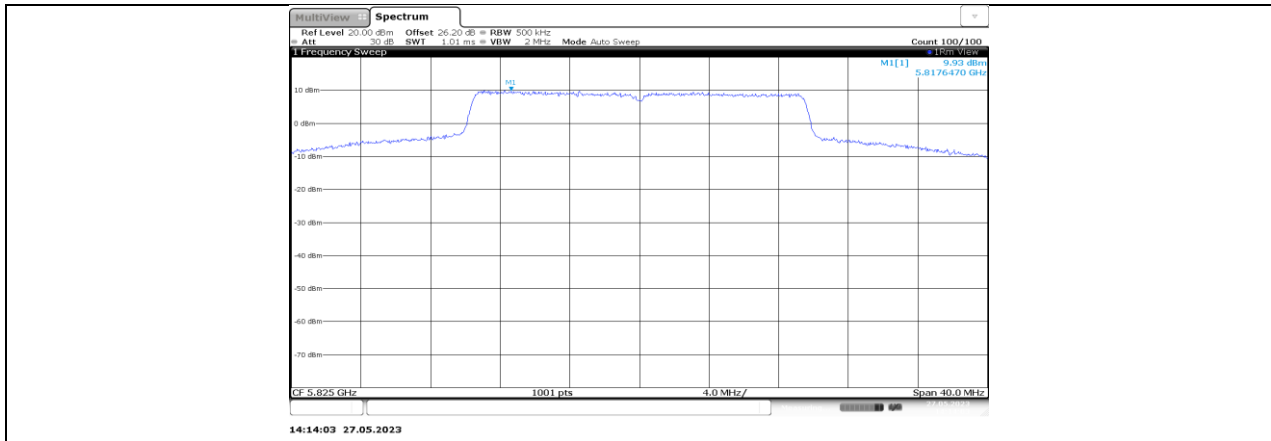
11AX20MIMO_Ant2_5785

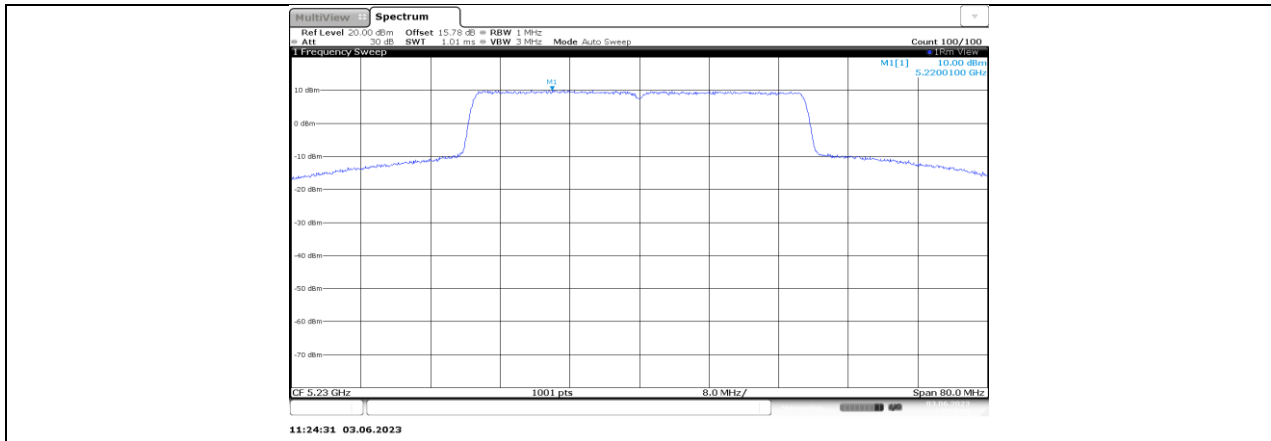


11AX20MIMO_Ant4_5785

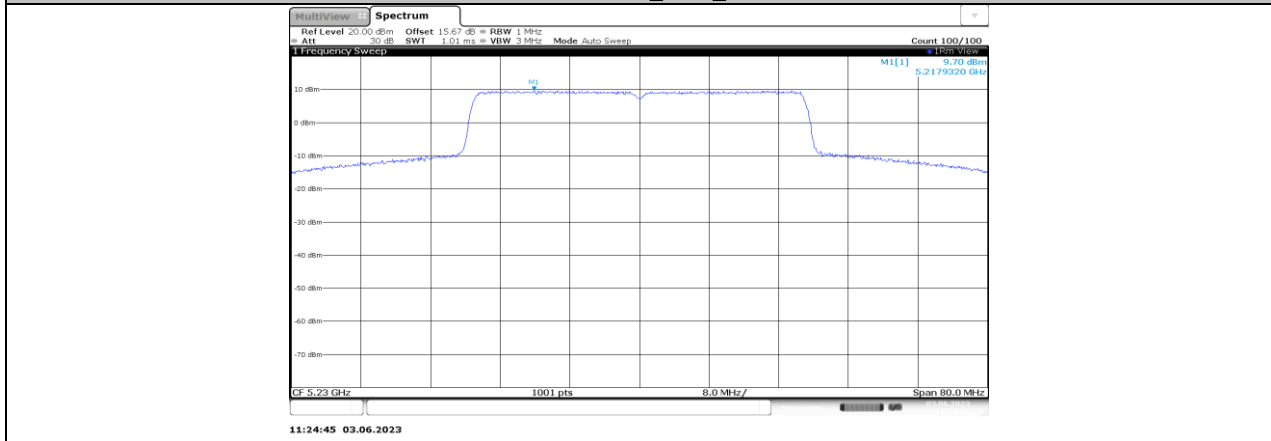


11AX20MIMO_Ant2_5825

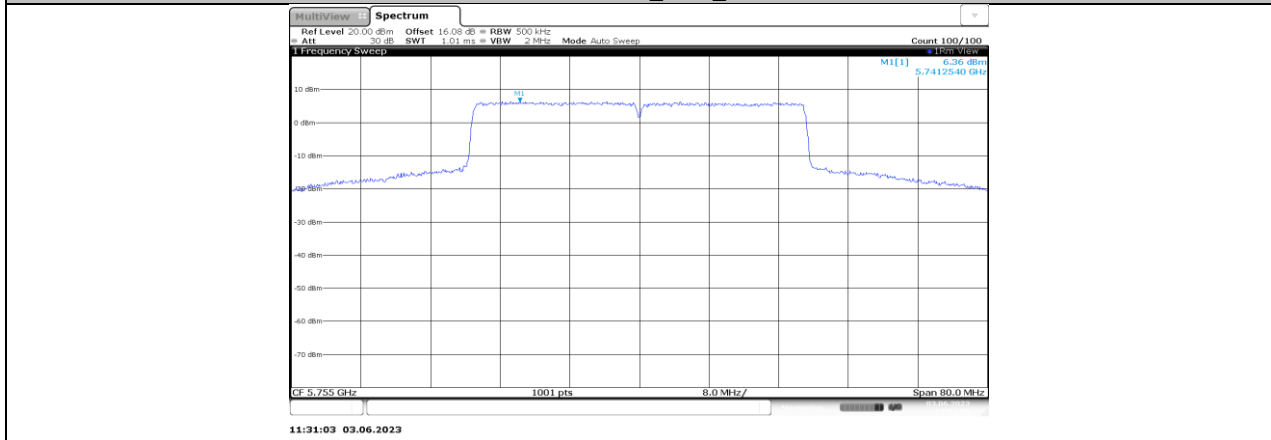




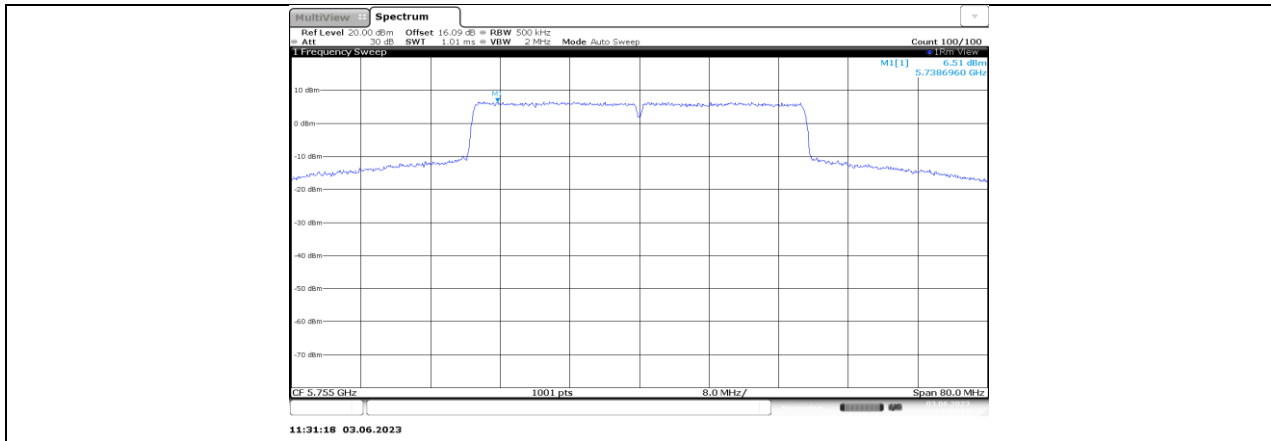
11AX40MIMO_Ant2_5230



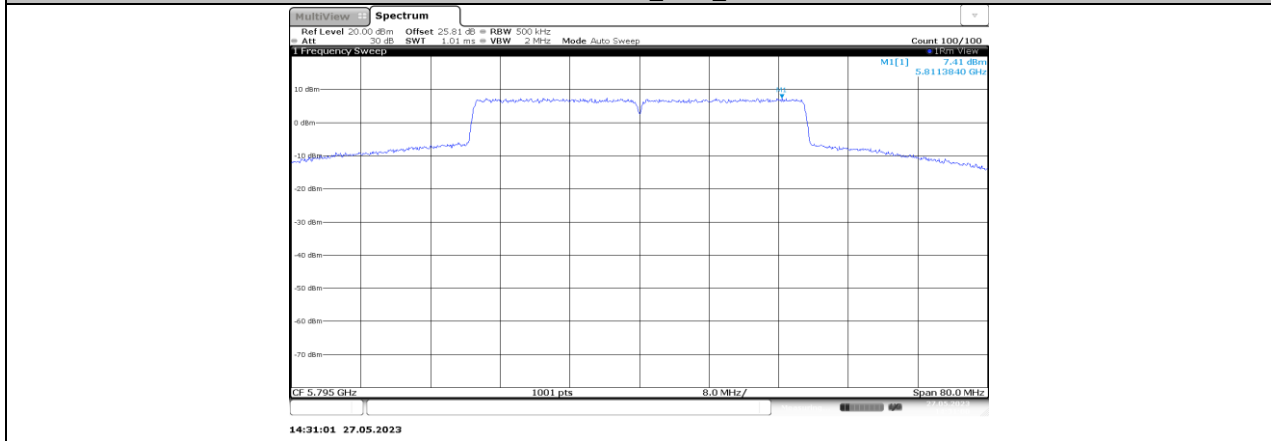
11AX40MIMO_Ant4_5230



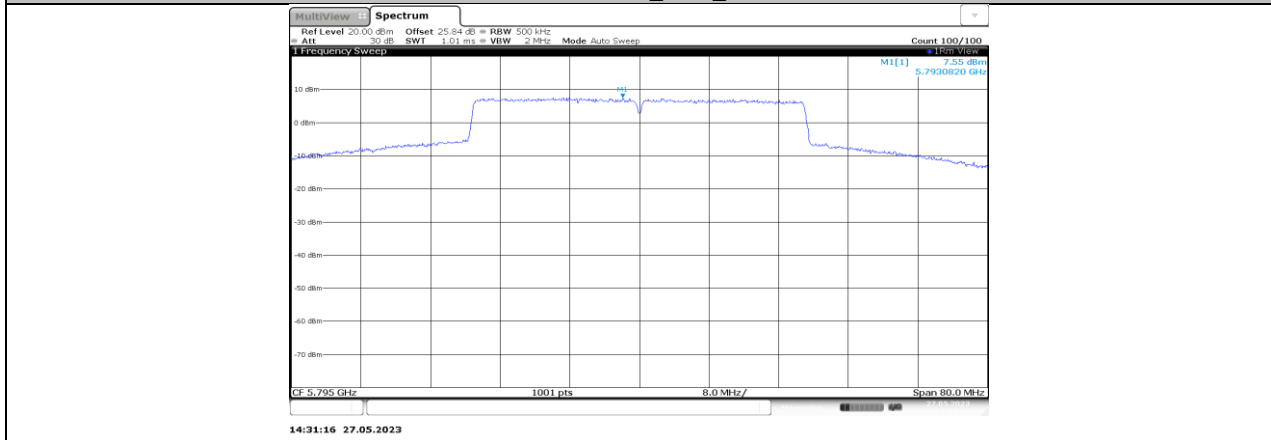
11AX40MIMO_Ant2_5755



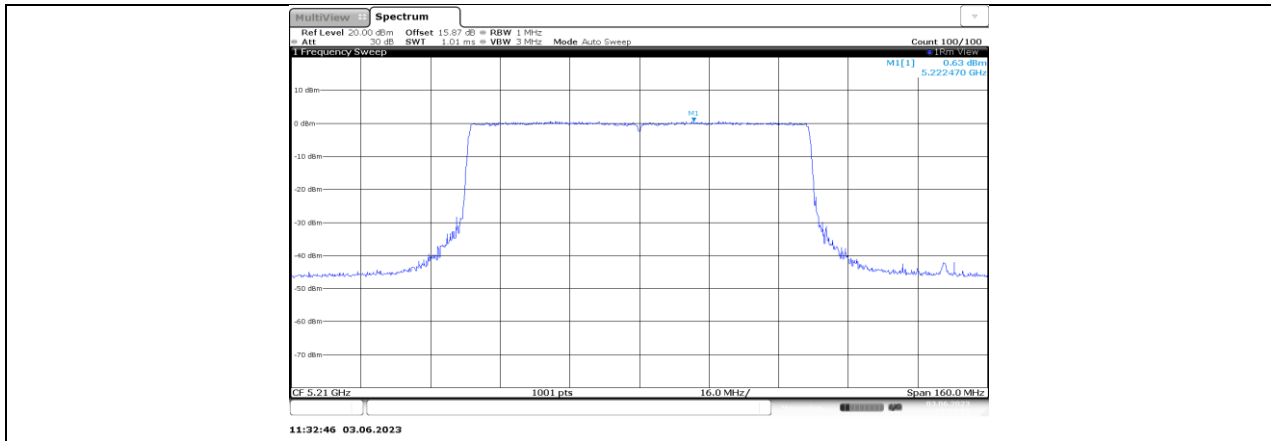
11AX40MIMO_Ant4_5755



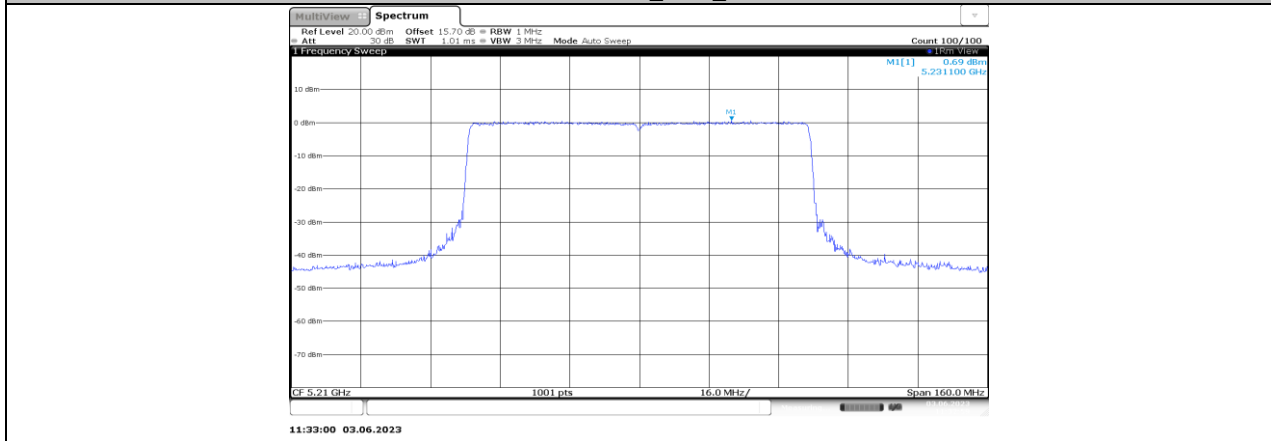
11AX40MIMO_Ant2_5795



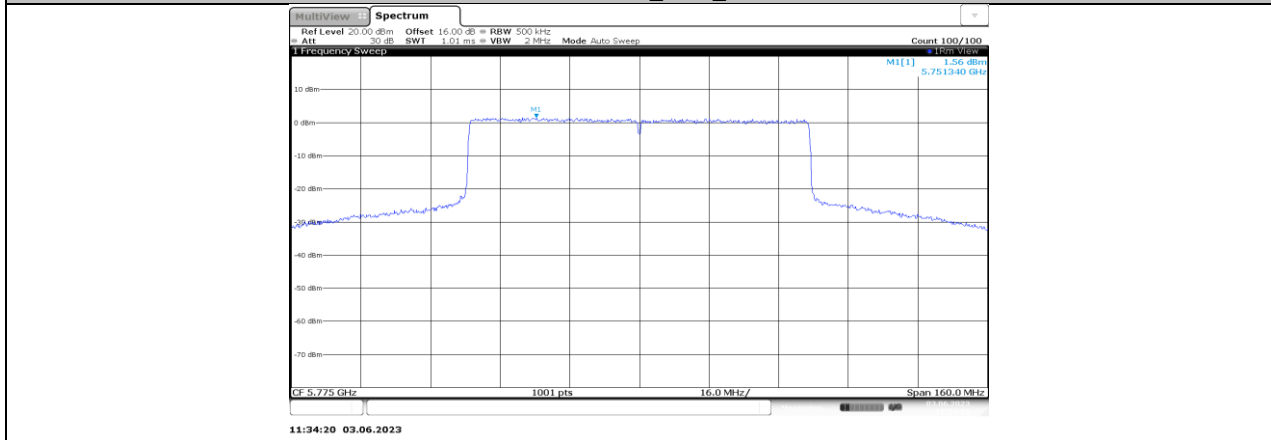
11AX40MIMO_Ant4_5795



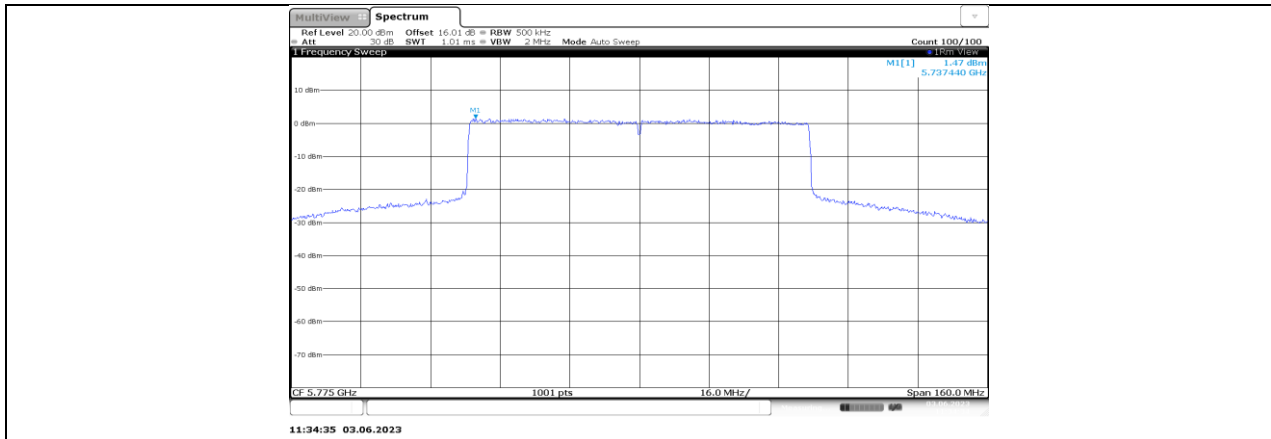
11AX80MIMO_Ant2_5210



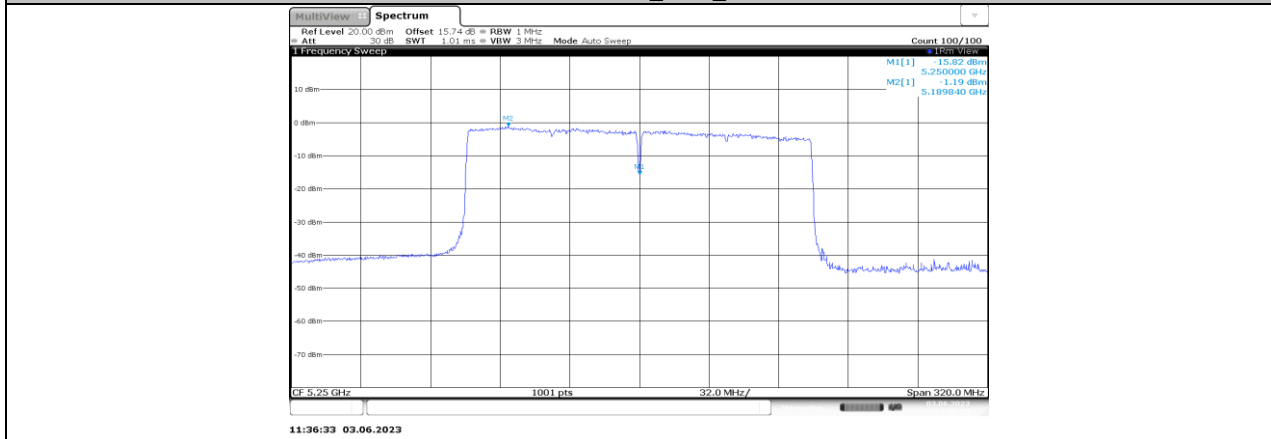
11AX80MIMO_Ant4_5210



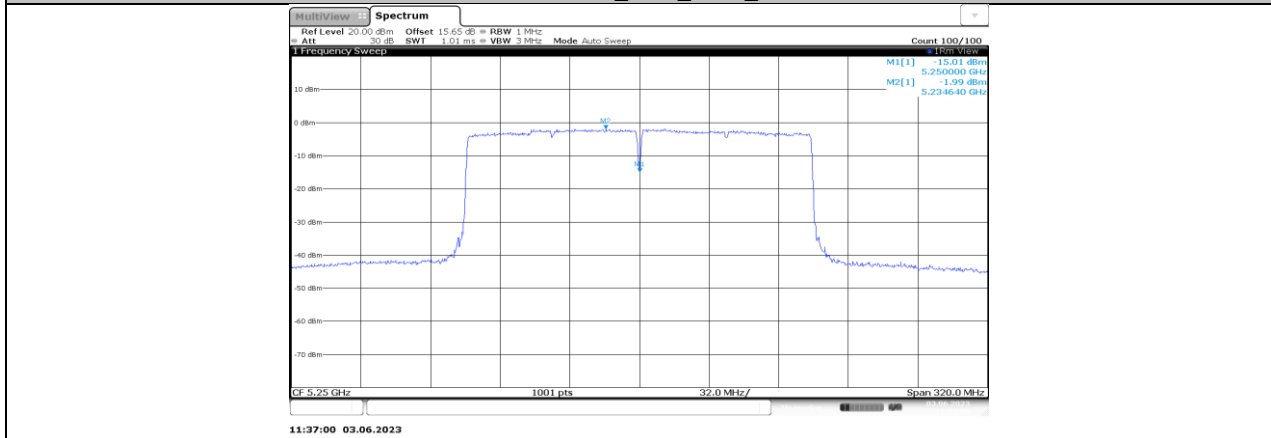
11AX80MIMO_Ant2_5775



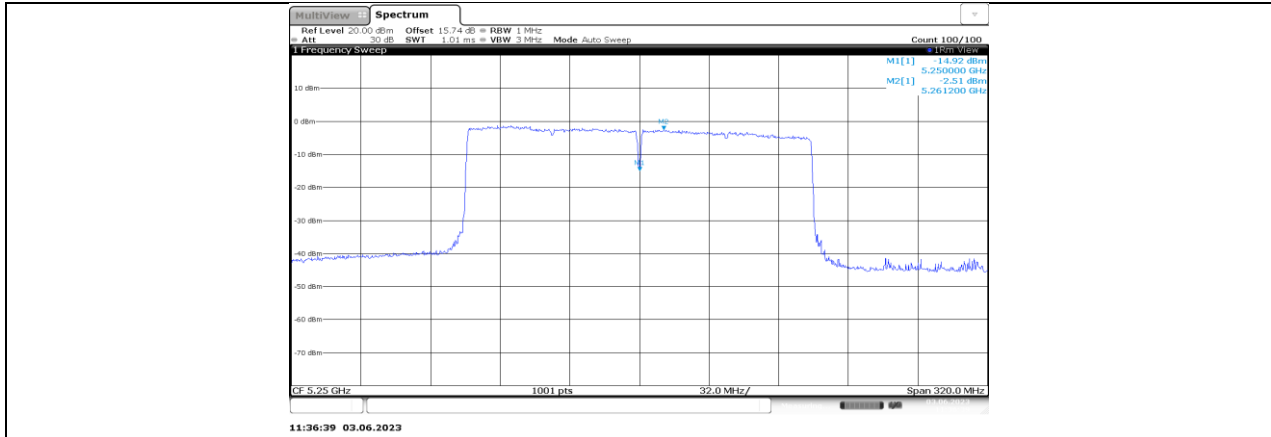
11AX80MIMO_Ant4_5775



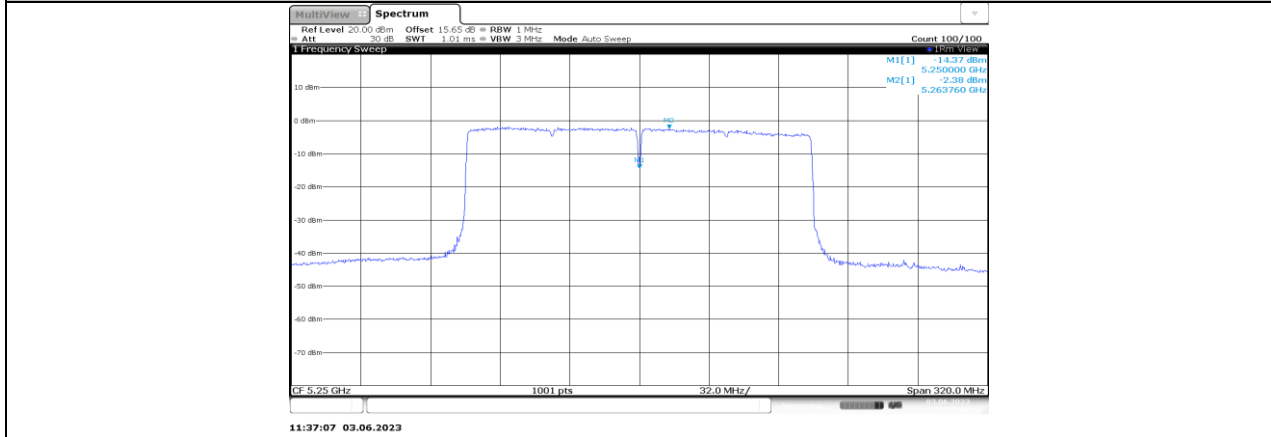
11AX160MIMO_Ant2_5250_UNII-1



11AX160MIMO_Ant4_5250_UNII-1



11AX160MIMO_Ant2_5250_UNII-2A



11AX160MIMO_Ant4_5250_UNII-2A

11.5.3. Test Result For ISED

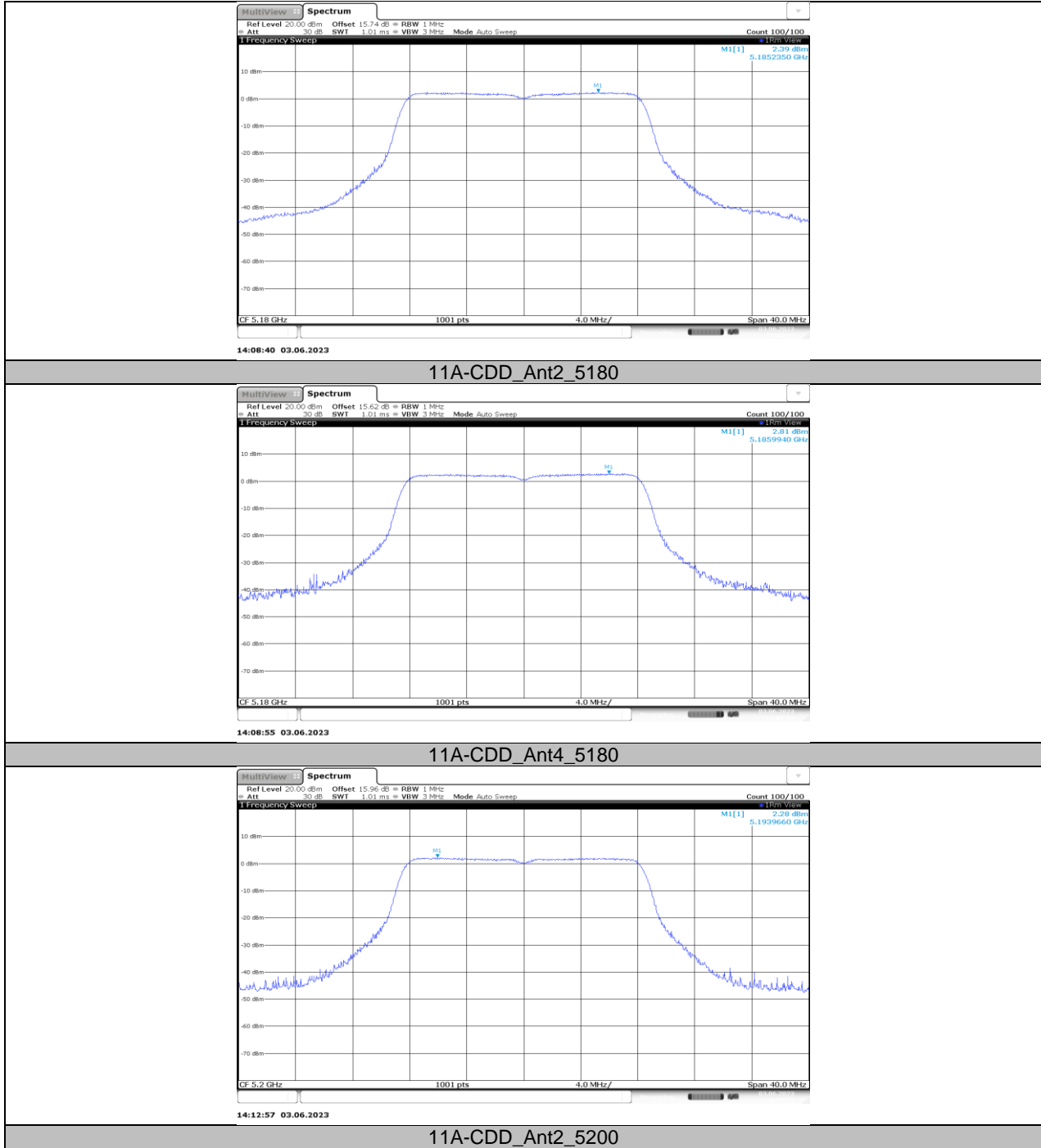
Test Mode	Antenna	Channel	Power [dBm/MHz]	EIRP [dBm/MHz]	Limit [dBm/MHz]	Verdict
11A-CDD	Ant2	5180	2.39	3.39	≤10.00	PASS
	Ant4	5180	2.81	3.81	≤10.00	PASS
	total	5180	5.62	9.63	≤10.00	PASS
	Ant2	5200	2.28	3.28	≤10.00	PASS
	Ant4	5200	3.09	4.09	≤10.00	PASS
	total	5200	5.71	9.72	≤10.00	PASS
	Ant2	5240	2.65	3.65	≤10.00	PASS
	Ant4	5240	2.74	3.74	≤10.00	PASS
	total	5240	5.71	9.72	≤10.00	PASS
	Ant2	5745	12.14	13.14	≤30.00	PASS
	Ant4	5745	11.89	12.89	≤30.00	PASS
	total	5745	15.03	19.04	≤30.00	PASS
	Ant2	5785	11.1	12.10	≤30.00	PASS
	Ant4	5785	11.29	12.29	≤30.00	PASS
	total	5785	14.21	18.22	≤30.00	PASS
	Ant2	5825	11.25	12.25	≤30.00	PASS
	Ant4	5825	10.56	11.56	≤30.00	PASS
	total	5825	13.93	17.94	≤30.00	PASS
11AC20MIMO	Ant2	5180	2.22	3.22	≤10.00	PASS
	Ant4	5180	2.14	3.14	≤10.00	PASS
	total	5180	5.19	9.20	≤10.00	PASS
	Ant2	5200	1.91	2.91	≤10.00	PASS
	Ant4	5200	2.93	3.93	≤10.00	PASS
	total	5200	5.46	9.47	≤10.00	PASS
	Ant2	5240	2.23	3.23	≤10.00	PASS
	Ant4	5240	1.88	2.88	≤10.00	PASS
	total	5240	5.07	9.08	≤10.00	PASS
	Ant2	5745	8.51	9.51	≤30.00	PASS
	Ant4	5745	8.94	9.94	≤30.00	PASS
	total	5745	11.74	15.75	≤30.00	PASS
	Ant2	5785	7.84	8.84	≤30.00	PASS
	Ant4	5785	8.34	9.34	≤30.00	PASS
	total	5785	11.11	15.12	≤30.00	PASS
	Ant2	5825	8.25	9.25	≤30.00	PASS
	Ant4	5825	9.05	10.05	≤30.00	PASS
	total	5825	11.68	15.69	≤30.00	PASS
11AC40MIMO	Ant2	5190	2.35	3.35	≤10.00	PASS
	Ant4	5190	2.28	3.28	≤10.00	PASS
	total	5190	5.33	9.34	≤10.00	PASS
	Ant2	5230	2.14	3.14	≤10.00	PASS
	Ant4	5230	2.05	3.05	≤10.00	PASS
	total	5230	5.11	9.12	≤10.00	PASS
	Ant2	5755	5.65	6.65	≤30.00	PASS
	Ant4	5755	6.18	7.18	≤30.00	PASS
	total	5755	8.93	12.94	≤30.00	PASS
	Ant2	5795	5.85	6.85	≤30.00	PASS
	Ant4	5795	6.05	7.05	≤30.00	PASS
	total	5795	8.96	12.97	≤30.00	PASS
11AC80MIMO	Ant2	5210	-1.7	-0.70	≤10.00	PASS
	Ant4	5210	-2.24	-1.24	≤10.00	PASS
	total	5210	1.05	5.06	≤10.00	PASS
	Ant2	5775	1.04	2.04	≤30.00	PASS
	total	5775	4.29	8.30	≤30.00	PASS
11AC160MIMO	Ant2	5250_UNII-1	-2.07	-1.07	≤10.00	PASS
	Ant4	5250_UNII-1	-2.91	-1.91	≤10.00	PASS
	total	5250_UNII-1	0.54	4.55	≤10.00	PASS

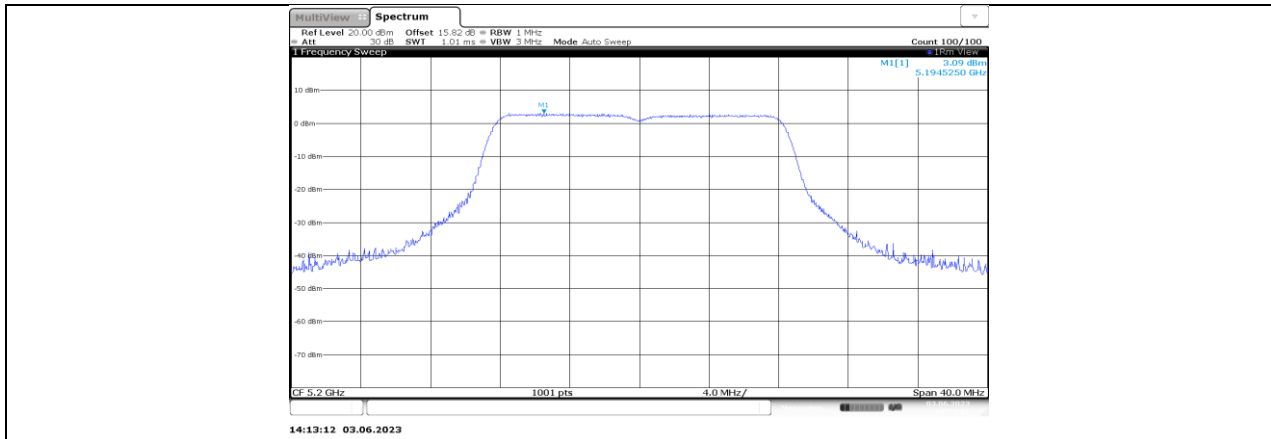
	Ant2	5250_UNII-2A	-3	-2.00	≤30.00	PASS
	Ant4	5250_UNII-2A	-3.41	-2.41	≤30.00	PASS
	total	5250_UNII-2A	-0.19	3.82	≤30.00	PASS
11AX20MIMO	Ant2	5180	2.67	3.67	≤10.00	PASS
	Ant4	5180	2.38	3.38	≤10.00	PASS
	total	5180	5.54	9.55	≤10.00	PASS
	Ant2	5200	2.27	3.27	≤10.00	PASS
	Ant4	5200	3.26	4.26	≤10.00	PASS
	total	5200	5.80	9.81	≤10.00	PASS
	Ant2	5240	2.65	3.65	≤10.00	PASS
	Ant4	5240	1.96	2.96	≤10.00	PASS
	total	5240	5.33	9.34	≤10.00	PASS
	Ant2	5745	11.63	12.63	≤30.00	PASS
	Ant4	5745	11.07	12.07	≤30.00	PASS
	total	5745	14.37	15.37	≤30.00	PASS
	Ant2	5785	10.45	11.45	≤30.00	PASS
	Ant4	5785	10.66	11.66	≤30.00	PASS
	total	5785	13.57	14.57	≤30.00	PASS
	Ant2	5825	10.63	11.63	≤30.00	PASS
	Ant4	5825	9.93	10.93	≤30.00	PASS
	total	5825	13.30	14.30	≤30.00	PASS
11AX40MIMO	Ant2	5190	2.72	3.72	≤10.00	PASS
	Ant4	5190	2.5	3.50	≤10.00	PASS
	total	5190	5.62	9.63	≤10.00	PASS
	Ant2	5230	2.28	3.28	≤10.00	PASS
	Ant4	5230	2.06	3.06	≤10.00	PASS
	total	5230	5.18	9.19	≤10.00	PASS
	Ant2	5755	6.36	7.36	≤30.00	PASS
	Ant4	5755	6.51	7.51	≤30.00	PASS
	total	5755	9.45	10.45	≤30.00	PASS
	Ant2	5795	7.41	8.41	≤30.00	PASS
	Ant4	5795	7.55	8.55	≤30.00	PASS
	total	5795	10.49	11.49	≤30.00	PASS
11AX80MIMO	Ant2	5210	0.63	1.63	≤10.00	PASS
	Ant4	5210	0.69	1.69	≤10.00	PASS
	total	5210	3.67	4.67	≤10.00	PASS
	Ant2	5775	1.56	2.56	≤30.00	PASS
	Ant4	5775	1.47	2.47	≤30.00	PASS
	total	5775	4.53	5.53	≤30.00	PASS
11AX160MIMO	Ant2	5250_UNII-1	-1.19	-0.19	≤10.00	PASS
	Ant4	5250_UNII-1	-1.99	-0.99	≤10.00	PASS
	total	5250_UNII-1	1.44	2.44	≤10.00	PASS
	Ant2	5250_UNII-2A	-2.51	-1.51	≤30.00	PASS
	Ant4	5250_UNII-2A	-2.38	-1.38	≤30.00	PASS
	total	5250_UNII-2A	0.57	1.57	≤30.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

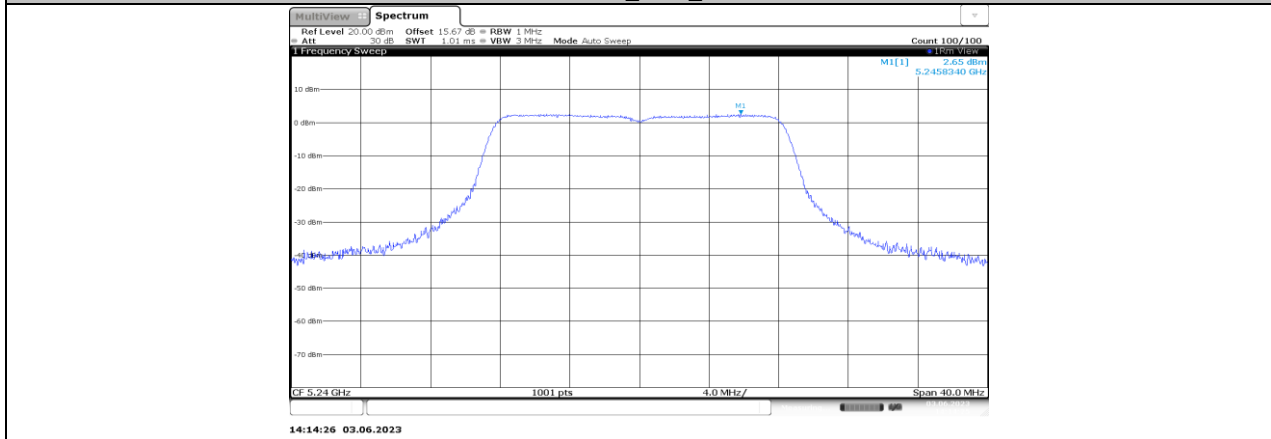
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

11.5.4. Test Graphs For ISED

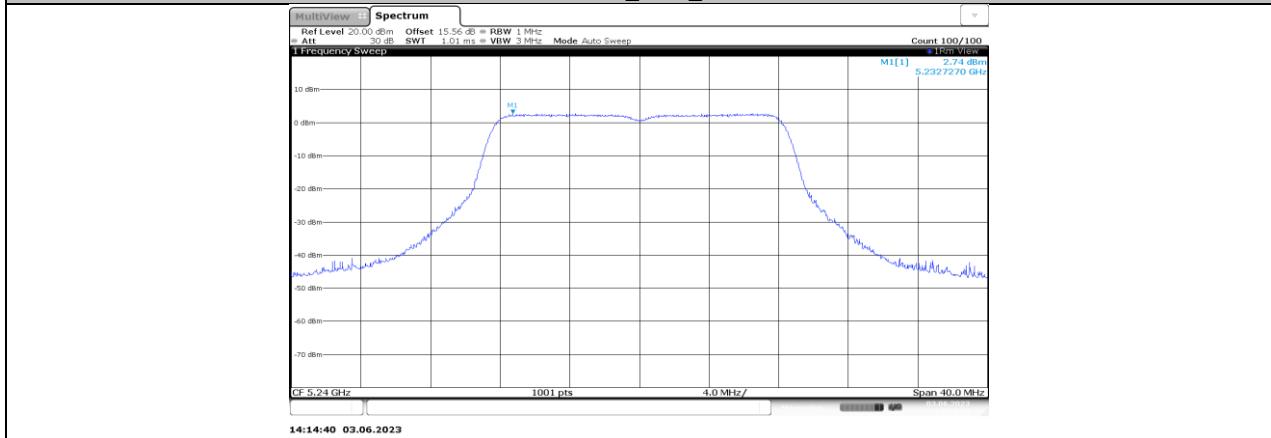




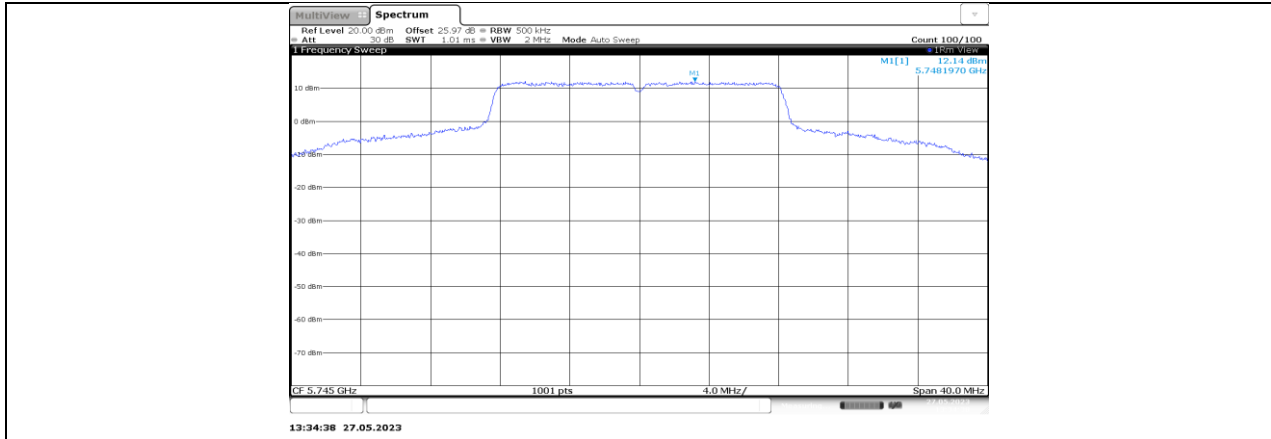
11A-CDD_Ant4_5200



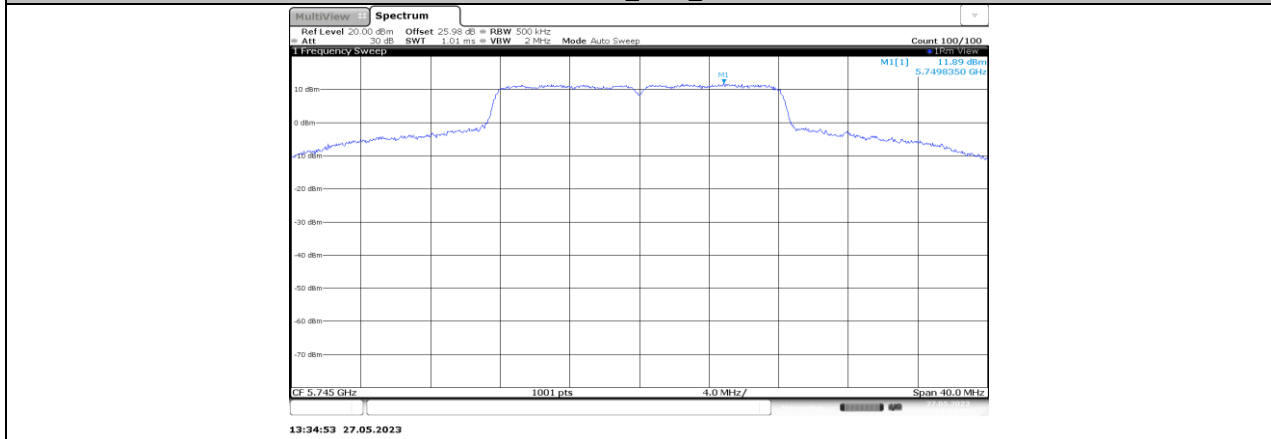
11A-CDD_Ant2_5240



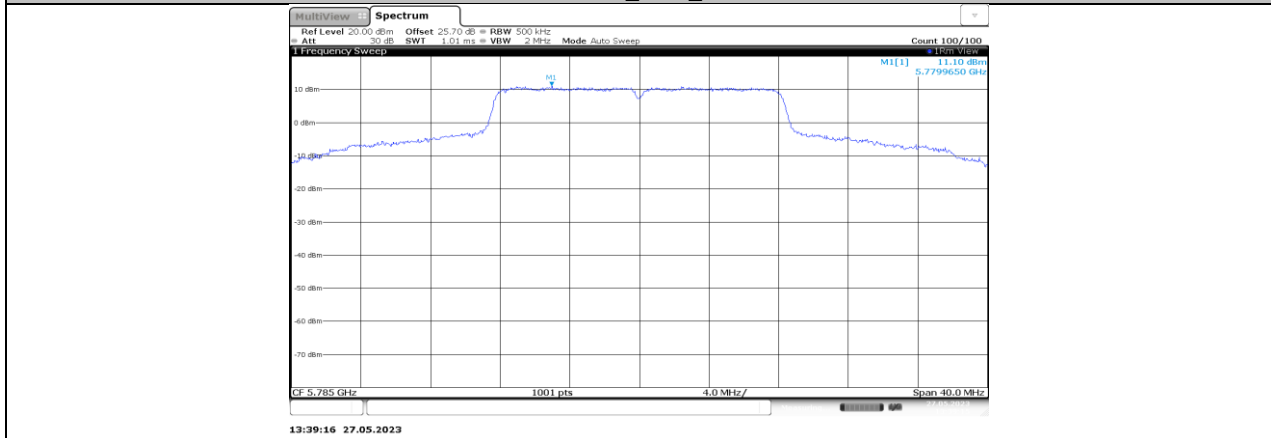
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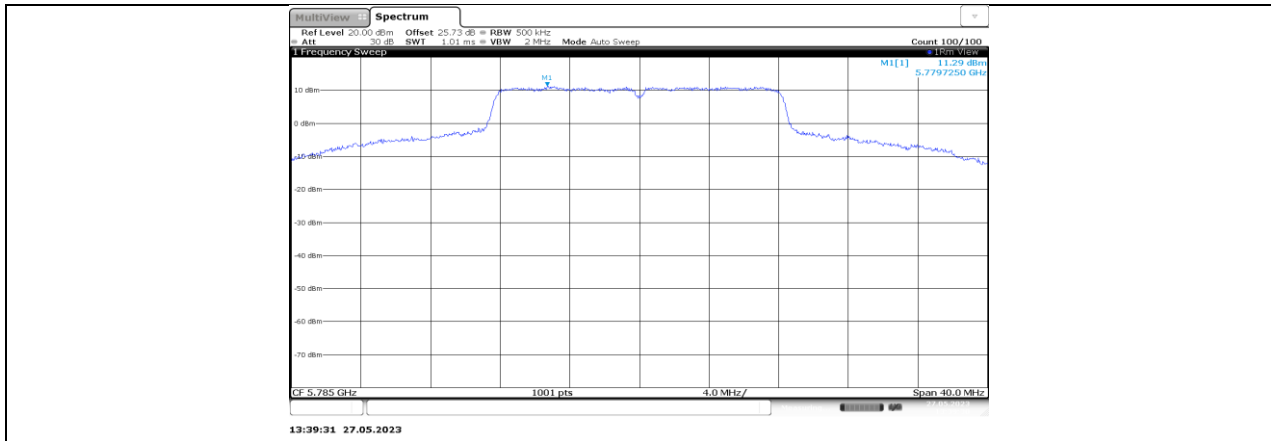
11A-CDD_Ant2_5745



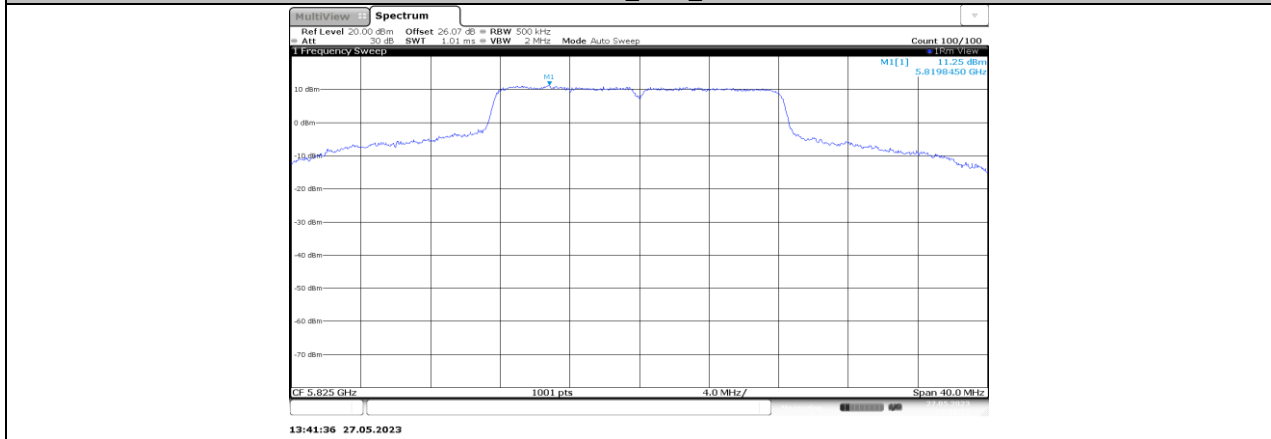
11A-CDD_Ant4_5745



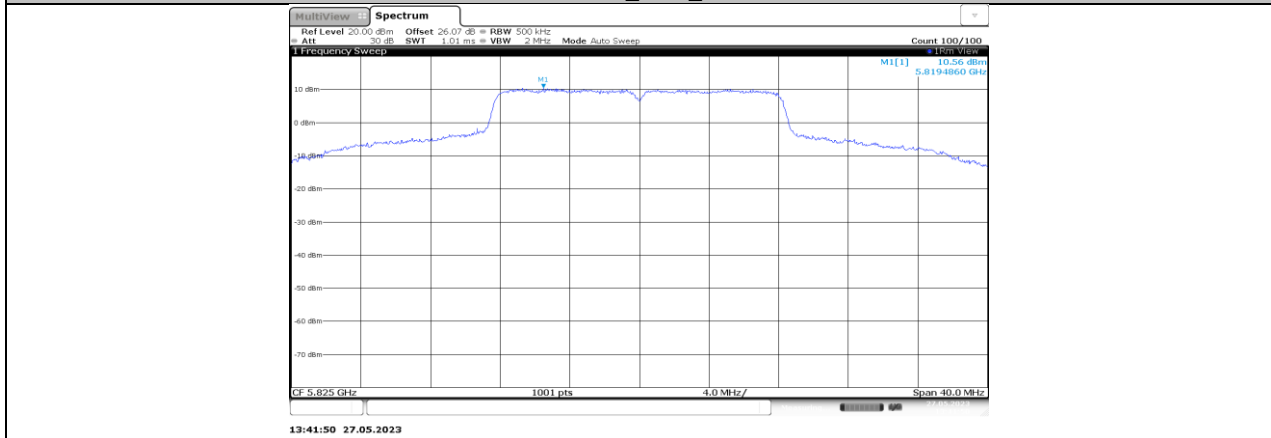
11A-CDD_Ant2_5785



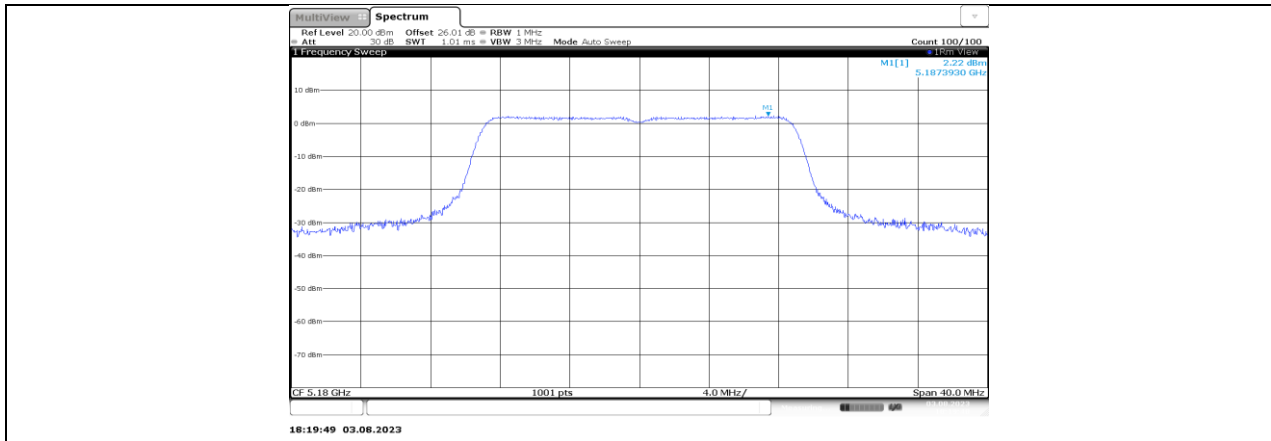
11A-CDD_Ant4_5785



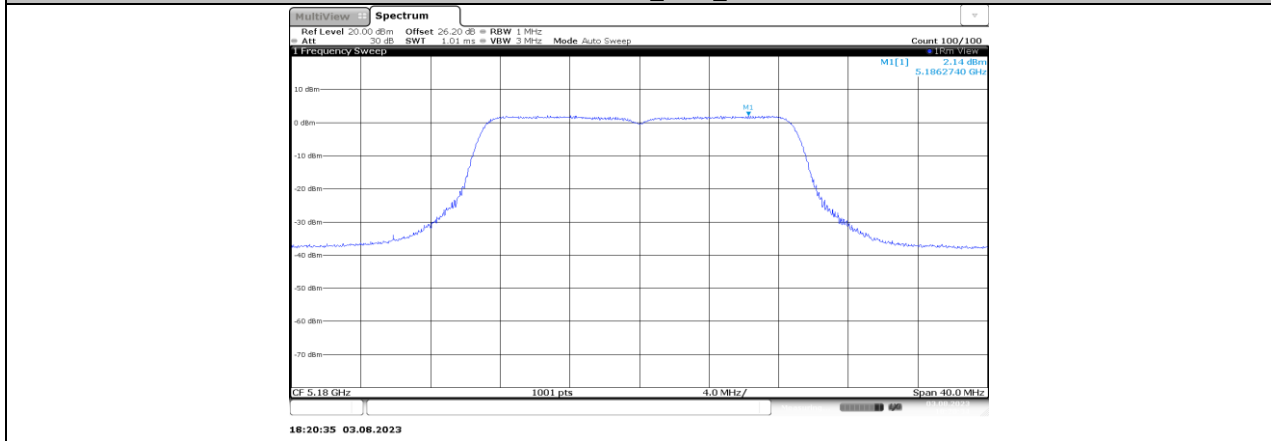
11A-CDD_Ant2_5825



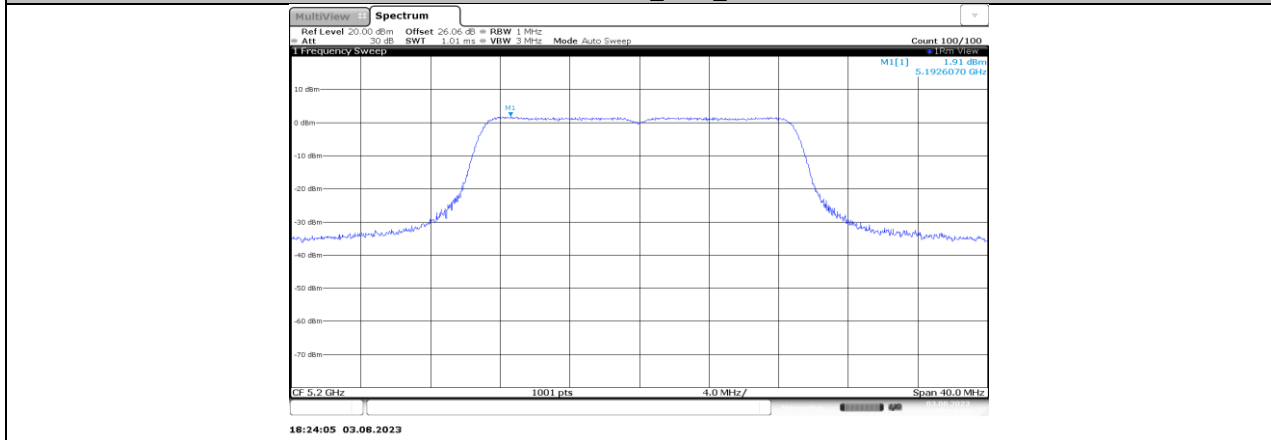
11A-CDD_Ant4_5825



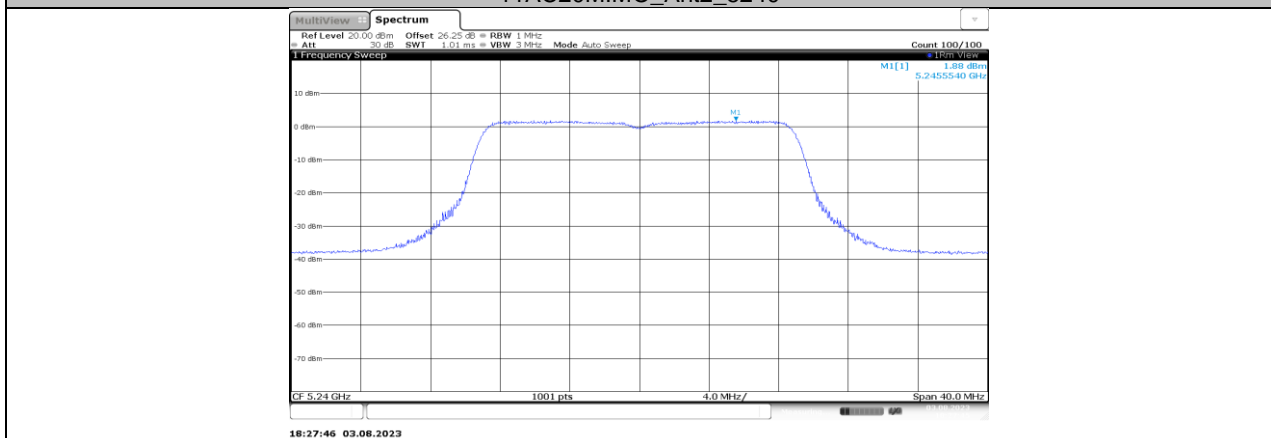
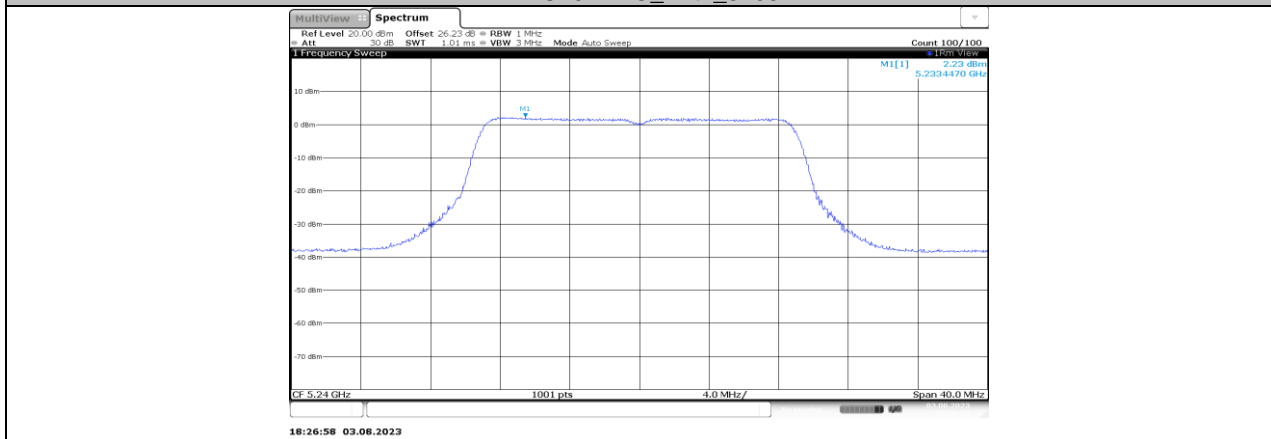
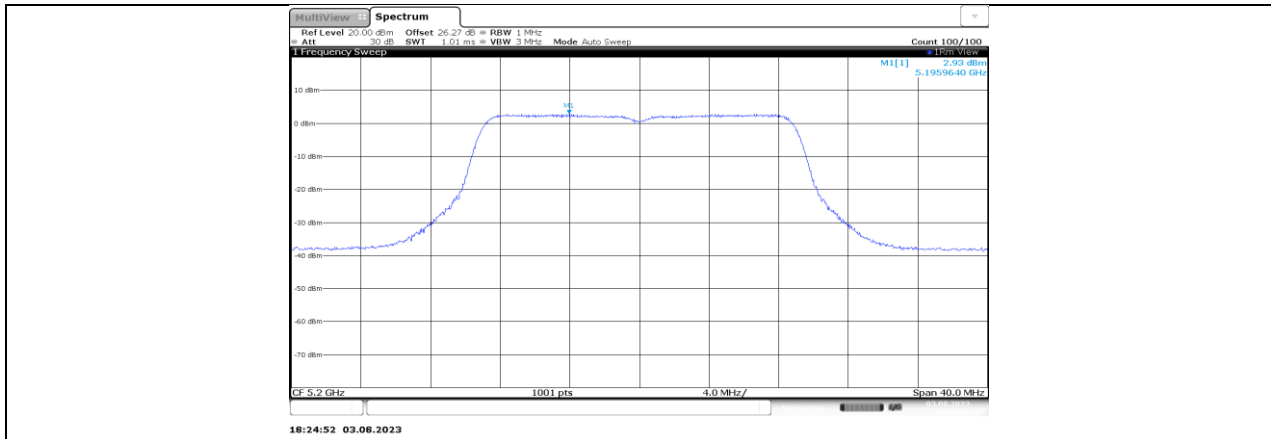
11AC20MIMO_Ant2_5180

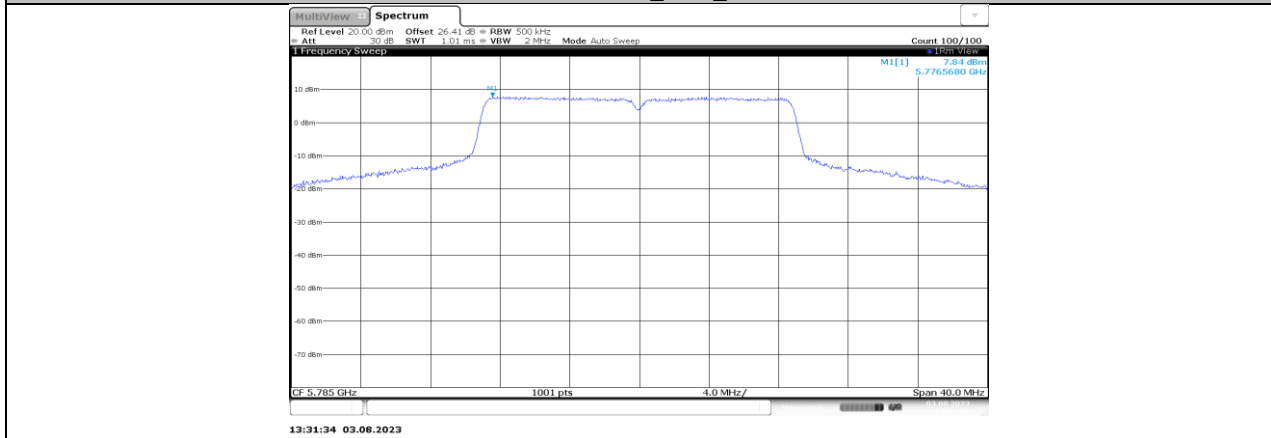
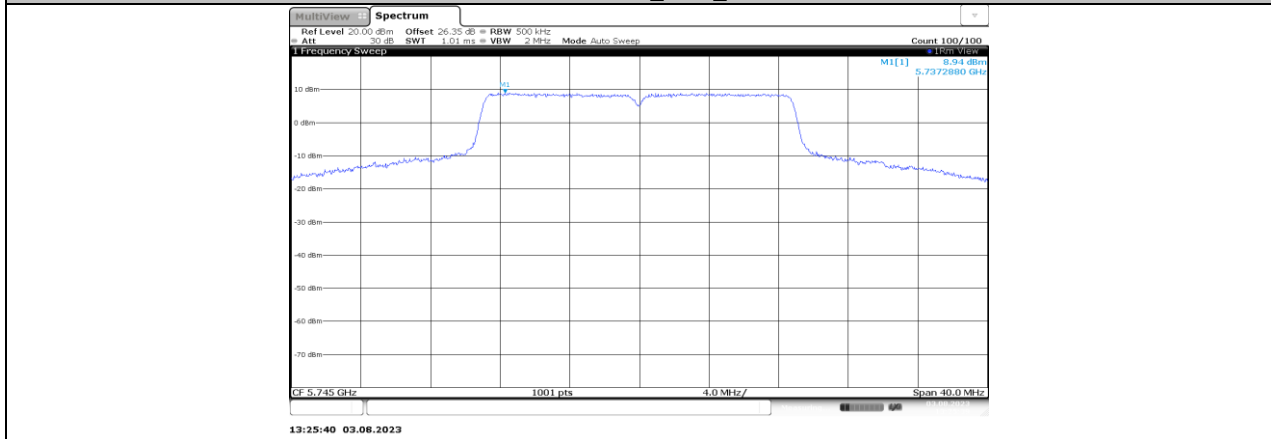
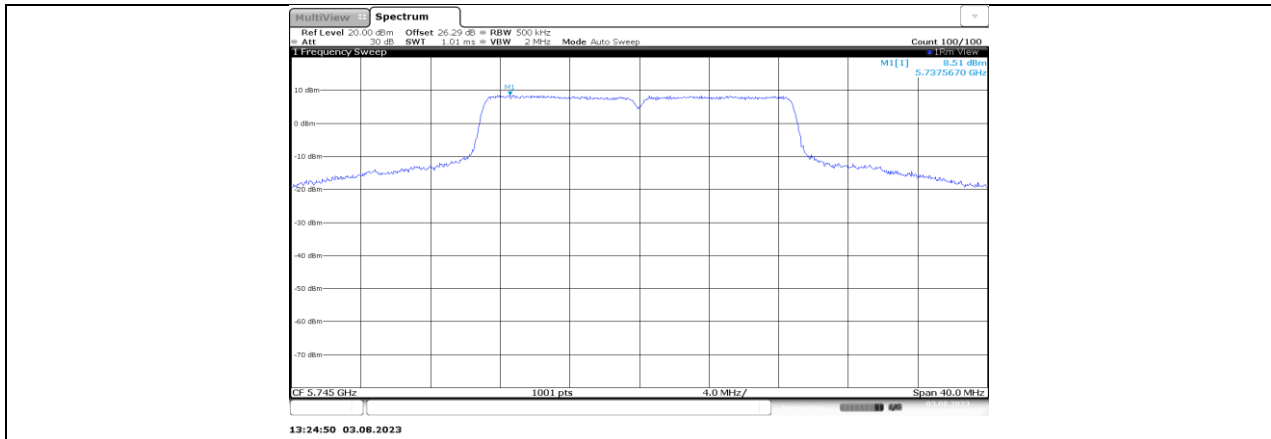


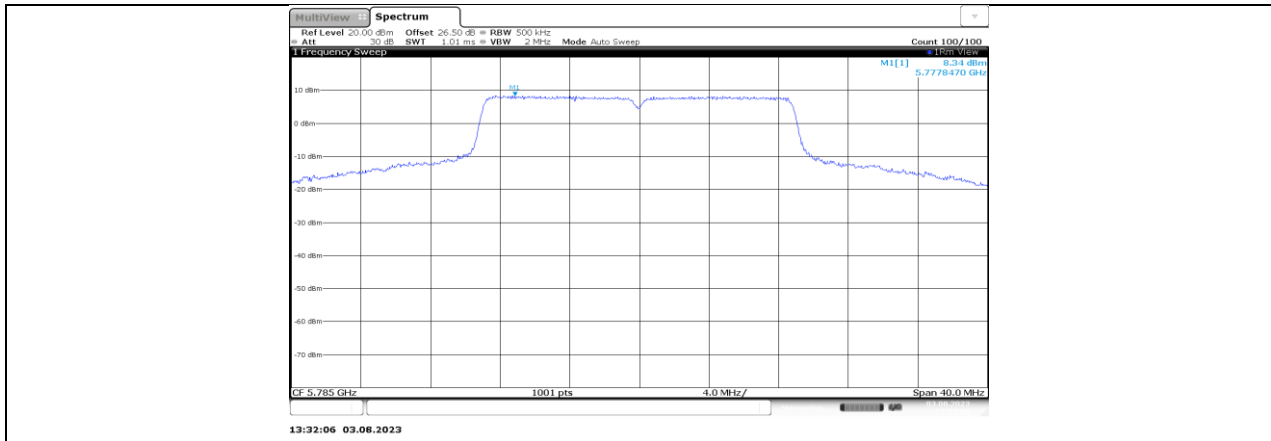
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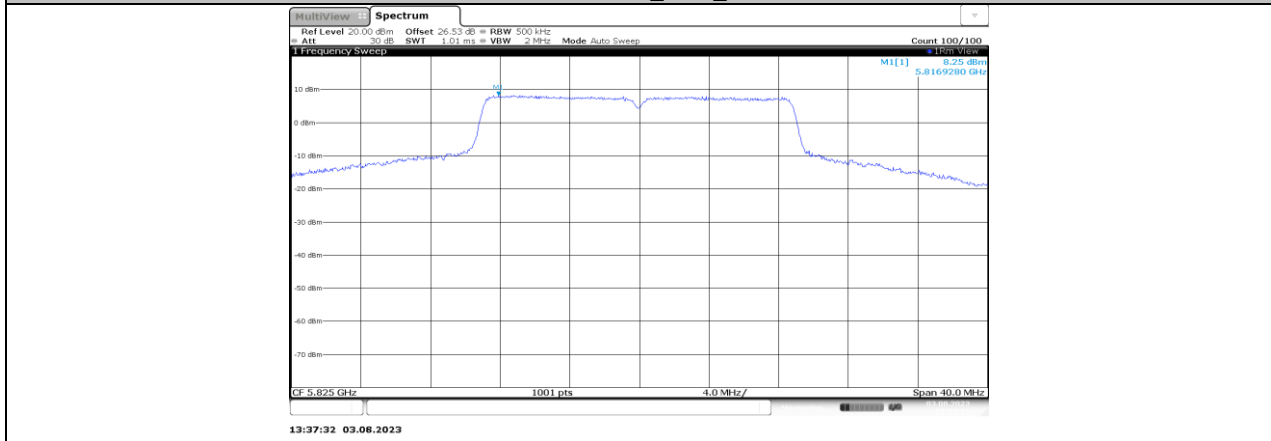
11AC20MIMO_Ant2_5200



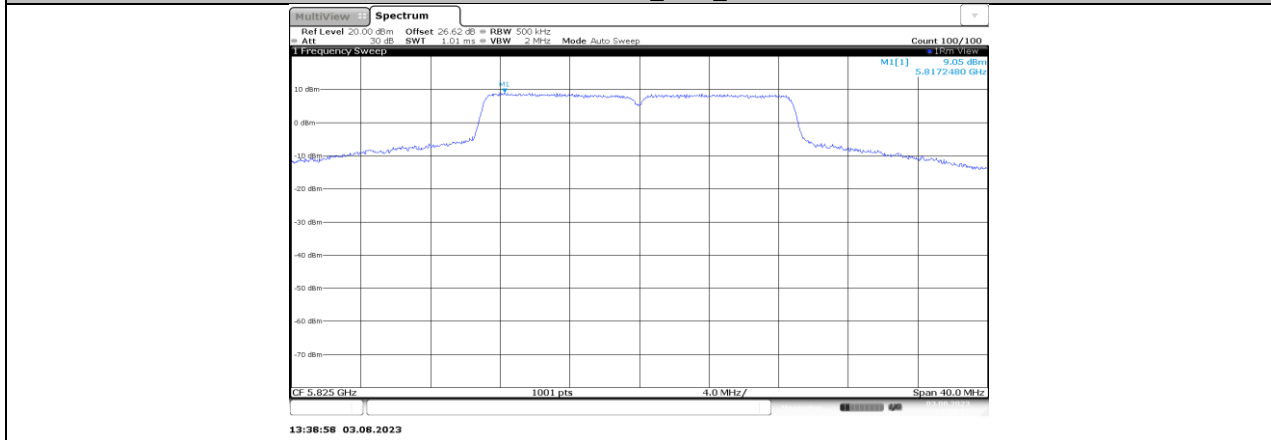




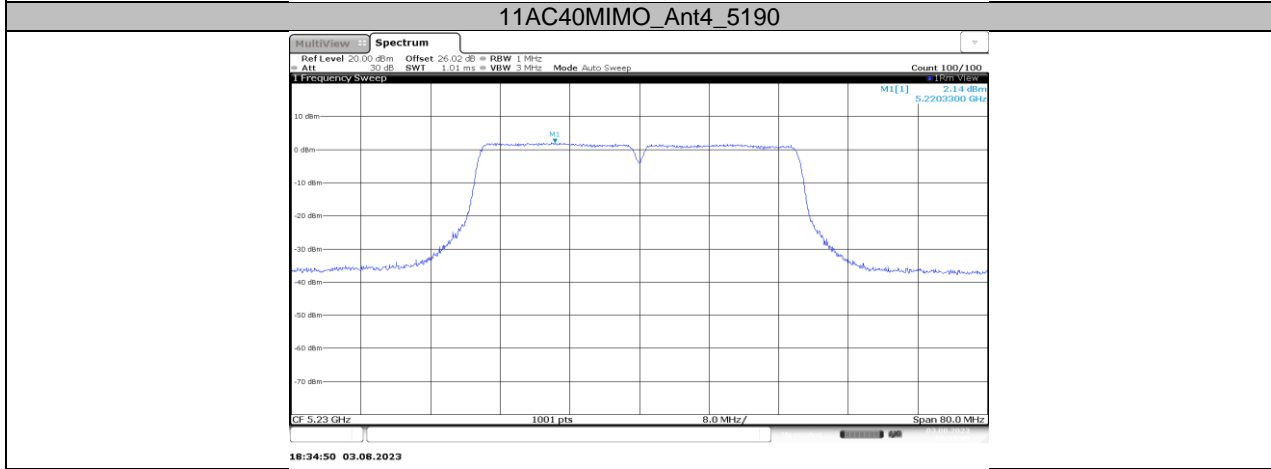
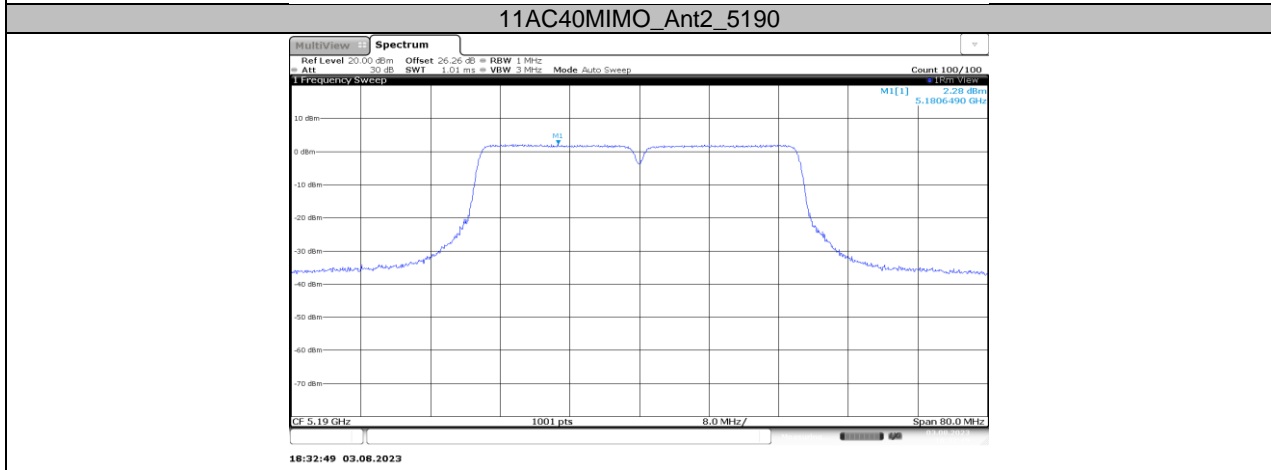
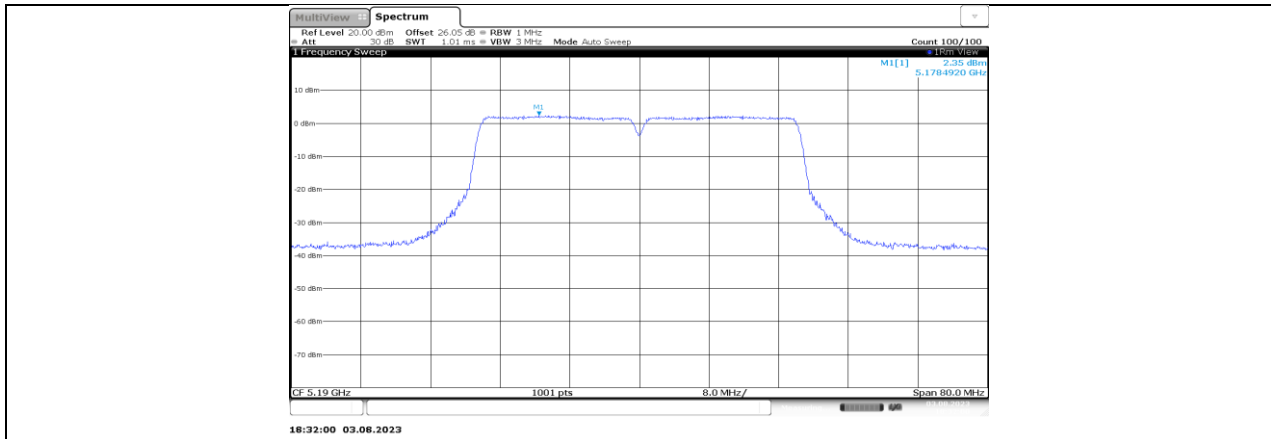
11AC20MIMO_Ant4_5785

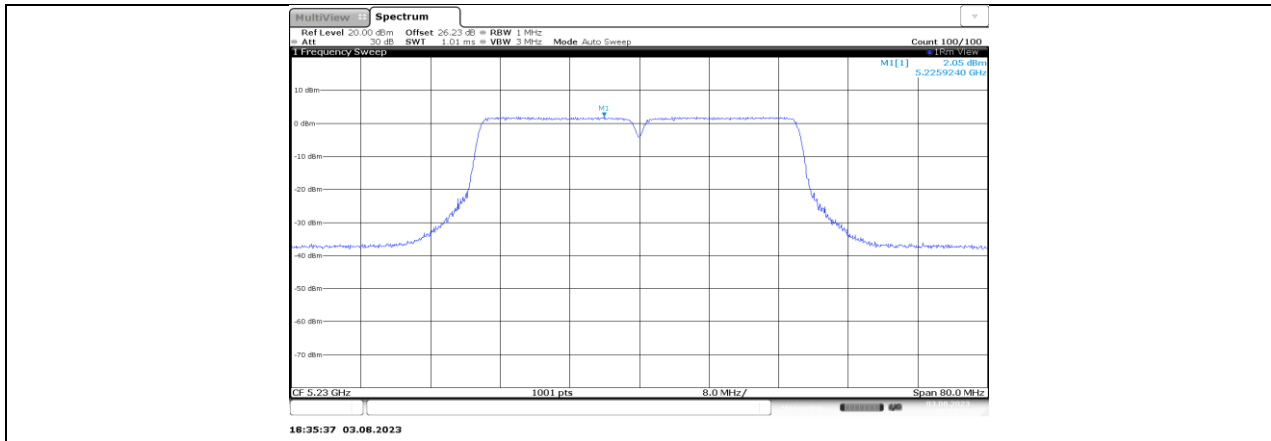


11AC20MIMO_Ant2_5825

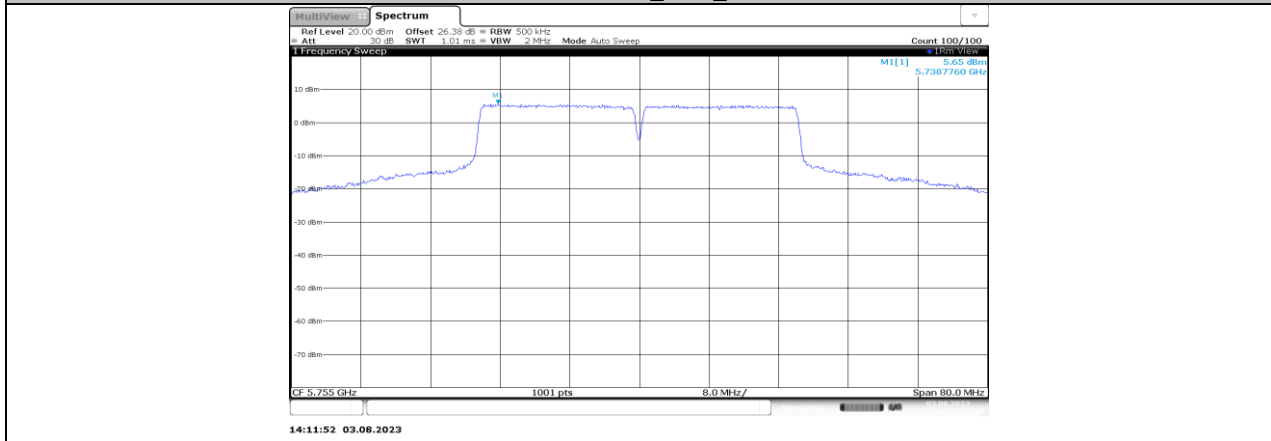


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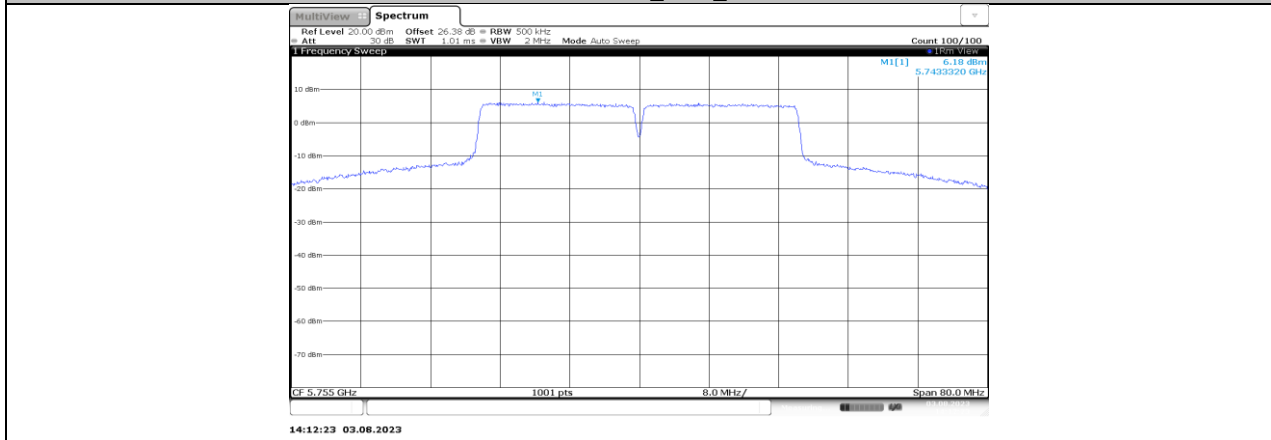




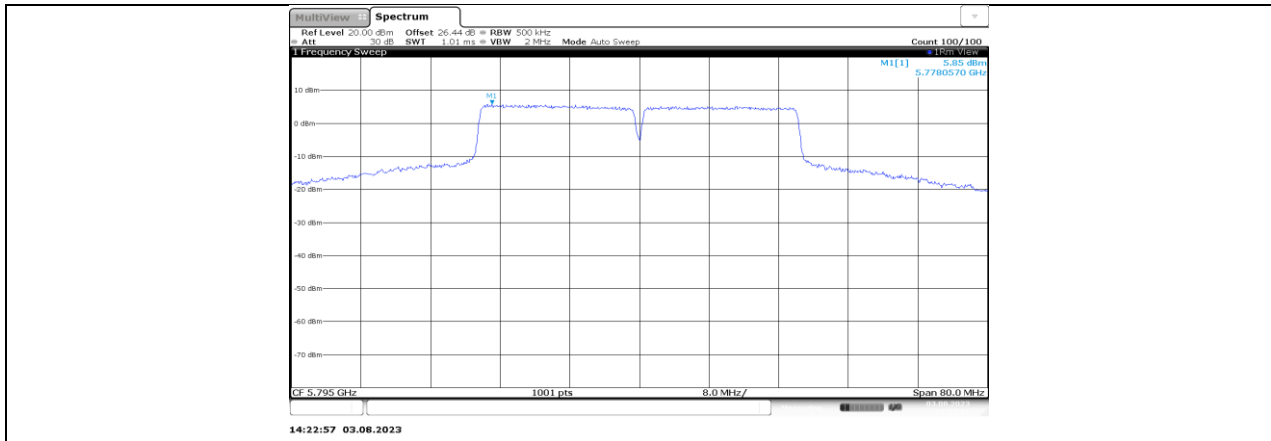
11AC40MIMO_Ant4_5230



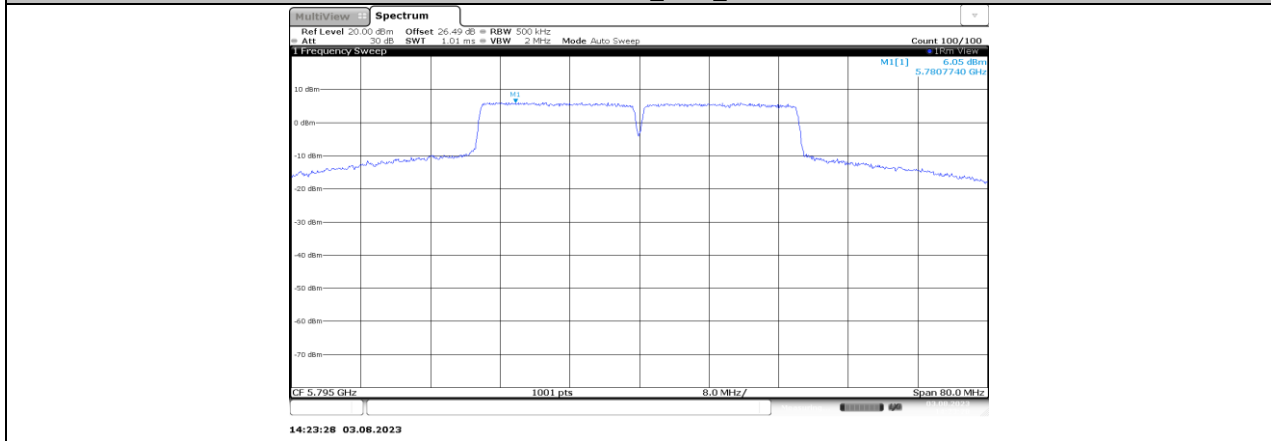
11AC40MIMO_Ant2_5755



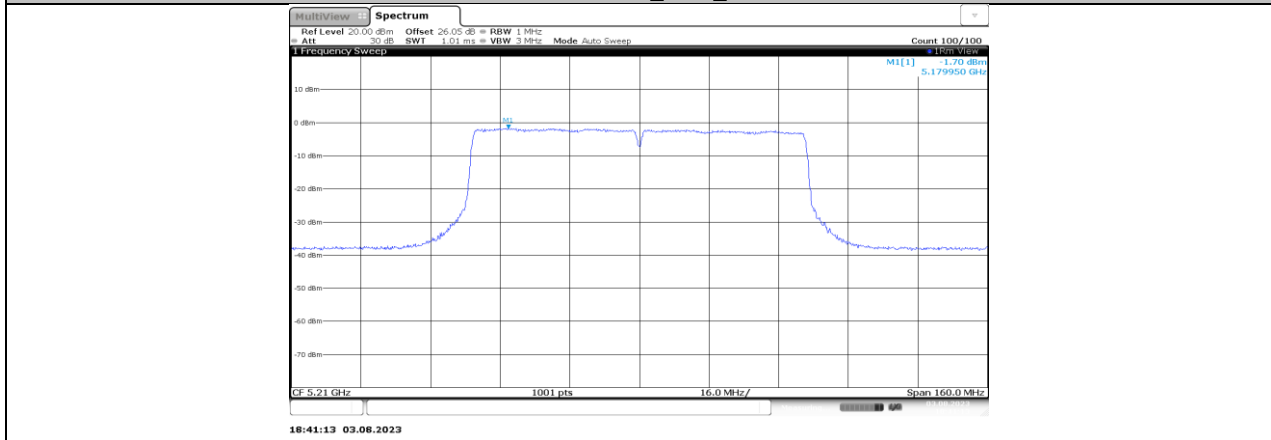
11AC40MIMO_Ant4_5755



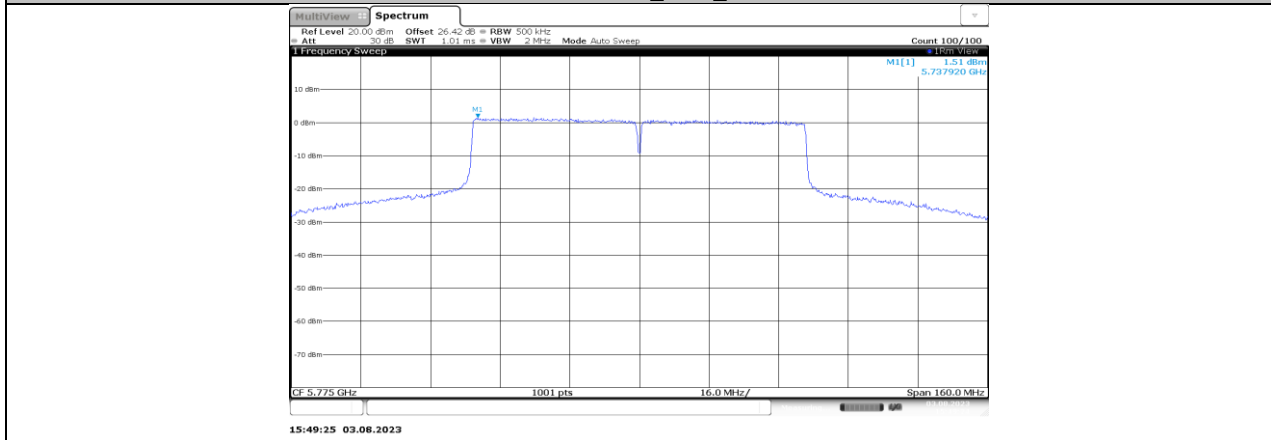
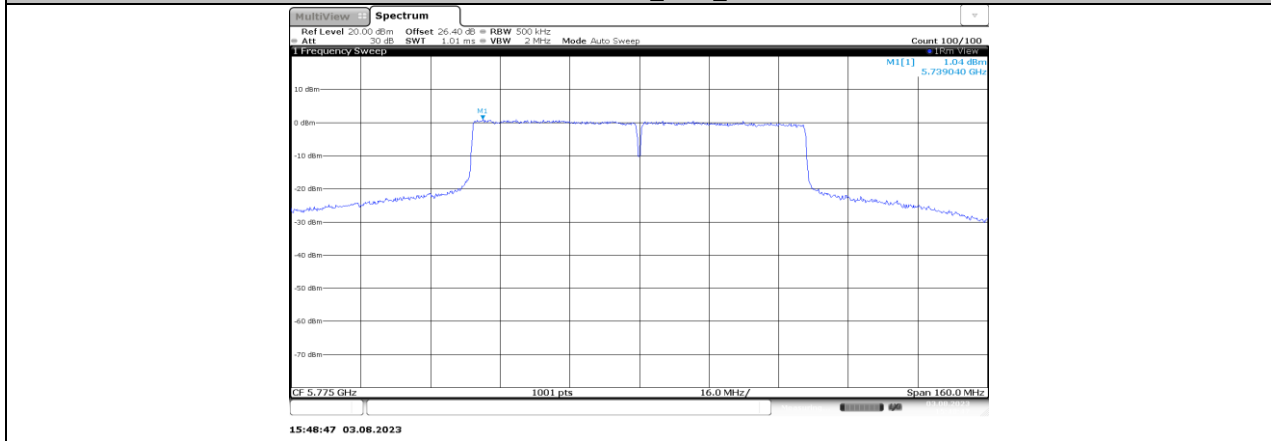
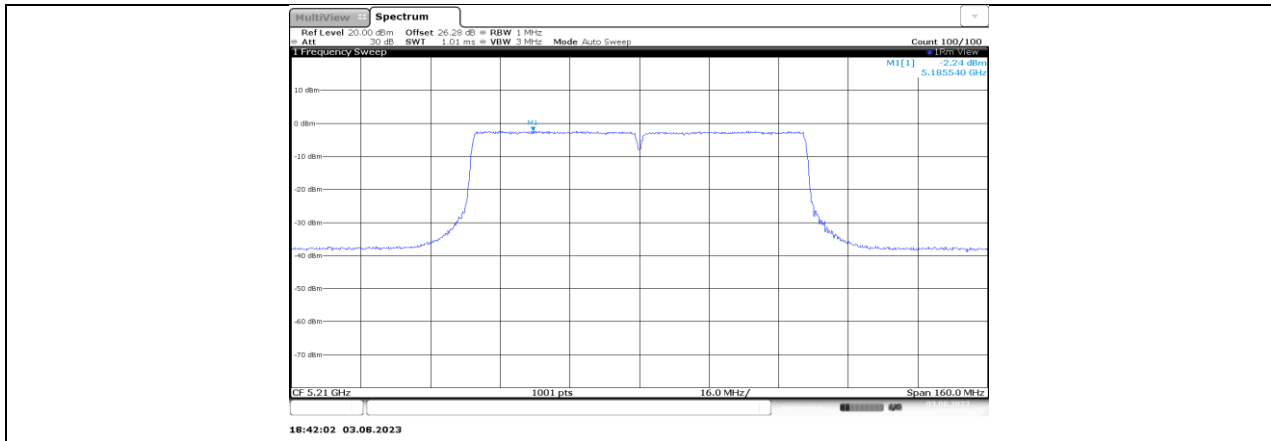
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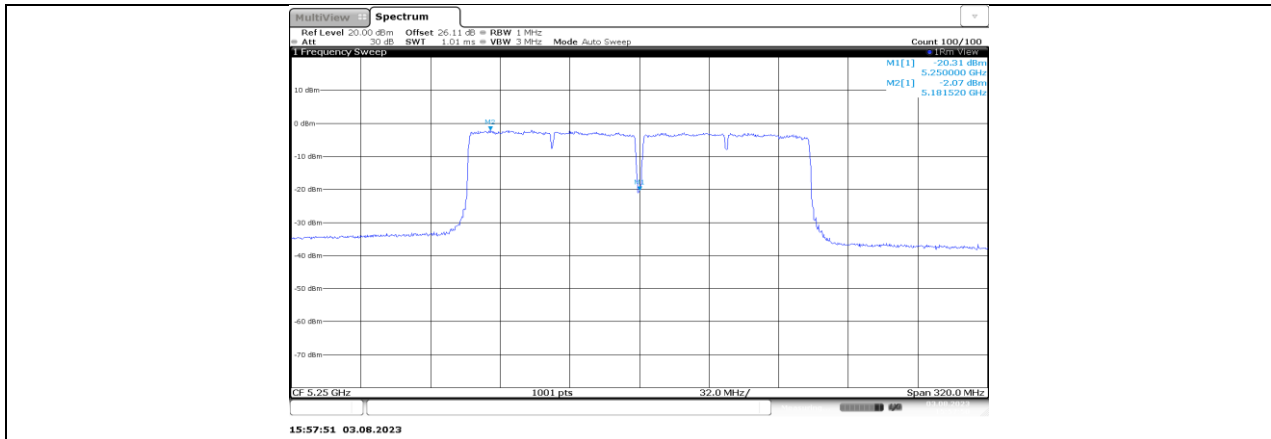


11AC40MIMO_Ant4_5795

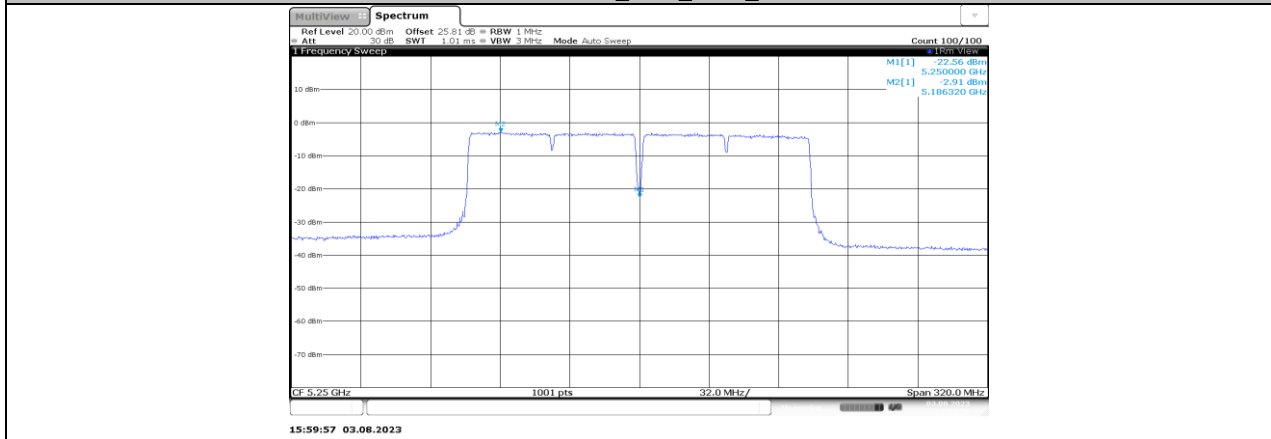


11AC80MIMO_Ant2_5210

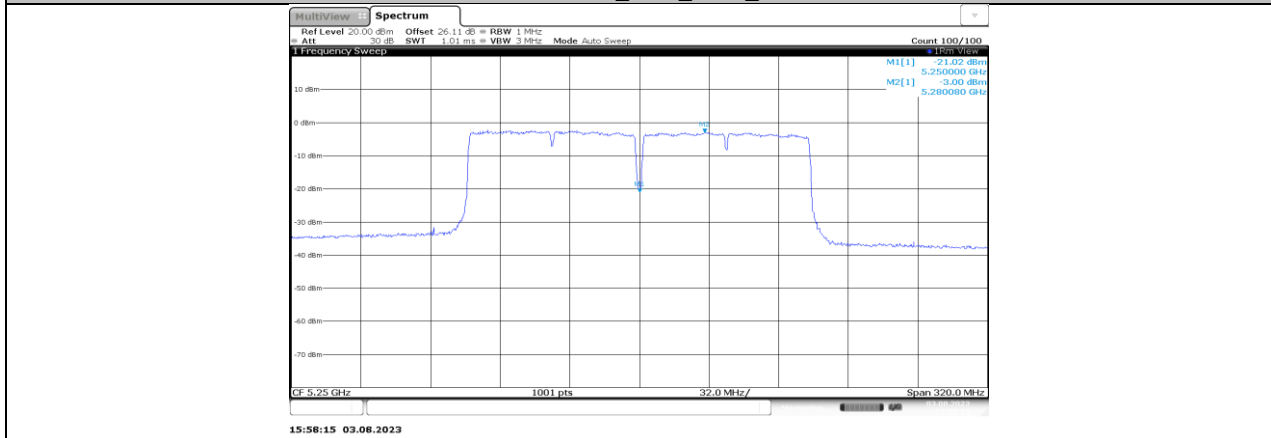




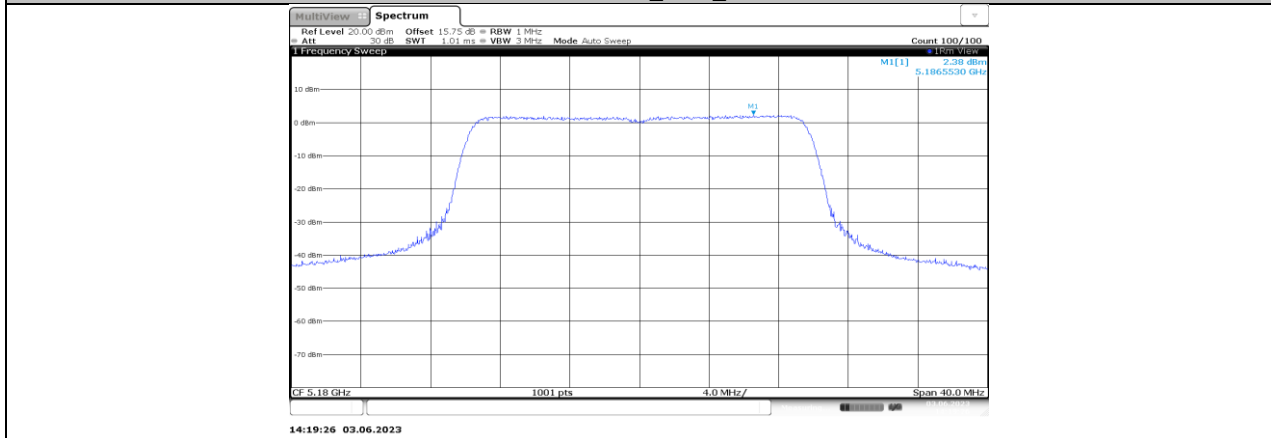
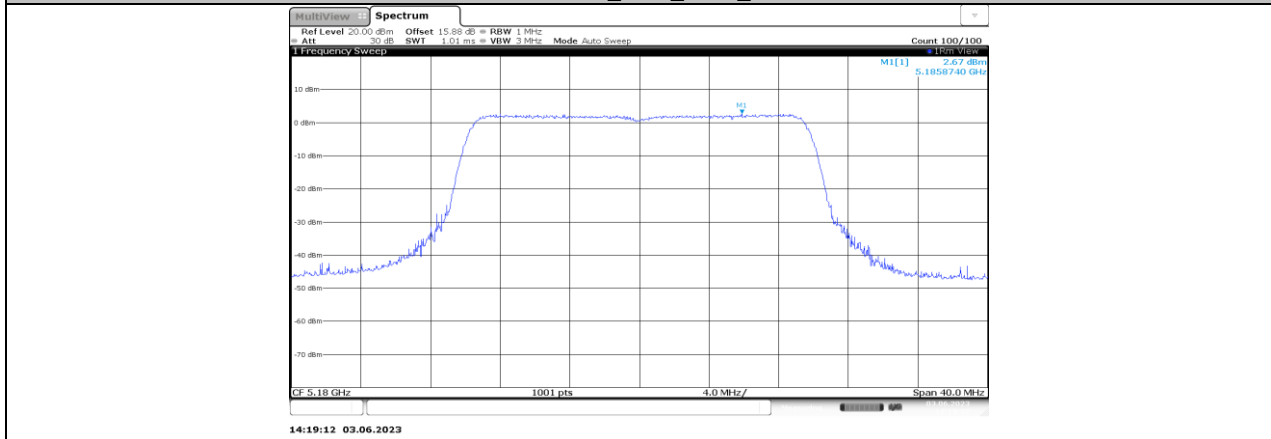
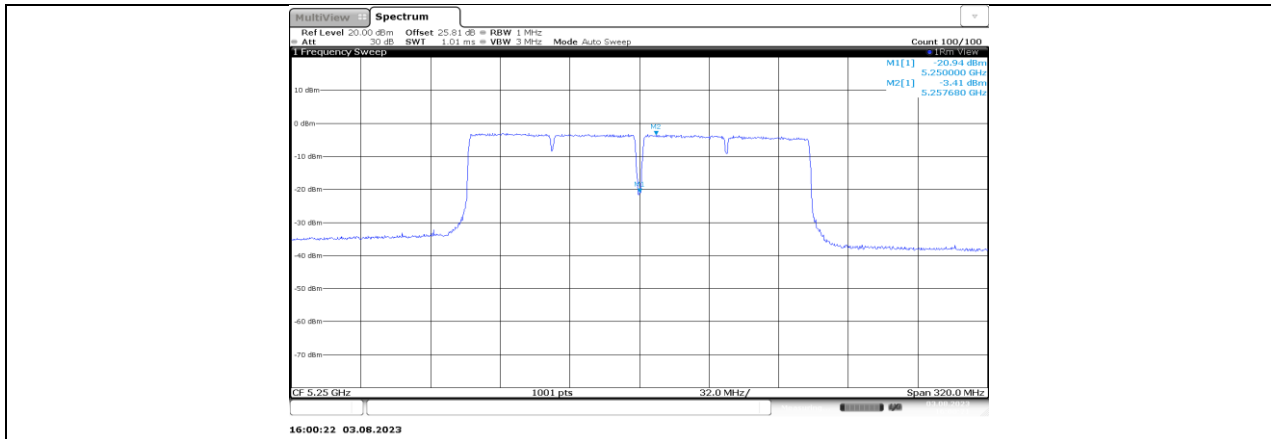
11AC160MIMO_Ant2_5250_UNII-1

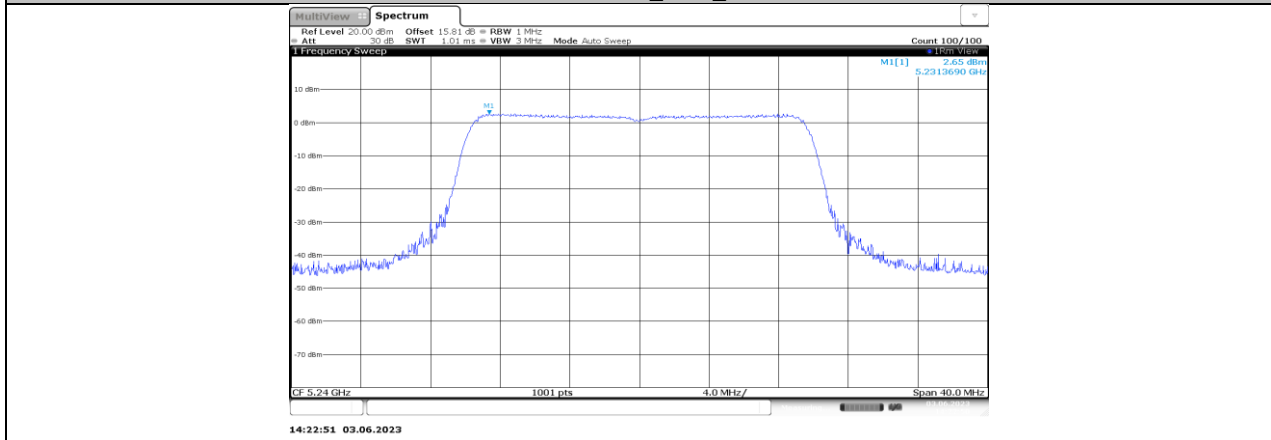
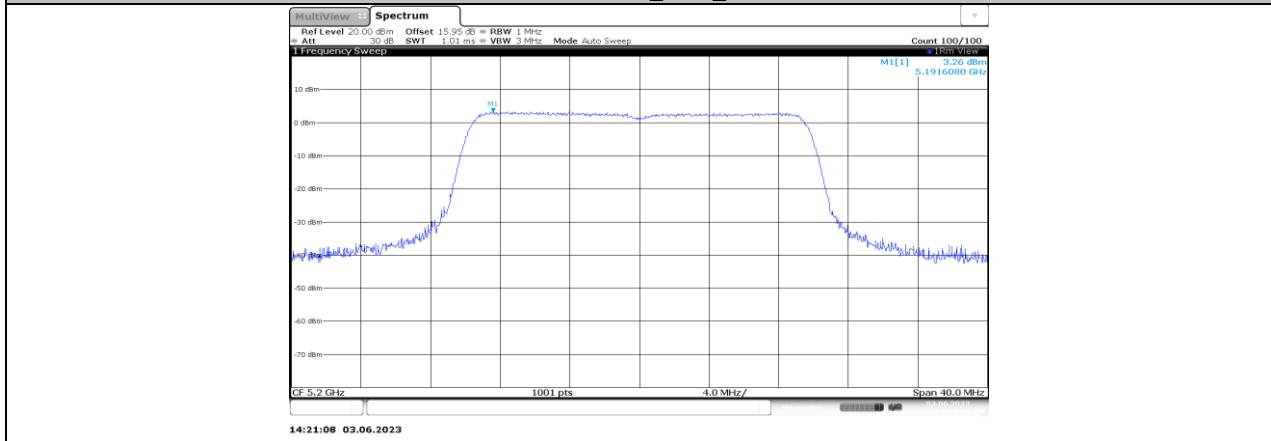
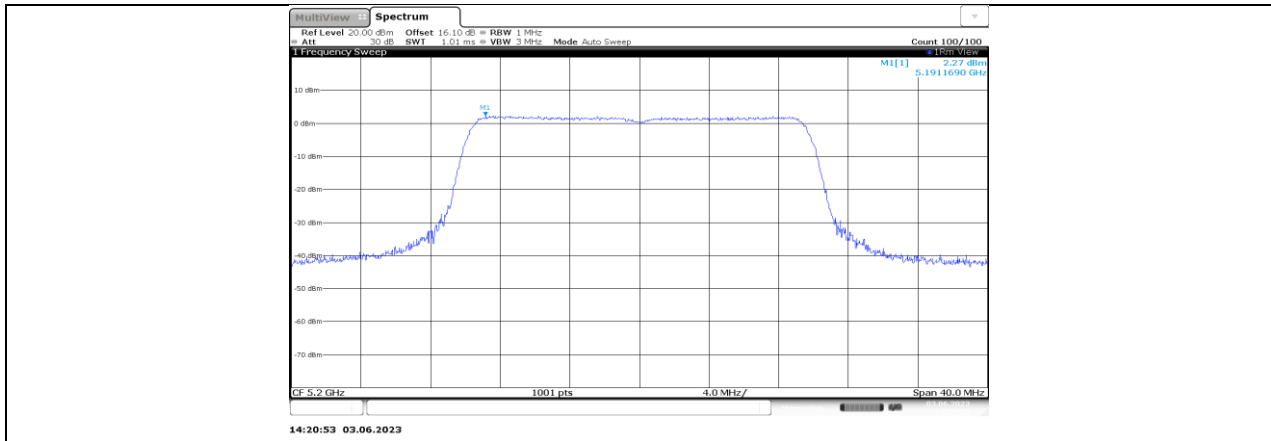


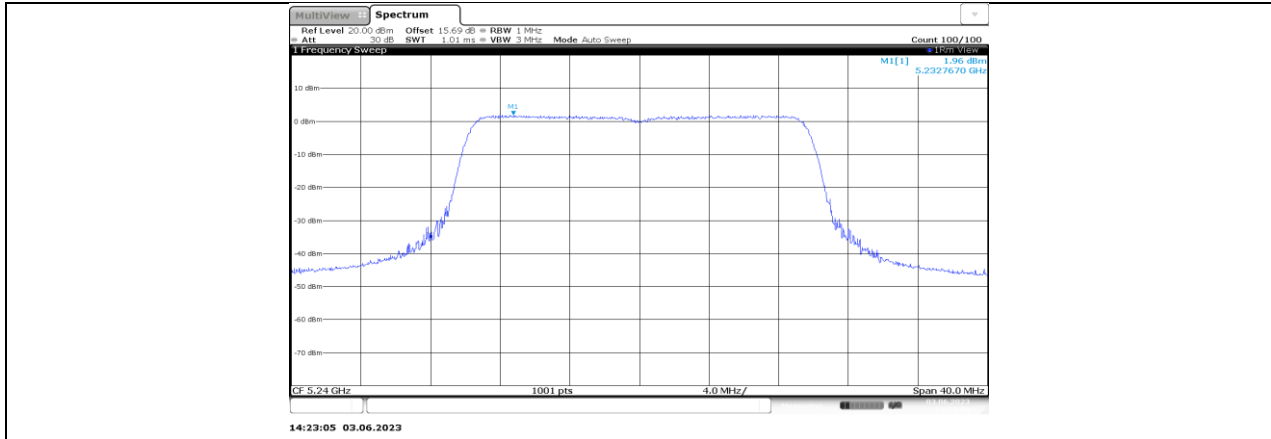
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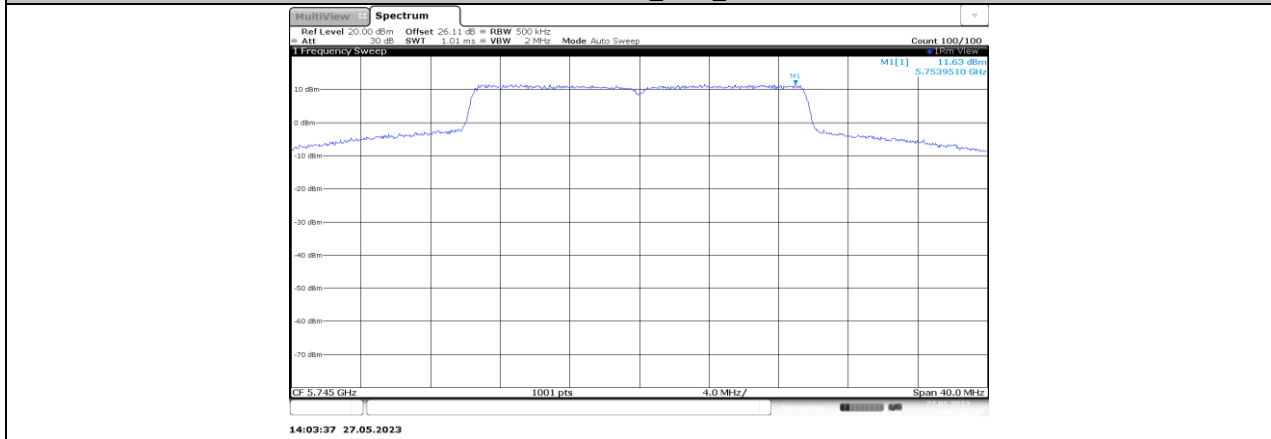
11AC160MIMO_Ant2_5250_UNII-2A



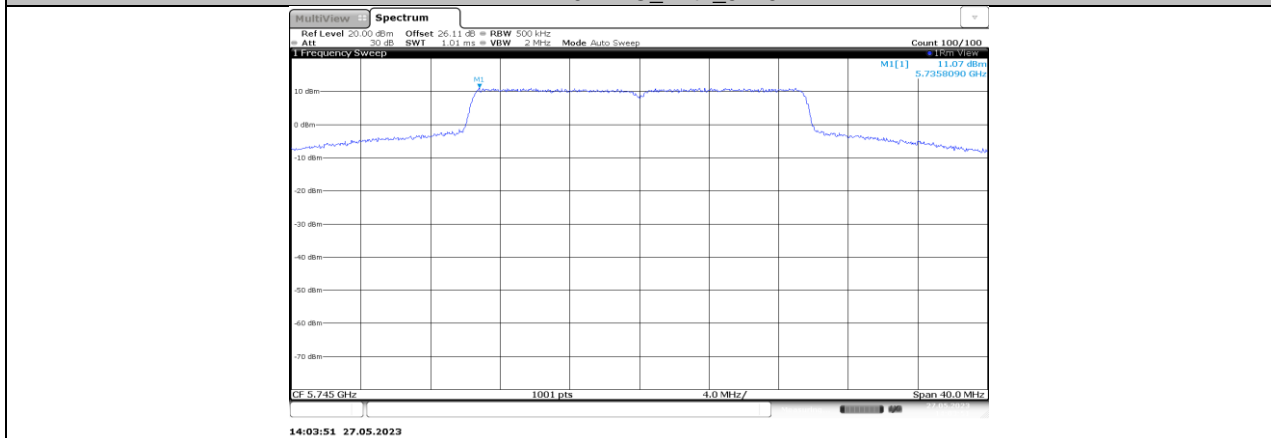




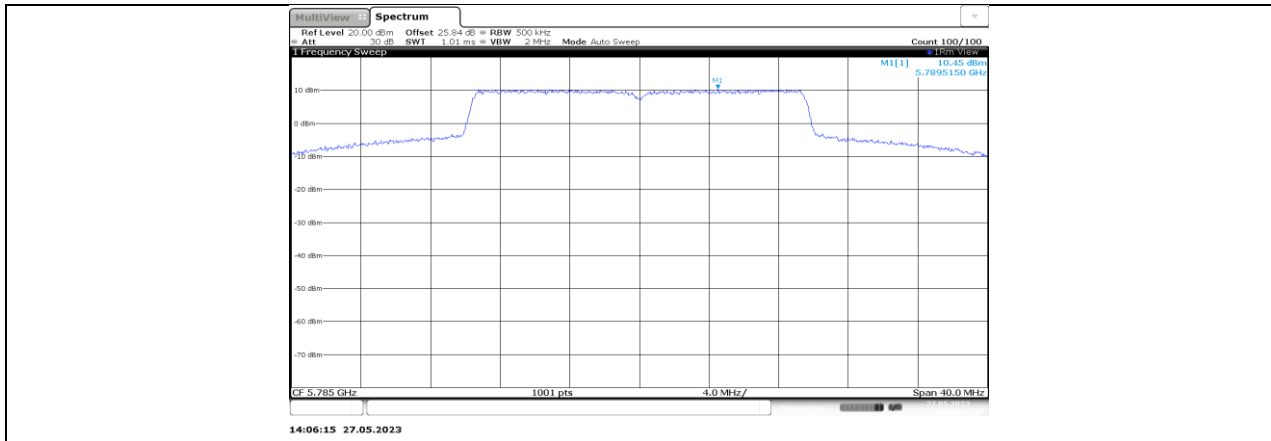
11AX20MIMO_Ant4_5240



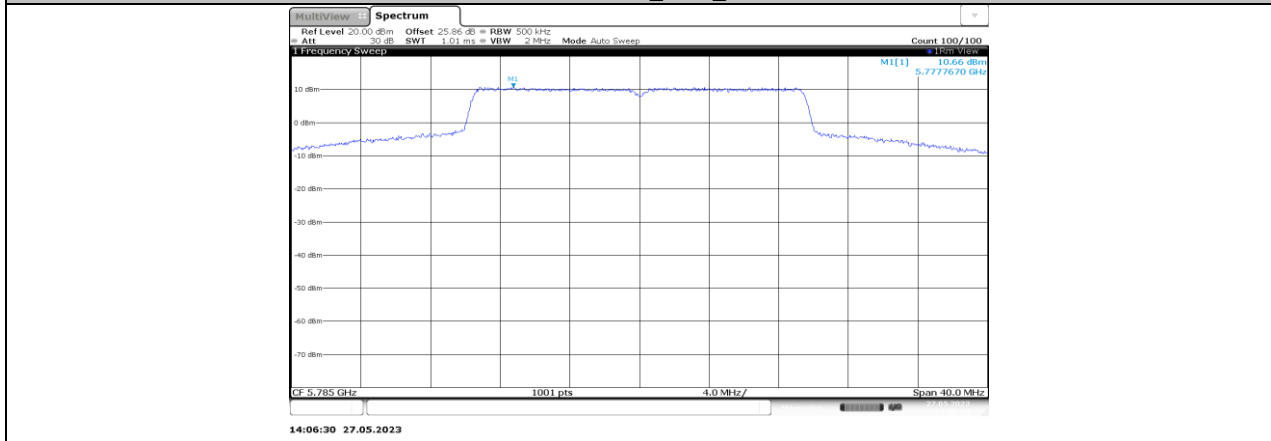
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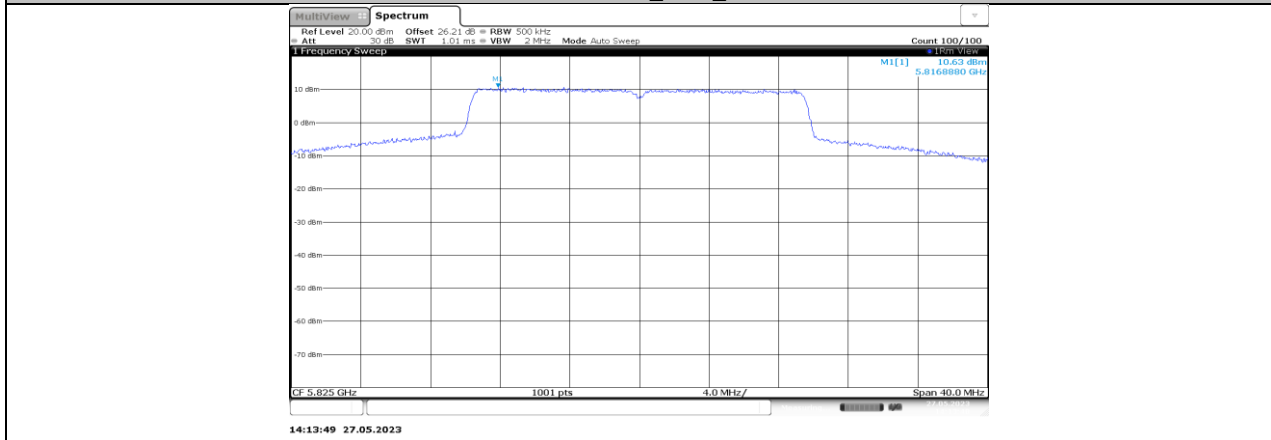
11AX20MIMO_Ant4_5745



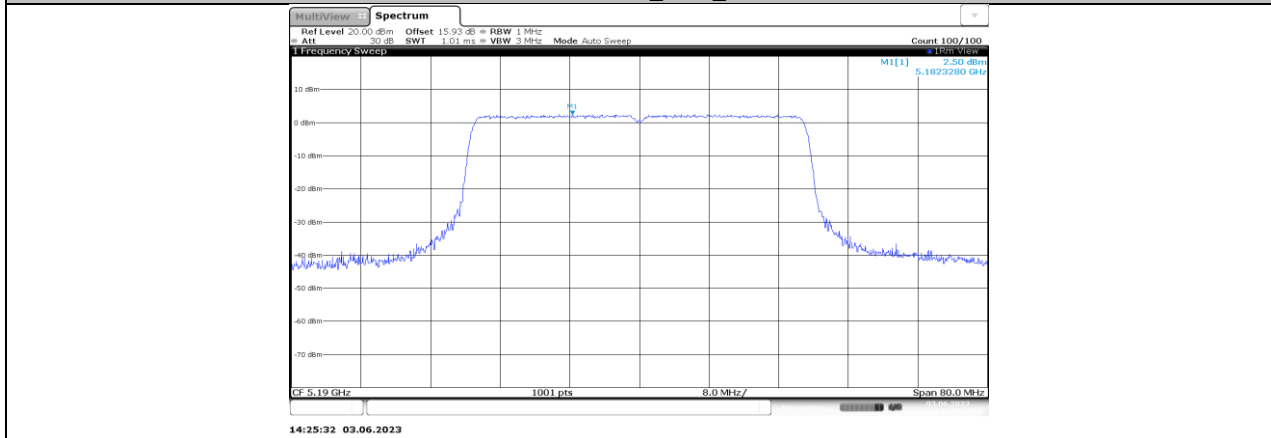
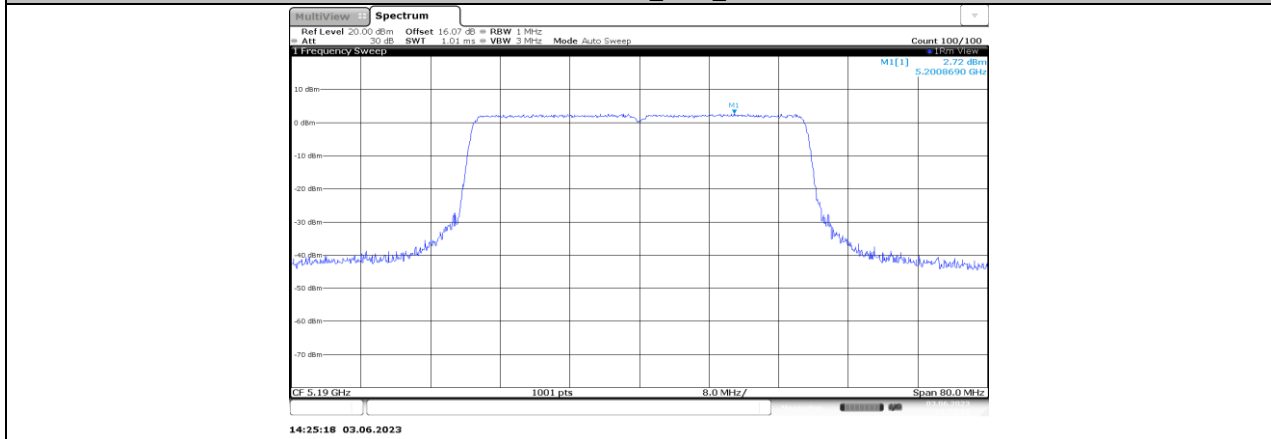
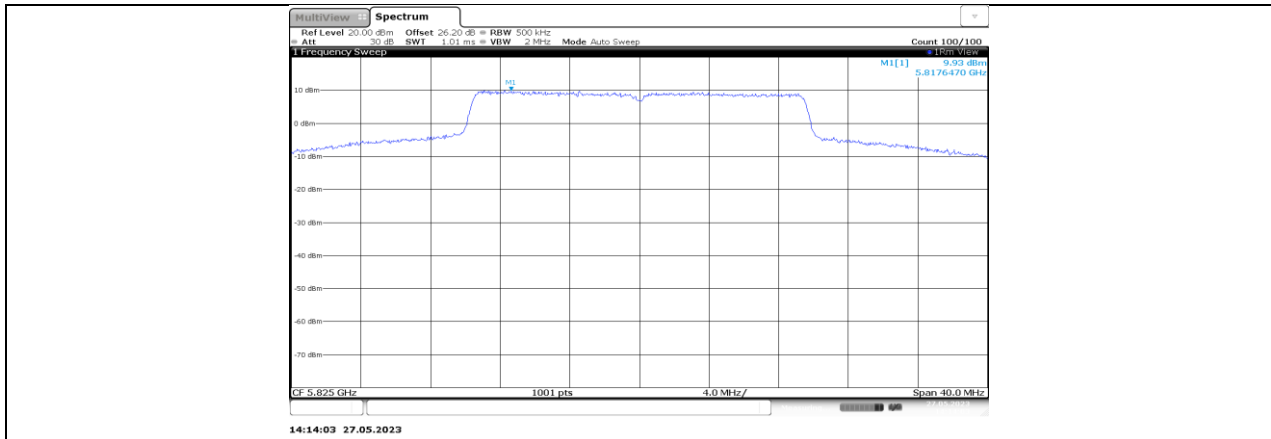
11AX20MIMO_Ant2_5785

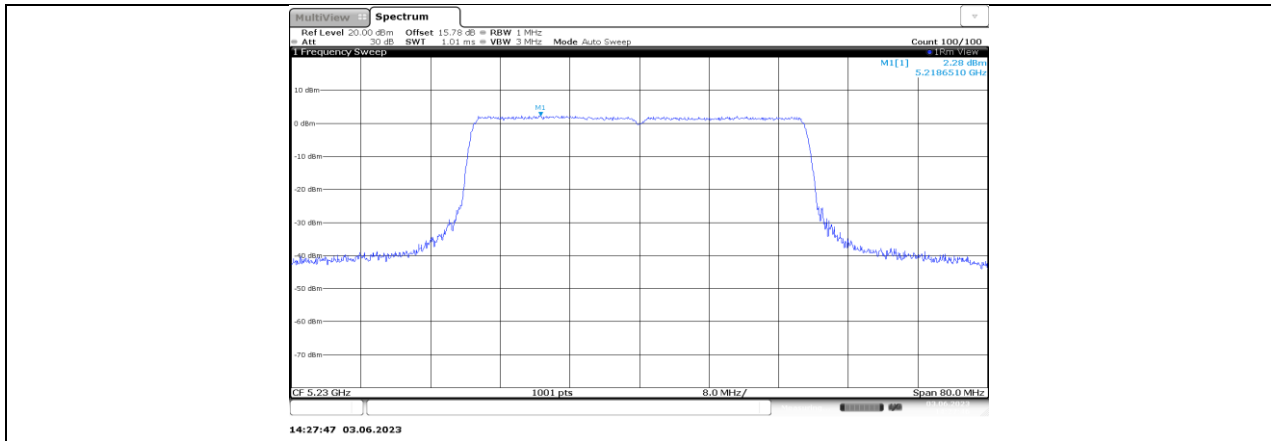


11AX20MIMO_Ant4_5785

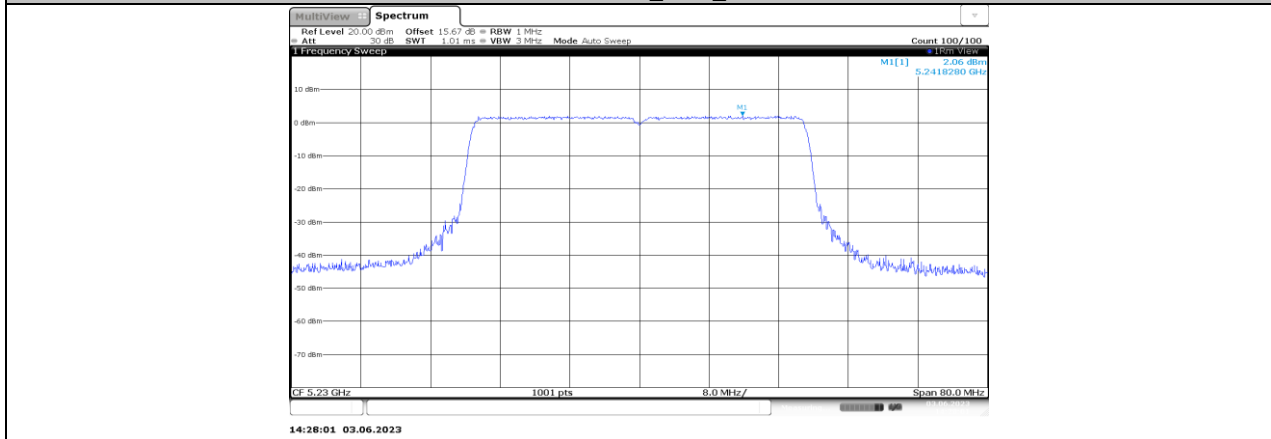


11AX20MIMO_Ant2_5825

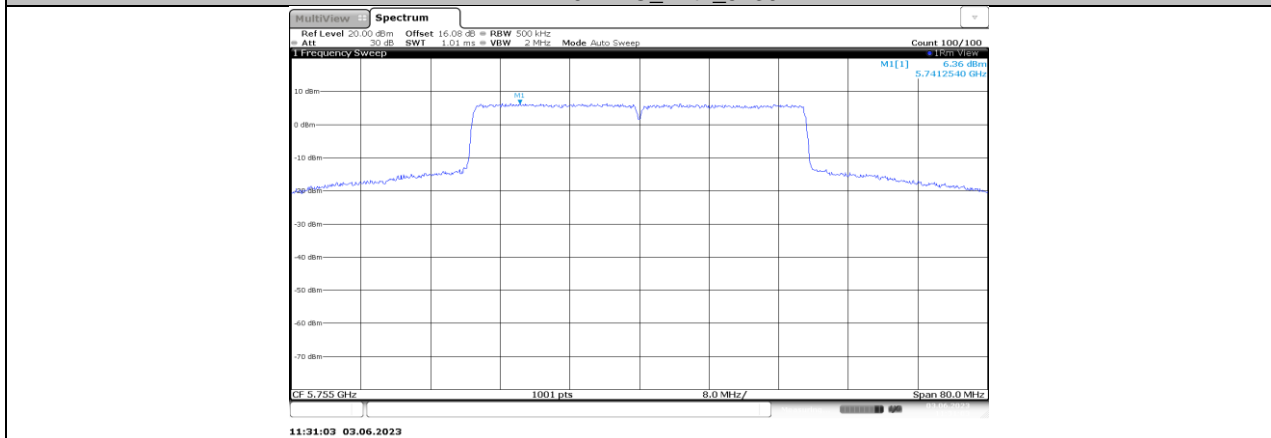




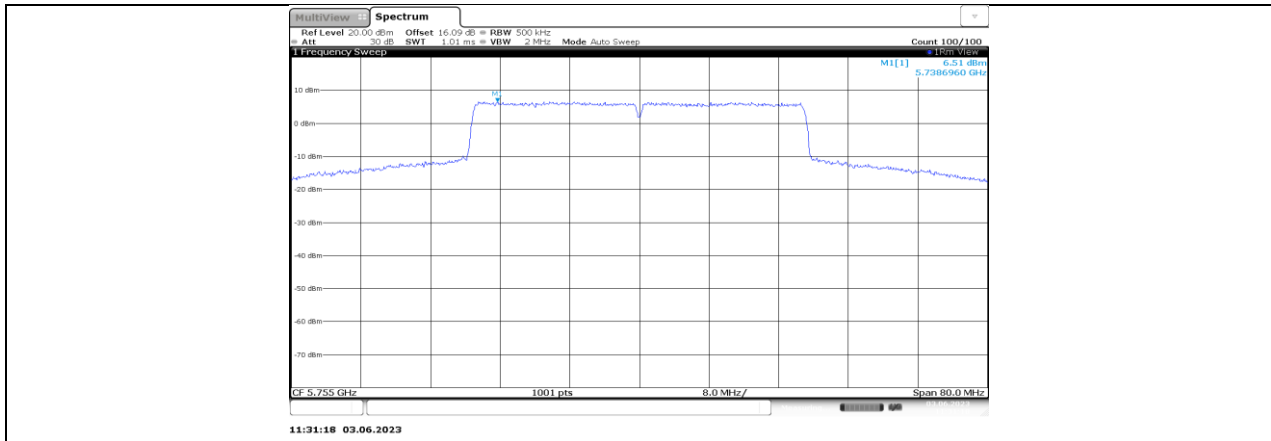
11AX40MIMO_Ant2_5230



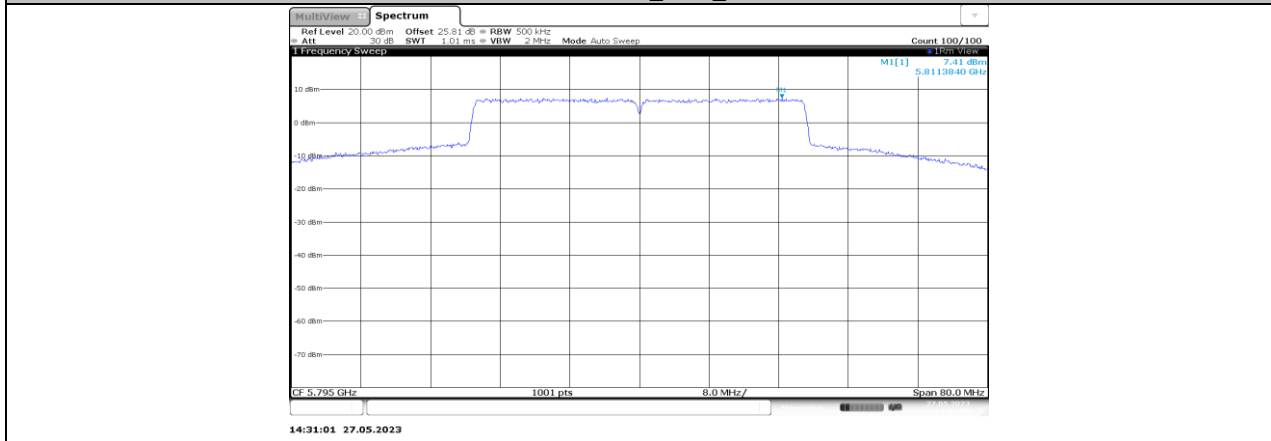
11AX40MIMO_Ant4_5230



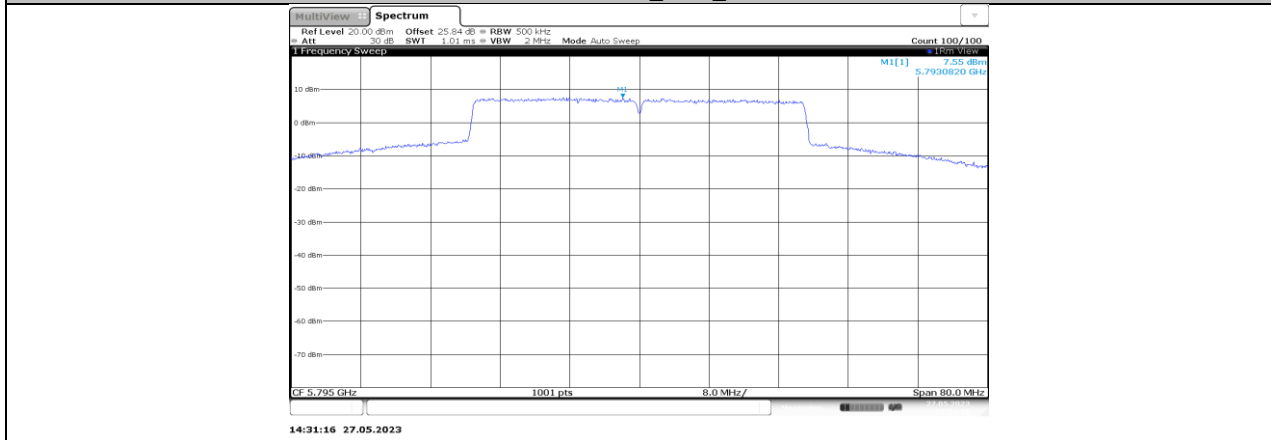
11AX40MIMO_Ant2_5755



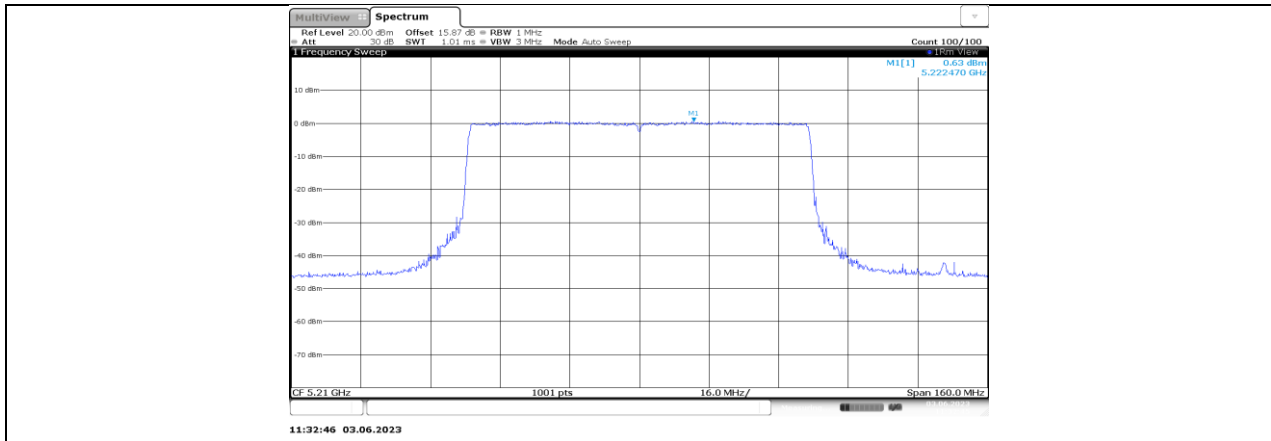
11AX40MIMO_Ant4_5755



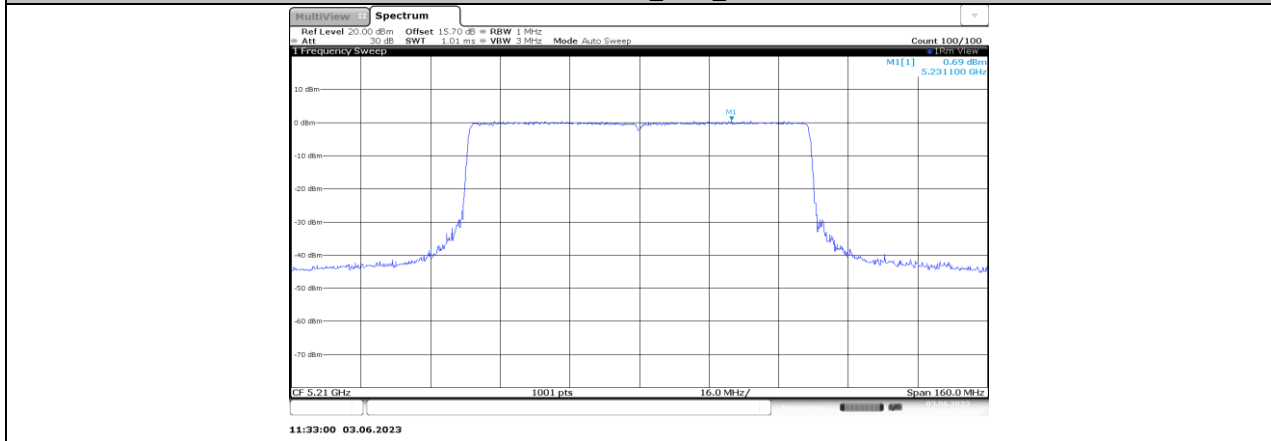
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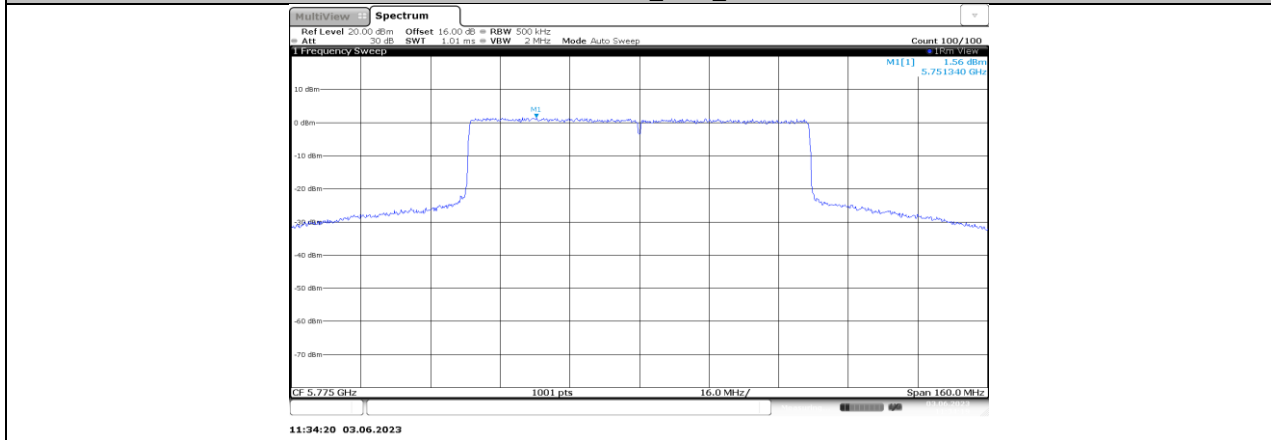
11AX40MIMO_Ant4_5795



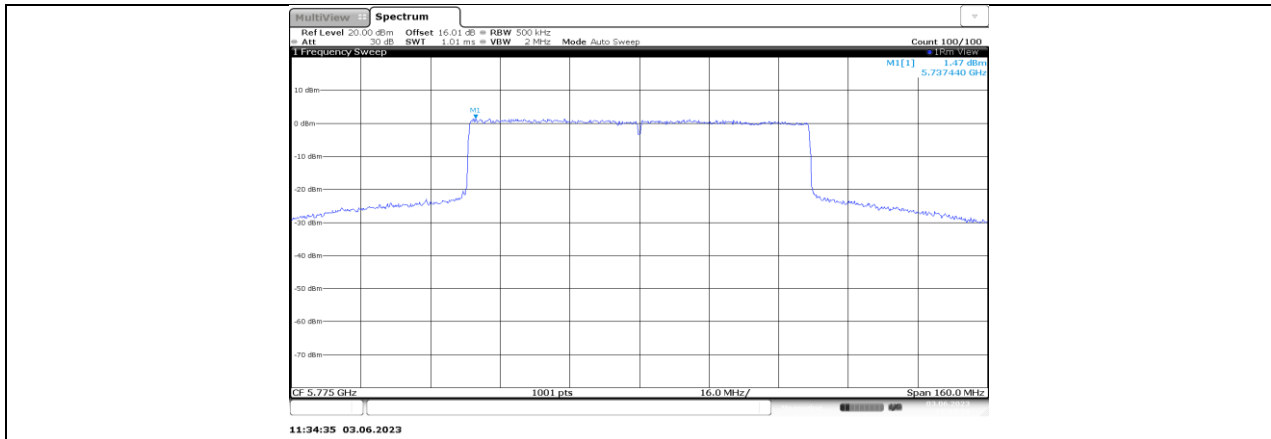
11AX80MIMO_Ant2_5210



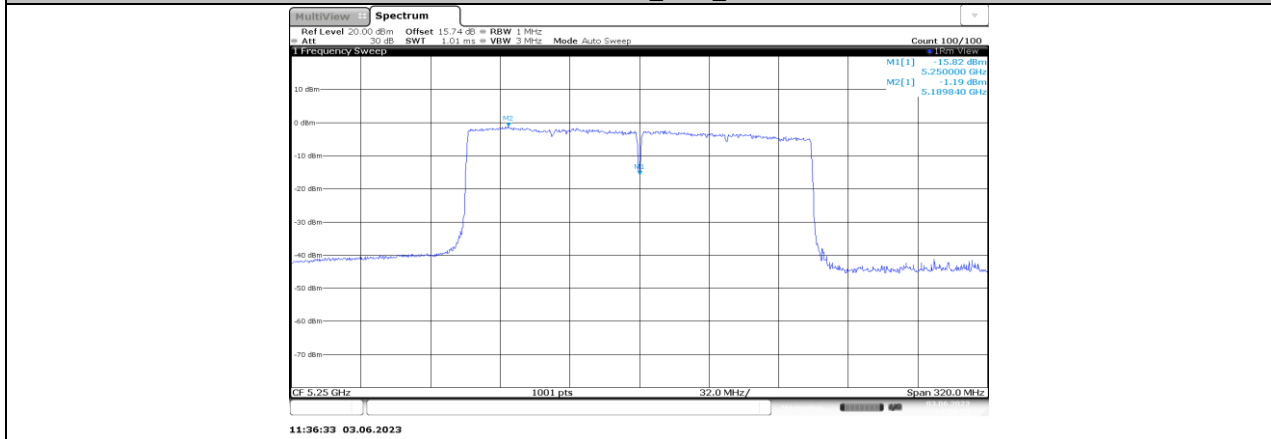
11AX80MIMO_Ant4_5210



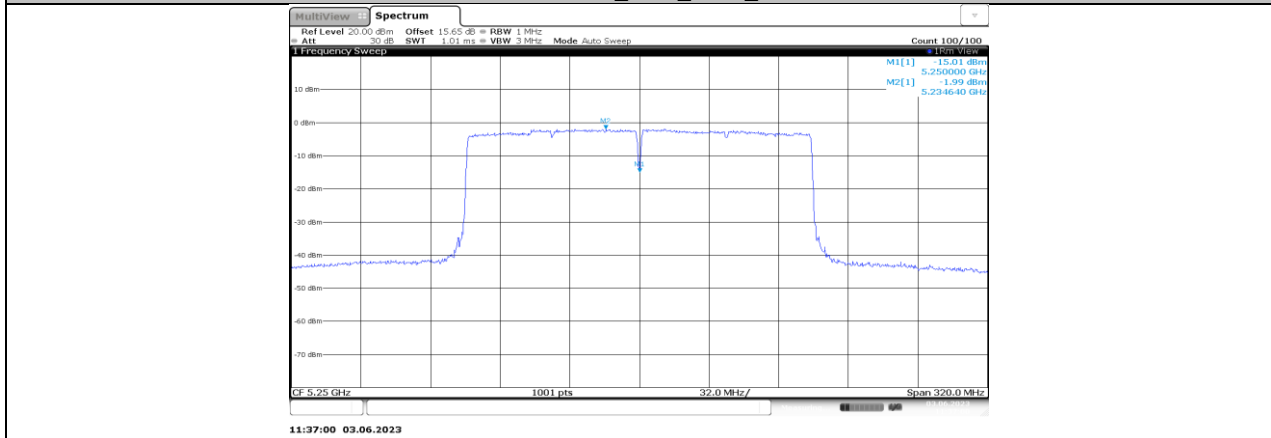
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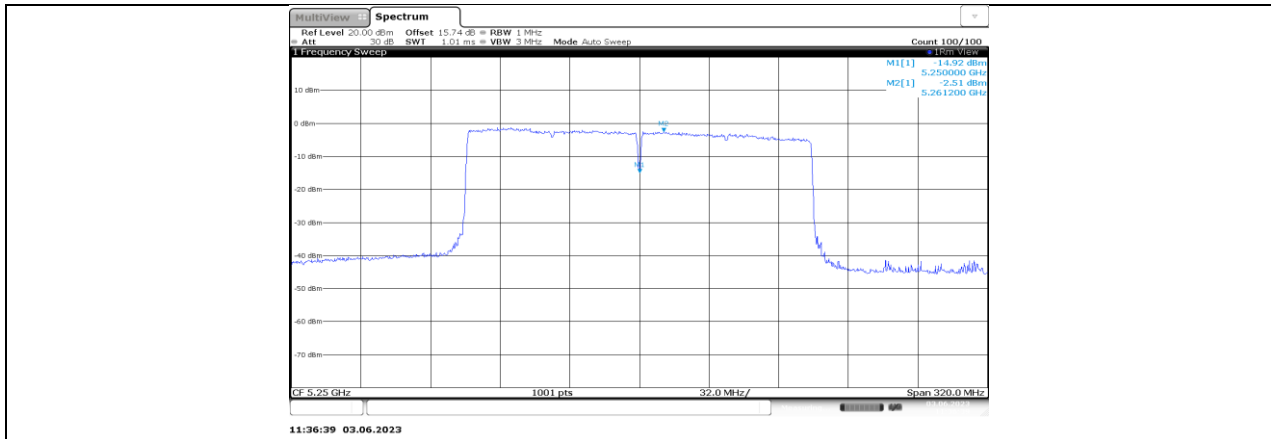
11AX80MIMO_Ant4_5775



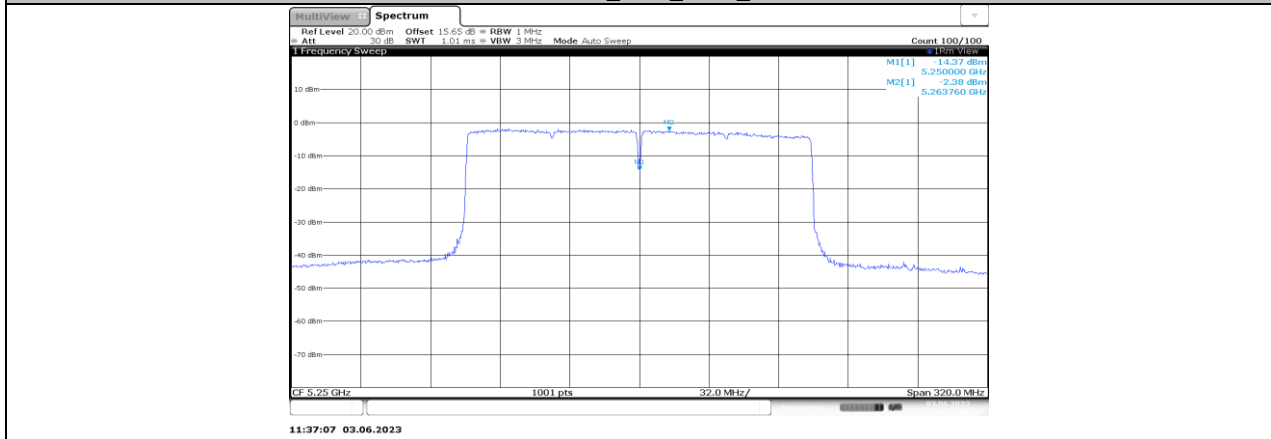
11AX160MIMO_Ant2_5250_UNII-1



11AX160MIMO_Ant4_5250_UNII-1



11AX160MIMO_Ant2_5250_UNII-2A



11AX160MIMO_Ant4_5250_UNII-2A

11.6. APPENDIX F: FREQUENCY STABILITY

11.6.1. Test Result

Frequency Error vs. Voltage									
802.11a:5200MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
TN	VL	5199.9958	-0.81	5200.0186	3.58	5200.0087	1.67	5199.9938	-1.20
TN	VN	5199.9865	-2.60	5199.9921	-1.52	5200.0045	0.87	5200.0020	0.39
TN	VH	5199.9788	-4.08	5199.9778	-4.28	5199.9912	-1.70	5200.0148	2.84

Frequency Error vs. Temperature									
802.11a:5200MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
40	VN	5200.0195	3.74	5199.9827	-3.33	5199.9953	-0.90	5200.0204	3.93
30	VN	5200.0149	2.86	5200.0200	3.84	5200.0008	0.16	5200.0095	1.83
20	VN	5200.0181	3.47	5199.9966	-0.66	5200.0065	1.25	5199.9887	-2.18
10	VN	5200.0098	1.89	5200.0191	3.68	5200.0069	1.33	5200.0189	3.64
0	VN	5199.9792	-4.00	5199.9914	-1.65	5199.9835	-3.18	5199.9962	-0.74

Note:

1. All antennas, test modes and test channels have been tested, only the worst data record in the report.
2. For the detail Test Conditions, please refer to section 7.5 TEST ENVIRONMENT.

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A-CDD	1.97	2.10	0.9381	93.81	0.28	0.51	1
11AC20MIMO	5.41	6.02	0.8987	89.87	0.46	0.18	0.5
11AC40MIMO	5.41	6.02	0.8987	89.87	0.46	0.18	0.5
11AC80MIMO	5.41	6.03	0.8972	89.72	0.47	0.18	0.5
11AC160MIMO	5.41	6.09	0.8883	88.83	0.51	0.18	0.5
11AX20MIMO	5.44	5.99	0.9082	90.82	0.42	0.18	0.5
11AX40MIMO	5.45	5.96	0.9144	91.44	0.39	0.18	0.5
11AX80MIMO	5.45	6.02	0.9053	90.53	0.43	0.18	0.5
11AX160MIMO	5.44	6.03	0.9022	90.22	0.45	0.18	0.5

Note:

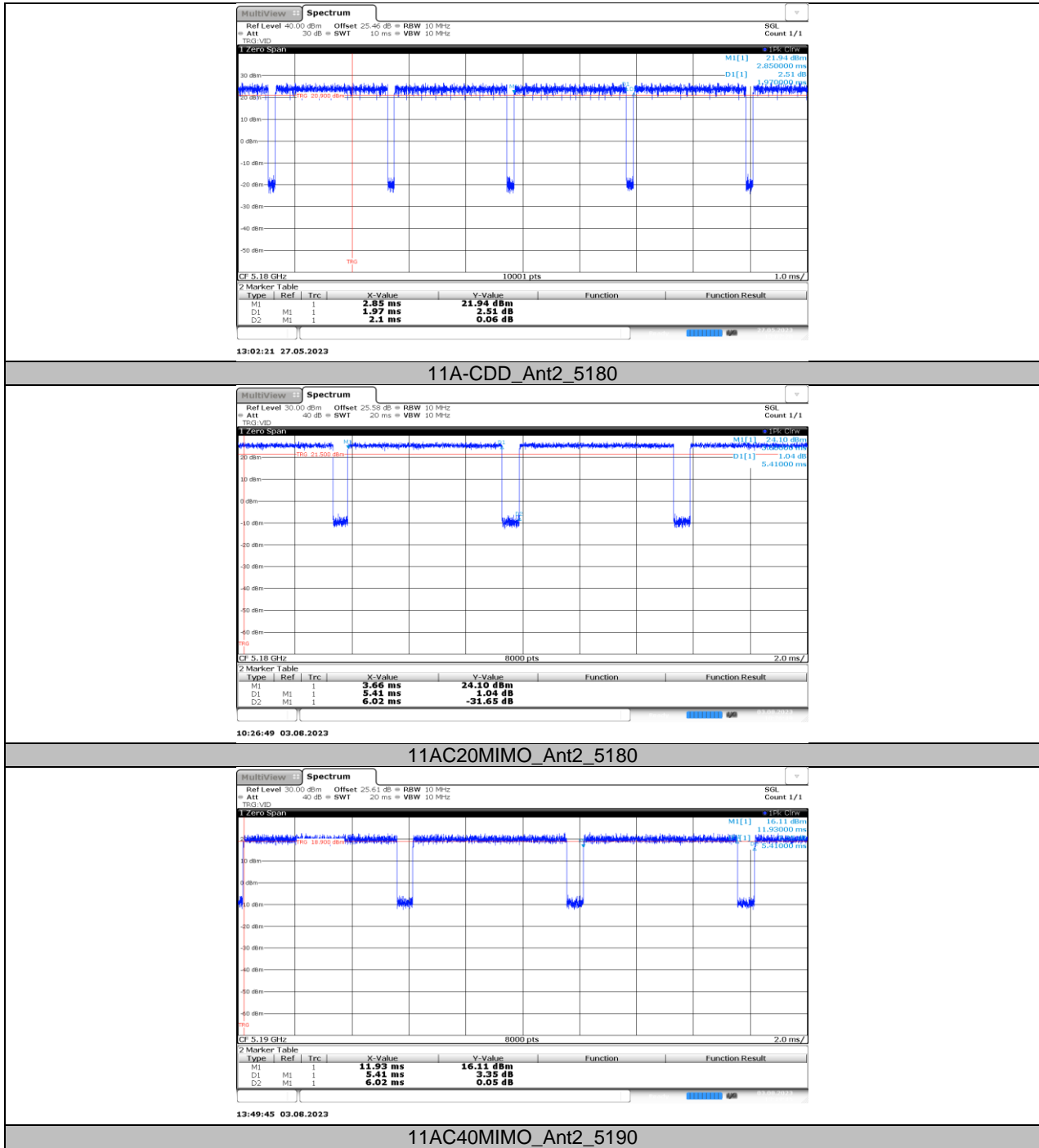
Duty Cycle Correction Factor=10log (1/x).

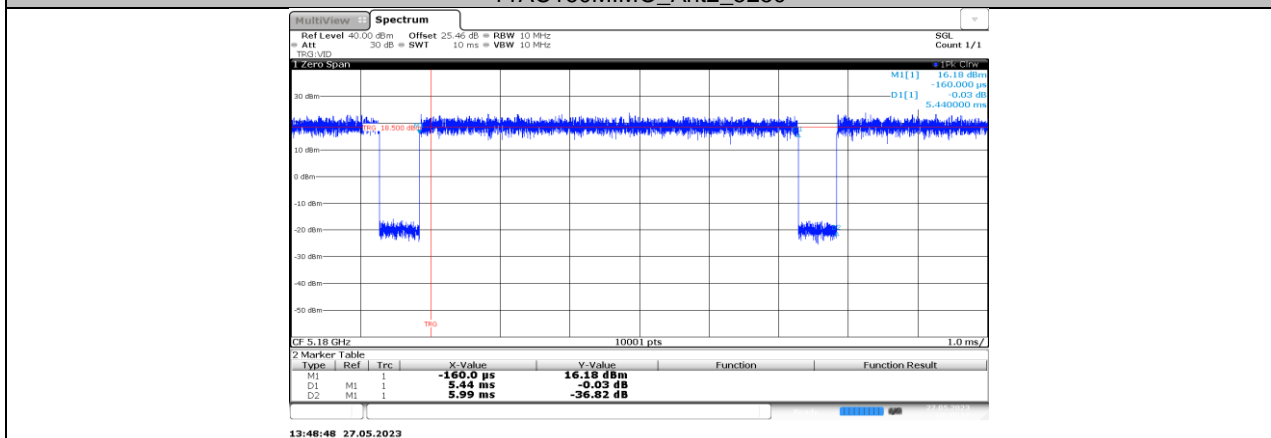
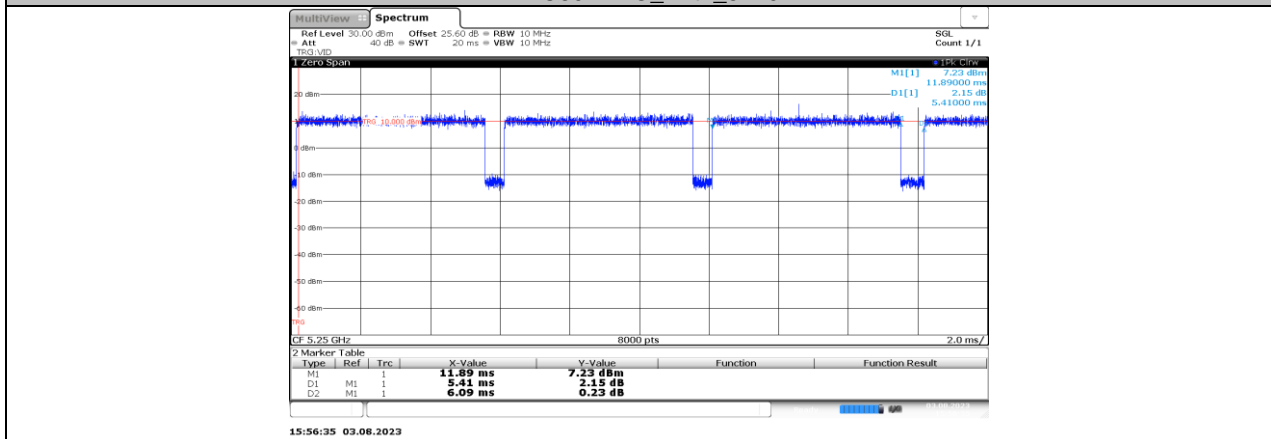
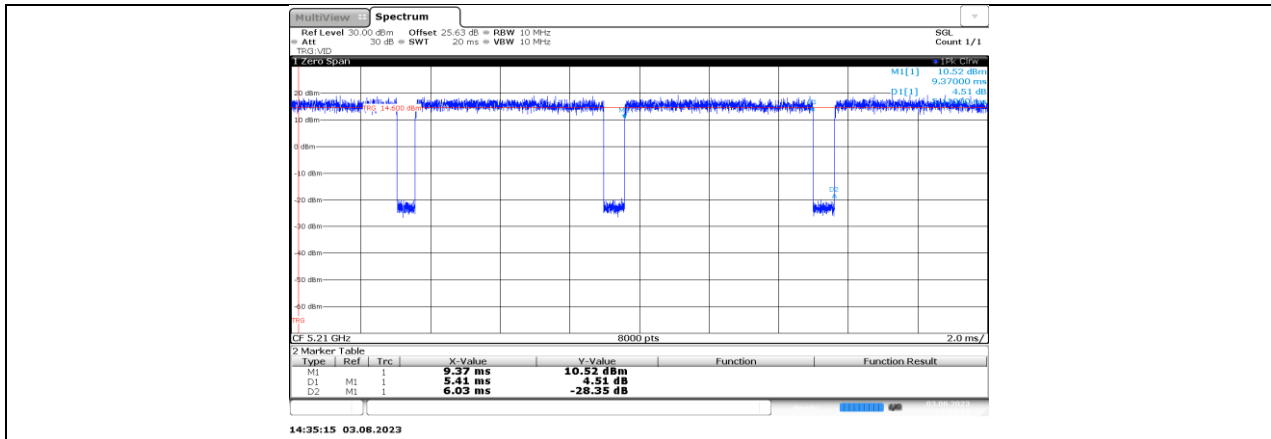
Where: x is Duty Cycle (Linear)

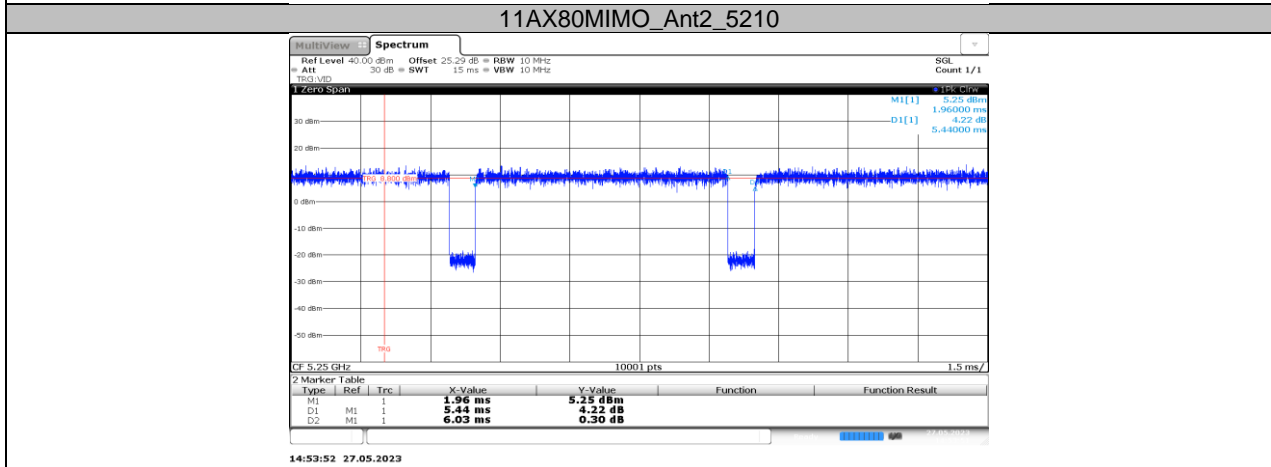
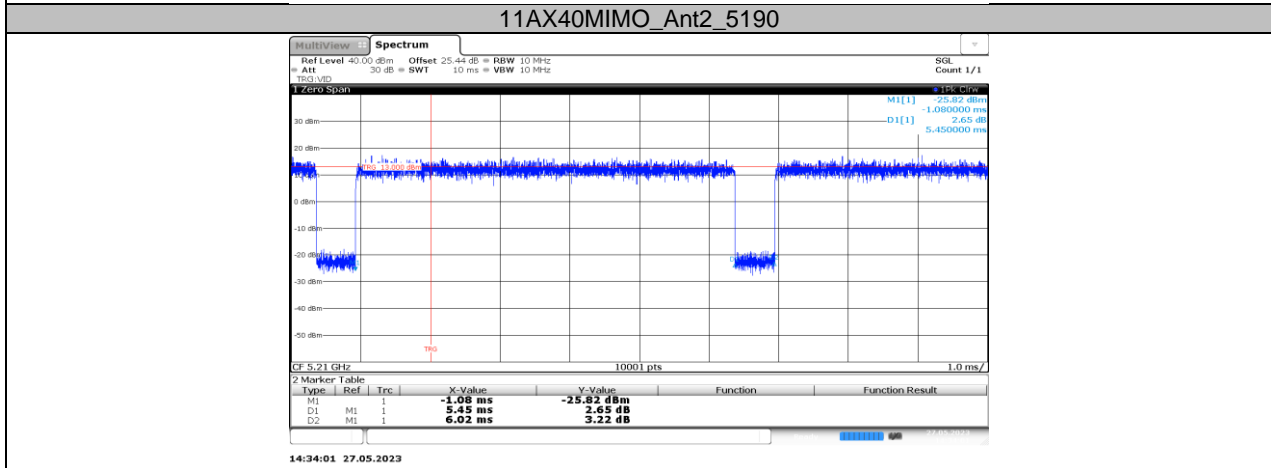
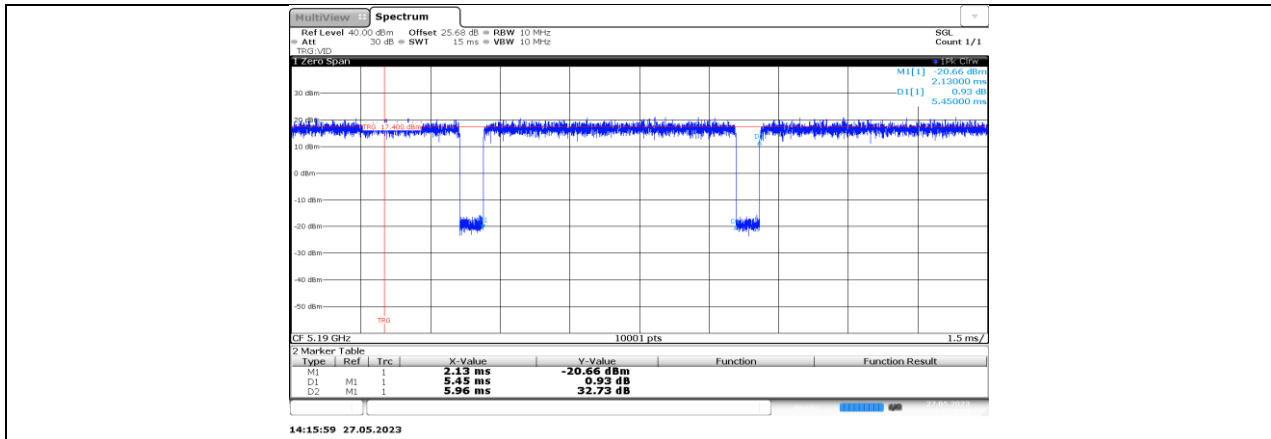
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs







END OF REPORT