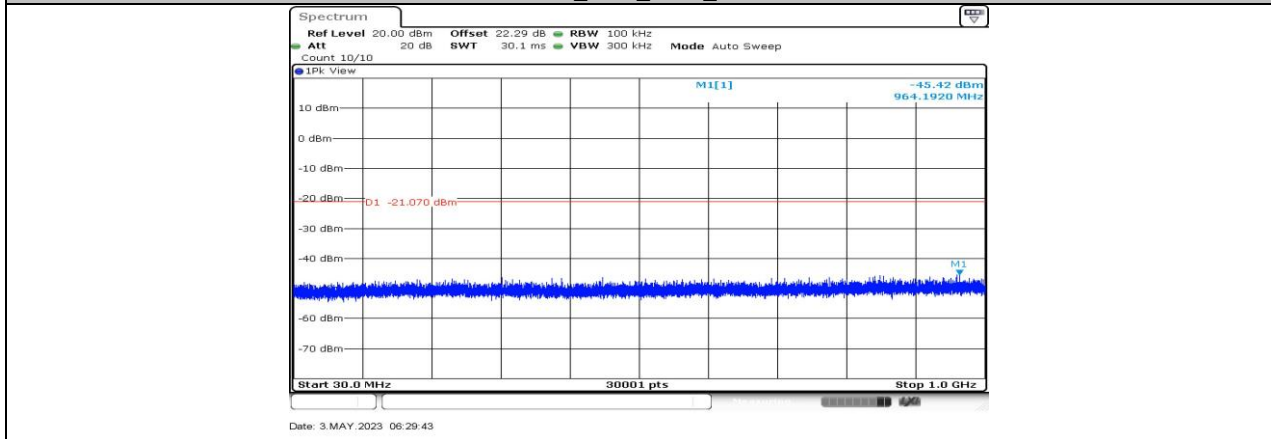
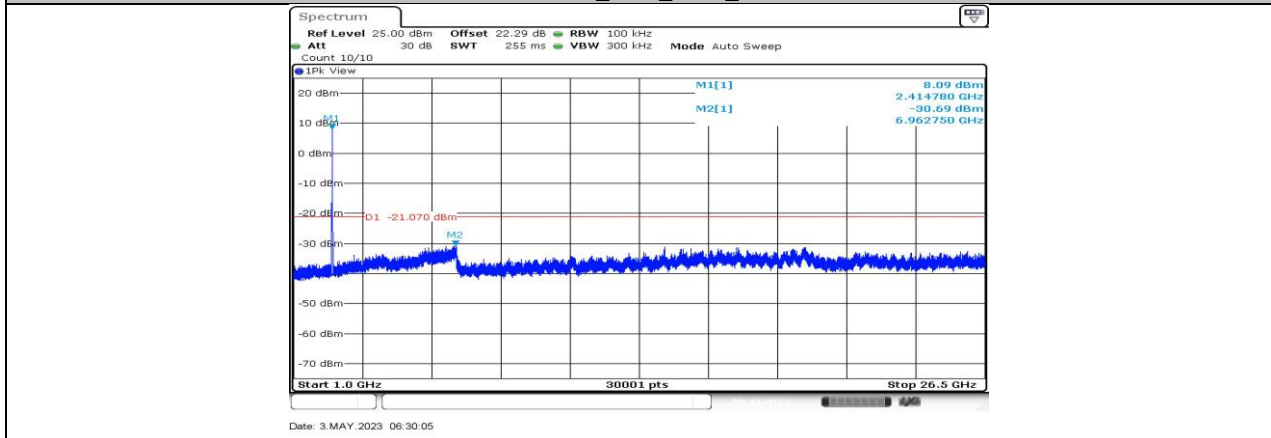


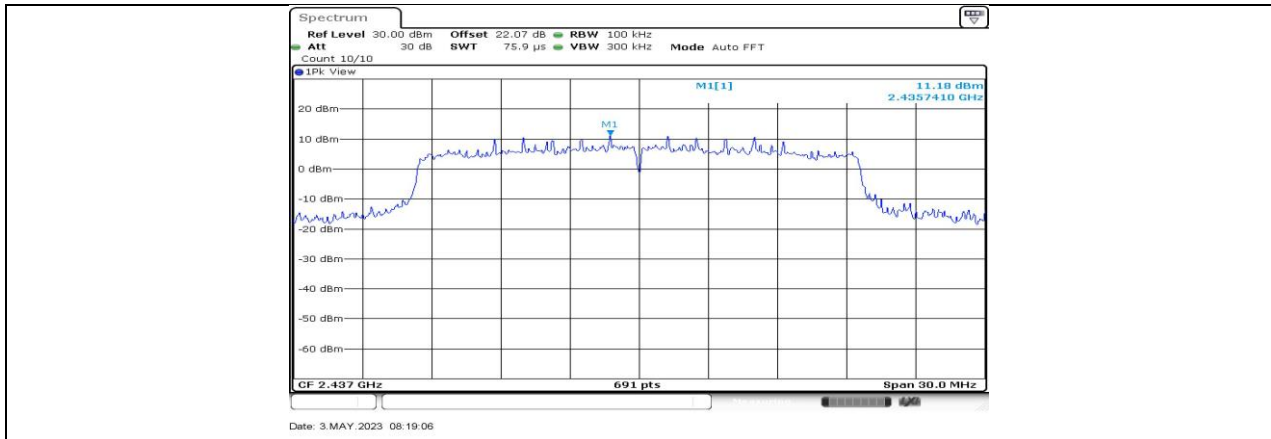
11AX20MIMO_Ant4_2417_0-Reference



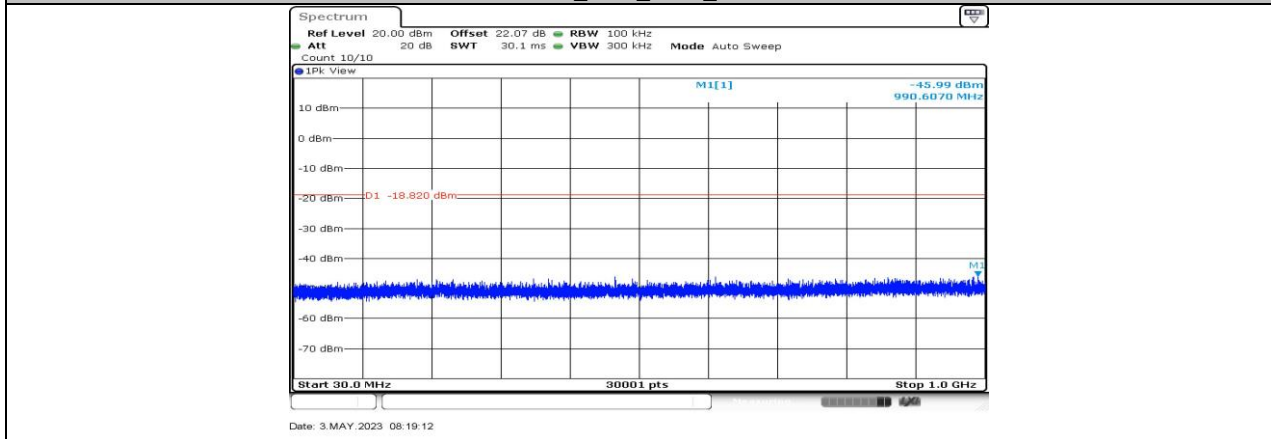
11AX20MIMO_Ant4_2417_30~1000



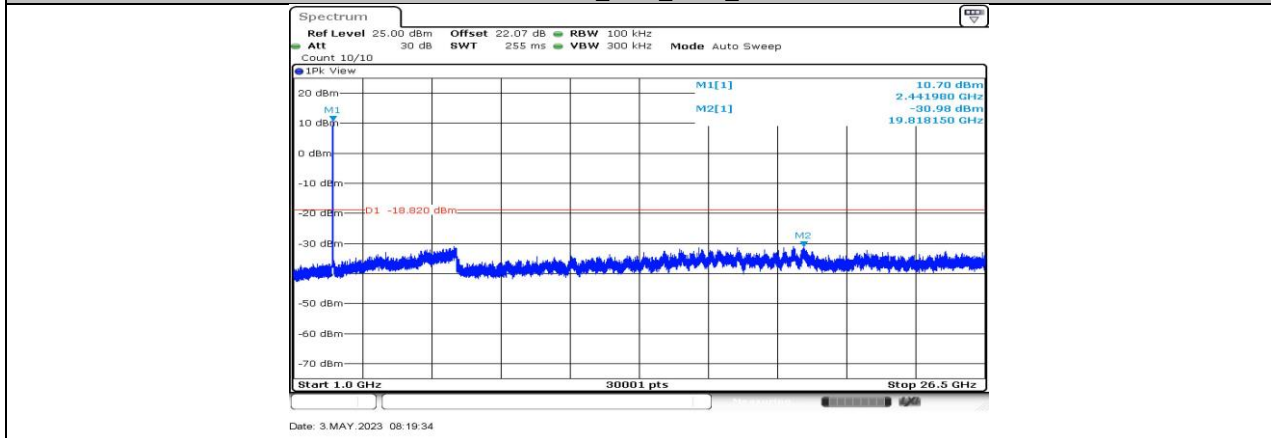
11AX20MIMO_Ant4_2417_1000~26500



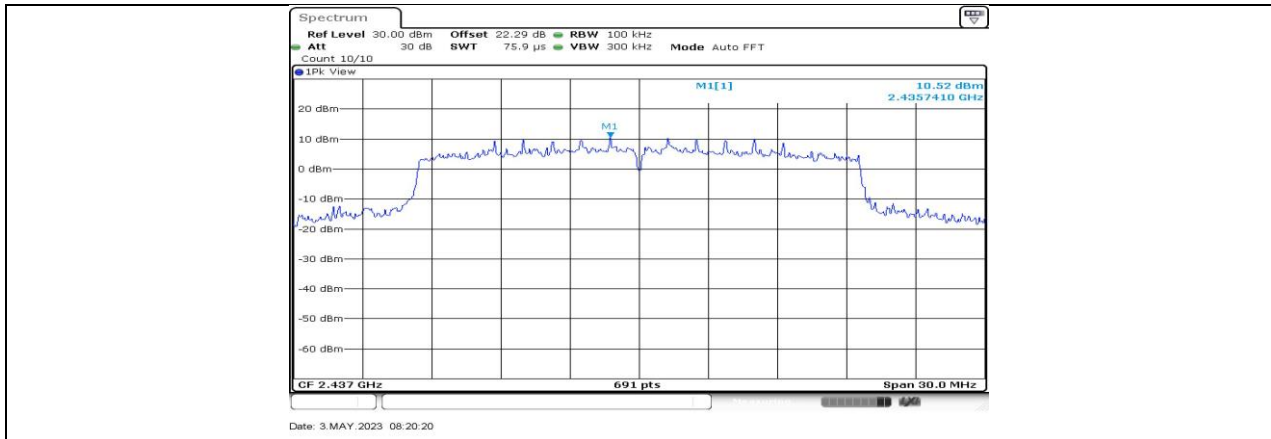
11AX20MIMO_Ant2_2437_0~Reference



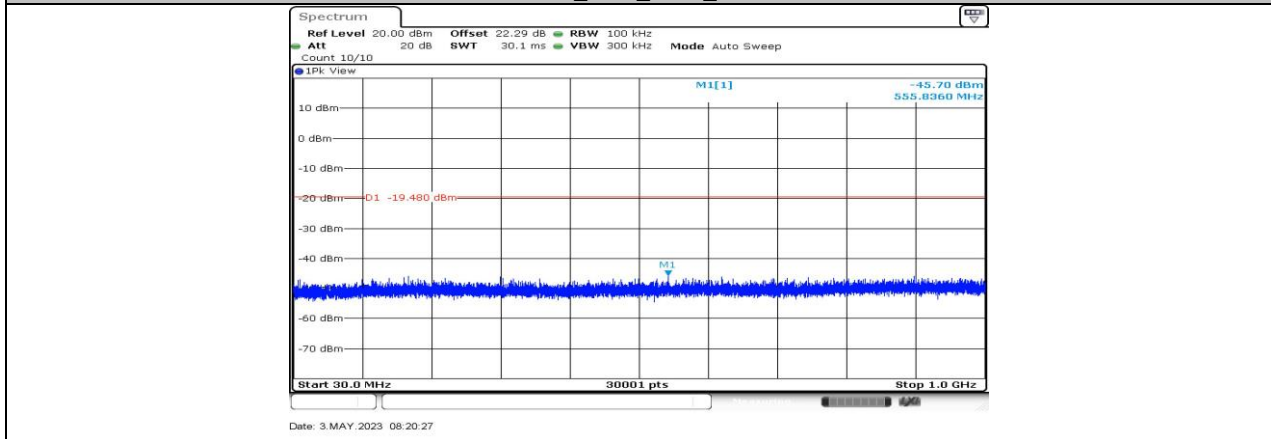
11AX20MIMO_Ant2_2437_30~1000



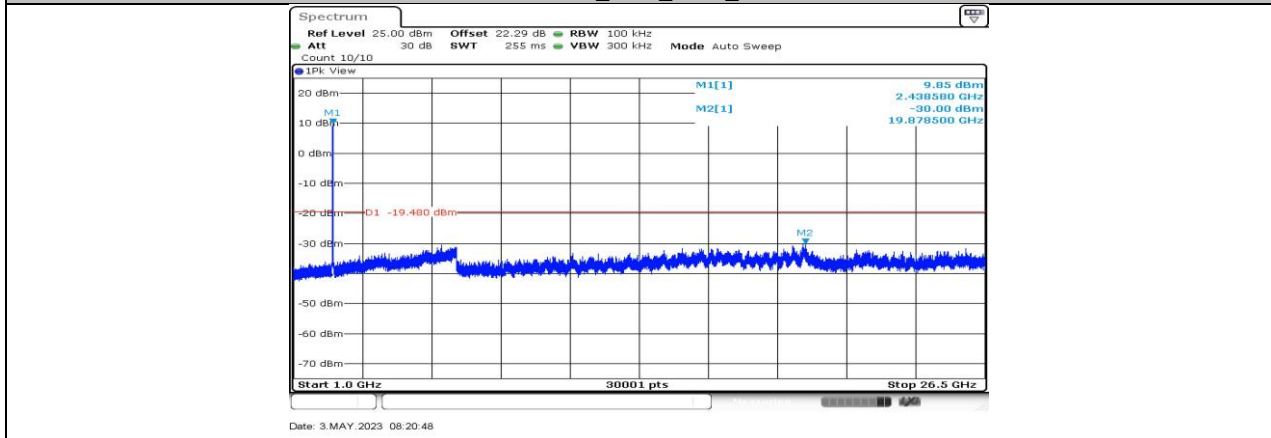
11AX20MIMO_Ant2_2437_1000~26500



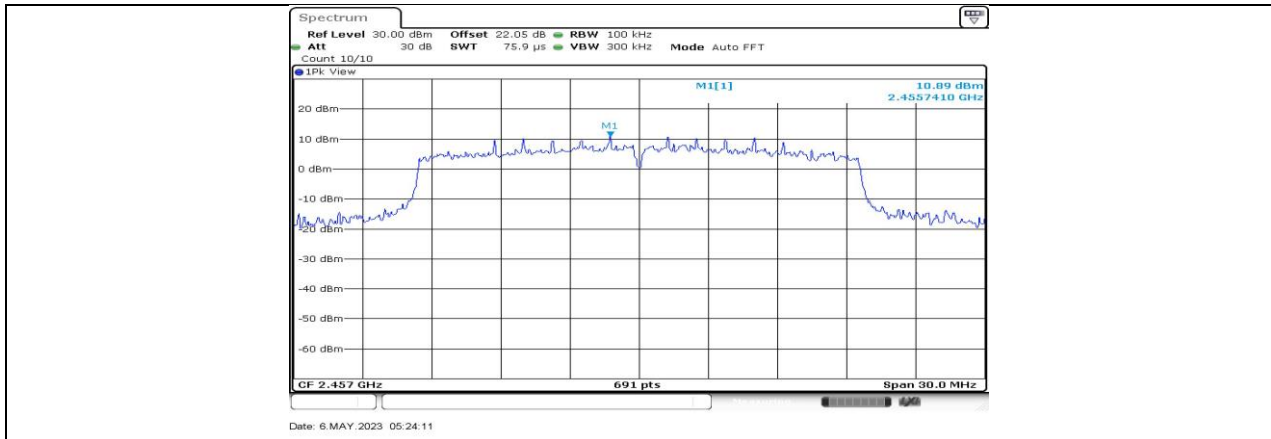
11AX20MIMO_Ant4_2437_0~Reference



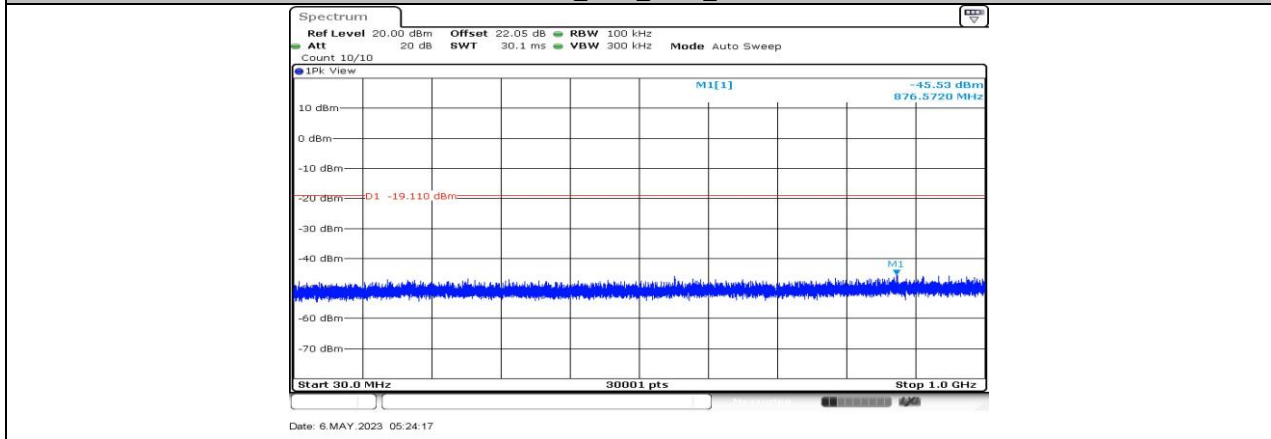
11AX20MIMO_Ant4_2437_30~1000



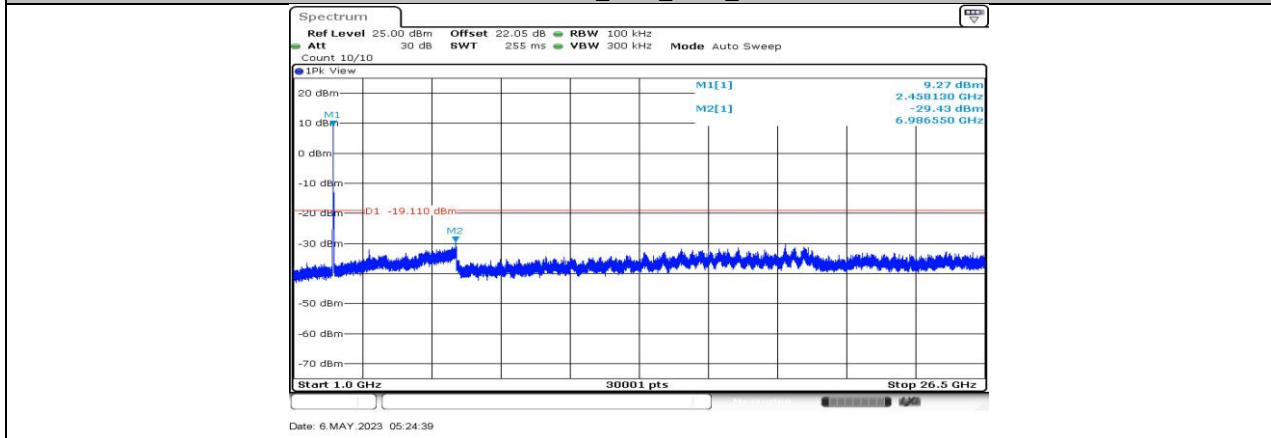
11AX20MIMO_Ant4_2437_1000~26500



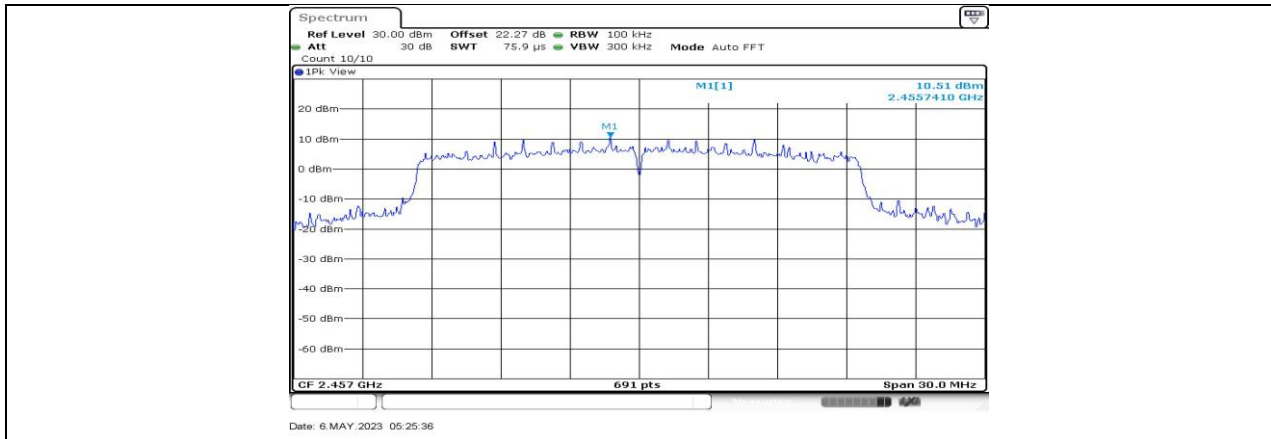
11AX20MIMO_Ant2_2457_0-Reference



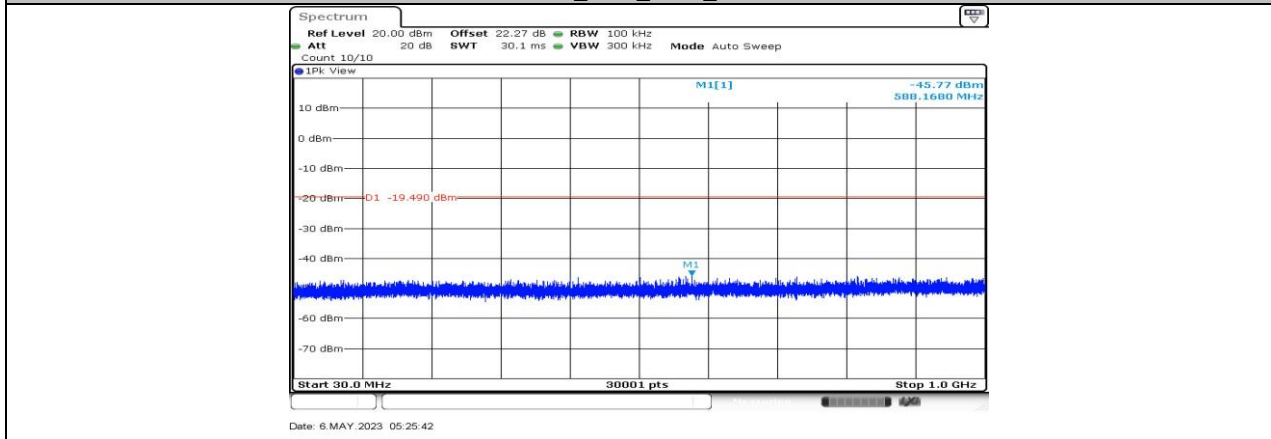
11AX20MIMO_Ant2_2457_30~1000



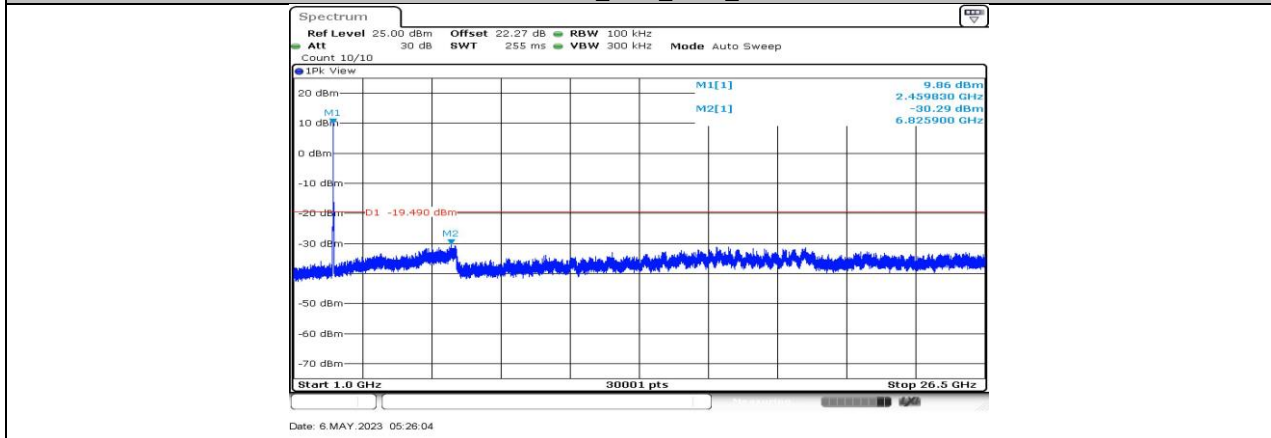
11AX20MIMO_Ant2_2457_1000~26500



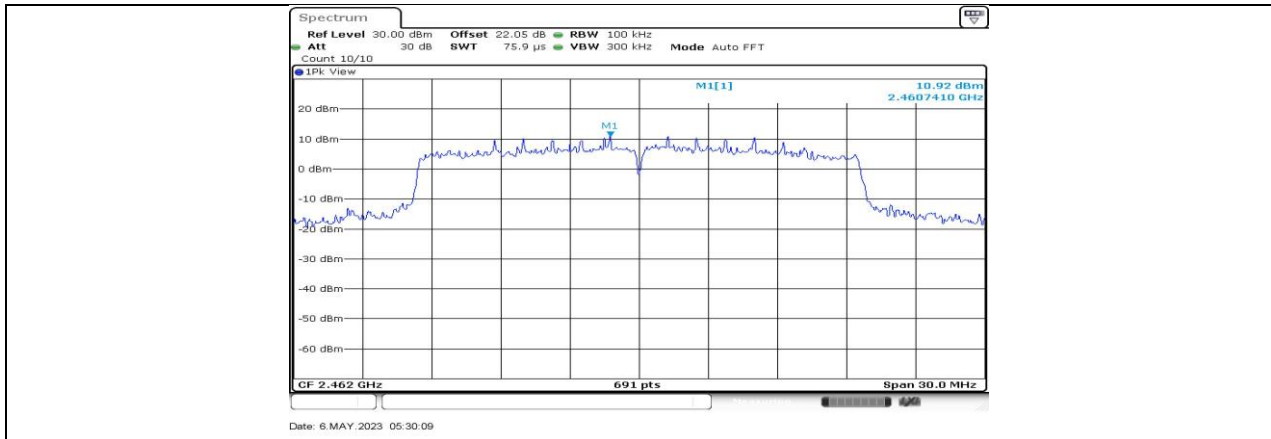
11AX20MIMO_Ant4_2457_0~Reference



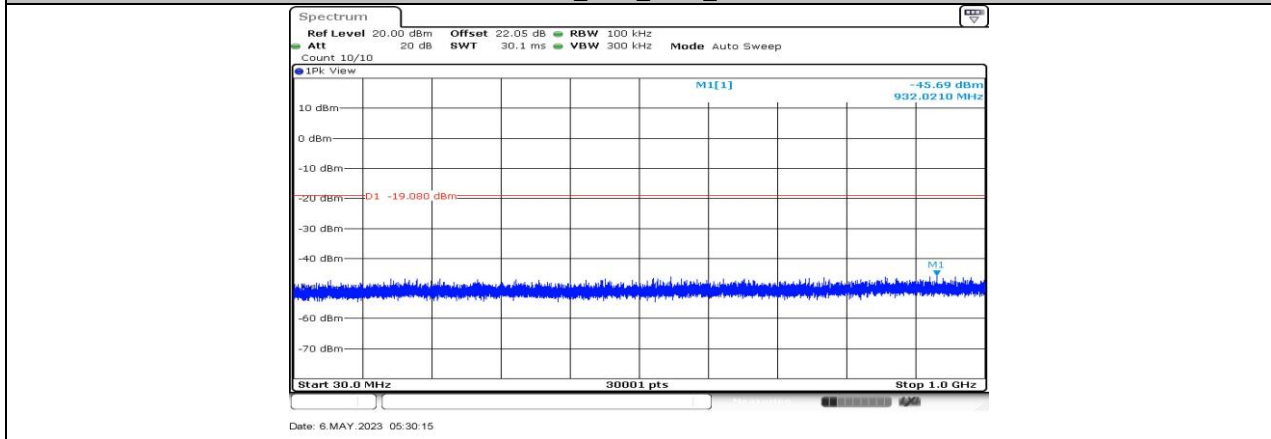
11AX20MIMO_Ant4_2457_30~1000



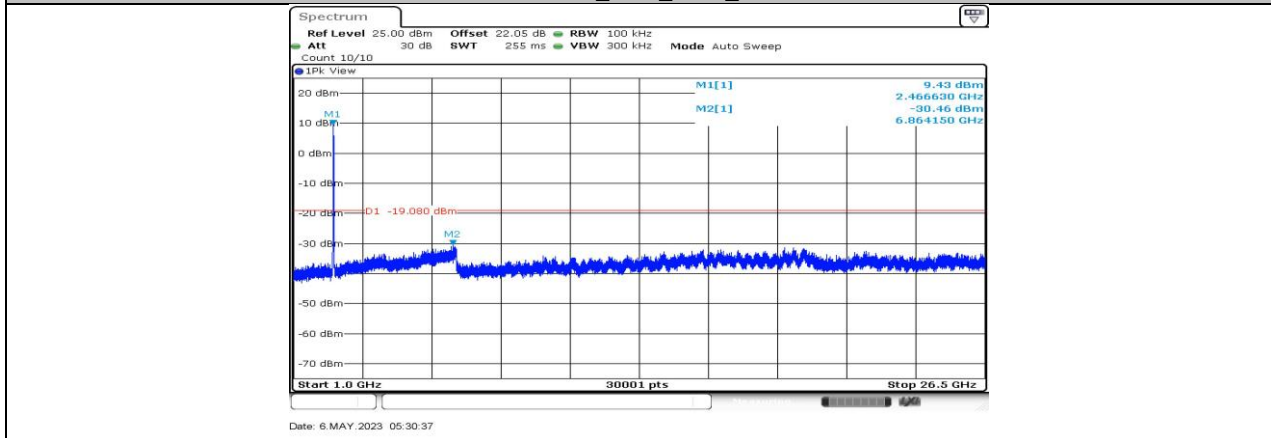
11AX20MIMO_Ant4_2457_1000~26500



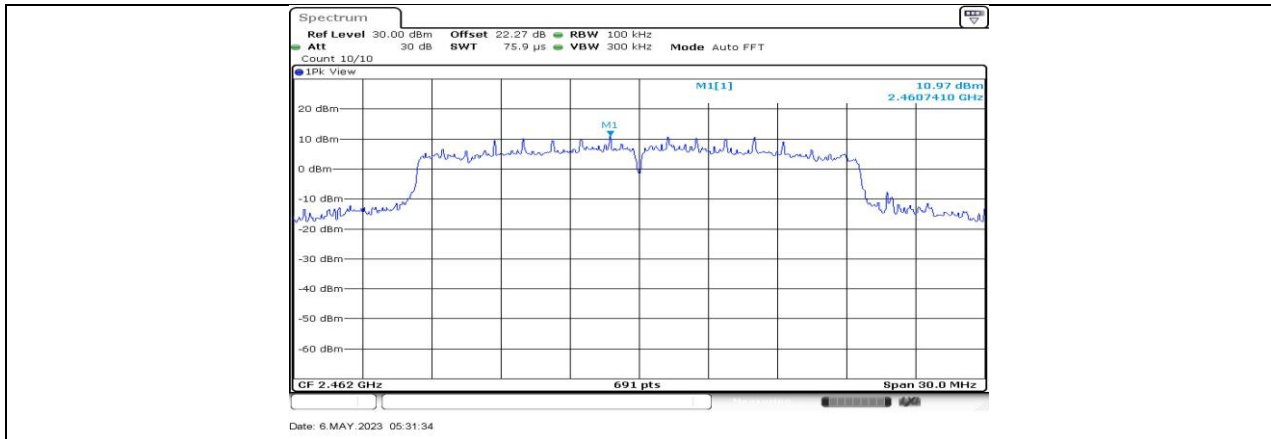
11AX20MIMO_Ant2_2462_0-Reference



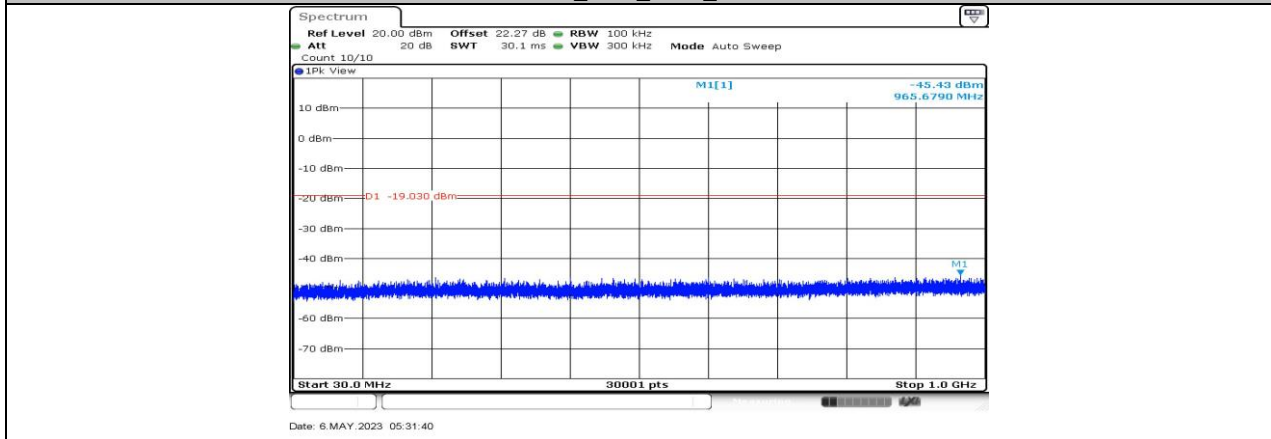
11AX20MIMO_Ant2_2462_30~1000



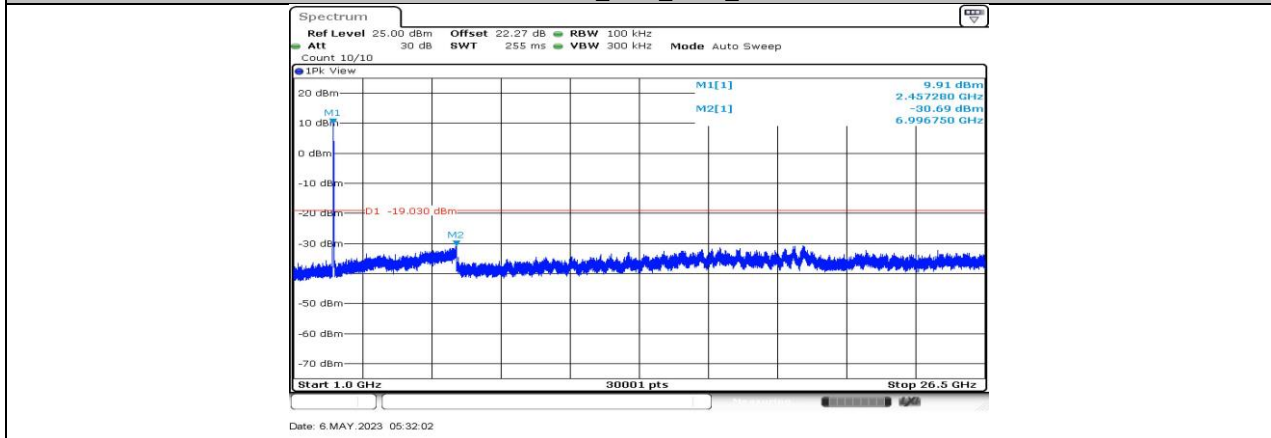
11AX20MIMO_Ant2_2462_1000~26500



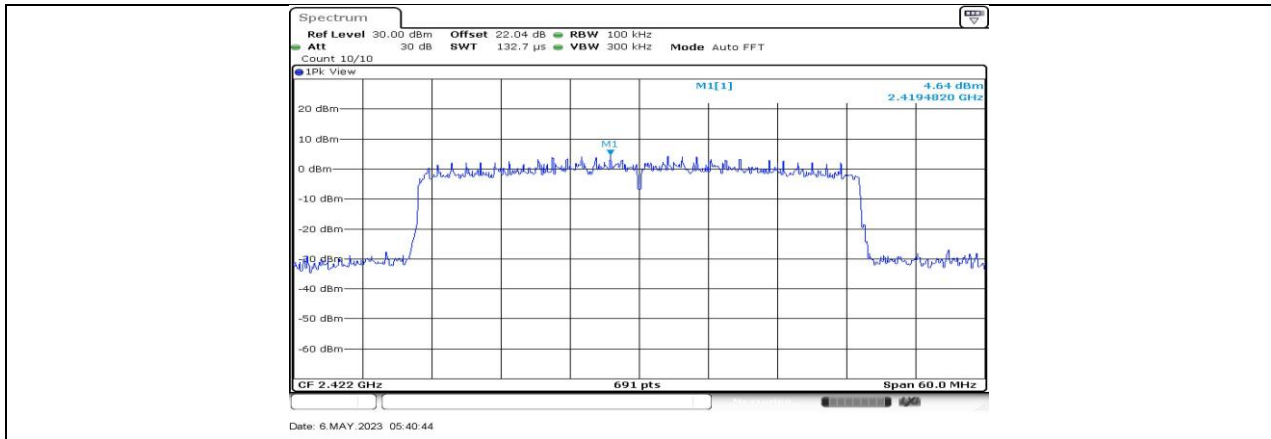
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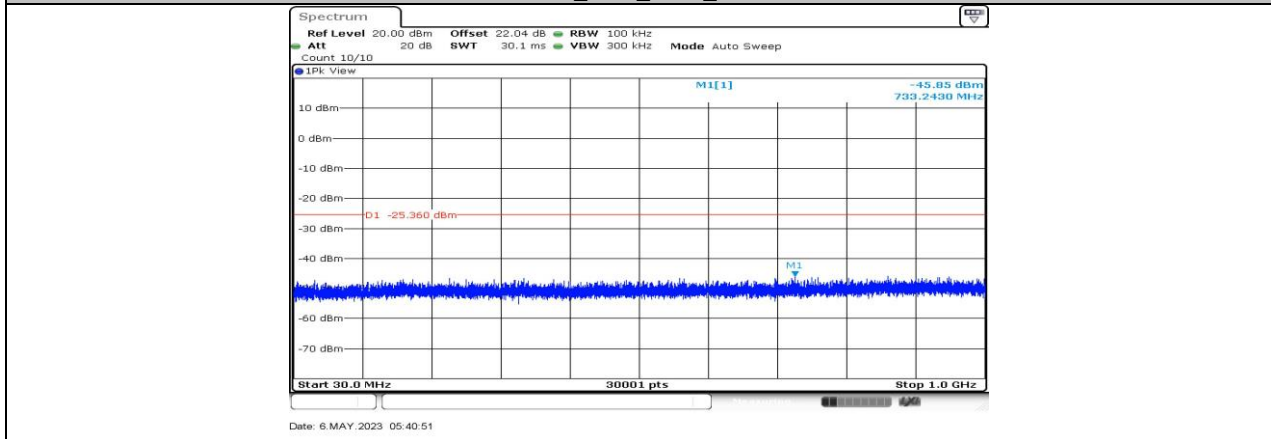
11AX20MIMO_Ant4_2462_30-1000



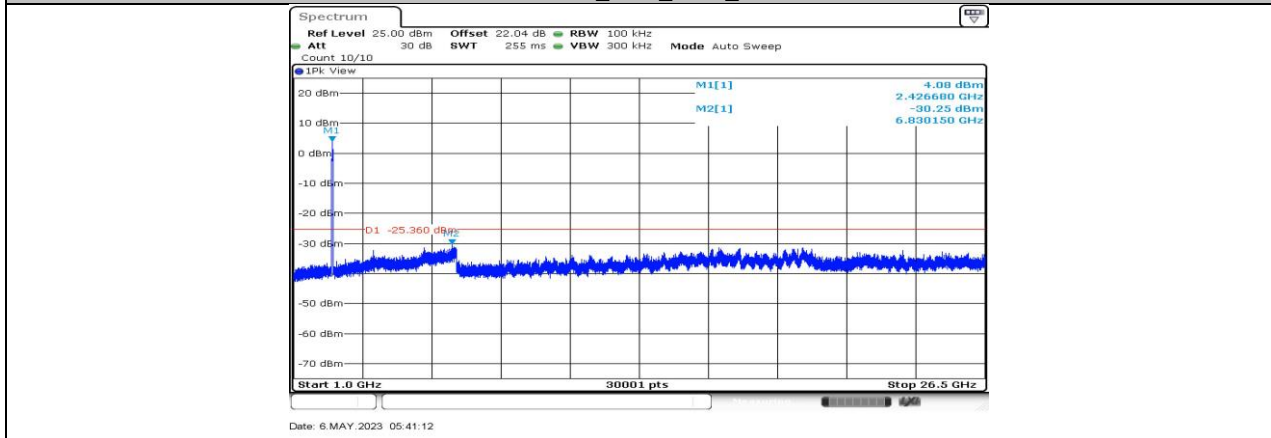
11AX20MIMO_Ant4_2462_1000-26500



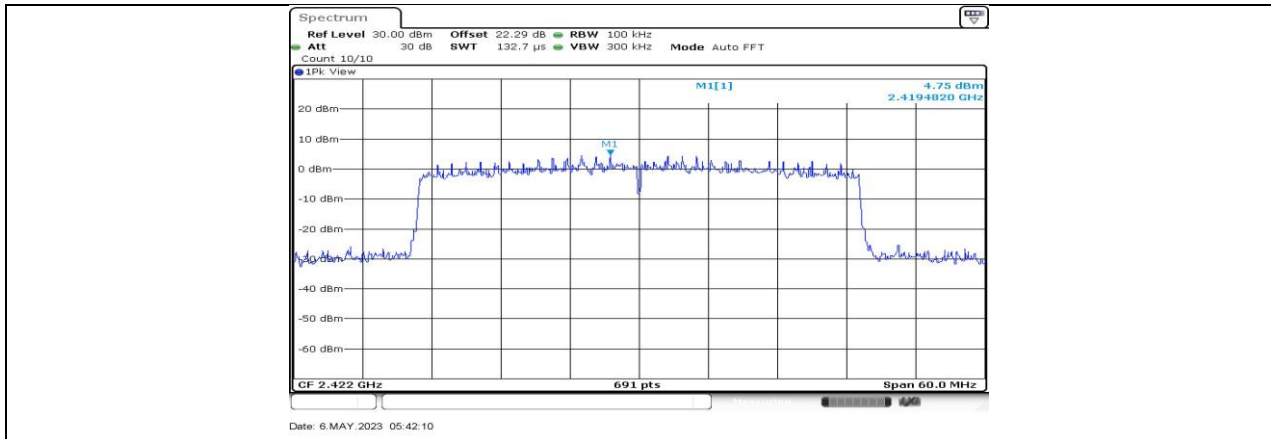
11AX40MIMO_Ant2_2422_0~Reference



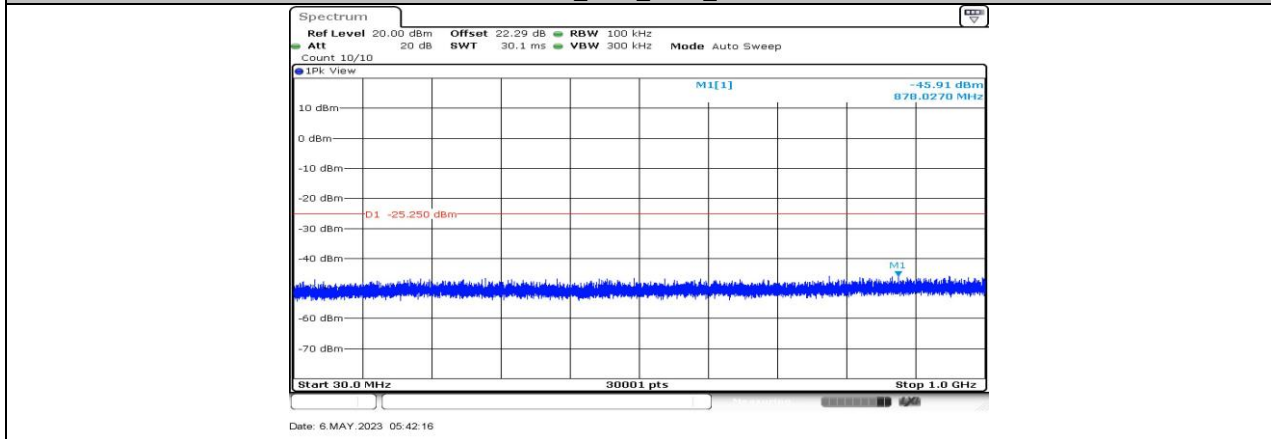
11AX40MIMO_Ant2_2422_30~1000



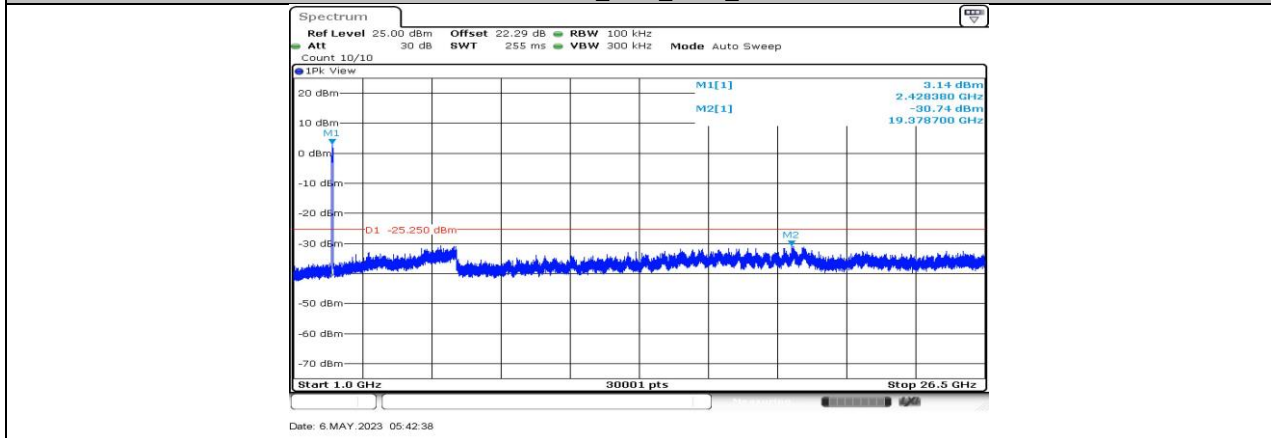
11AX40MIMO_Ant2_2422_1000~26500



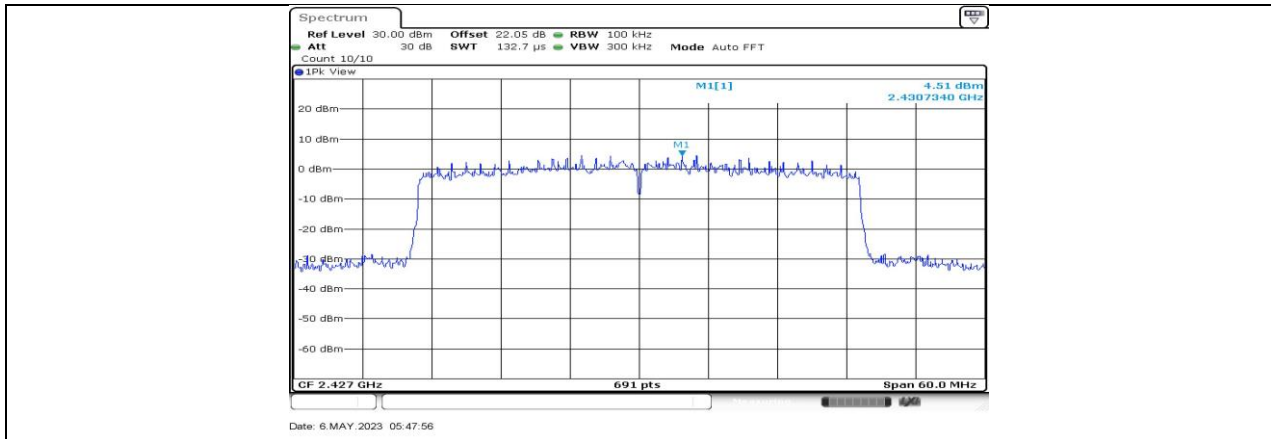
11AX40MIMO_Ant4_2422_0~Reference



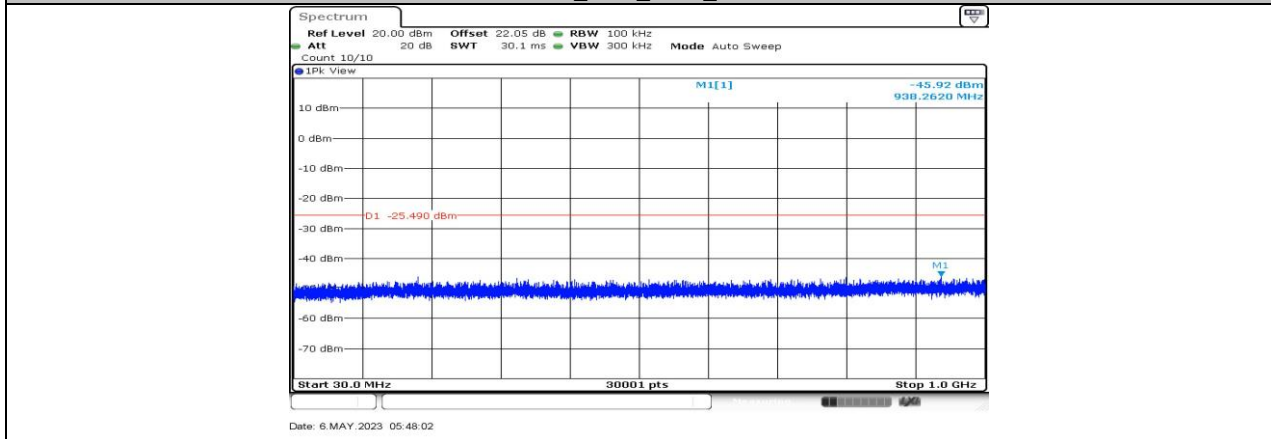
11AX40MIMO_Ant4_2422_30~1000



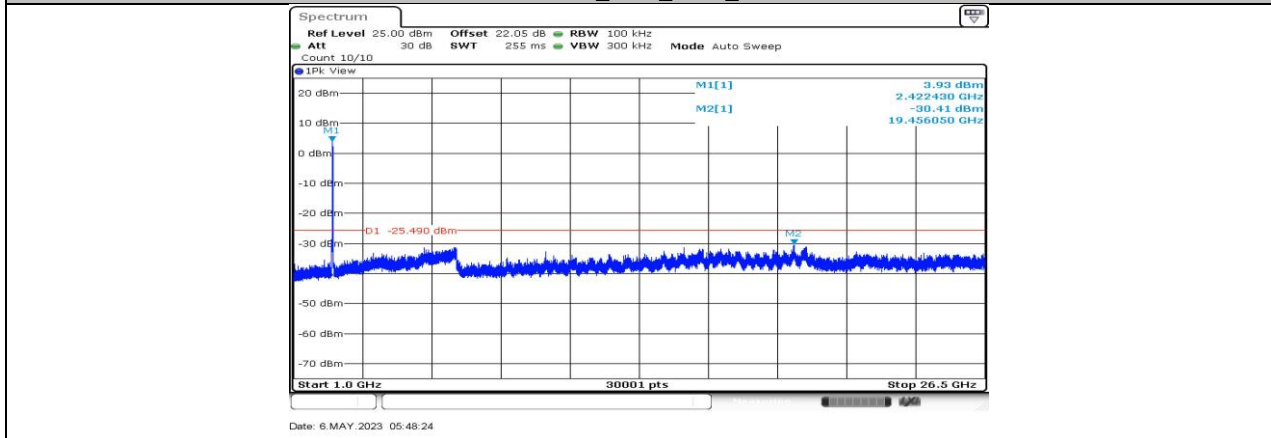
11AX40MIMO_Ant4_2422_1000~26500



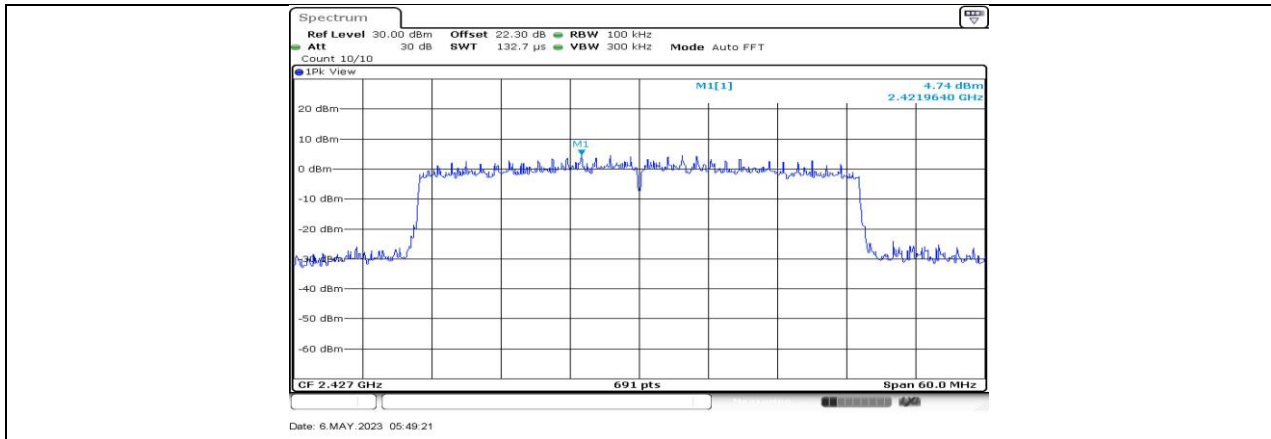
11AX40MIMO_Ant2_2427_0~Reference



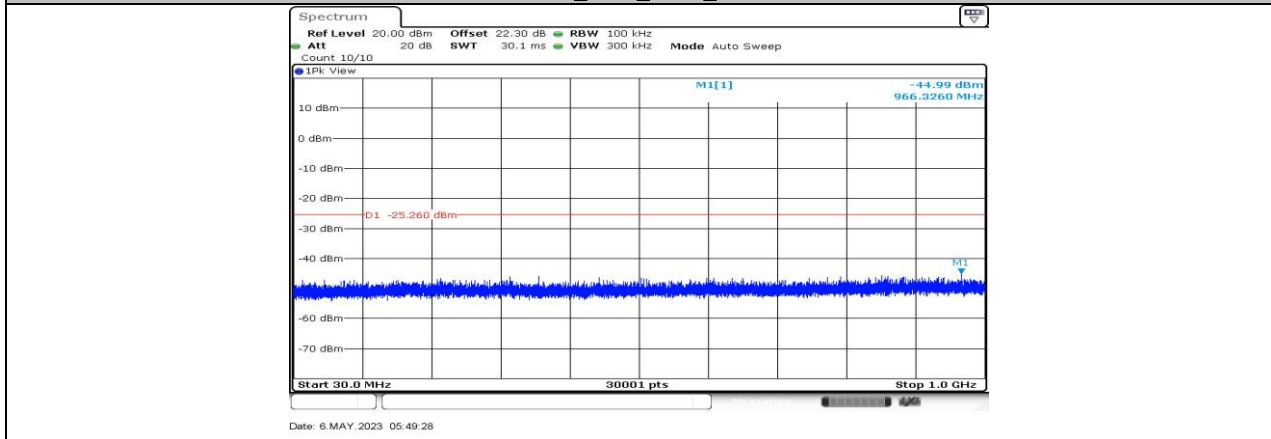
11AX40MIMO_Ant2_2427_30~1000



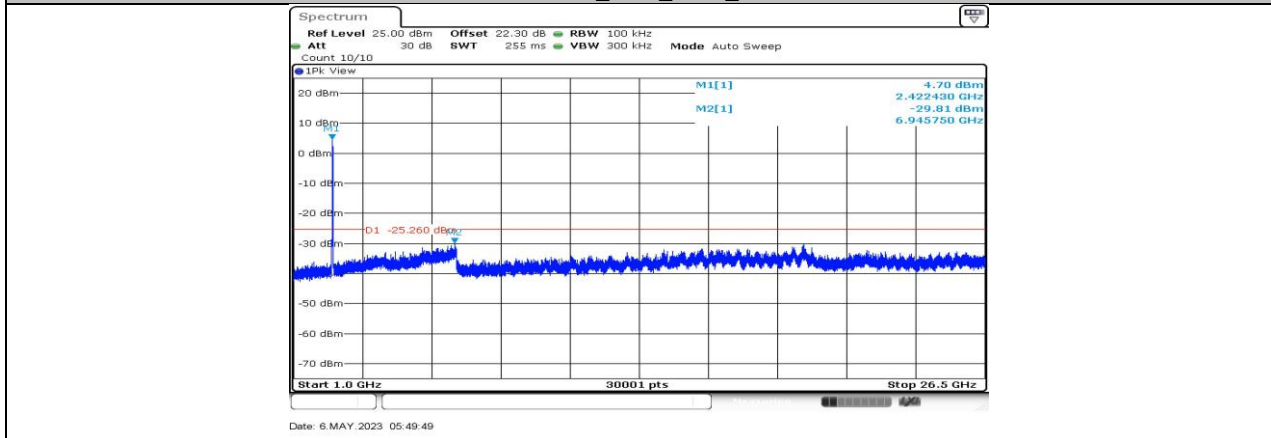
11AX40MIMO_Ant2_2427_1000~26500



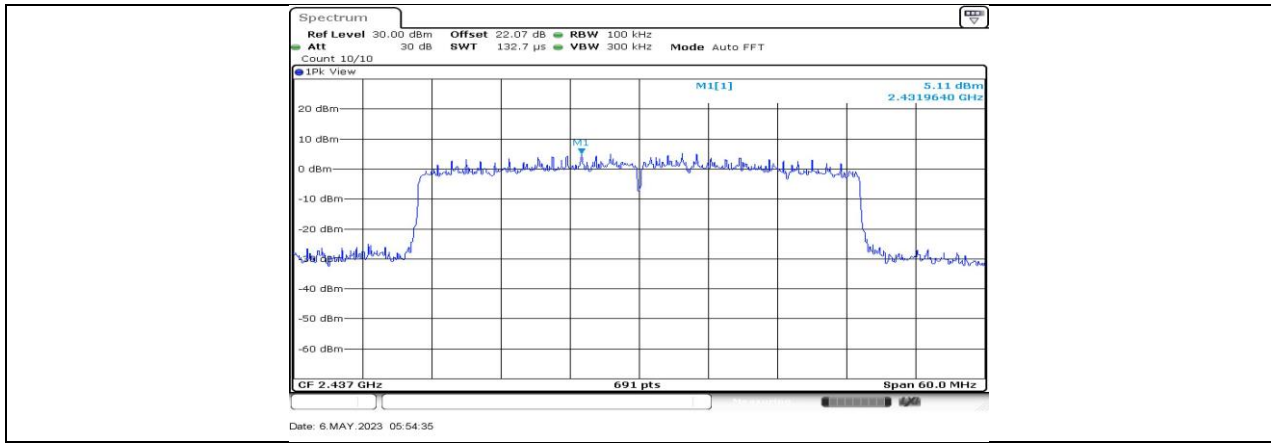
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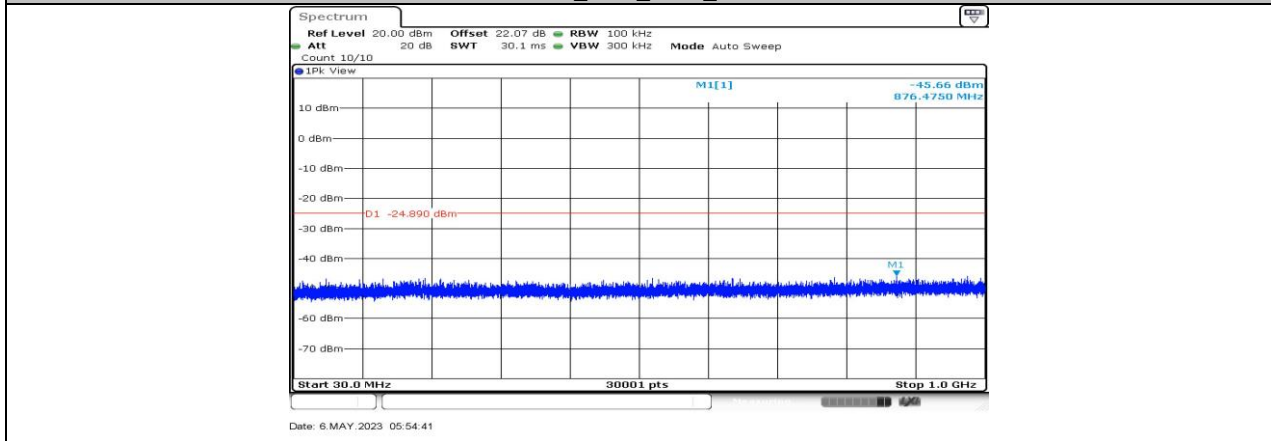
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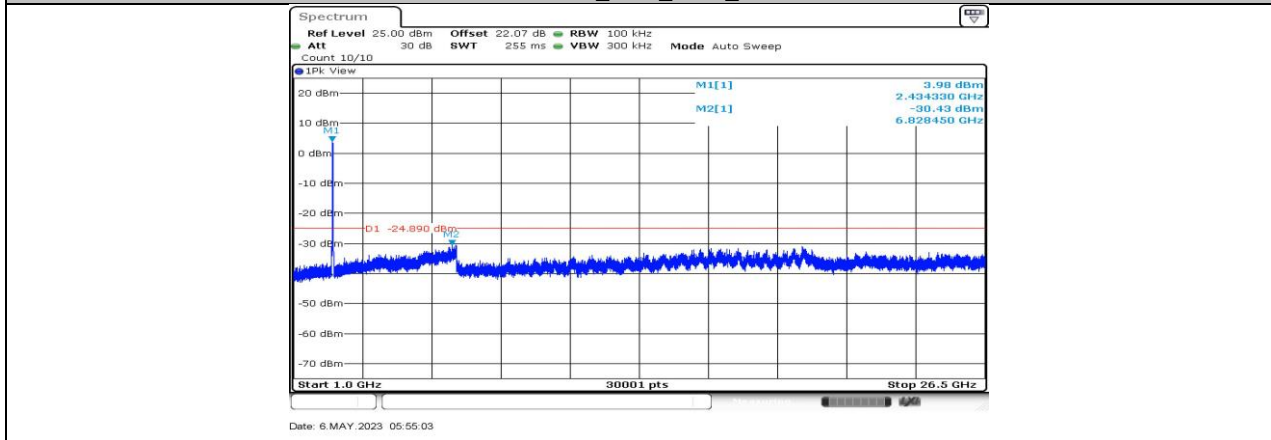
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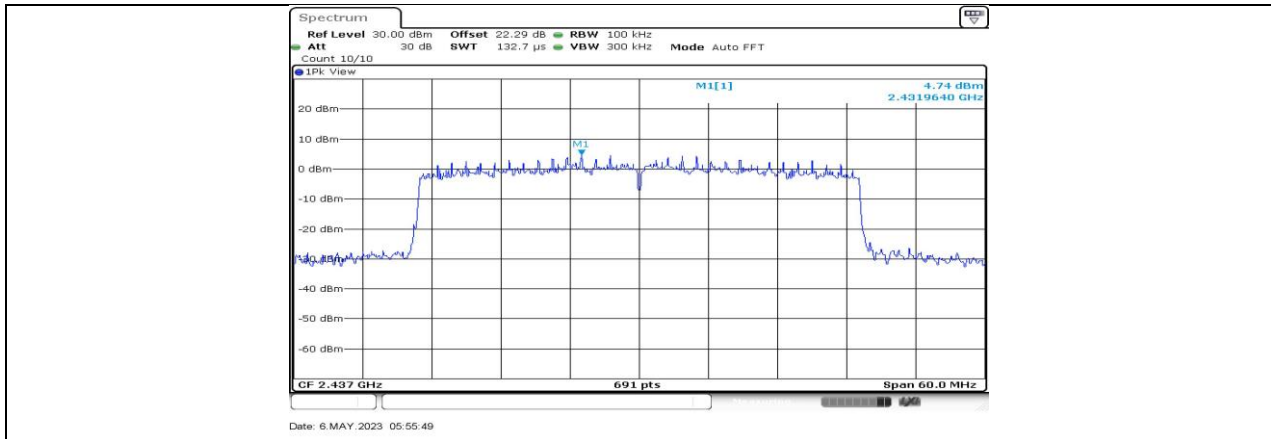
11AX40MIMO_Ant2_2437_0-Reference



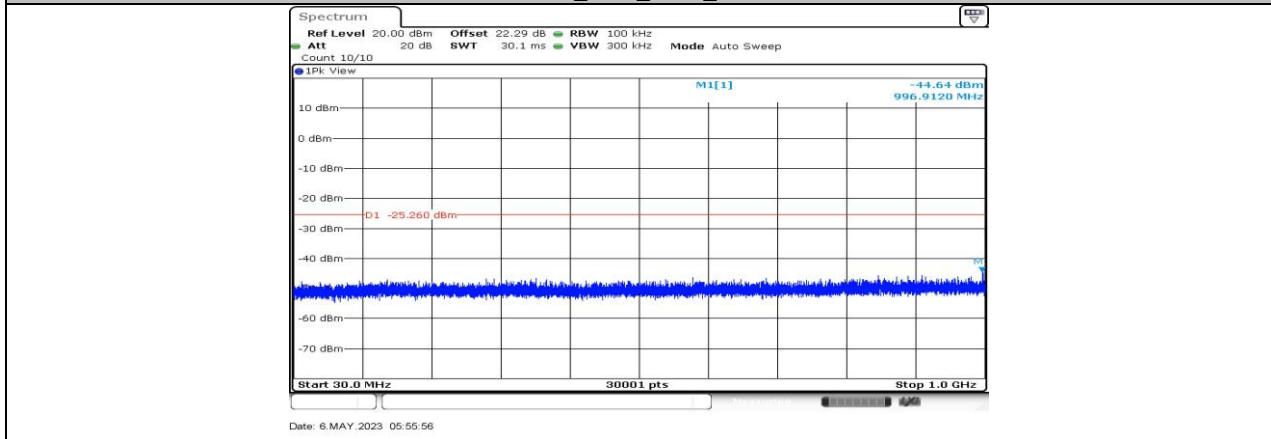
11AX40MIMO_Ant2_2437_30-1000



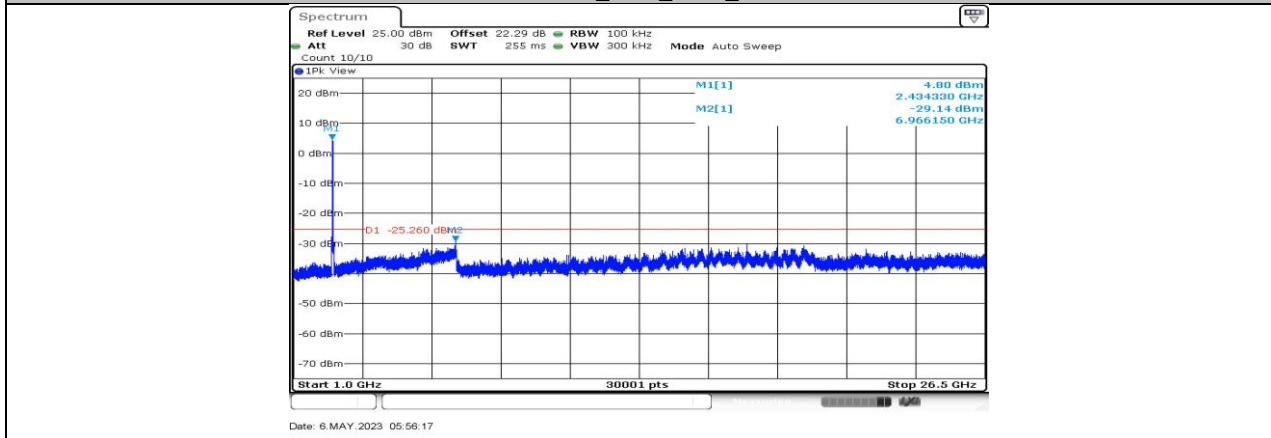
11AX40MIMO_Ant2_2437_1000-26500



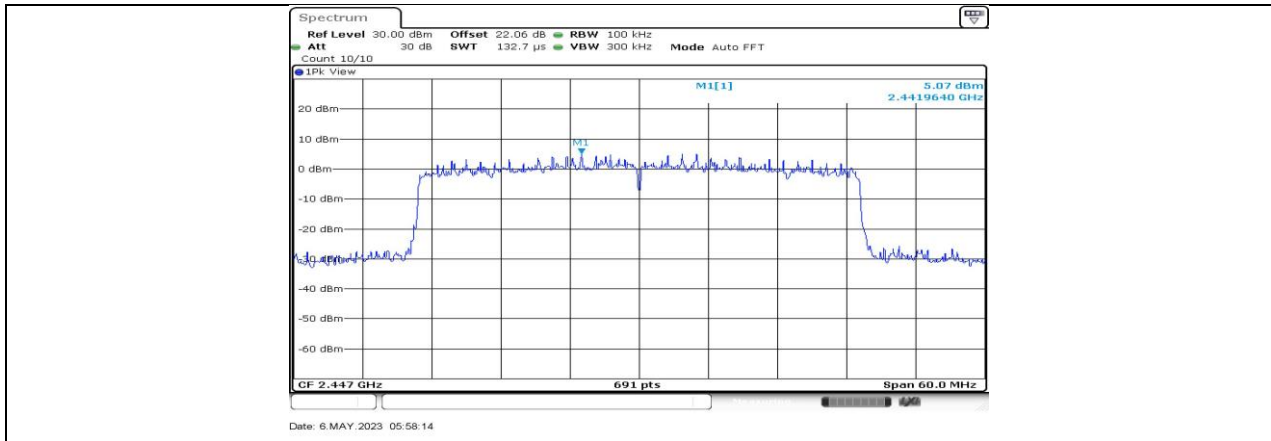
11AX40MIMO_Ant4_2437_0~Reference



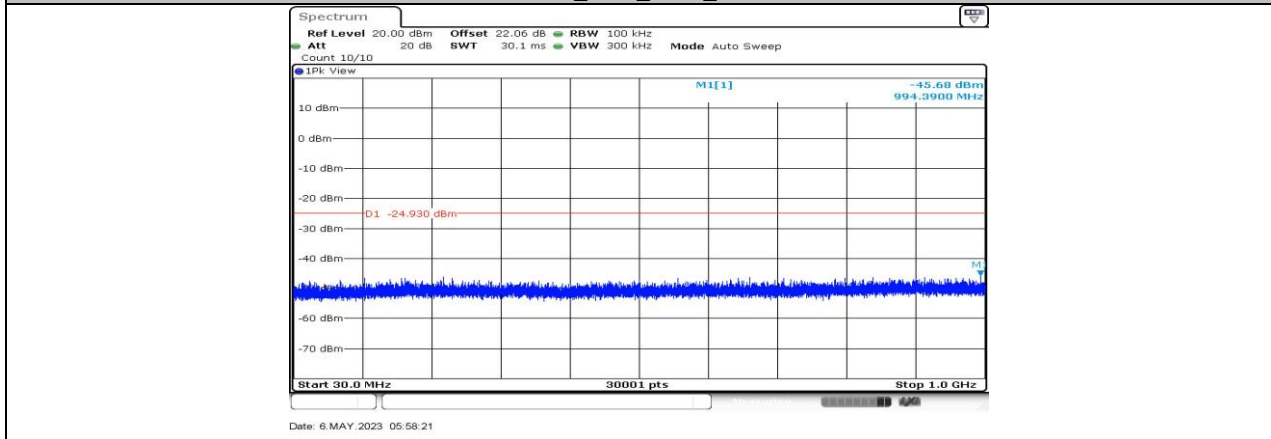
11AX40MIMO_Ant4_2437_30~1000



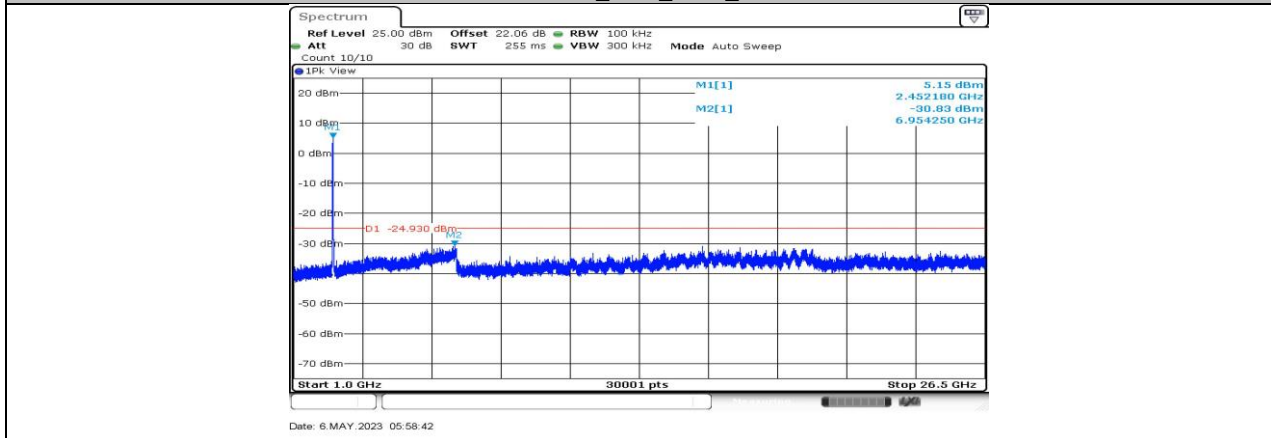
11AX40MIMO_Ant4_2437_1000~26500



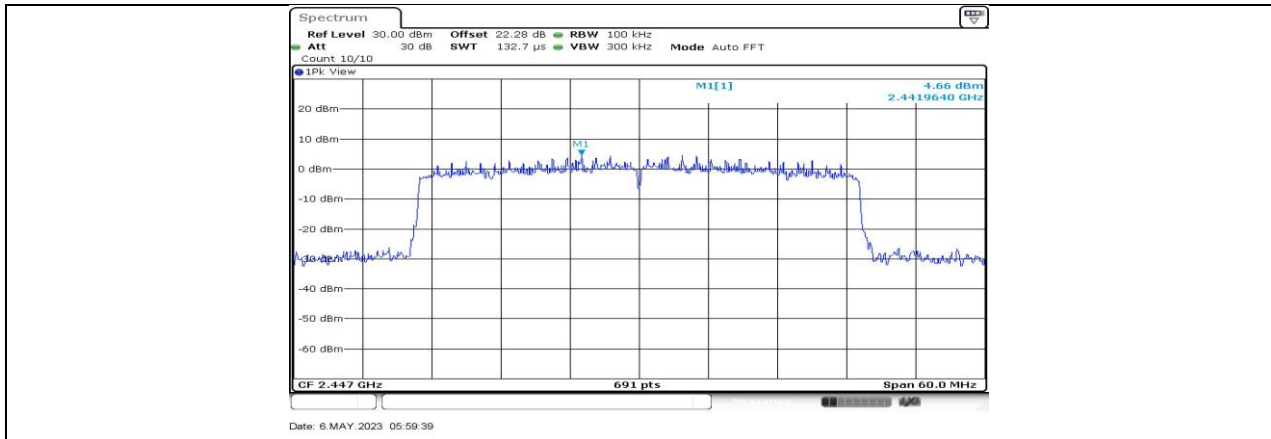
11AX40MIMO_Ant2_2447_0-Reference



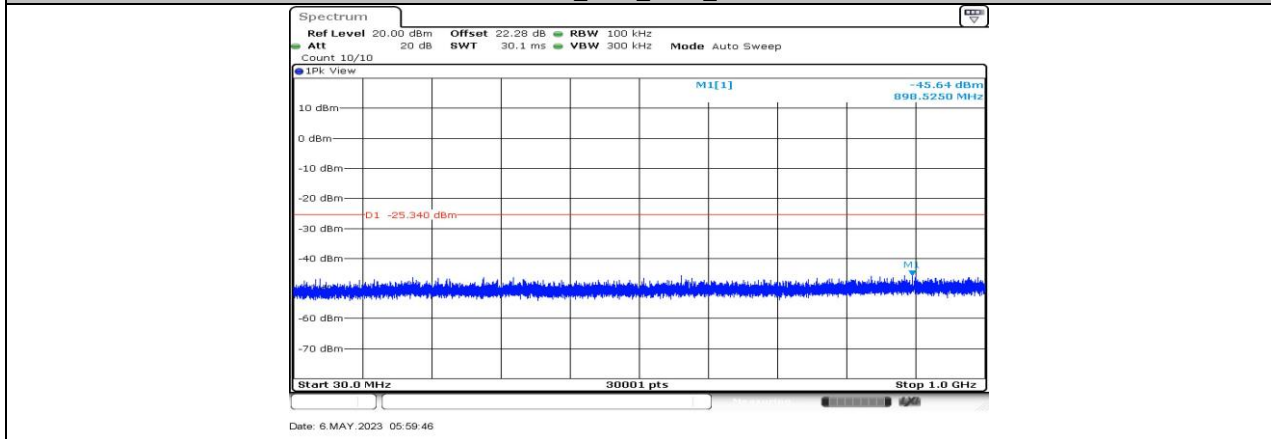
11AX40MIMO_Ant2_2447_30-1000



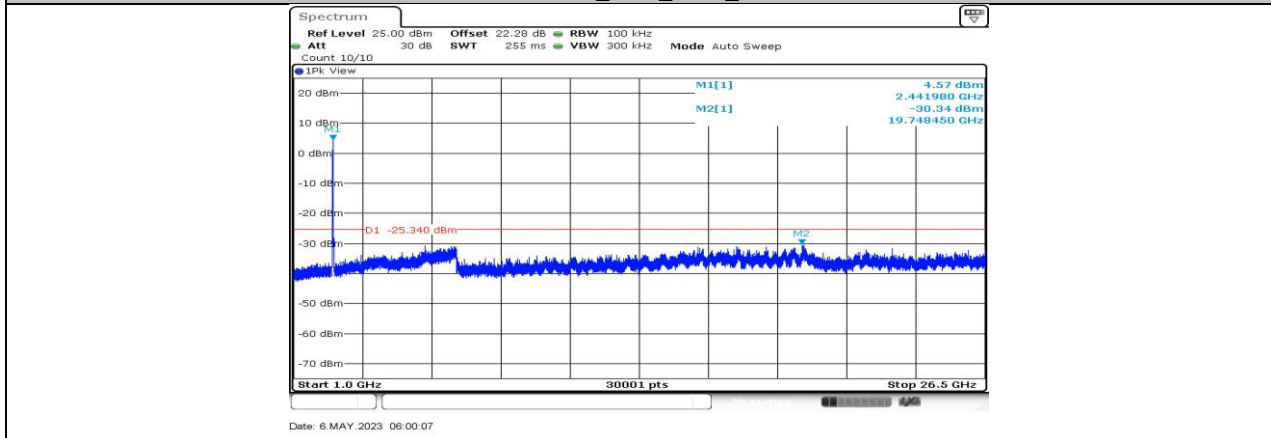
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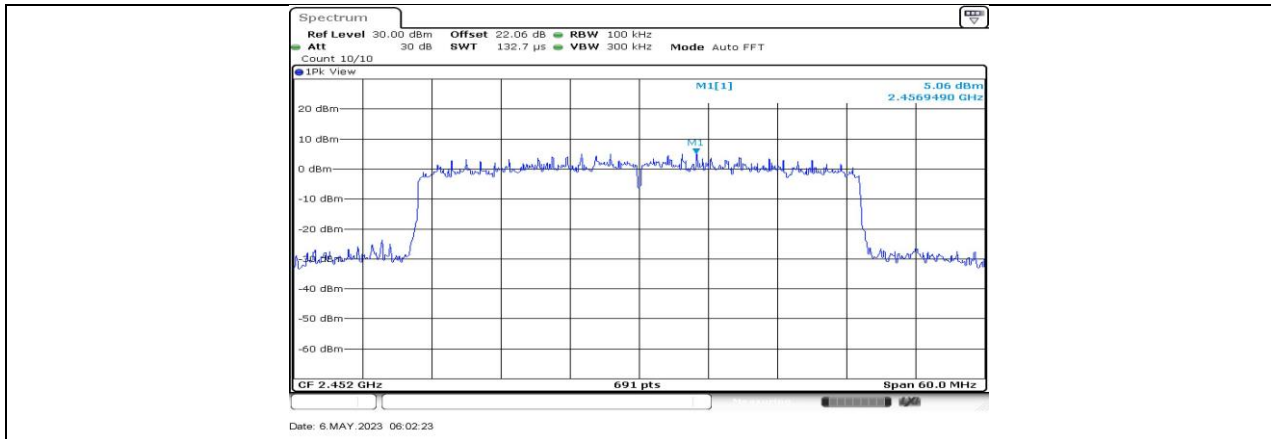
11AX40MIMO_Ant4_2447_0-Reference



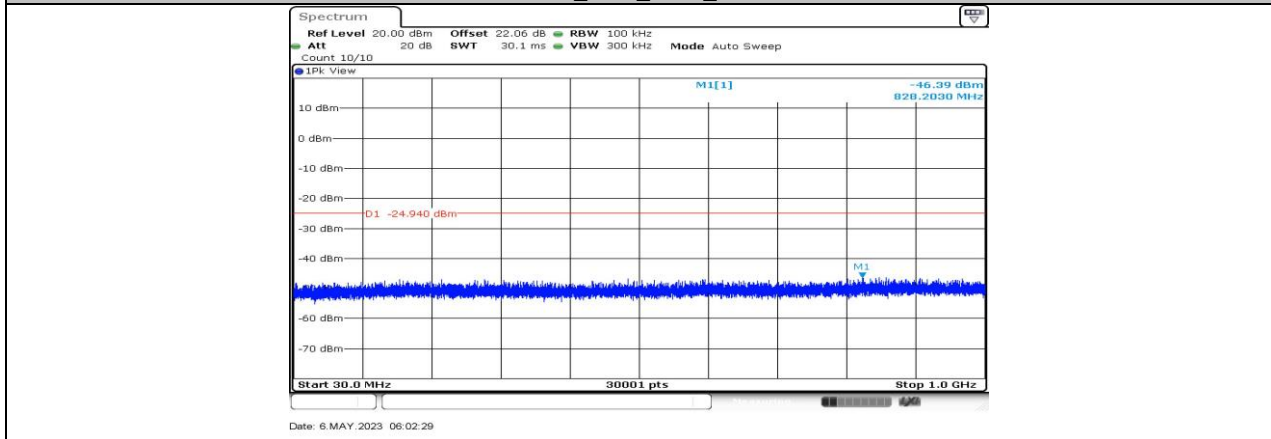
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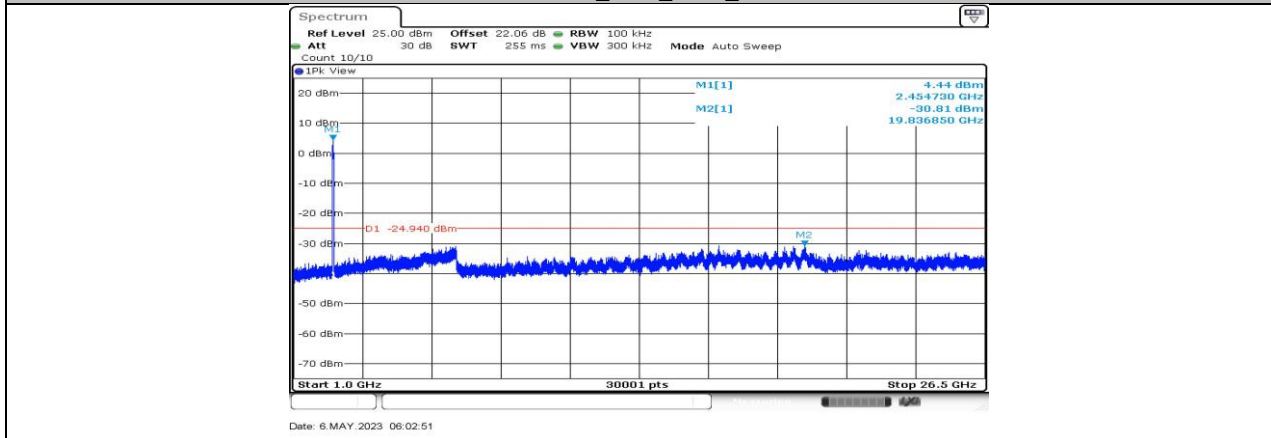
11AX40MIMO_Ant4_2447_1000-26500



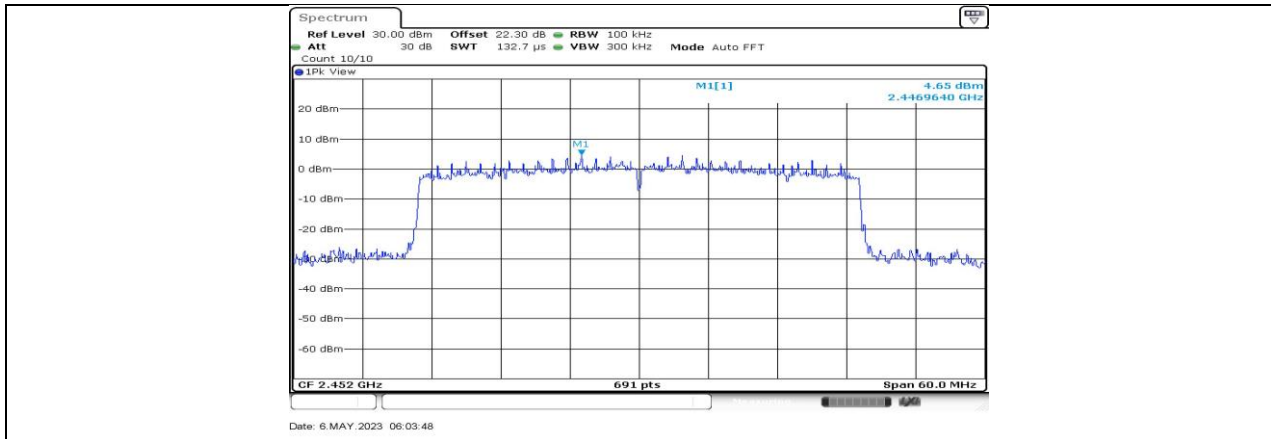
11AX40MIMO_Ant2_2452_0~Reference



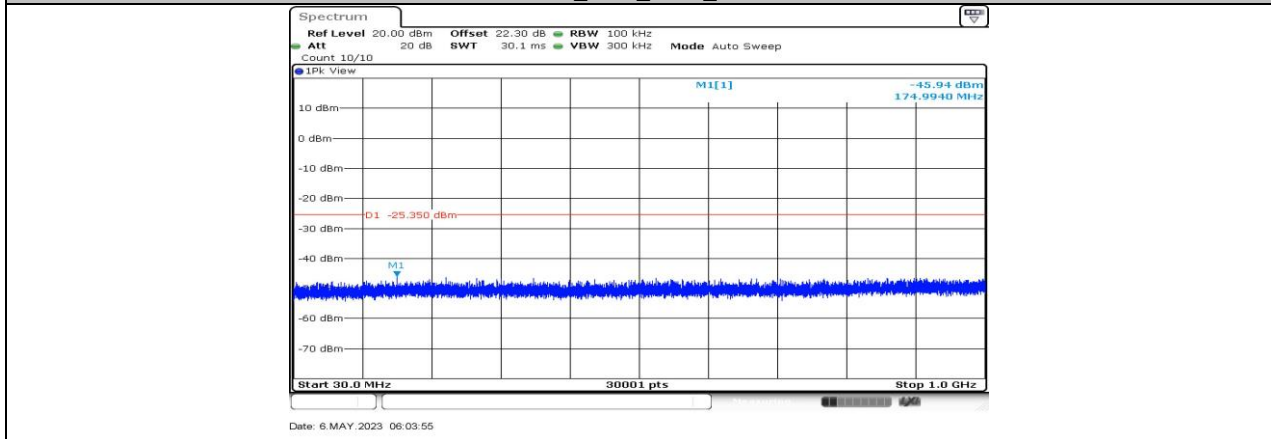
11AX40MIMO_Ant2_2452_30~1000



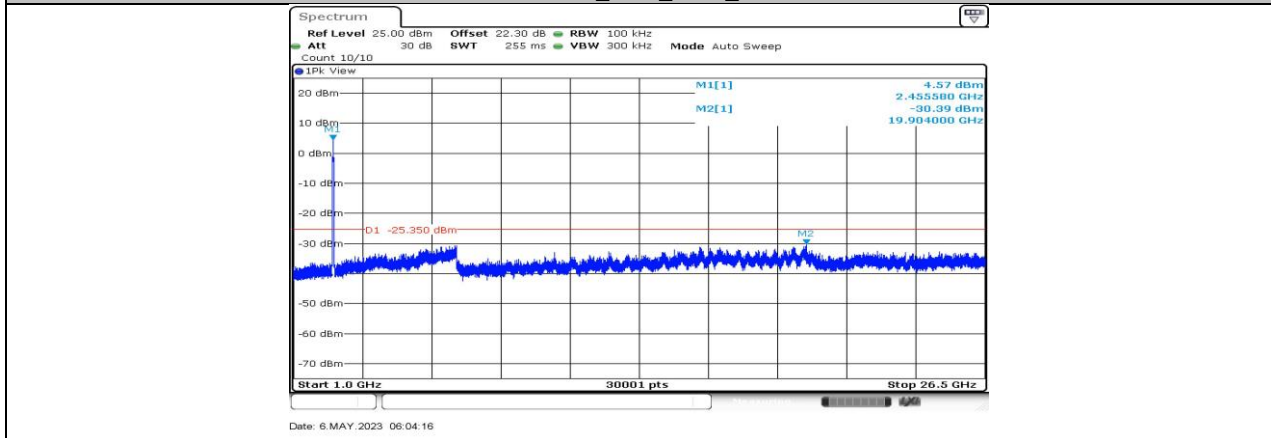
11AX40MIMO_Ant2_2452_1000~26500



11AX40MIMO_Ant4_2452_0~Reference



11AX40MIMO_Ant4_2452_30~1000



11AX40MIMO_Ant4_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	8.39	8.45	0.9929	99.29	0.03	N/A	0.01
11G-CDD	1.38	1.44	0.9583	95.83	0.18	0.72	1
11AX20MIMO	1.01	1.07	0.9439	94.39	0.25	0.99	1
11AX40MIMO	0.30	0.35	0.8571	85.71	0.67	3.33	4

Note:

Duty Cycle Correction Factor=10log (1/x).

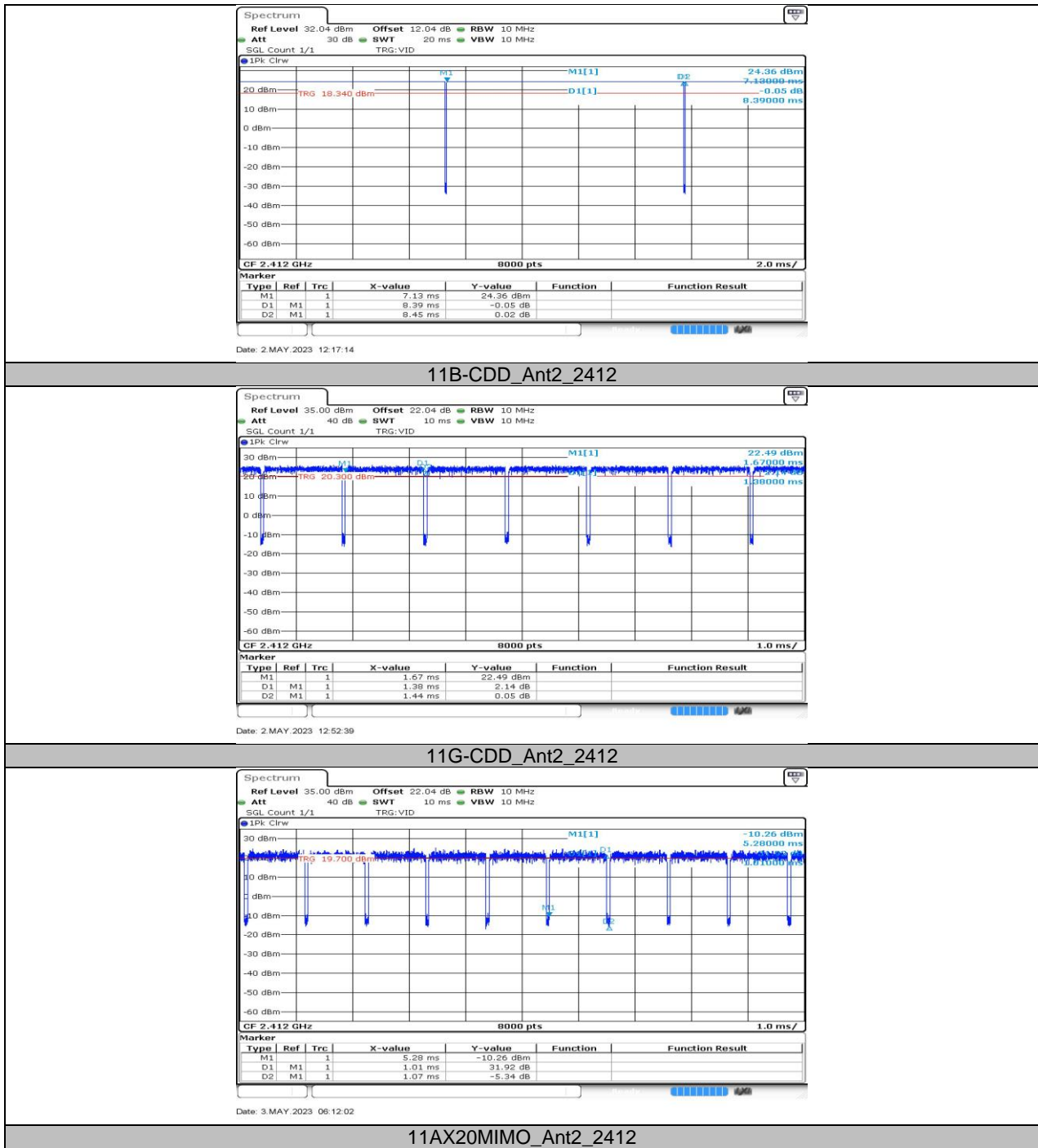
Where: x is Duty Cycle (Linear)

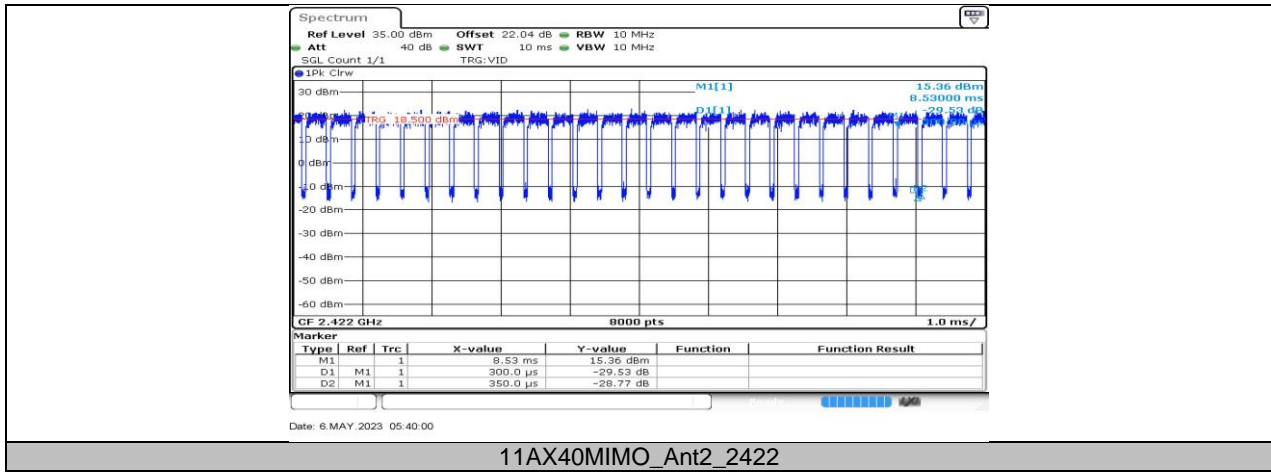
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

If the EUT is configured to transmit with duty cycle $\geq 98\%$, set VBW \leq RBW/100 (i.e., 10 kHz) but not less than 10 Hz.

11.7.2. Test Graphs





END OF REPORT