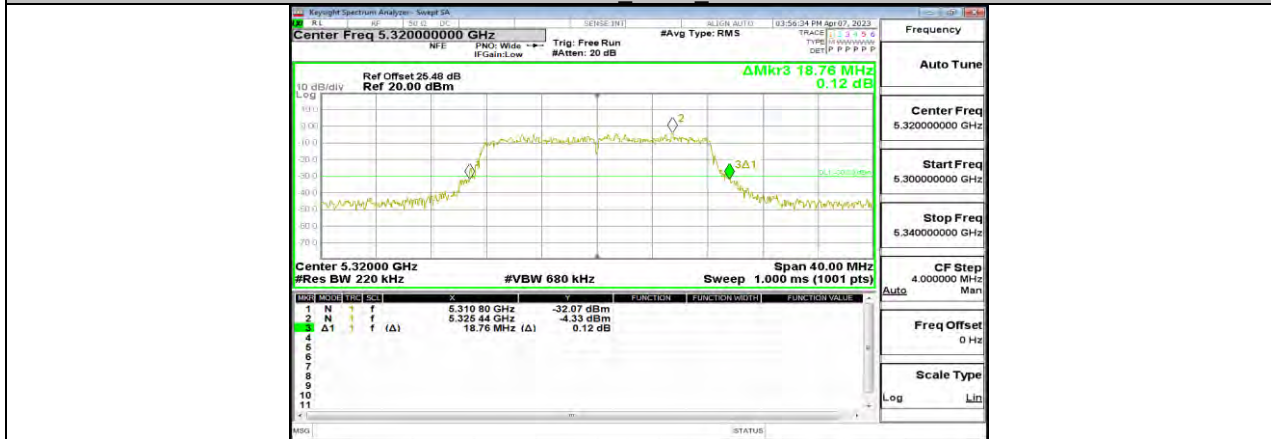


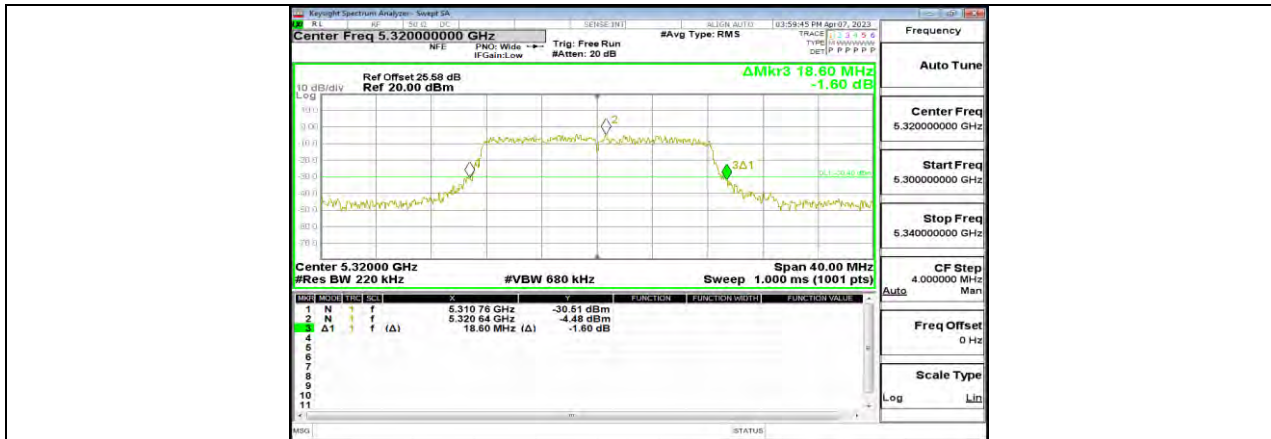
11A-CDD Ant16 5280



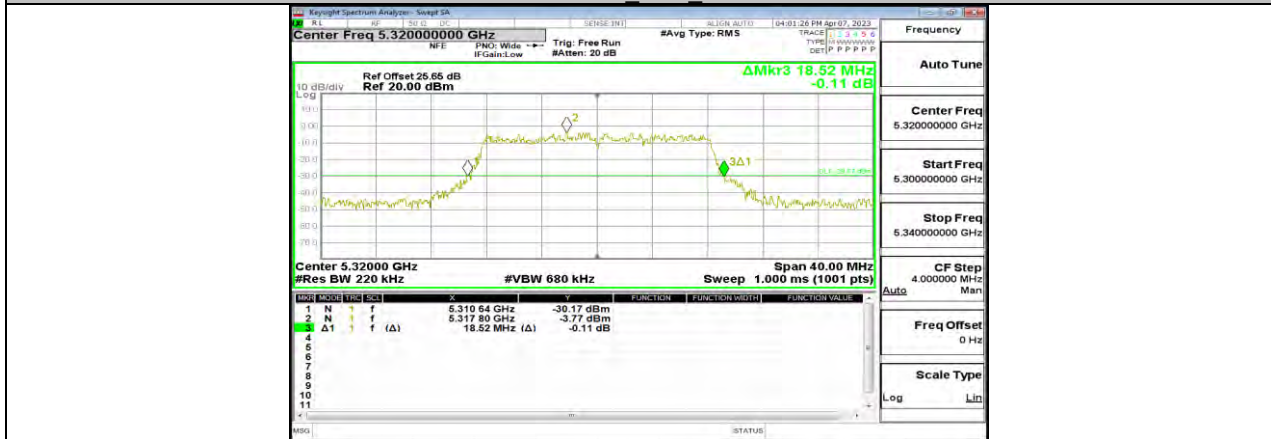
11A-CDD Ant1 5320



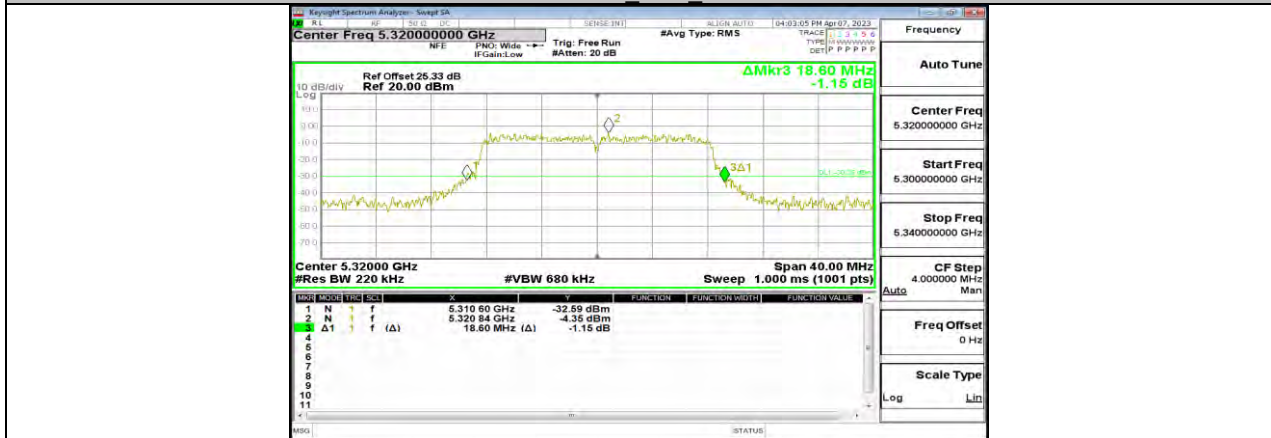
11A-CDD Ant2 5320



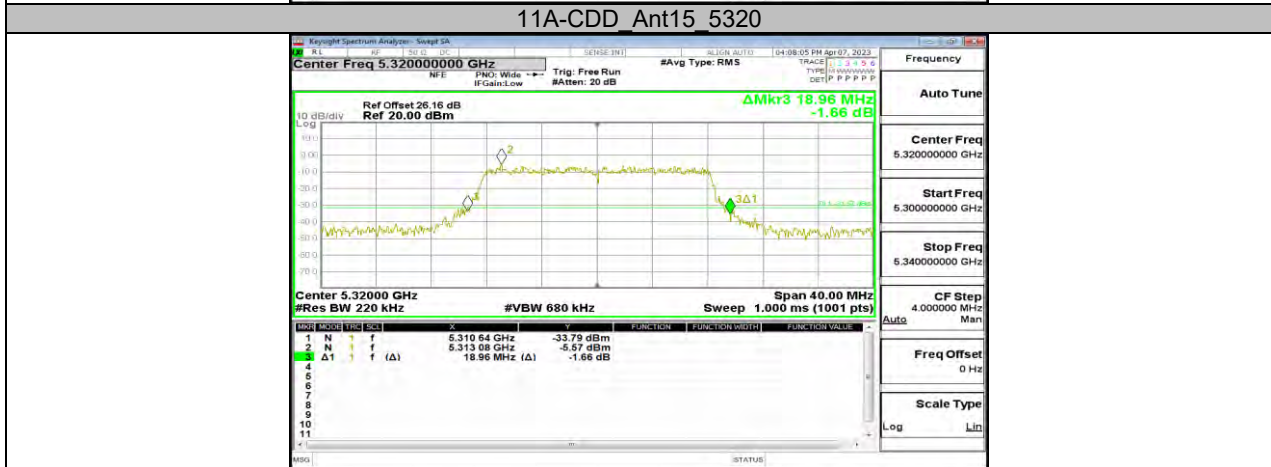
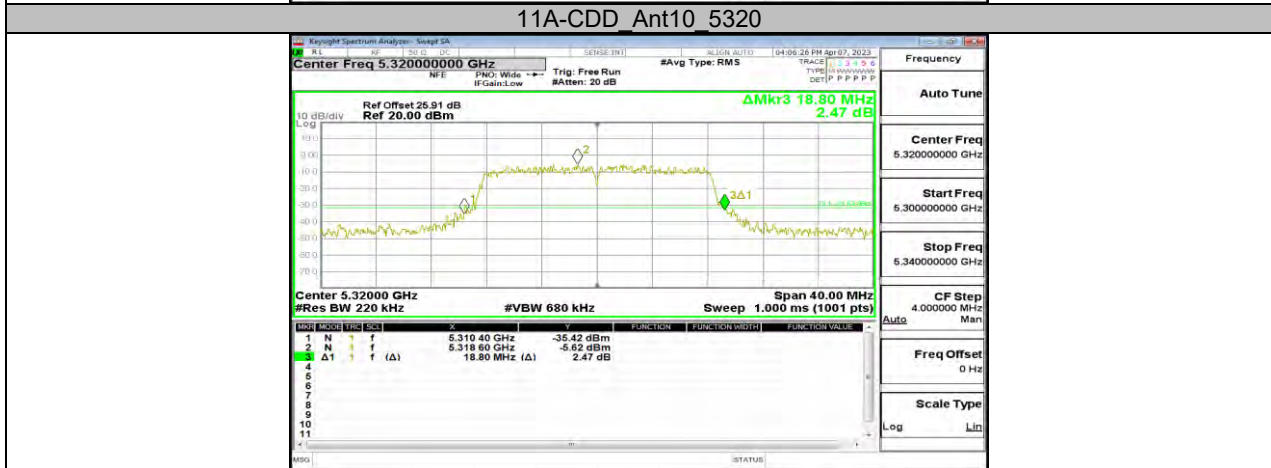
11A-CDD Ant3 5320



11A-CDD Ant4 5320

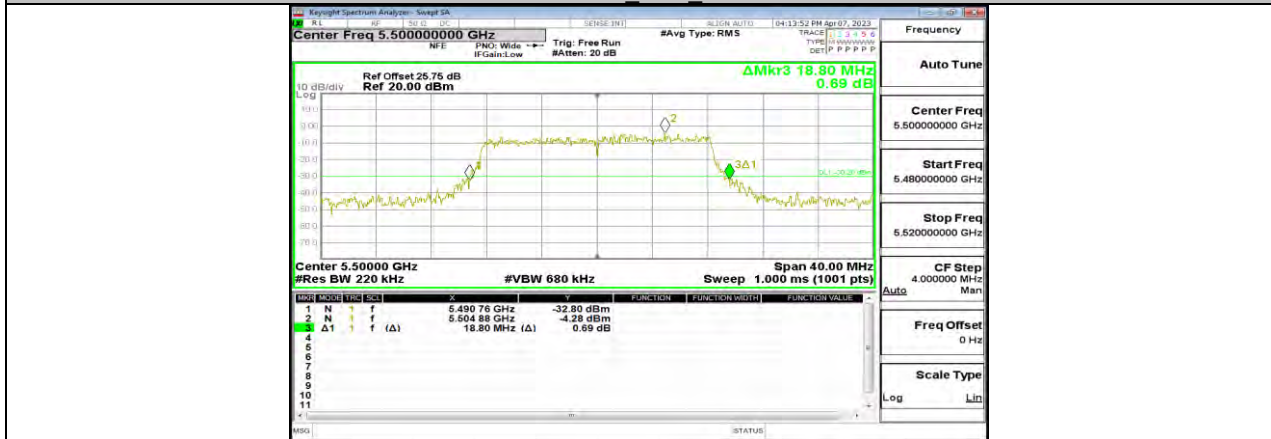


11A-CDD Ant9 5320

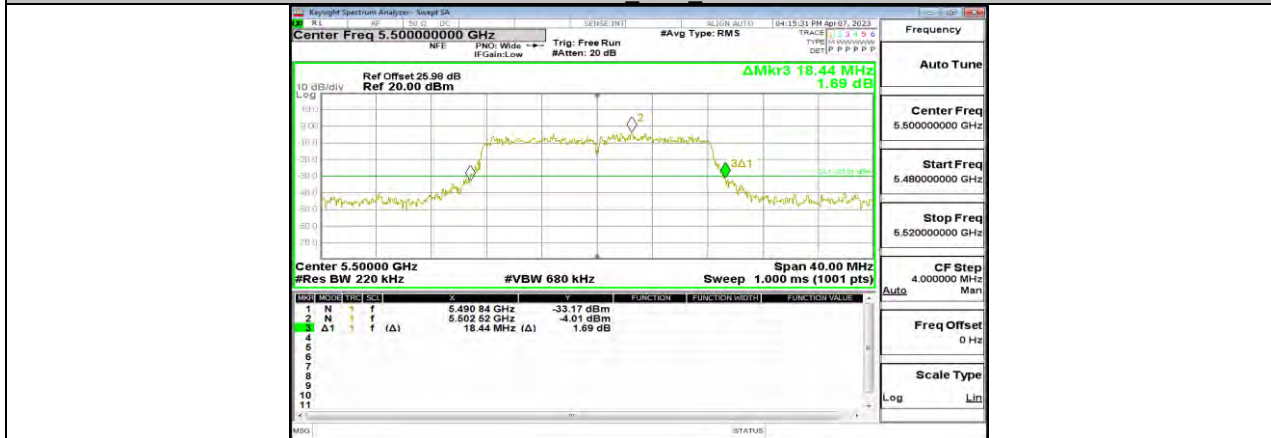




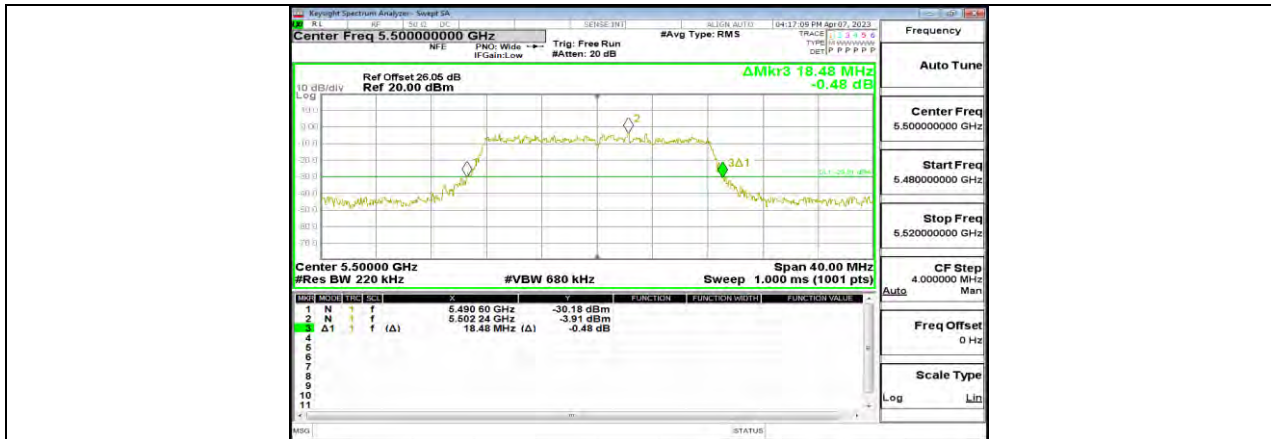
11A-CDD Ant1 5500



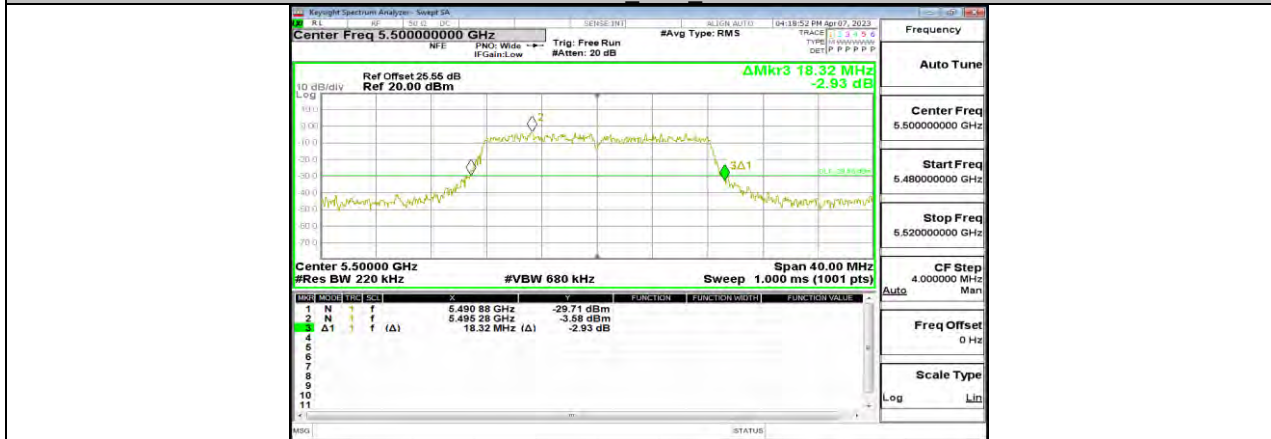
11A-CDD Ant2 5500



11A-CDD Ant3 5500



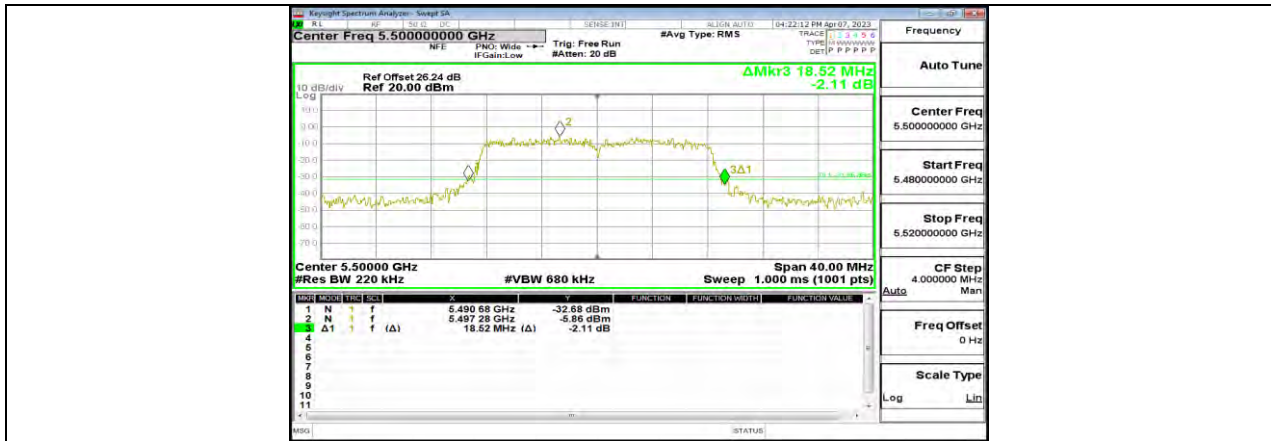
11A-CDD Ant4 5500



11A-CDD Ant9 5500



11A-CDD Ant10 5500



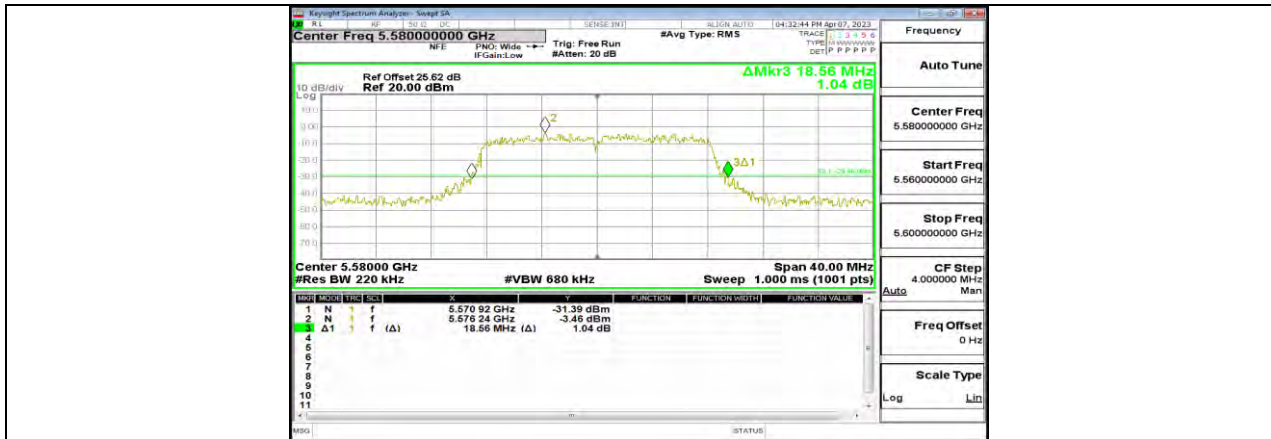
11A-CDD Ant15 5500



11A-CDD Ant16 5500



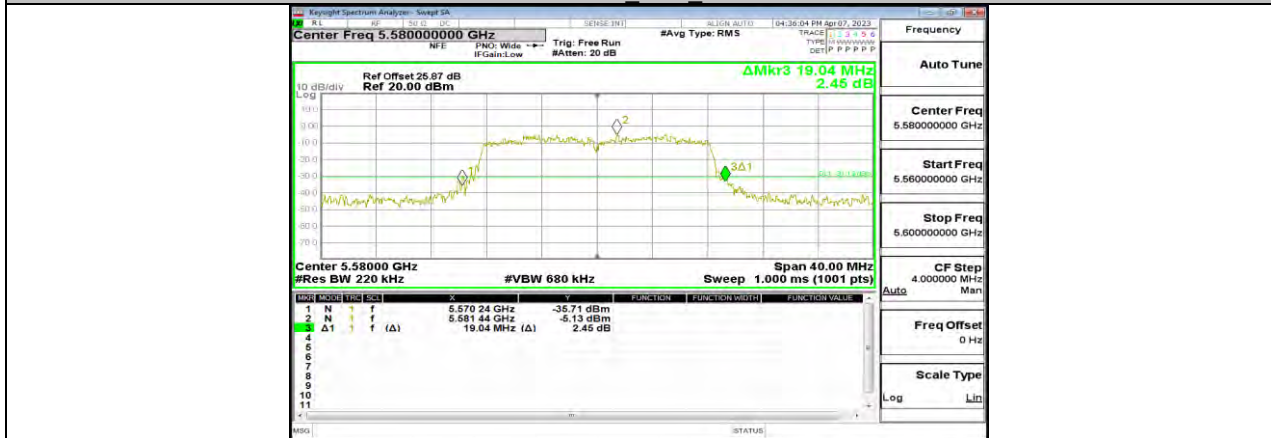
11A-CDD Ant1 5580



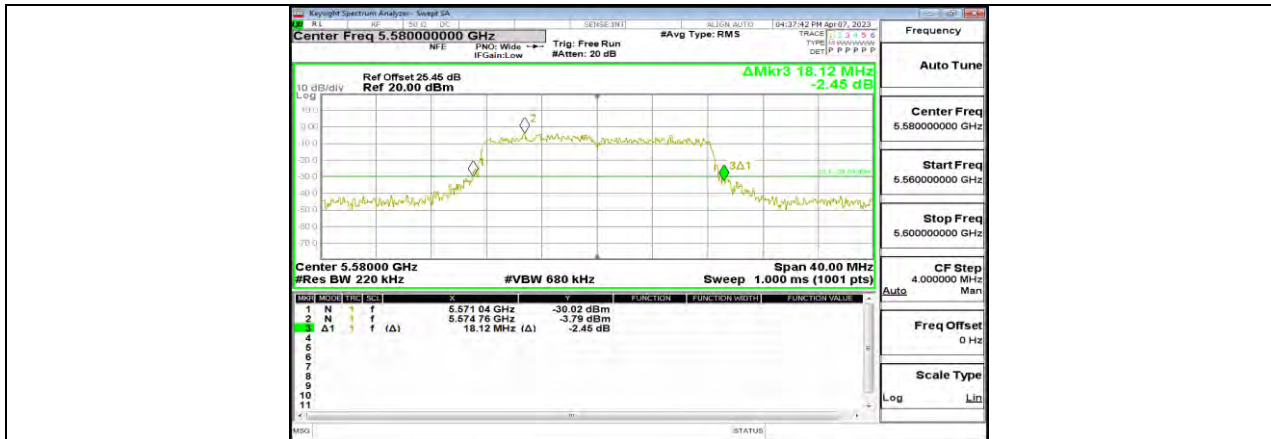
11A-CDD Ant2 5580



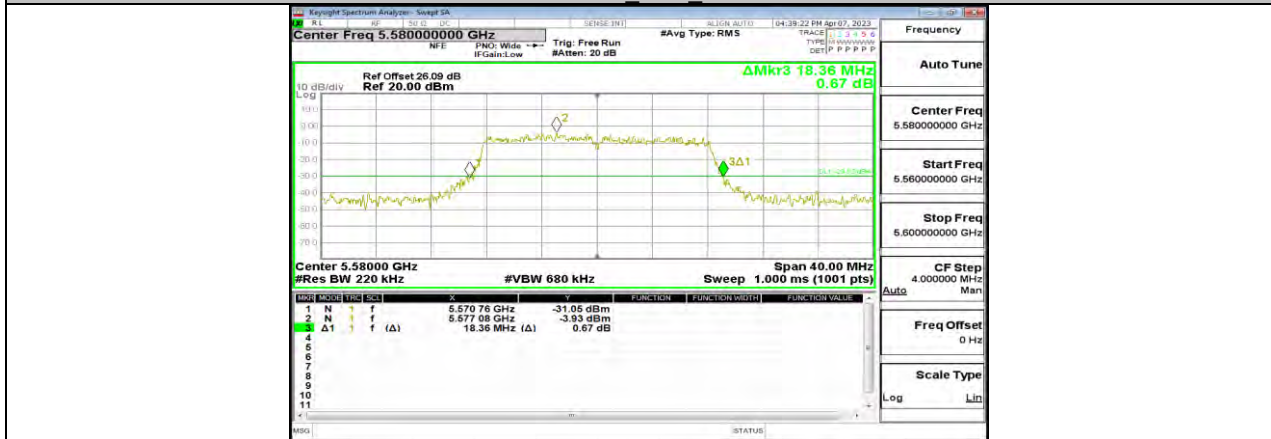
11A-CDD Ant3 5580



11A-CDD Ant4 5580



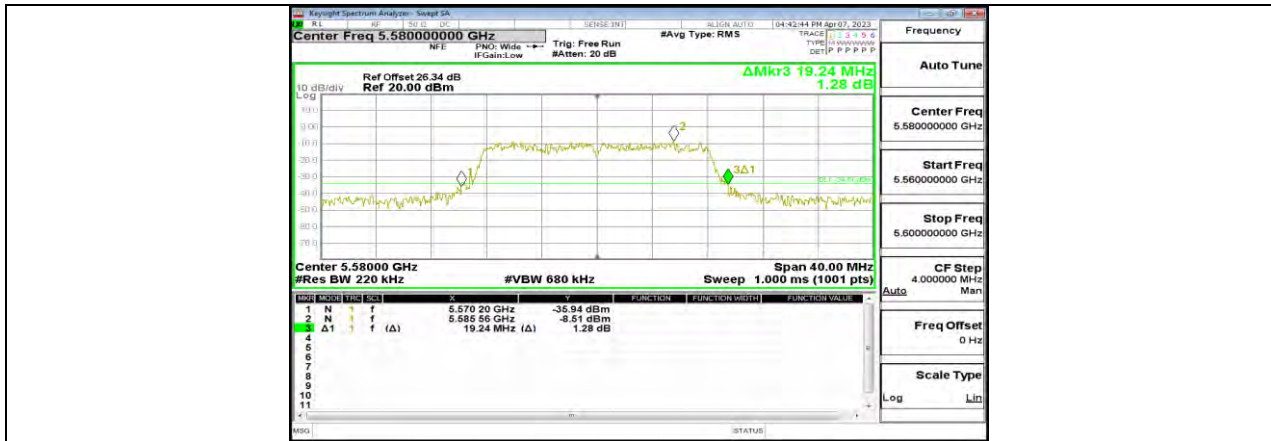
11A-CDD Ant9 5580



11A-CDD Ant10 5580



11A-CDD Ant15 5580



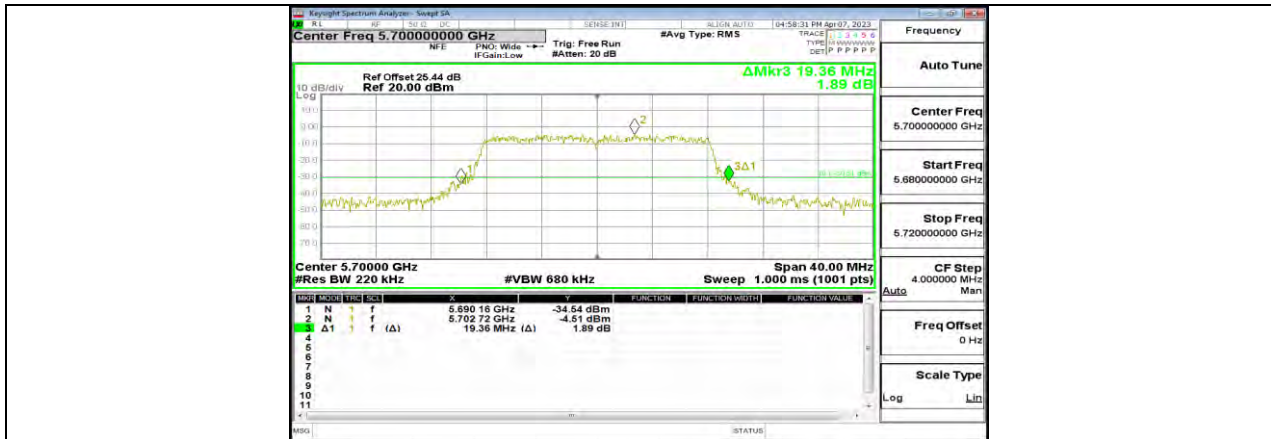
11A-CDD Ant16 5580



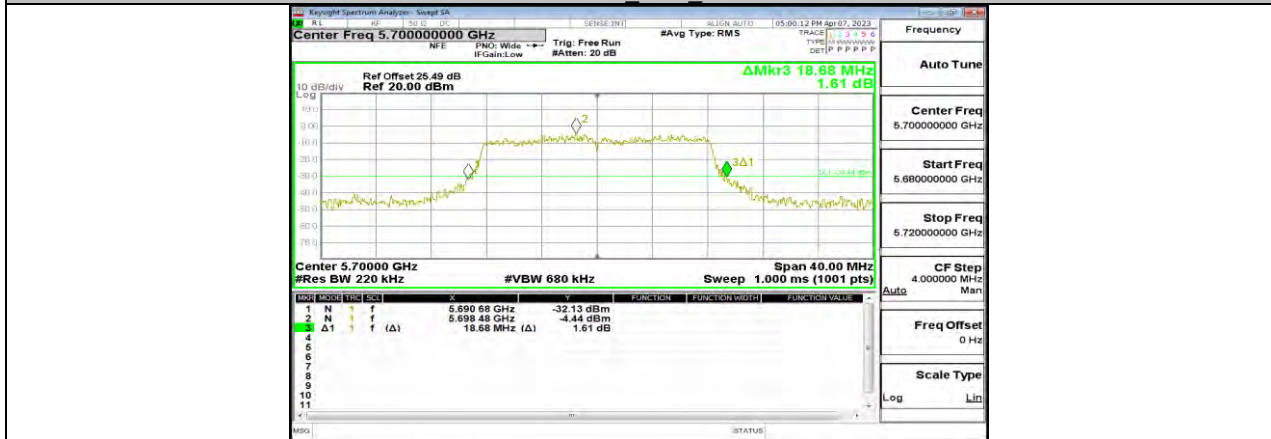
11A-CDD Ant1 5700



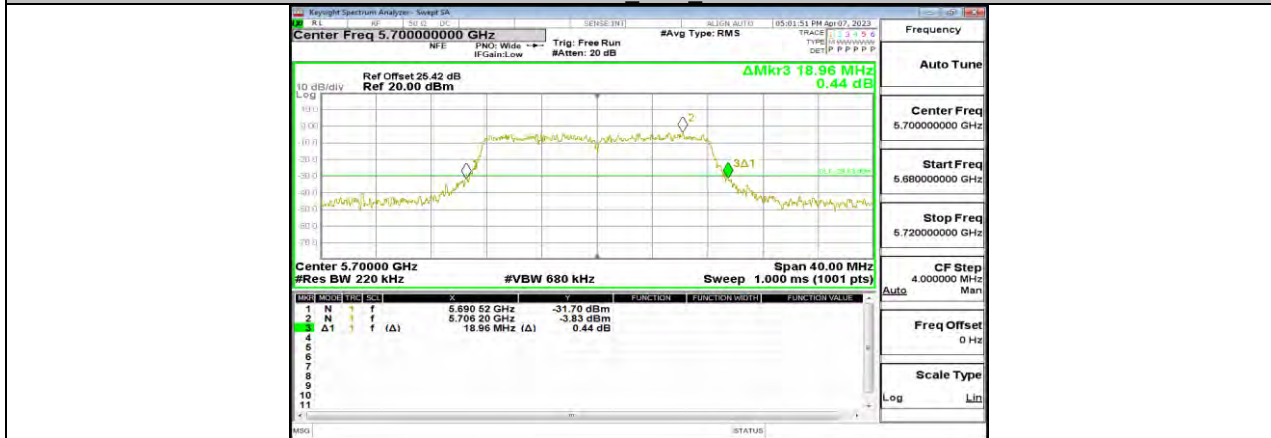
11A-CDD Ant2 5700



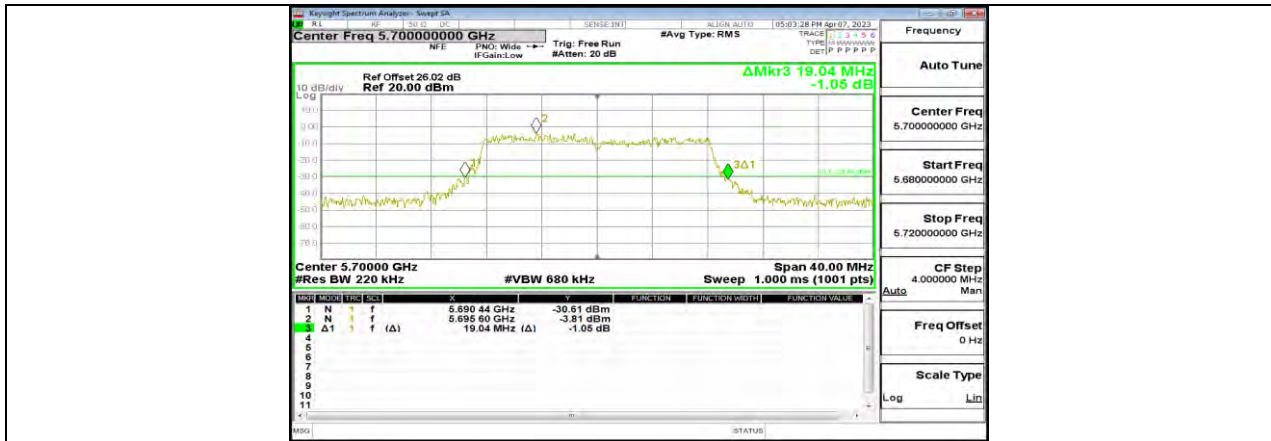
11A-CDD Ant3 5700



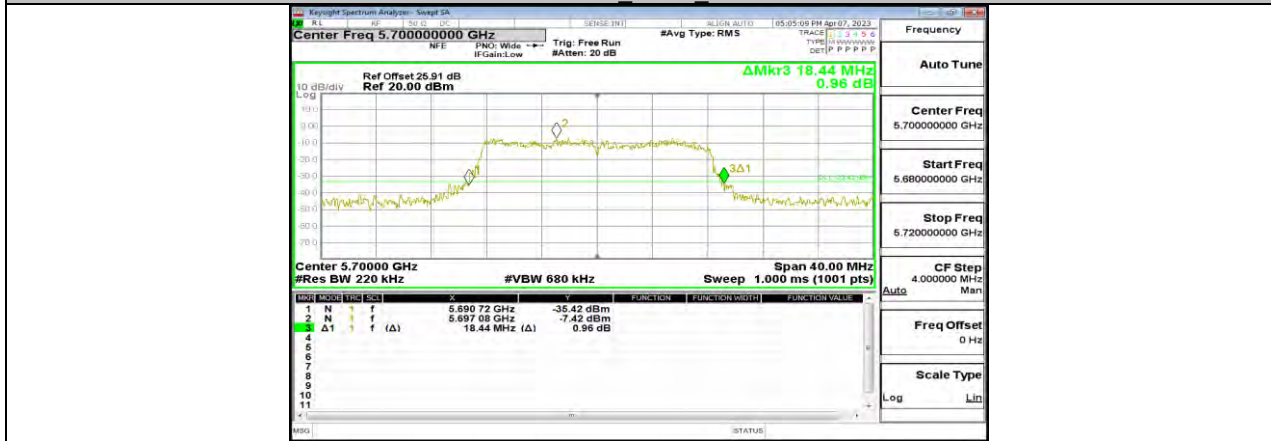
11A-CDD Ant4 5700



11A-CDD Ant9 5700



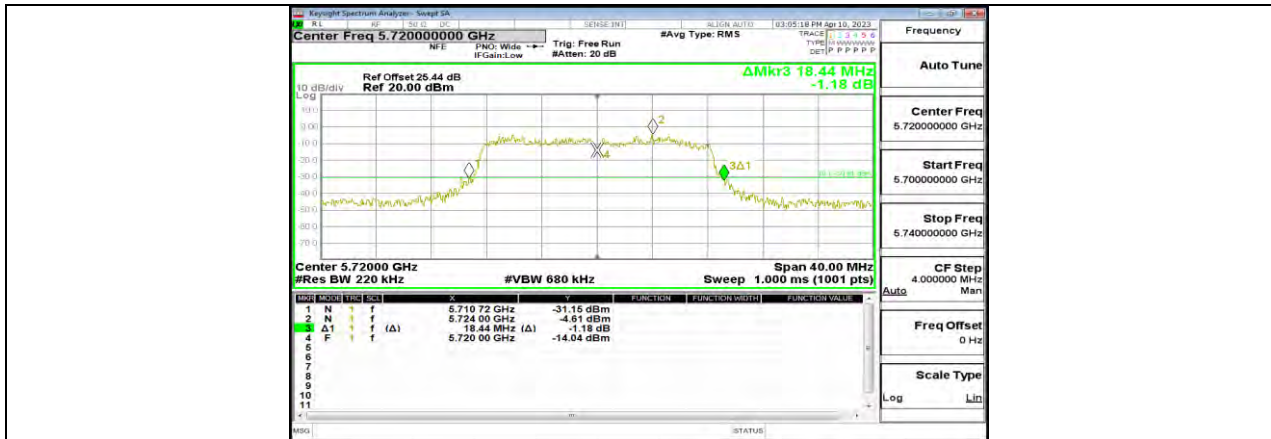
11A-CDD Ant10 5700



11A-CDD Ant15 5700



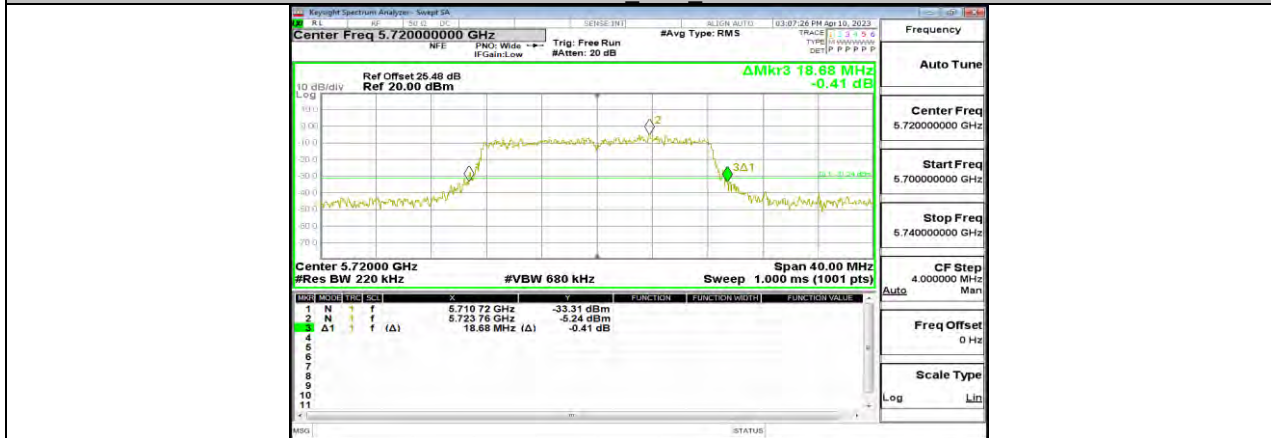
11A-CDD Ant16 5700



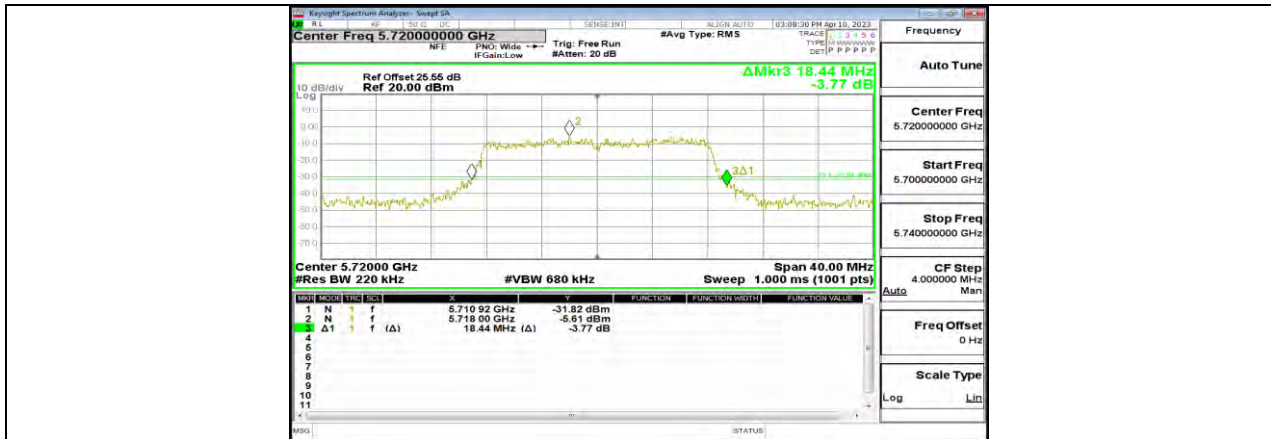
11A-CDD Ant1 5720



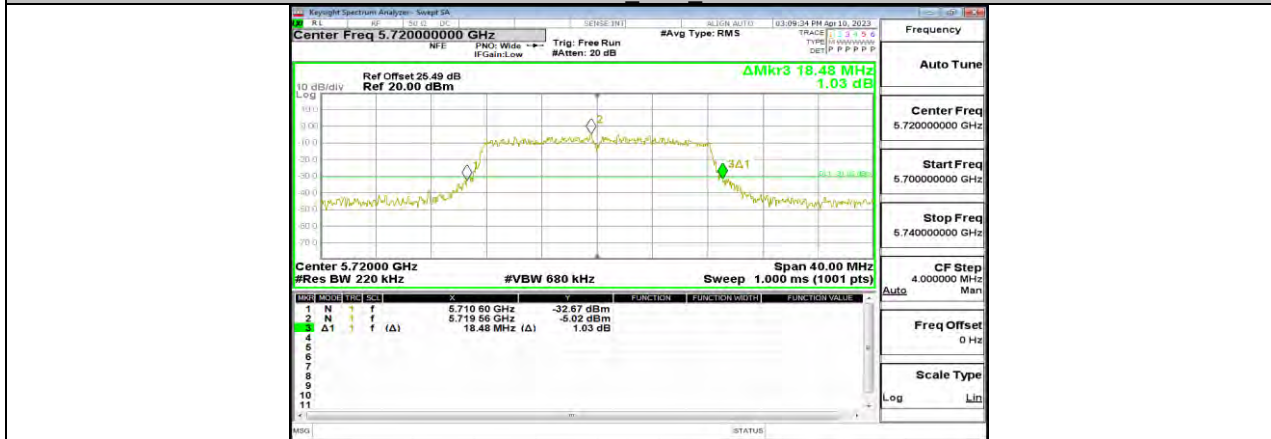
11A-CDD Ant2 5720



11A-CDD Ant3 5720



11A-CDD Ant4 5720



11A-CDD Ant9 5720



11A-CDD Ant10 5720



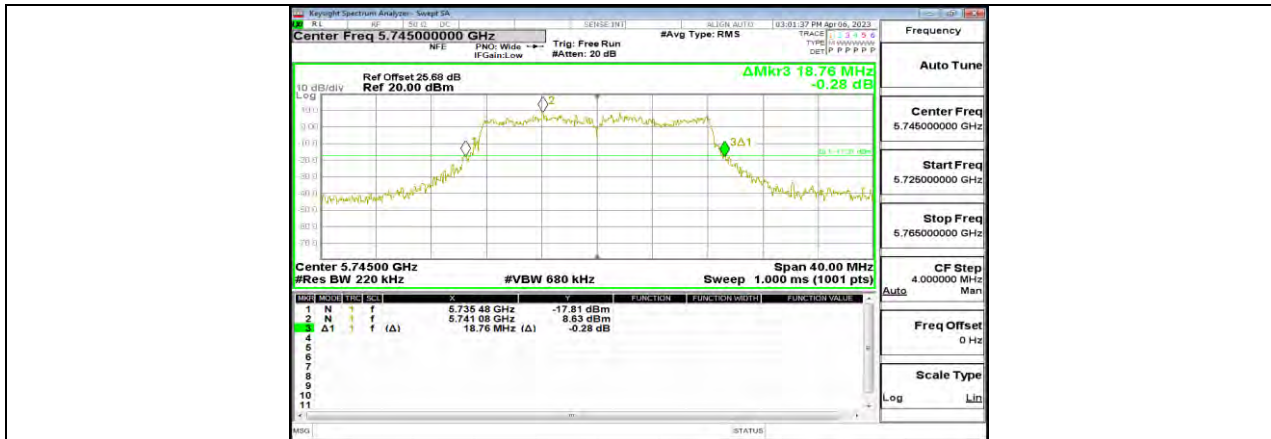
11A-CDD Ant15 5720



11A-CDD Ant16 5720



11A-CDD Ant1 5745



11A-CDD Ant2 5745



11A-CDD Ant3 5745



11A-CDD Ant4 5745



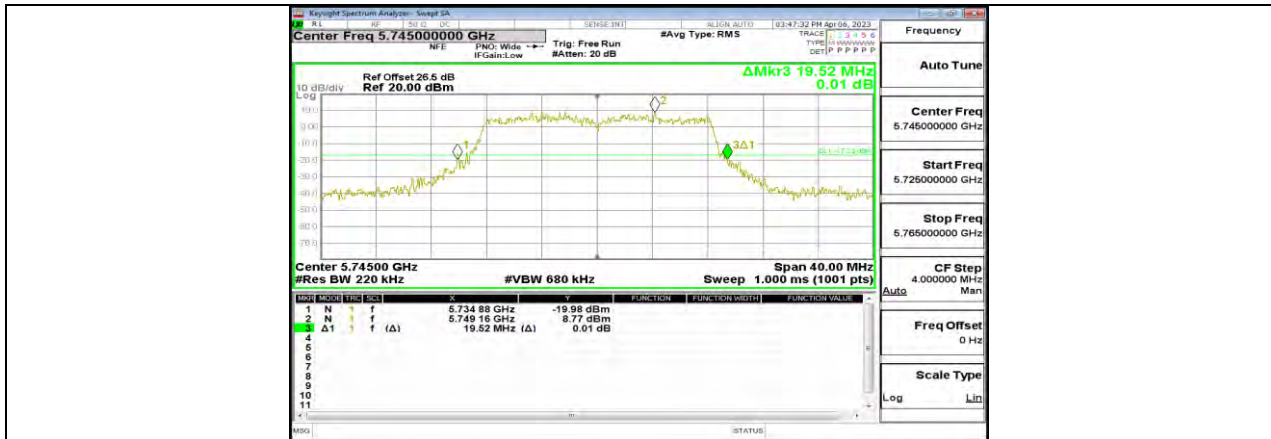
11A-CDD Ant9 5745



11A-CDD Ant10 5745



11A-CDD Ant15 5745



11A-CDD Ant16 5745



11A-CDD Ant1 5785



11A-CDD Ant2 5785



11A-CDD Ant3 5785



11A-CDD Ant4 5785



11A-CDD Ant9 5785



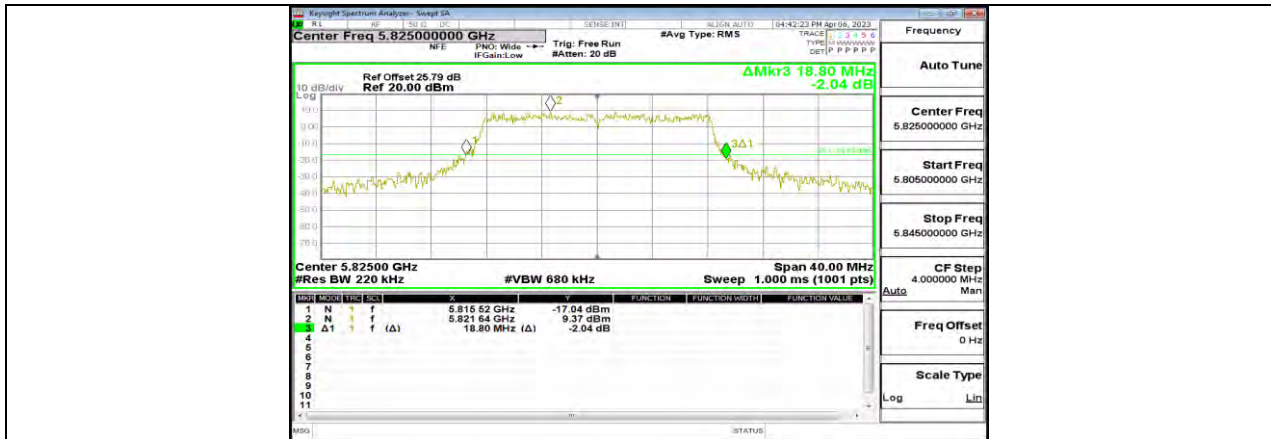
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11A-CDD Ant15 5785



11A-CDD Ant16 5785



11A-CDD Ant1 5825



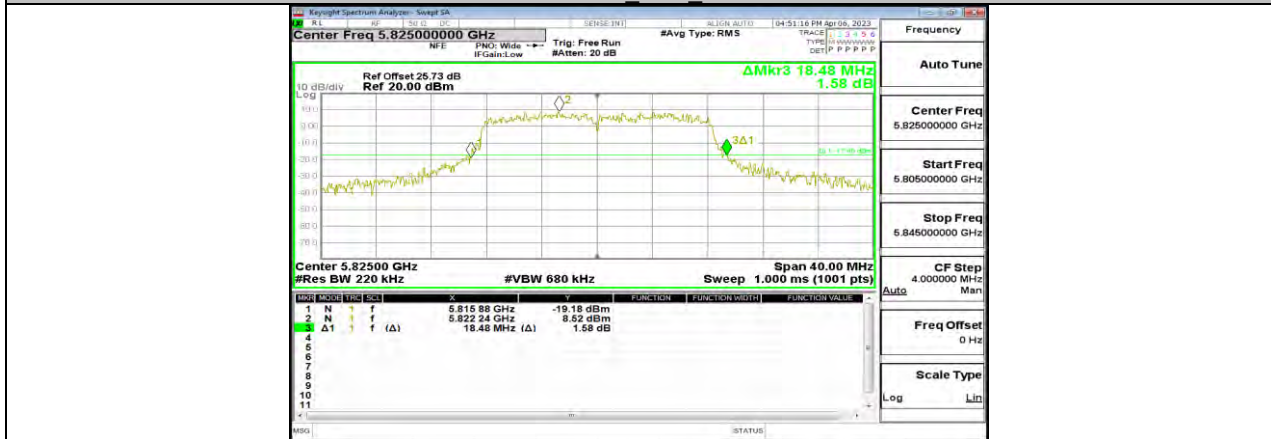
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11A-CDD Ant3 5825



11A-CDD Ant4 5825



11A-CDD Ant9 5825



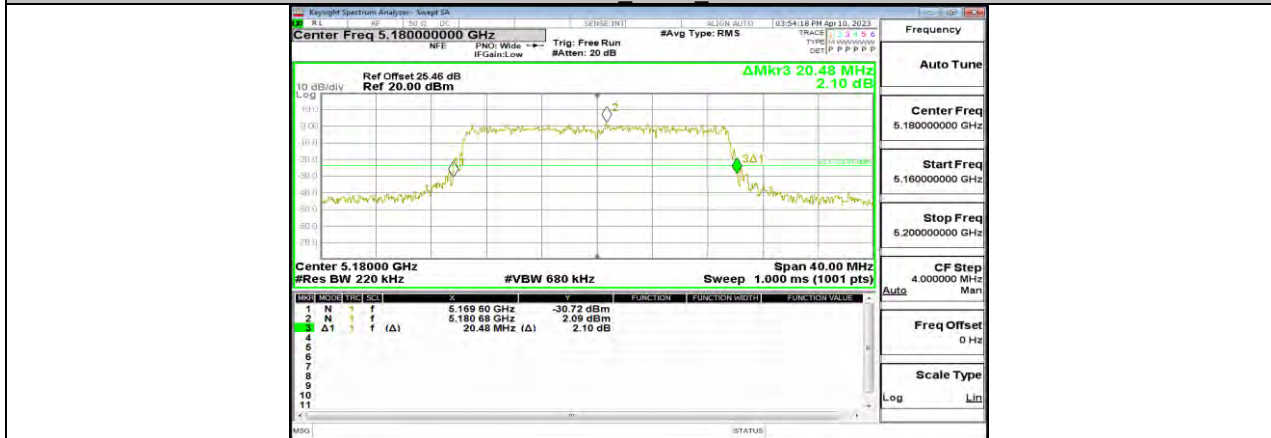
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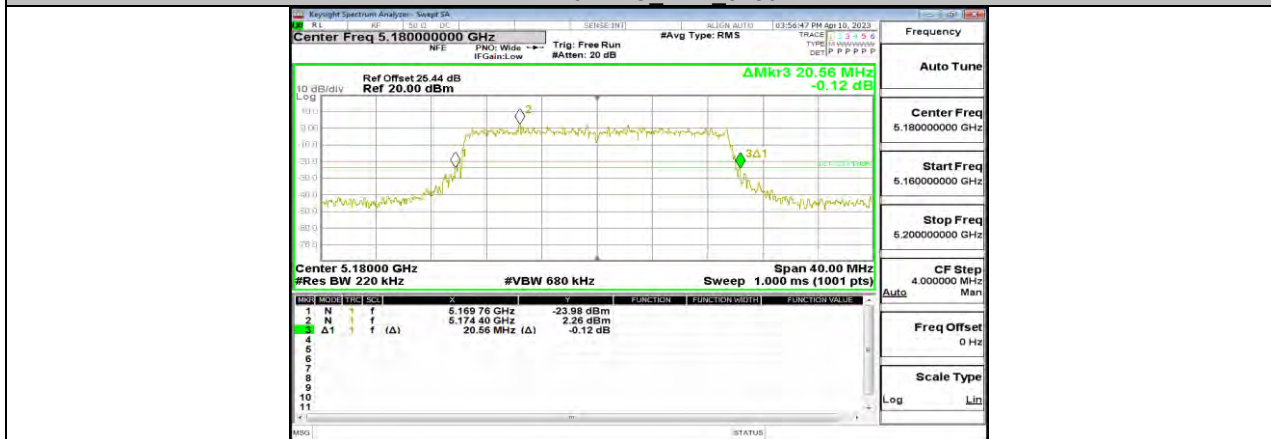
11A-CDD Ant15 5825



11A-CDD Ant16 5825



11AX20MIMO_Ant1 5180

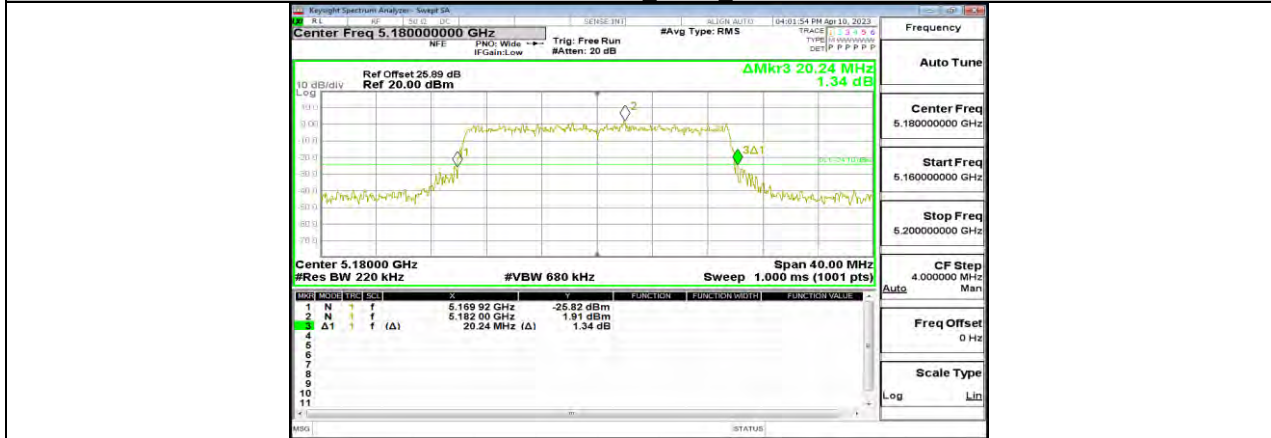




11AX20MIMO Ant9 5180



11AX20MIMO Ant10 5180



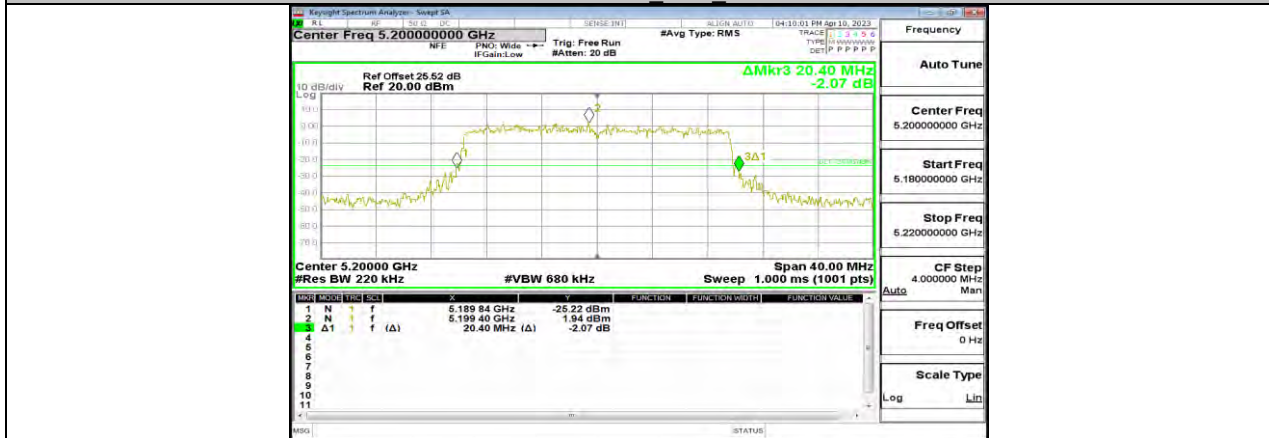
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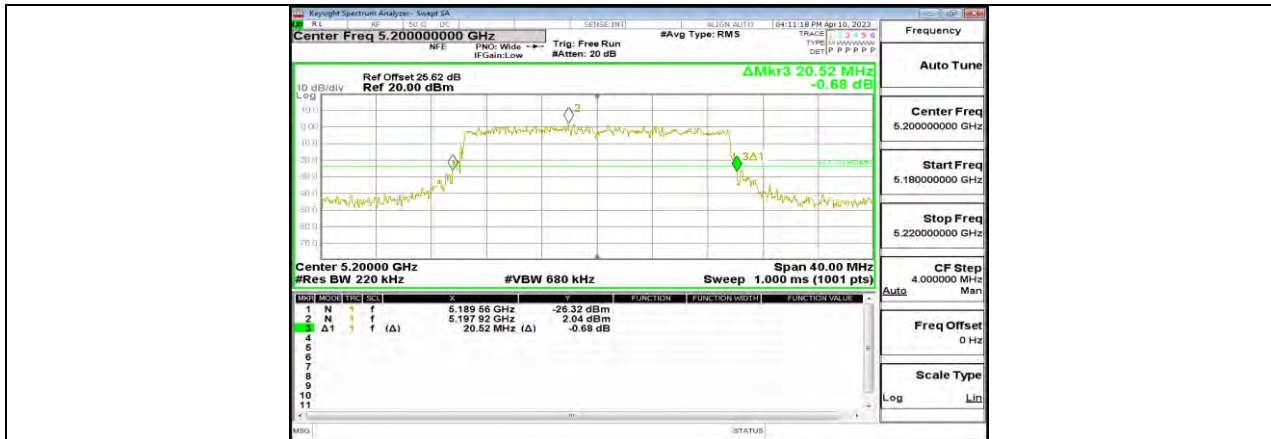
11AX20MIMO Ant16 5180



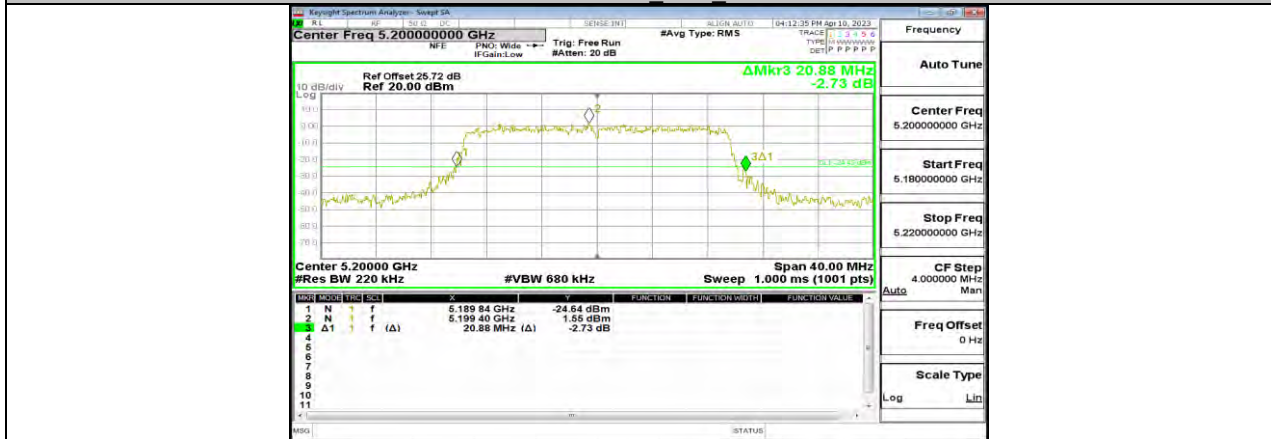
11AX20MIMO Ant1 5200



11AX20MIMO Ant2 5200



11AX20MIMO Ant3 5200



11AX20MIMO Ant4 5200



11AX20MIMO Ant9 5200



11AX20MIMO Ant10 5200



11AX20MIMO Ant15 5200



11AX20MIMO Ant16 5200



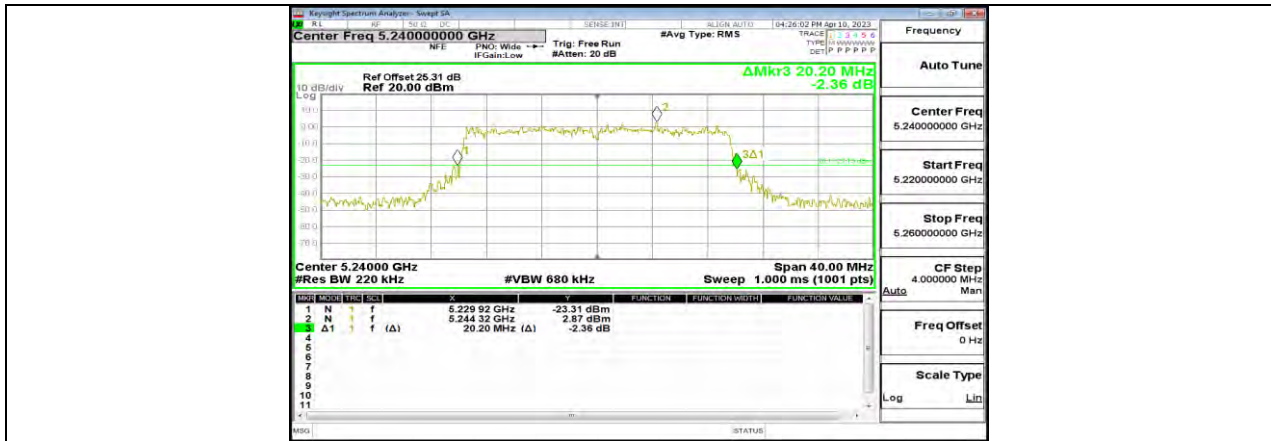
11AX20MIMO Ant1 5240



11AX20MIMO Ant2 5240



11AX20MIMO Ant3 5240



11AX20MIMO Ant4 5240



11AX20MIMO Ant9 5240



11AX20MIMO Ant10 5240



11AX20MIMO Ant15 5240



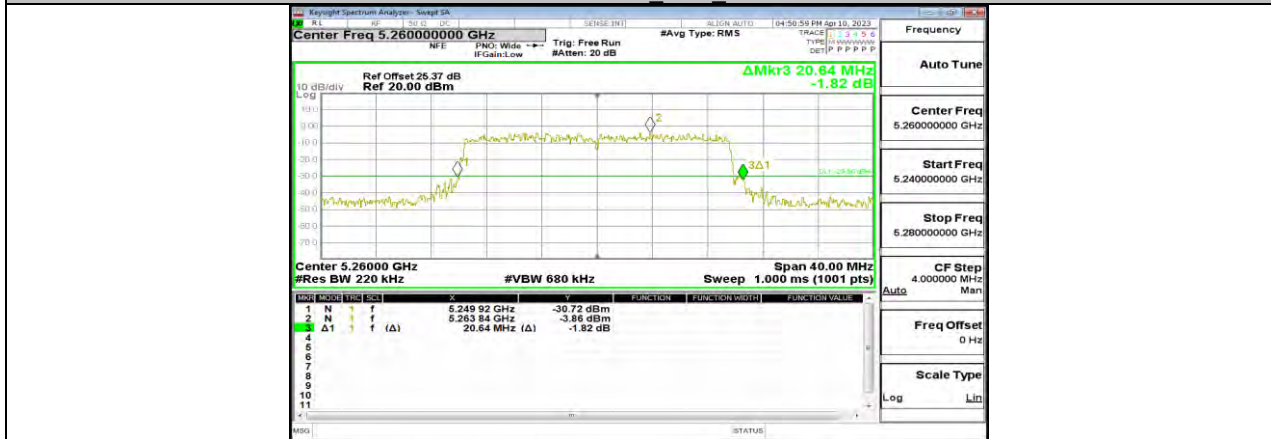
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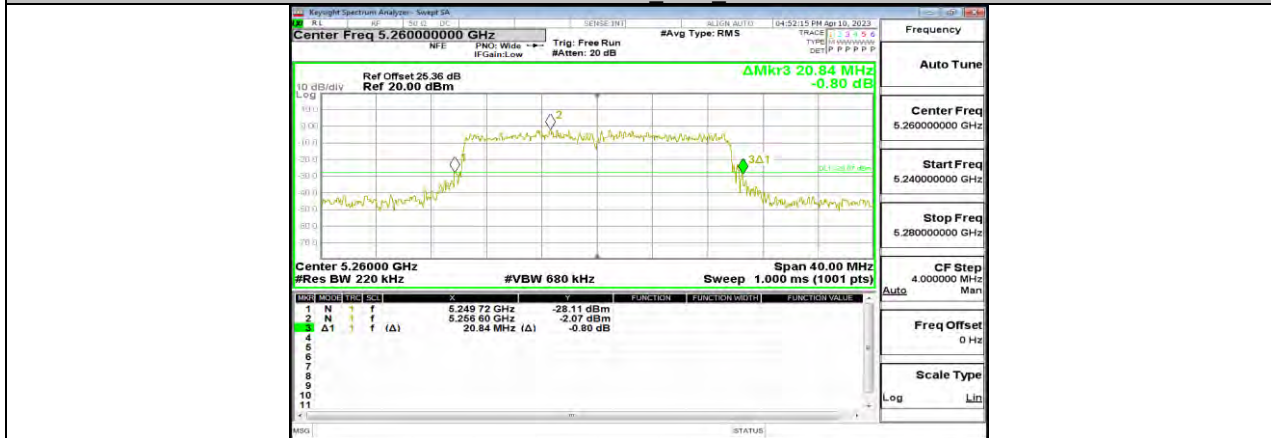
11AX20MIMO Ant1 5260



11AX20MIMO Ant2 5260



11AX20MIMO Ant3 5260



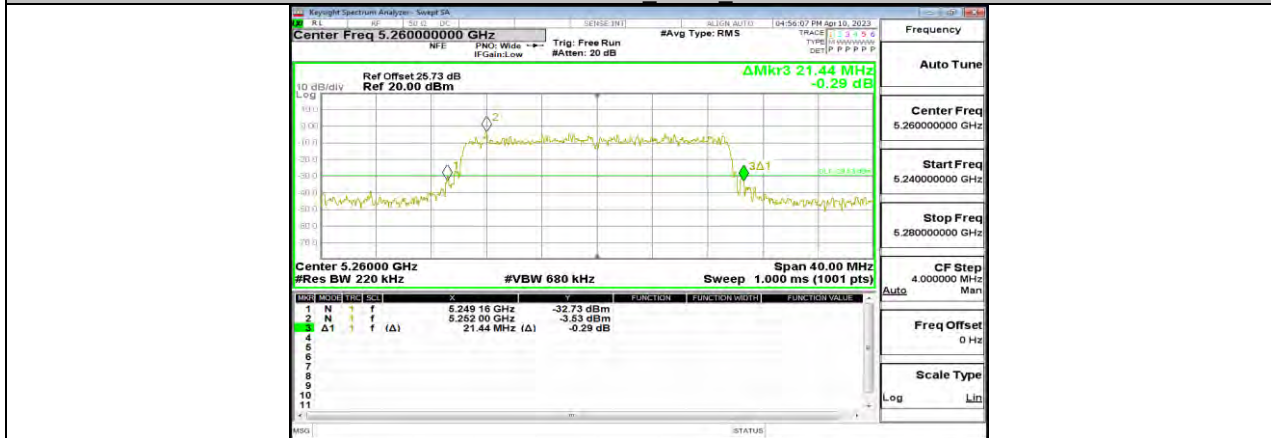
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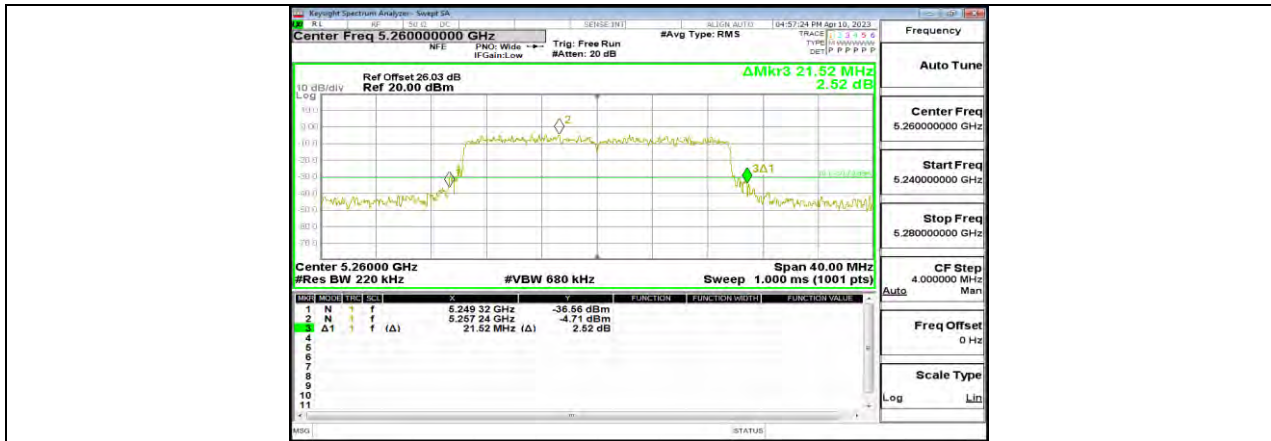
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11AX20MIMO Ant10 5260



11AX20MIMO Ant15 5260



11AX20MIMO Ant16 5260



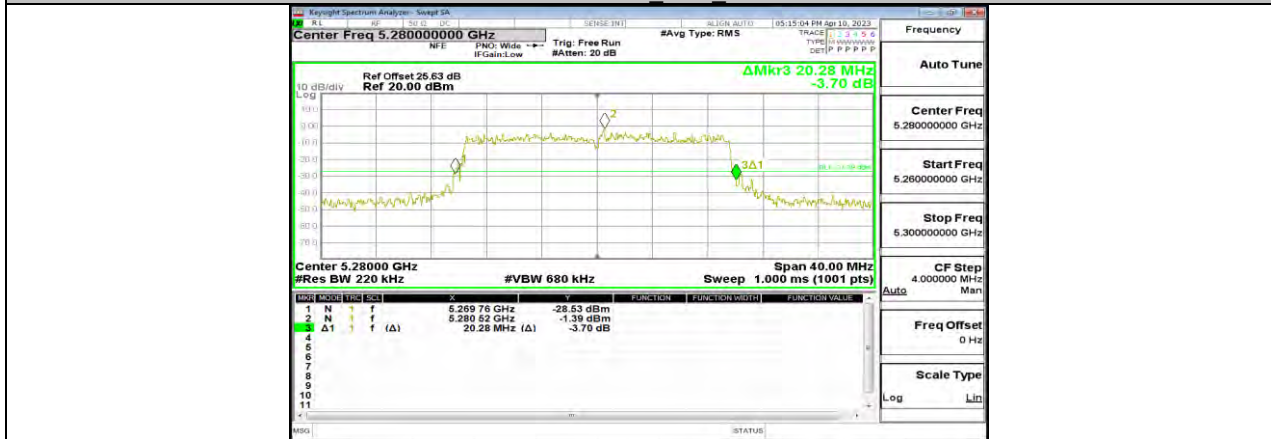
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11AX20MIMO Ant2 5280



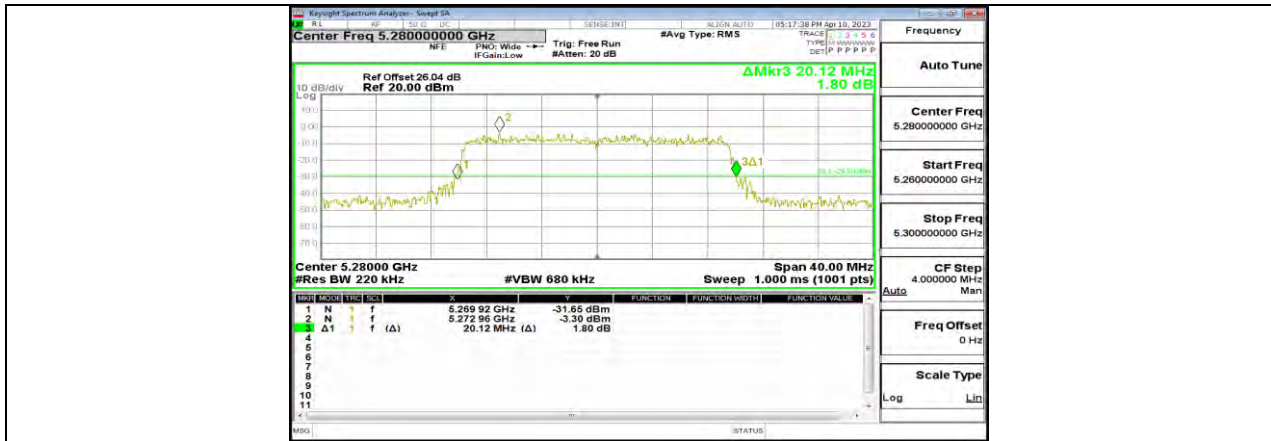
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11AX20MIMO Ant4 5280



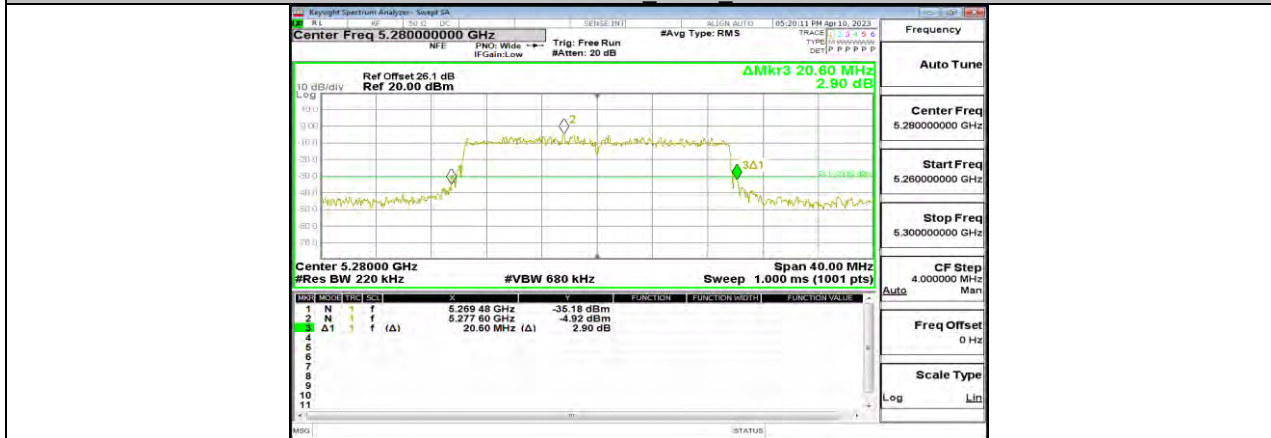
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11AX20MIMO Ant10 5280



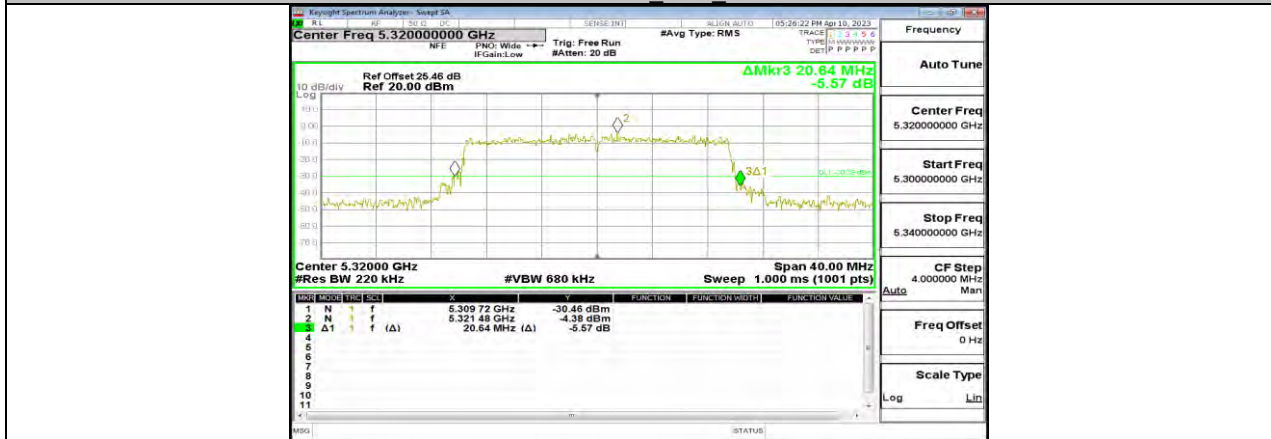
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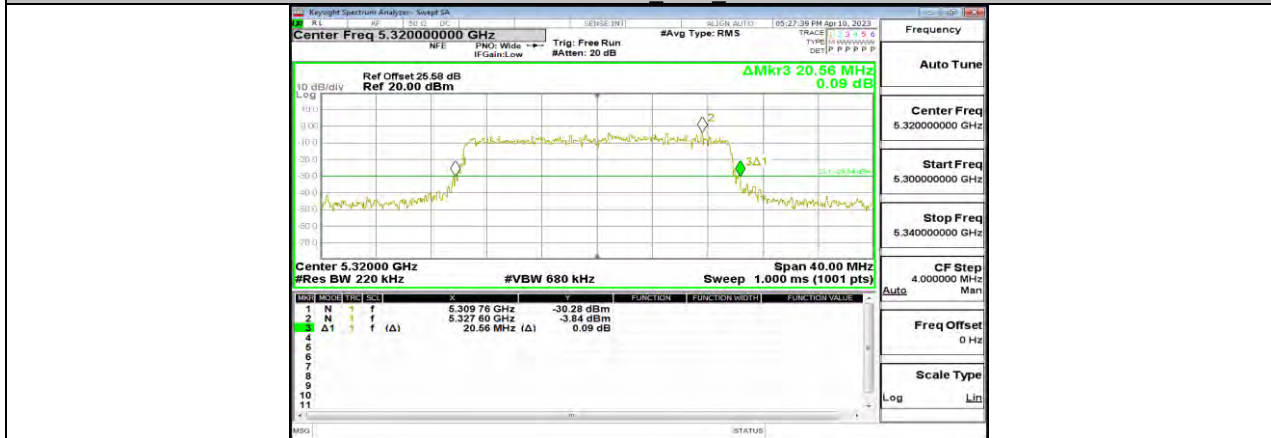
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11AX20MIMO Ant1 5320



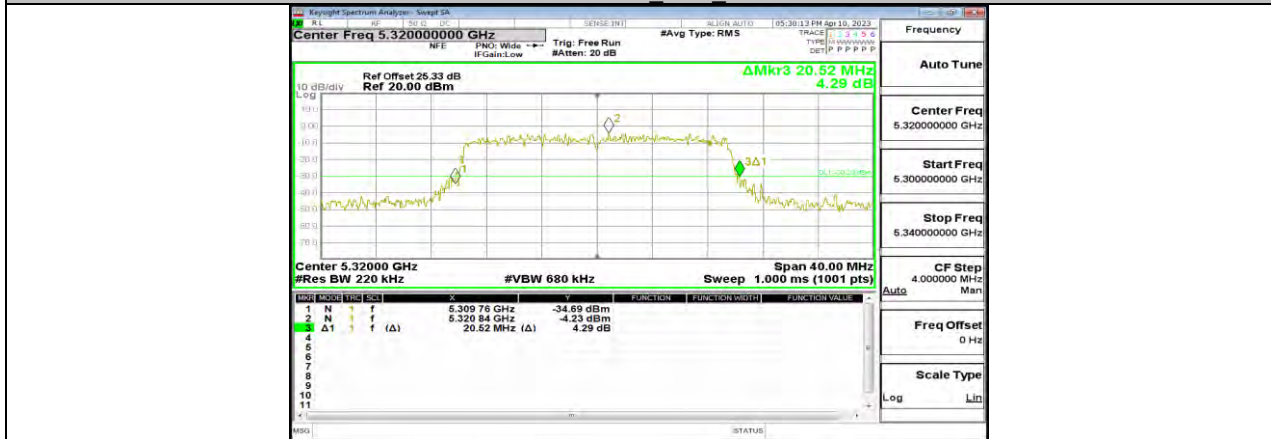
11AX20MIMO Ant2 5320



11AX20MIMO Ant3 5320



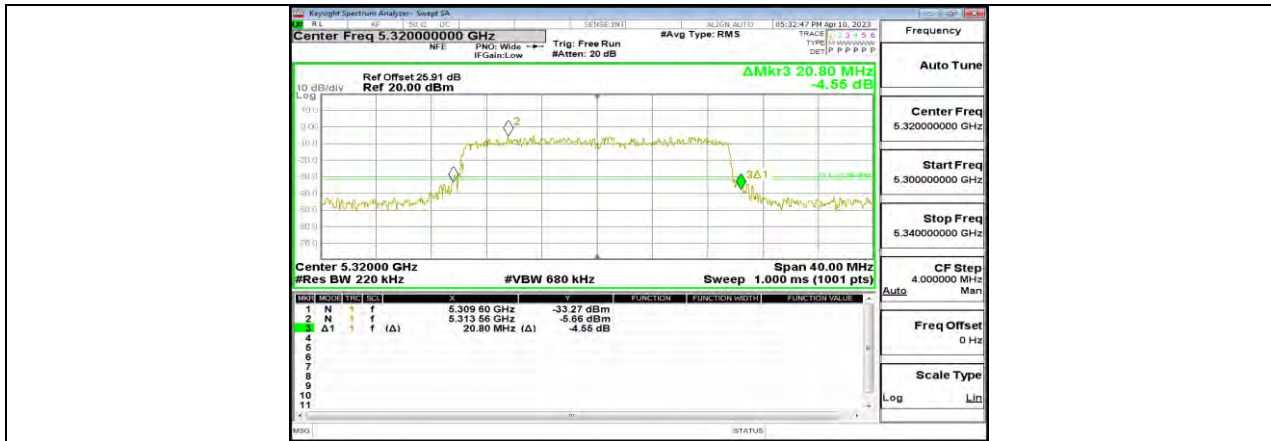
11AX20MIMO Ant4 5320



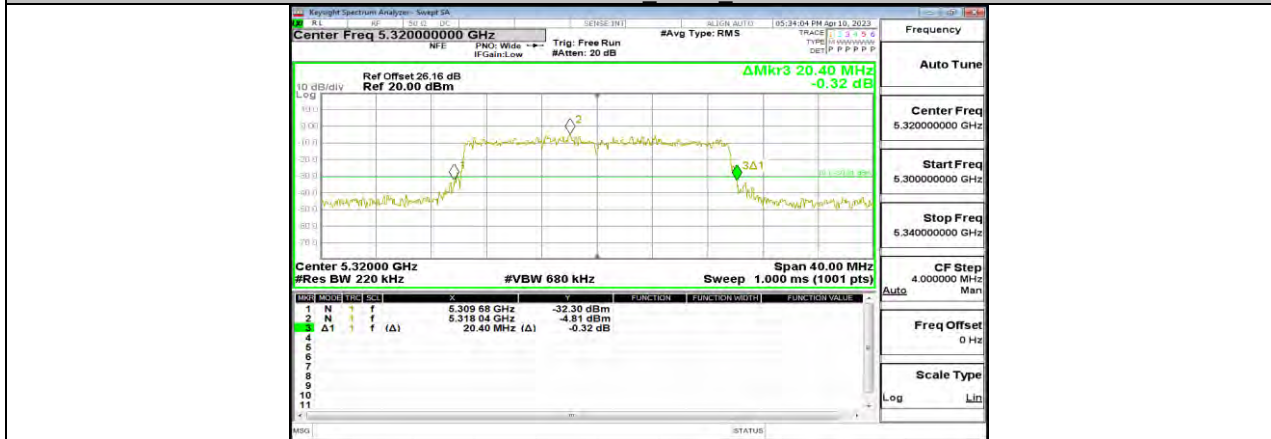
11AX20MIMO Ant9 5320



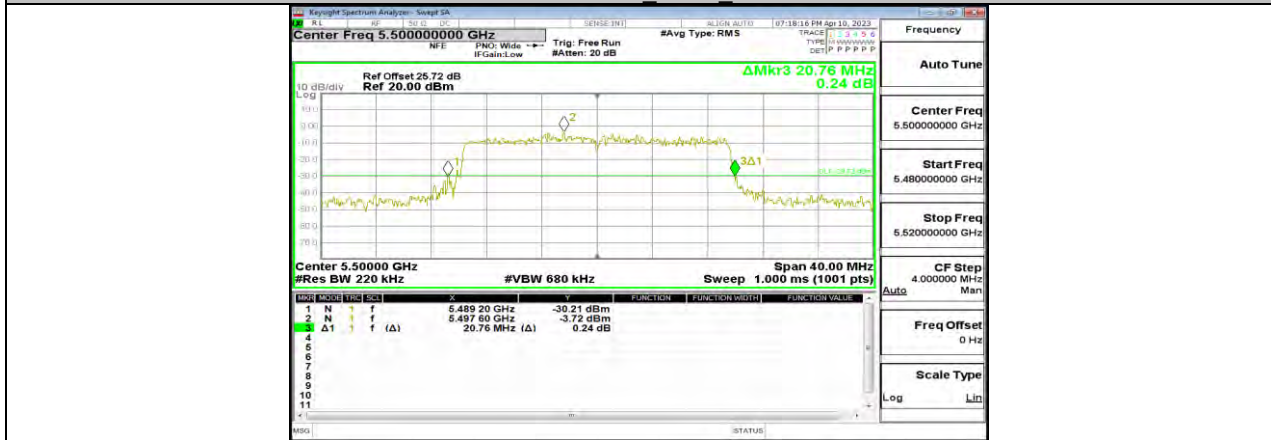
11AX20MIMO Ant10 5320



11AX20MIMO Ant15 5320



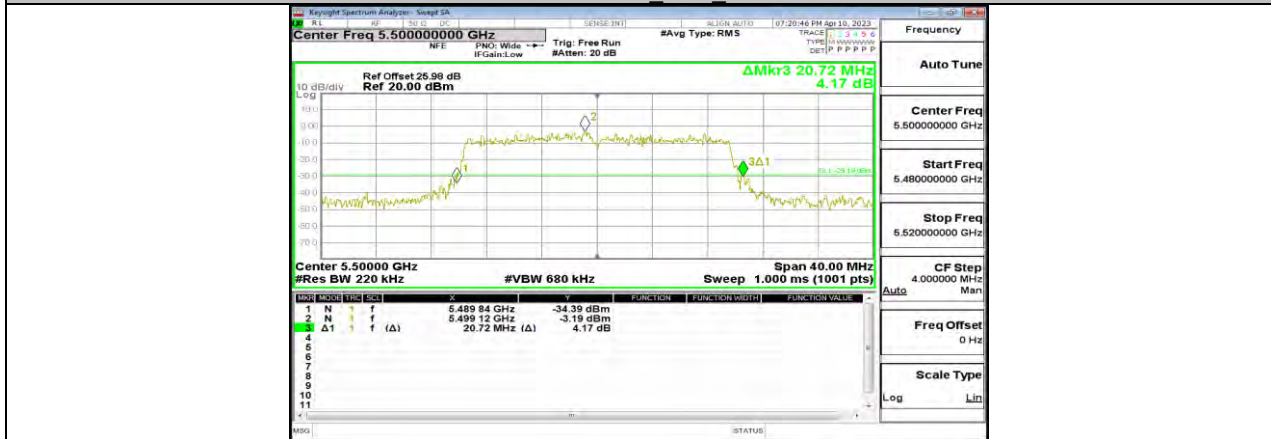
11AX20MIMO Ant16 5320



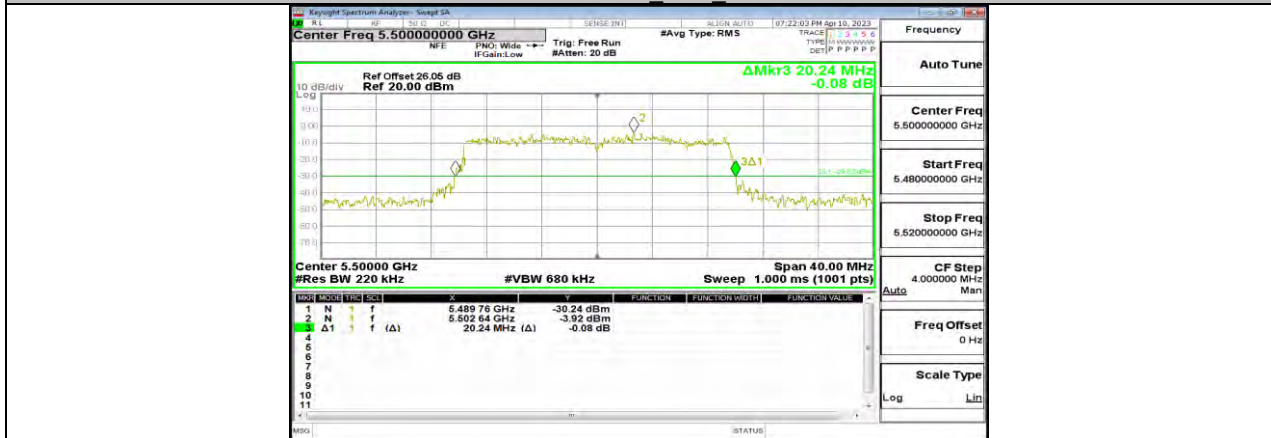
11AX20MIMO Ant1 5500



11AX20MIMO Ant2 5500



11AX20MIMO Ant3 5500



11AX20MIMO Ant4 5500



11AX20MIMO Ant9 5500



11AX20MIMO Ant10 5500



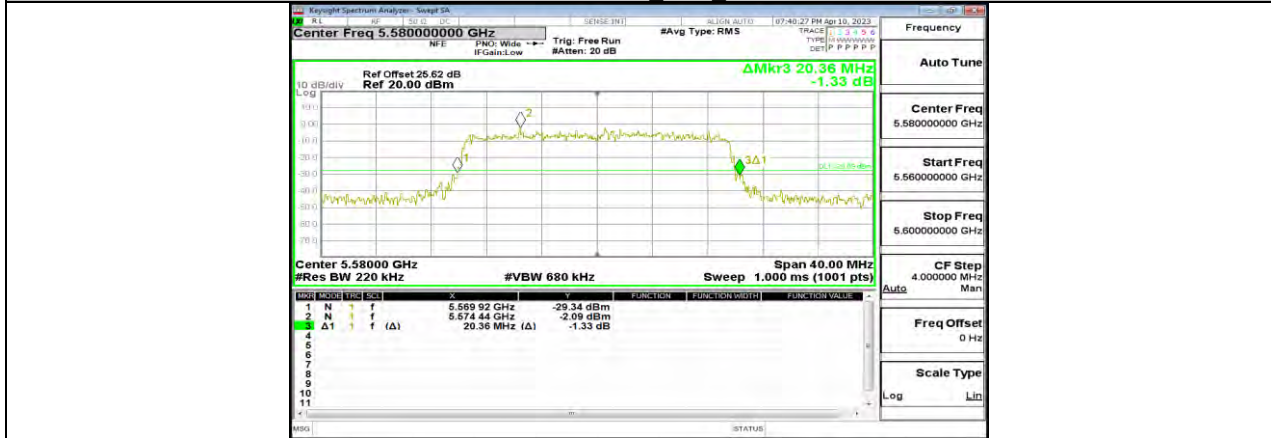
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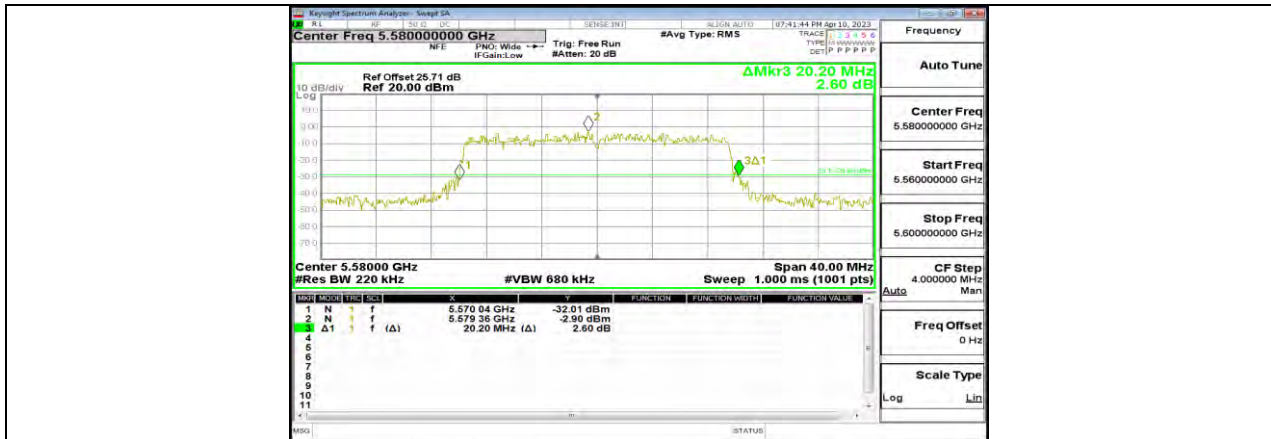
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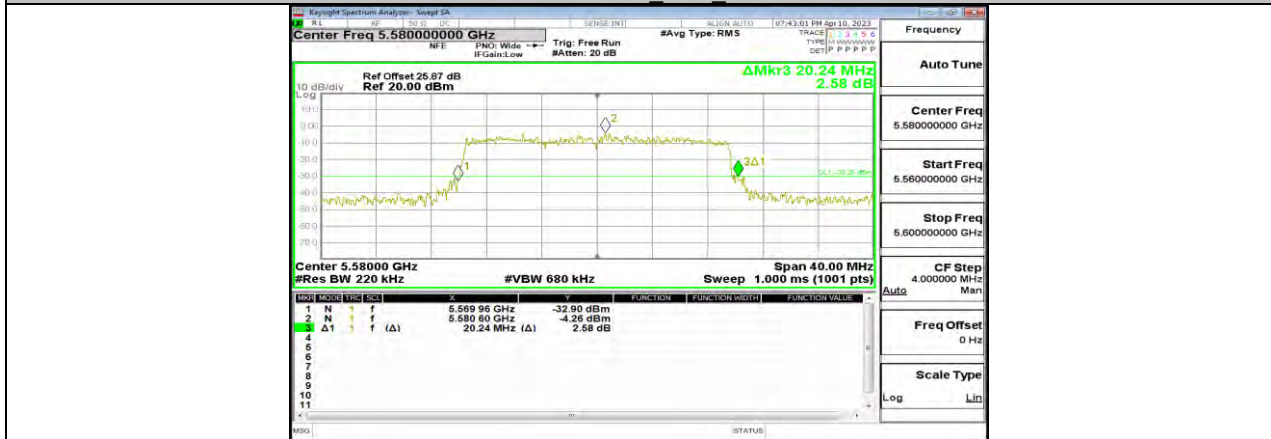
11AX20MIMO Ant1 5580



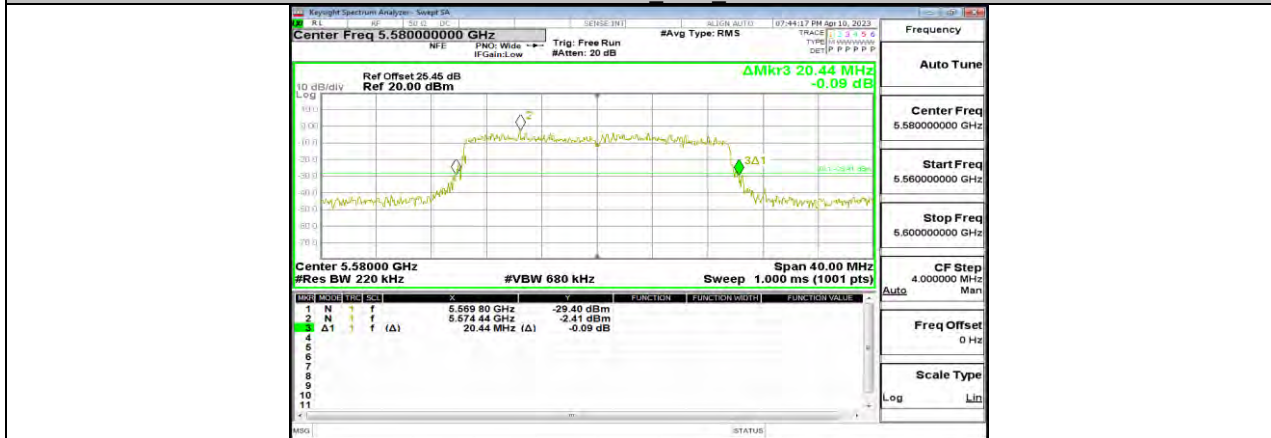
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11AX20MIMO Ant3 5580



11AX20MIMO Ant4 5580



11AX20MIMO Ant9 5580



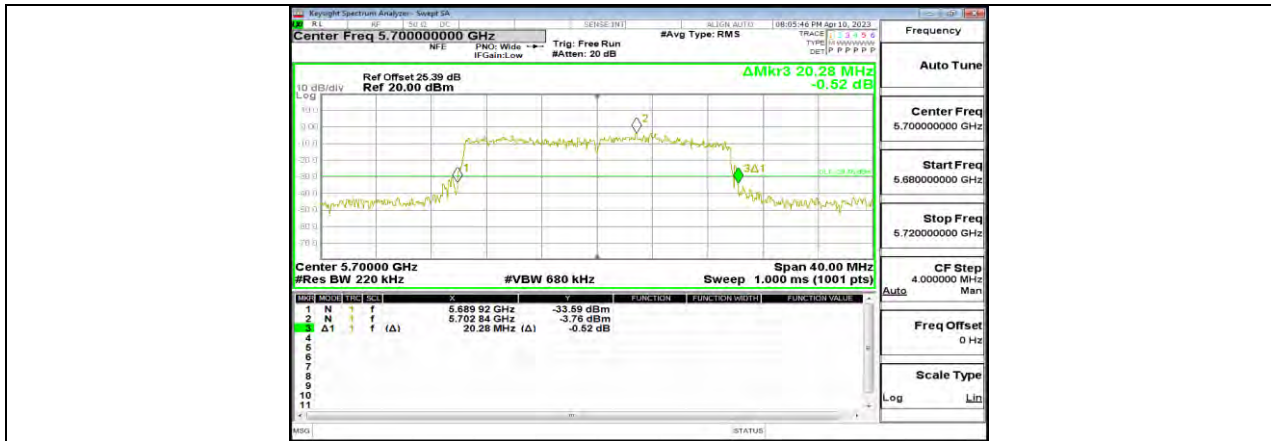
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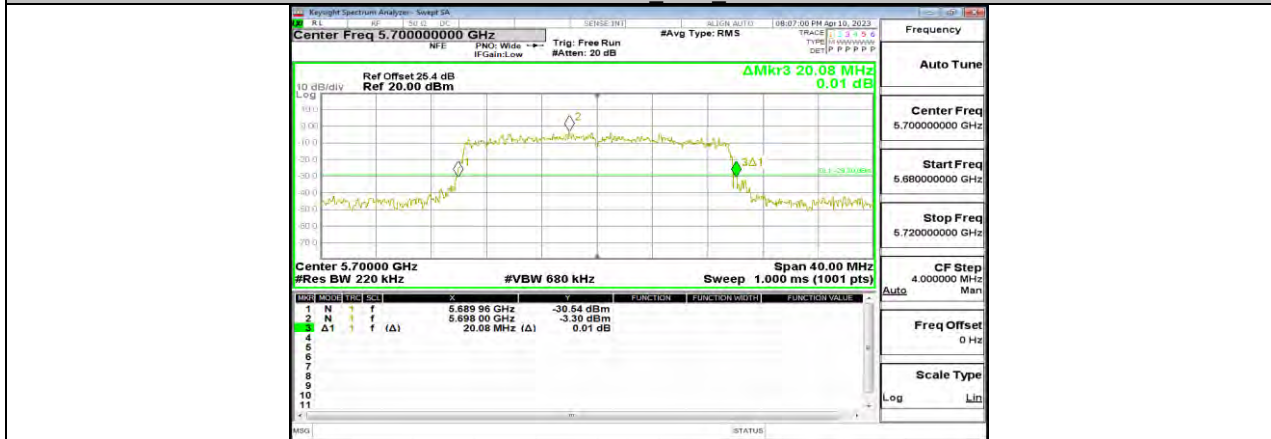
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11AX20MIMO Ant16 5580



11AX20MIMO Ant1 5700



11AX20MIMO Ant2 5700



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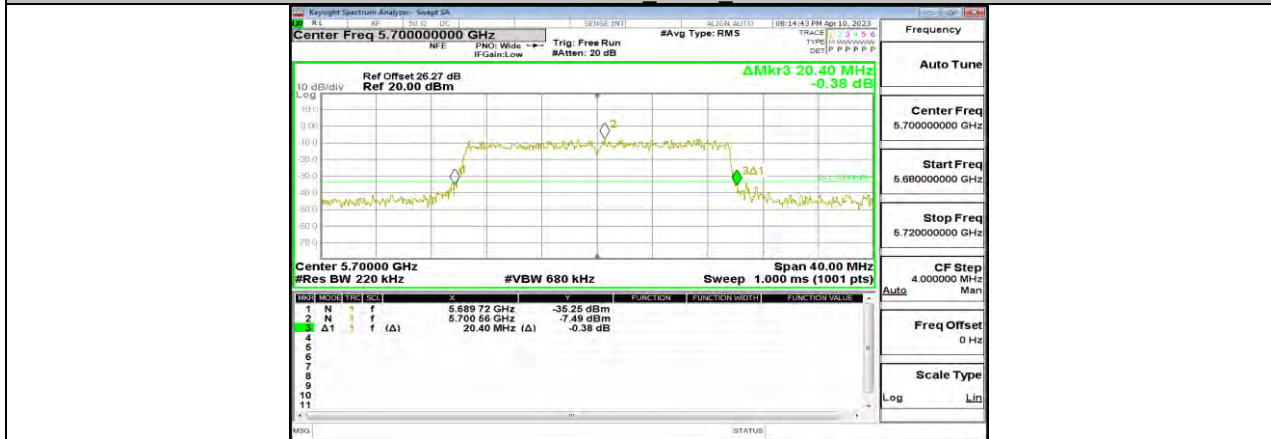
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11AX20MIMO Ant15 5700



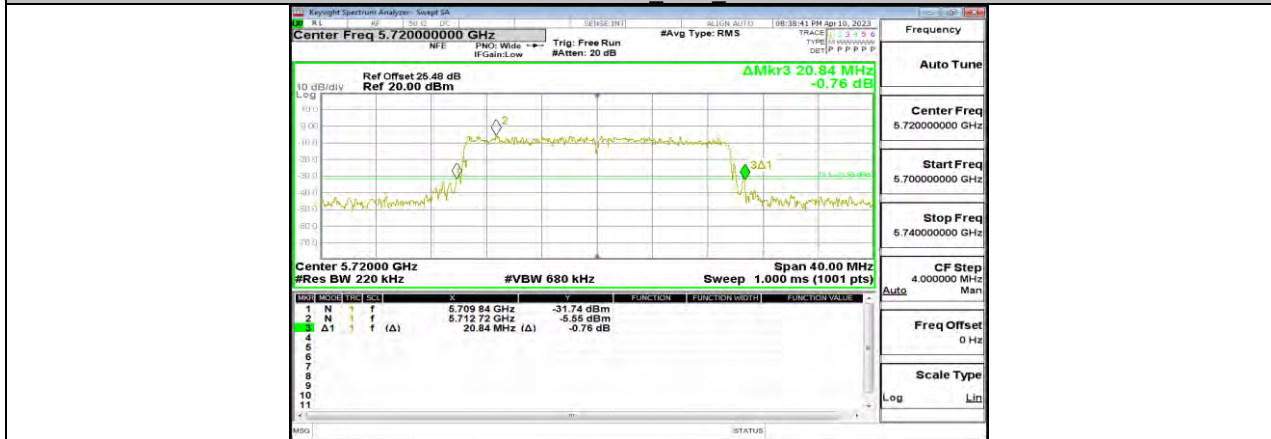
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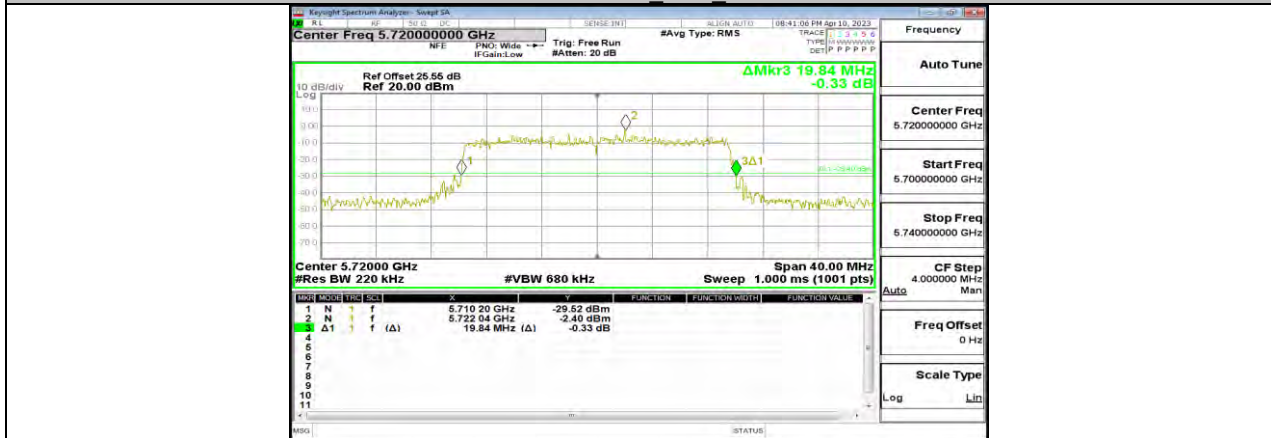
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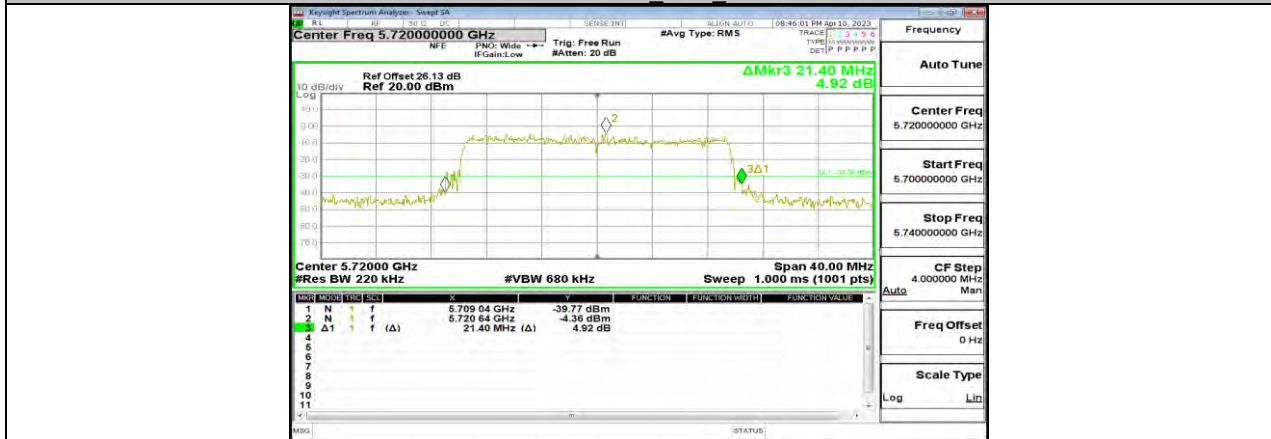
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11AX20MIMO Ant4 5720



11AX20MIMO Ant9 5720



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11AX20MIMO Ant2 5745



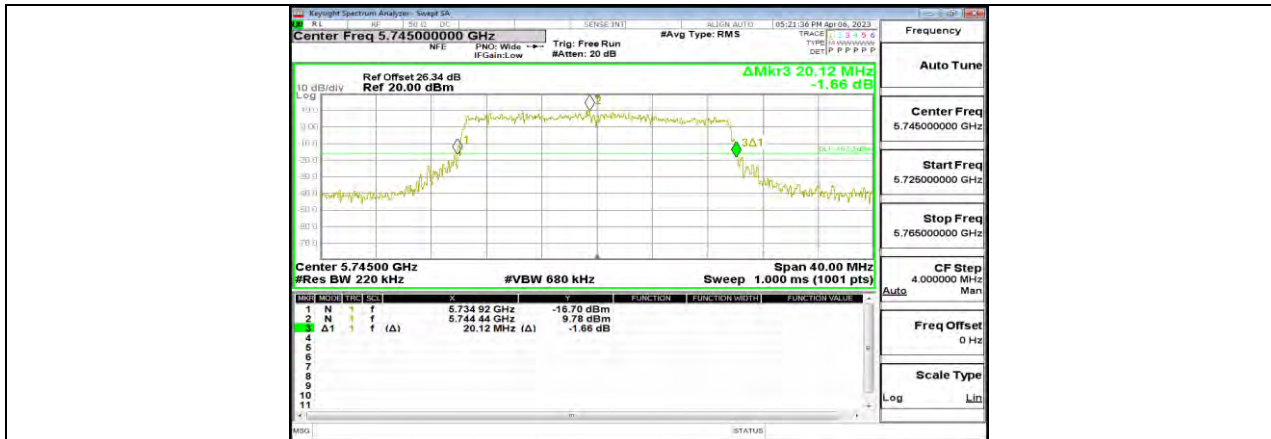
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11AX20MIMO Ant4 5745



11AX20MIMO Ant9 5745



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11AX20MIMO Ant1 5785



11AX20MIMO Ant2 5785



11AX20MIMO Ant3 5785



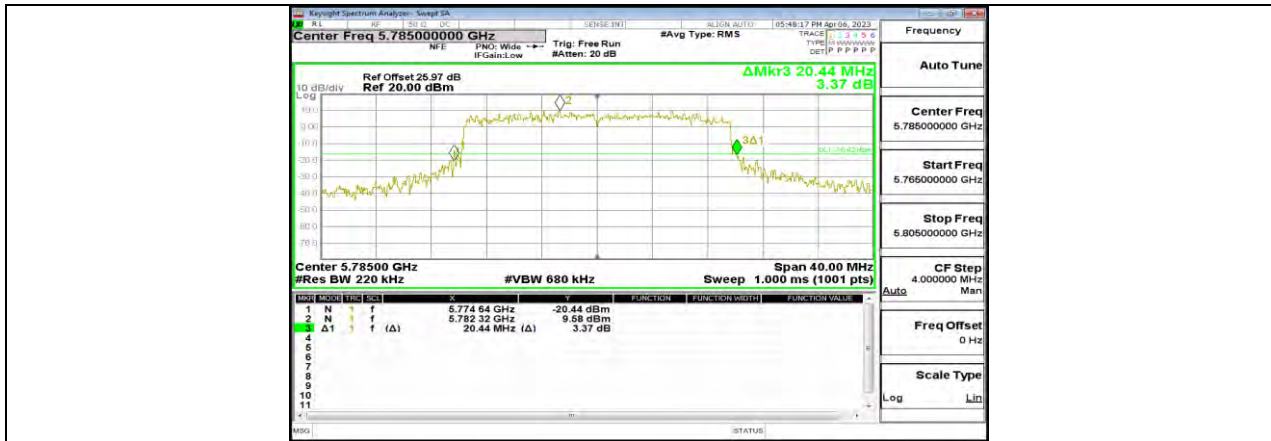
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11AX20MIMO Ant9 5785



11AX20MIMO Ant10 5785



11AX20MIMO Ant15 5785



11AX20MIMO Ant16 5785



11AX20MIMO Ant1 5825



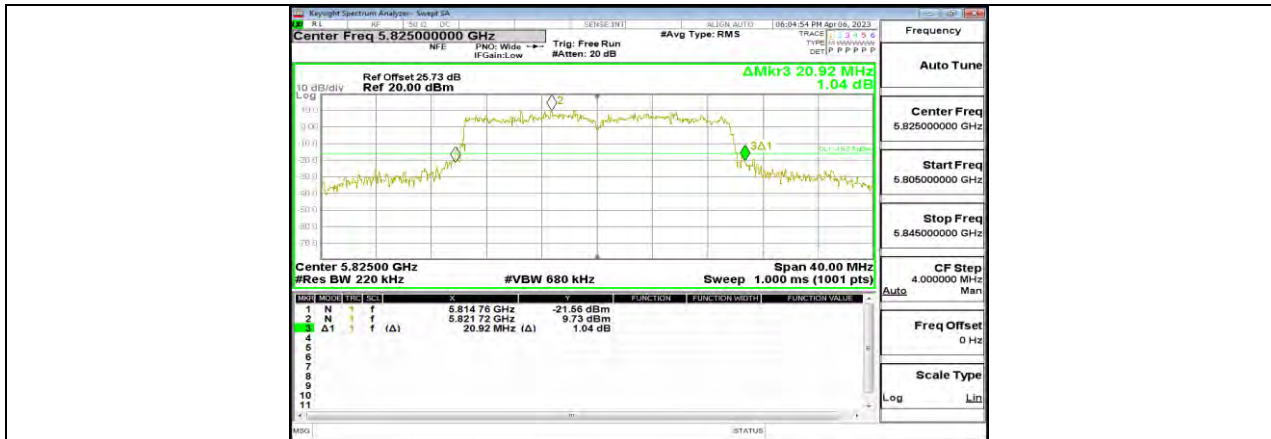
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11AX20MIMO Ant3 5825



11AX20MIMO Ant4 5825



11AX20MIMO Ant9 5825



11AX20MIMO Ant10 5825



11AX20MIMO Ant15 5825