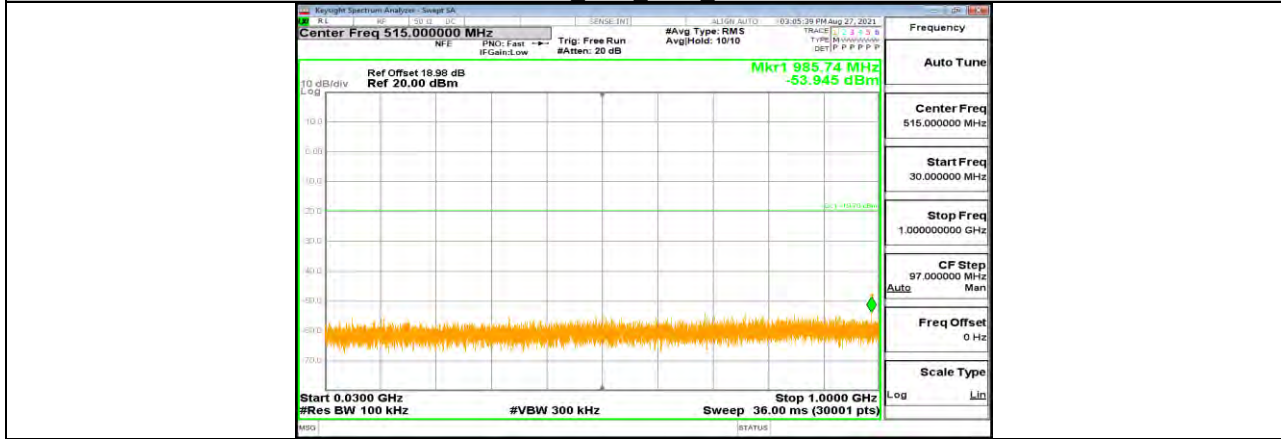
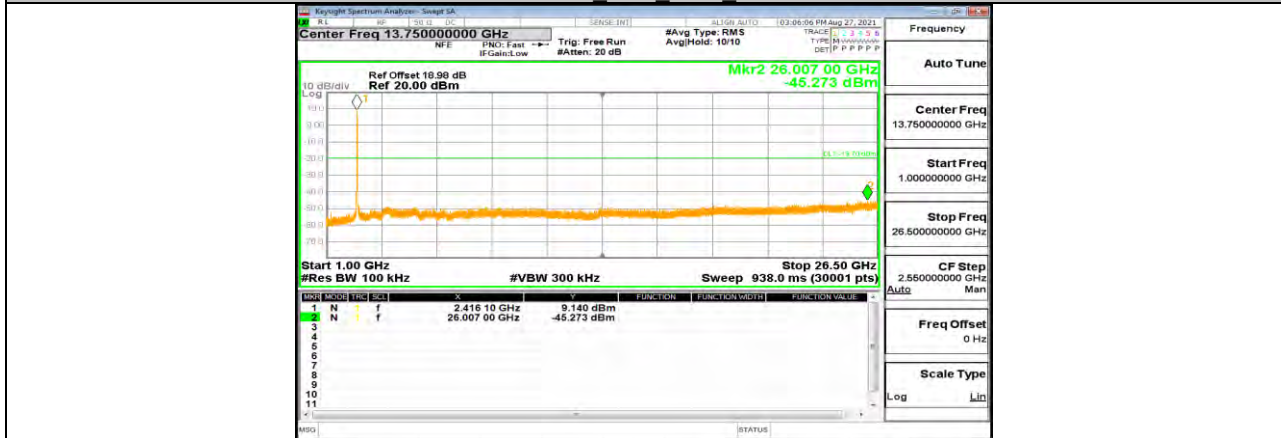




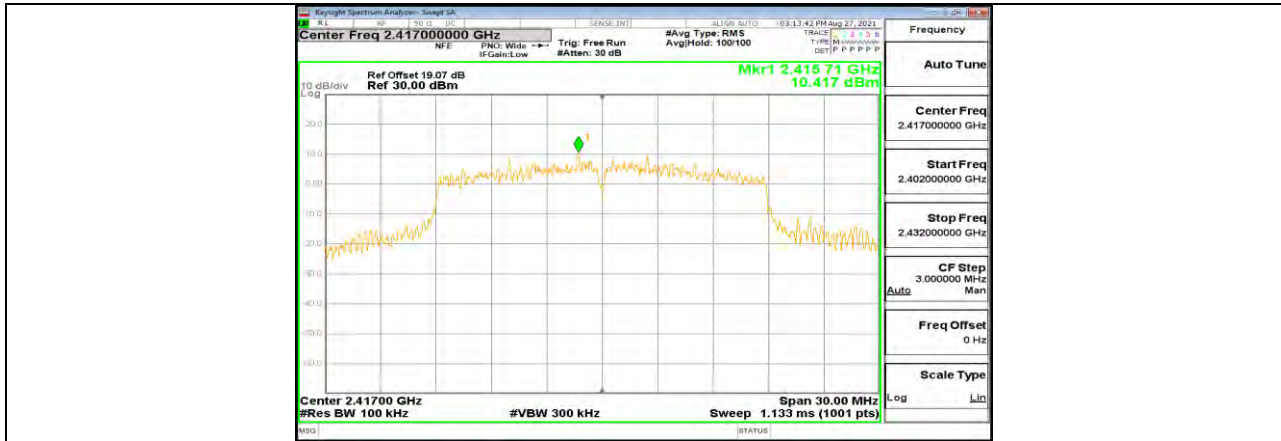
11N20MIMO Ant1 2417 0~Reference



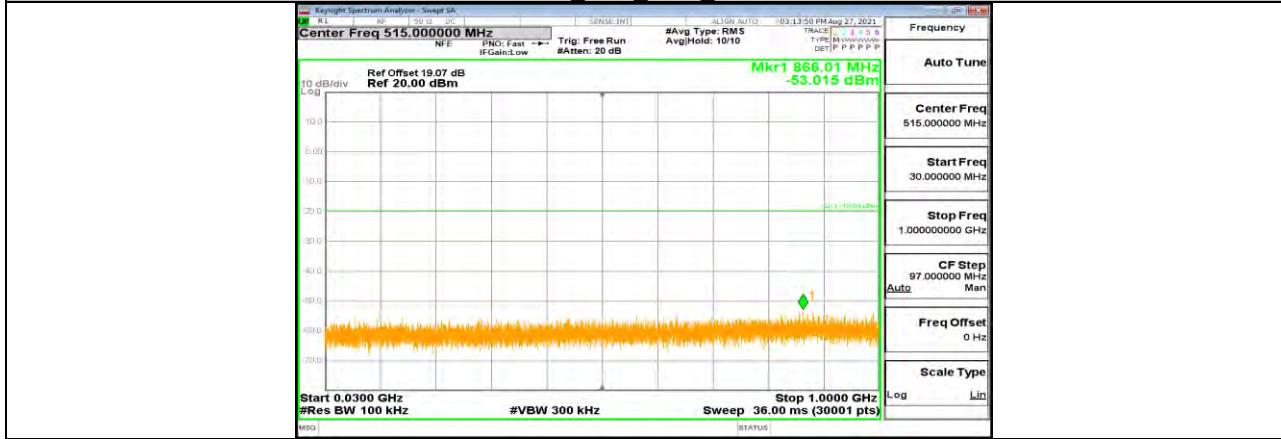
11N20MIMO Ant1 2417 30~1000



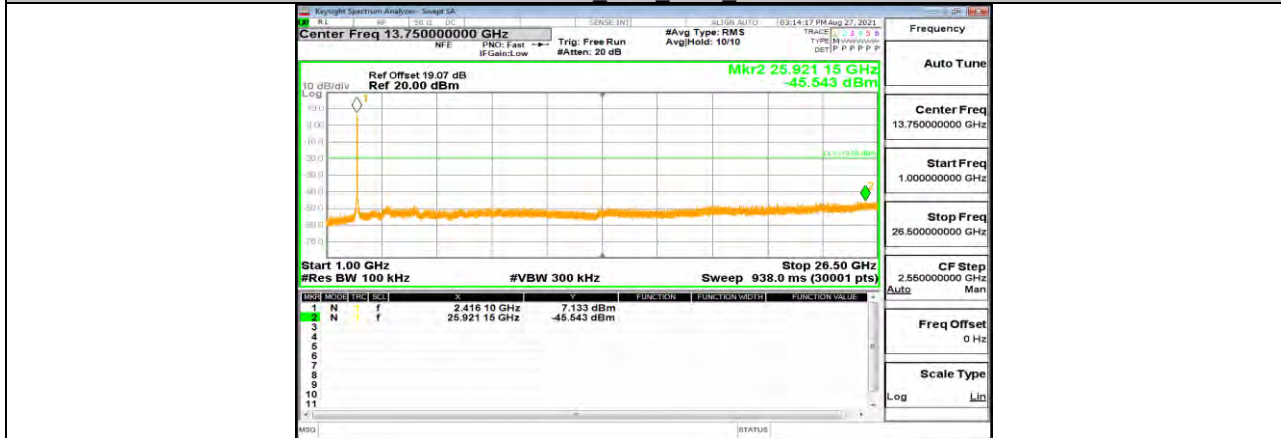
11N20MIMO Ant1 2417 1000~26500



11N20MIMO Ant2 2417 0~Reference



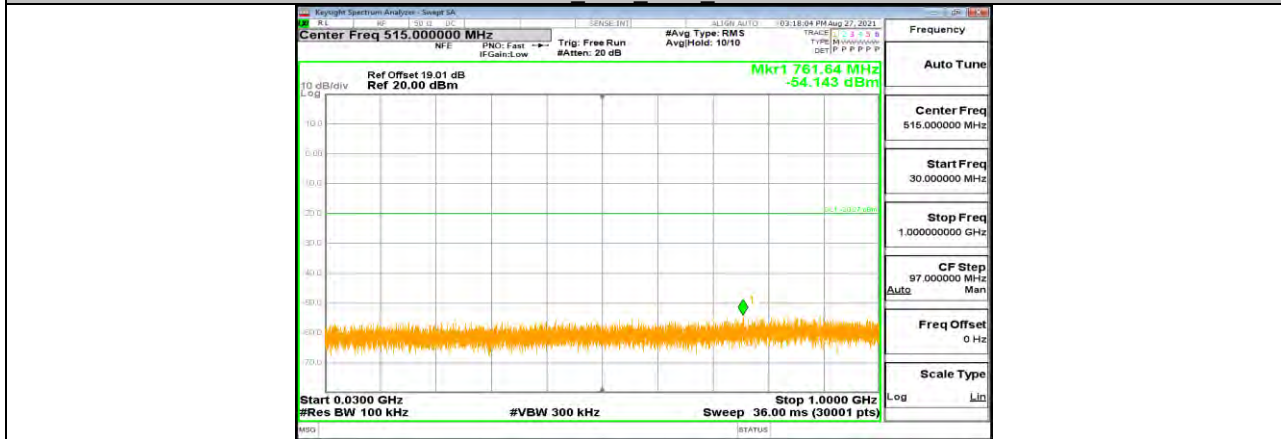
11N20MIMO Ant2 2417 30~1000



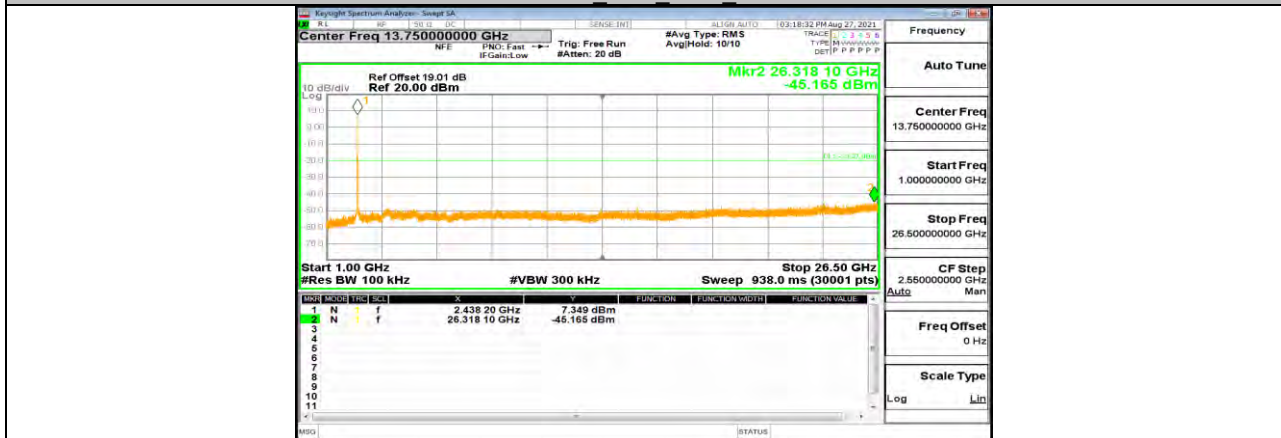
11N20MIMO Ant2 2417 1000~26500



11N20MIMO Ant1 2437 0~Reference



11N20MIMO Ant1 2437 30~1000

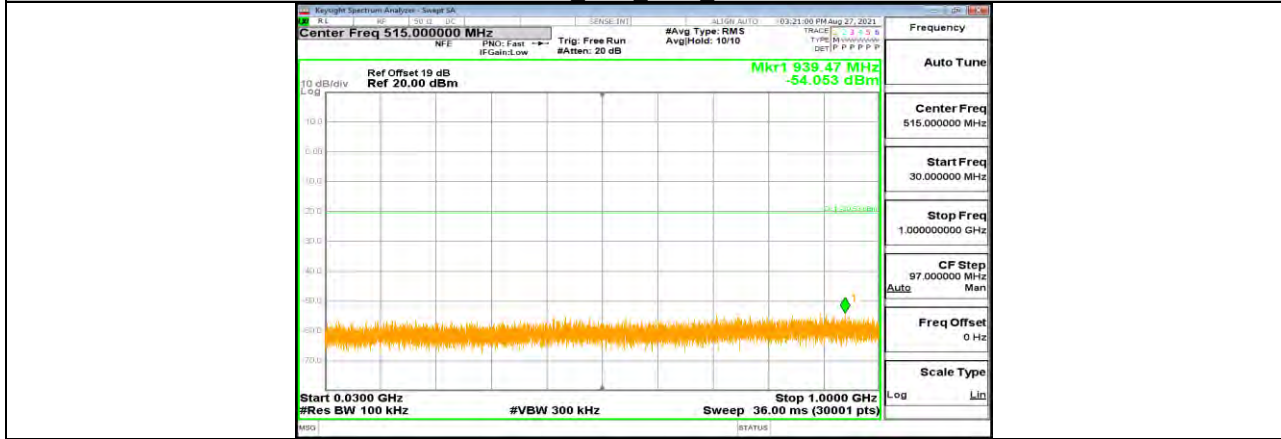


11N20MIMO Ant1 2437 1000~26500

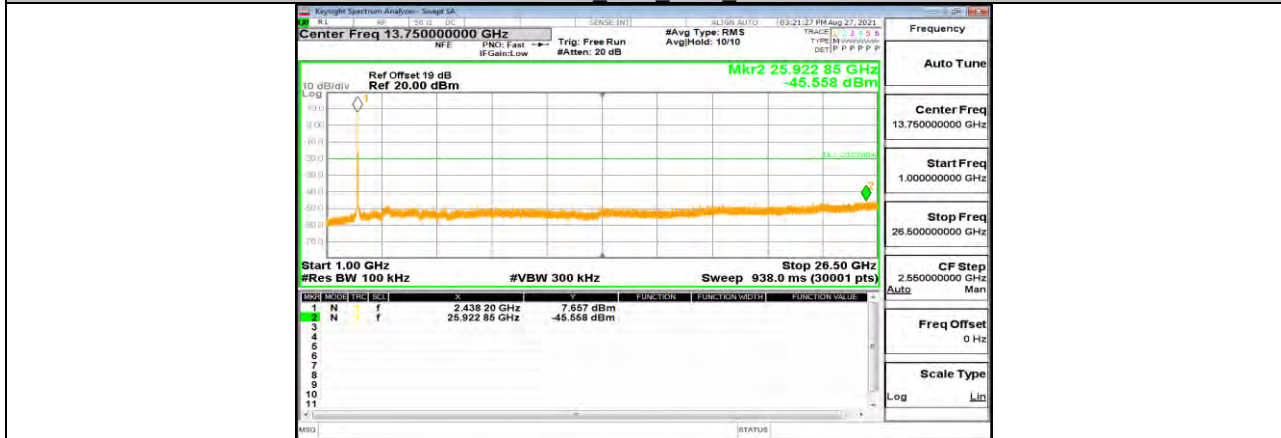




11N20MIMO Ant2 2437 0~Reference



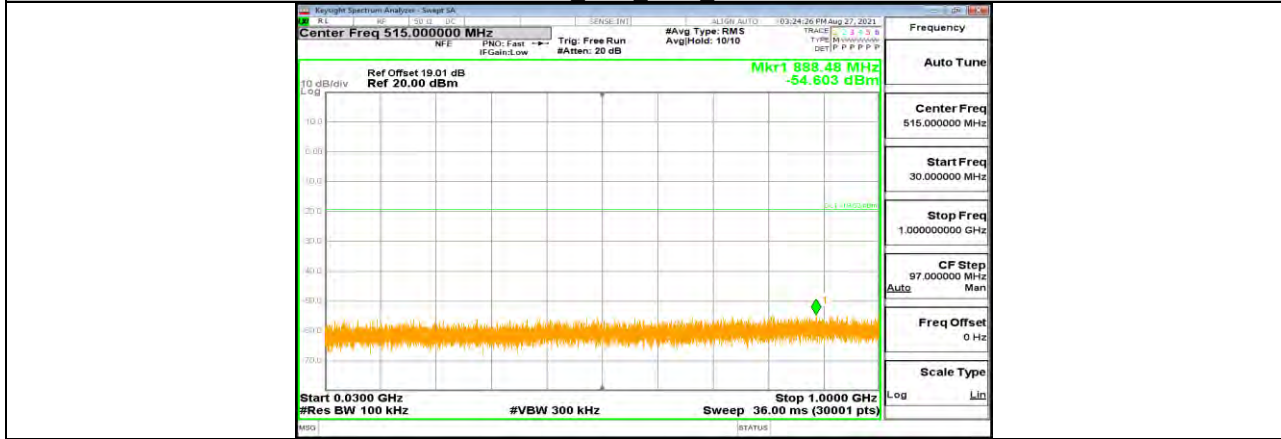
11N20MIMO Ant2 2437 30~1000



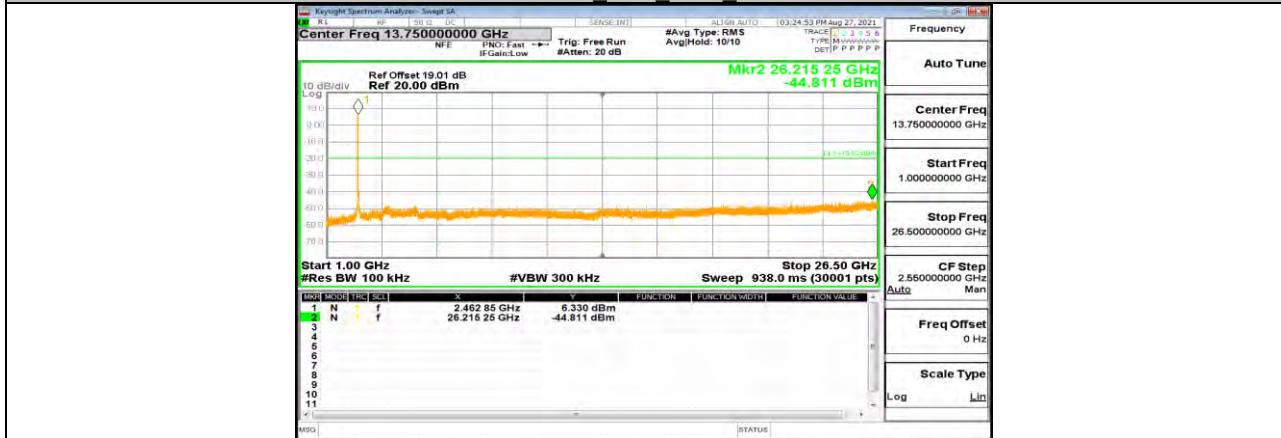
11N20MIMO Ant2 2437 1000~26500



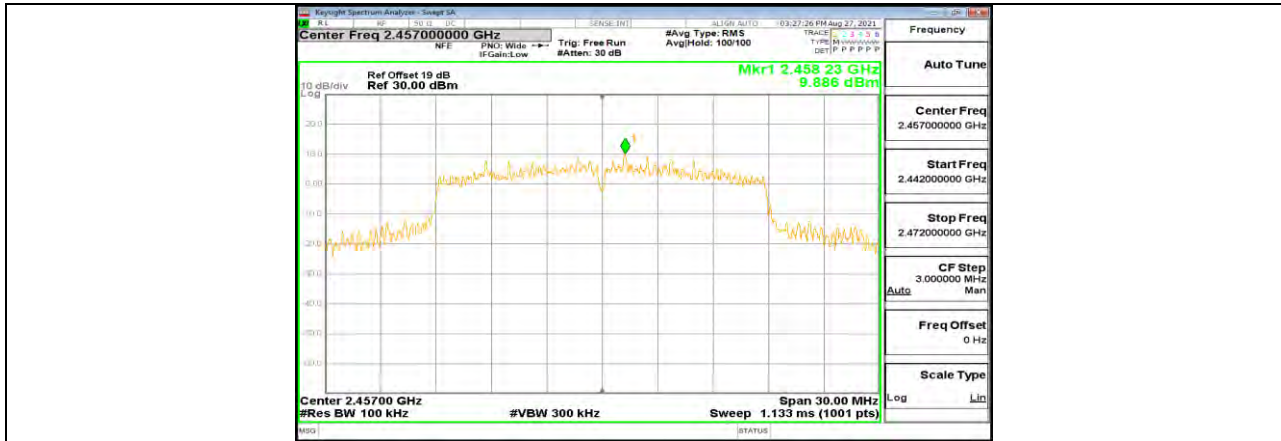
11N20MIMO Ant1 2457 0~Reference



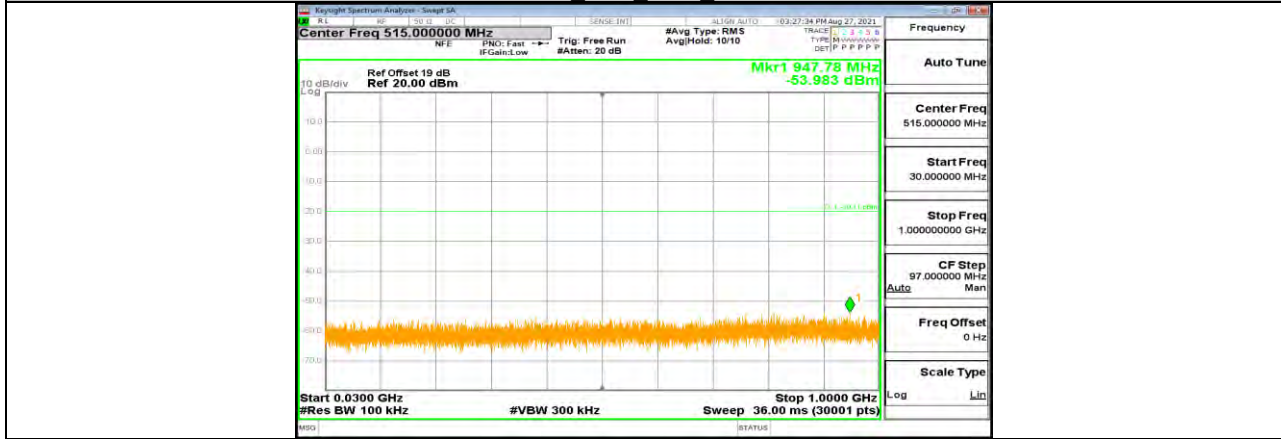
11N20MIMO Ant1 2457 30~1000



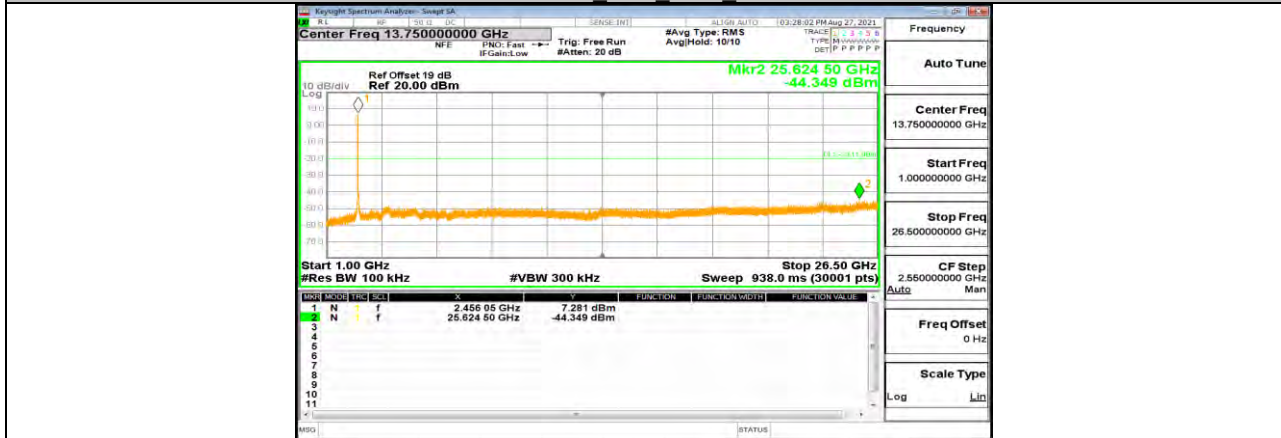
11N20MIMO Ant1 2457 1000~26500



11N20MIMO Ant2 2457 0~Reference



11N20MIMO Ant2 2457 30~1000

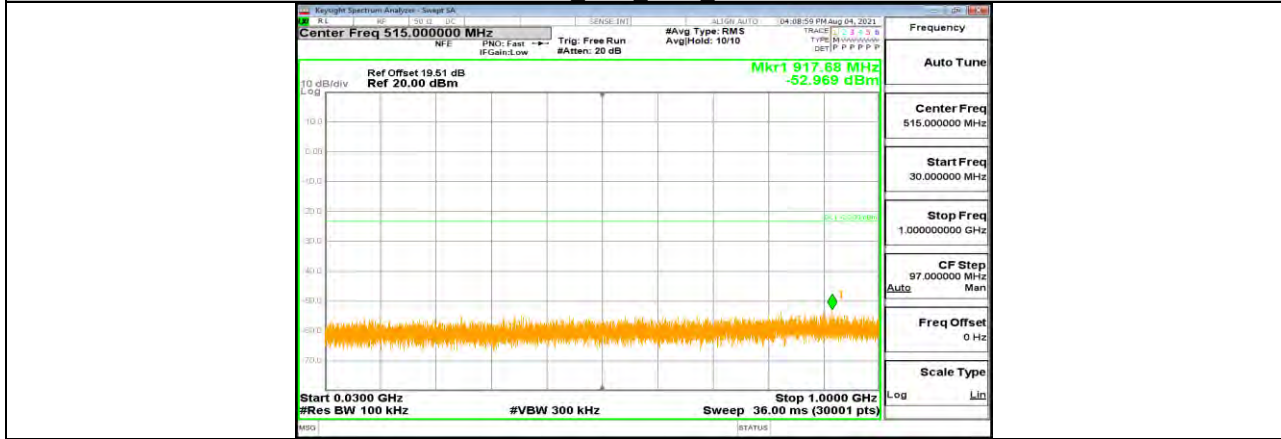


11N20MIMO Ant2 2457 1000~26500

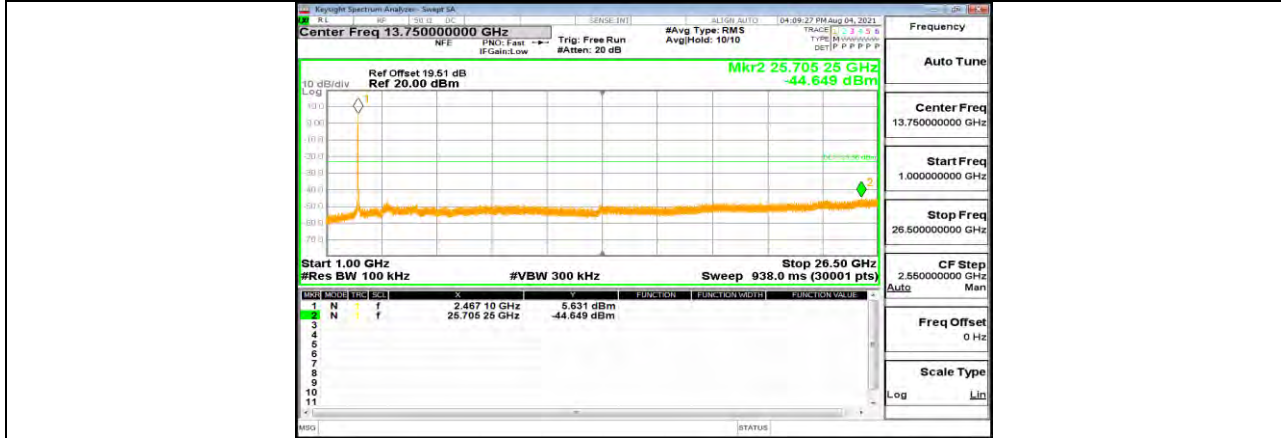




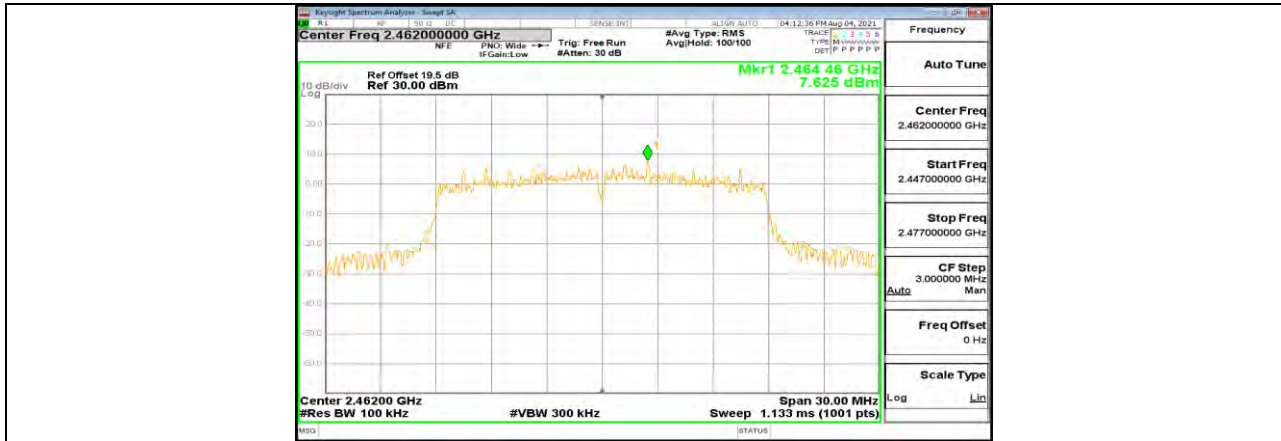
11N20MIMO Ant1 2462 0~Reference



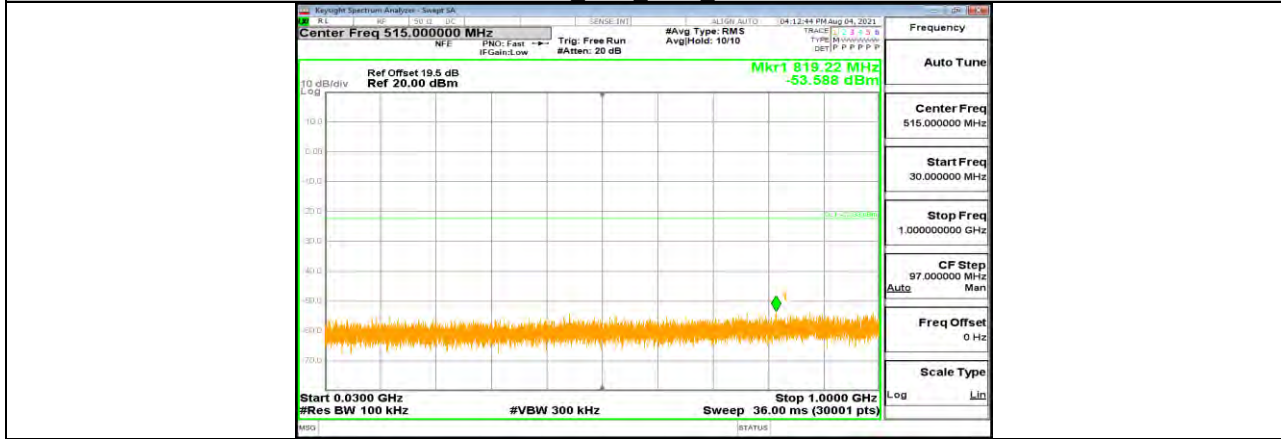
11N20MIMO Ant1 2462 30~1000



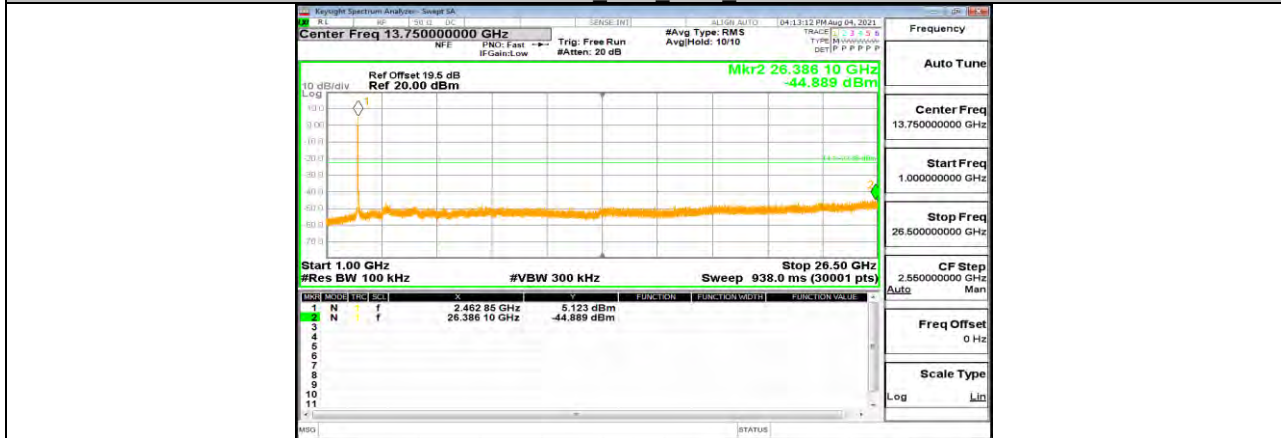
11N20MIMO Ant1 2462 1000~26500



11N20MIMO Ant2 2462 0~Reference



11N20MIMO Ant2 2462 30~1000

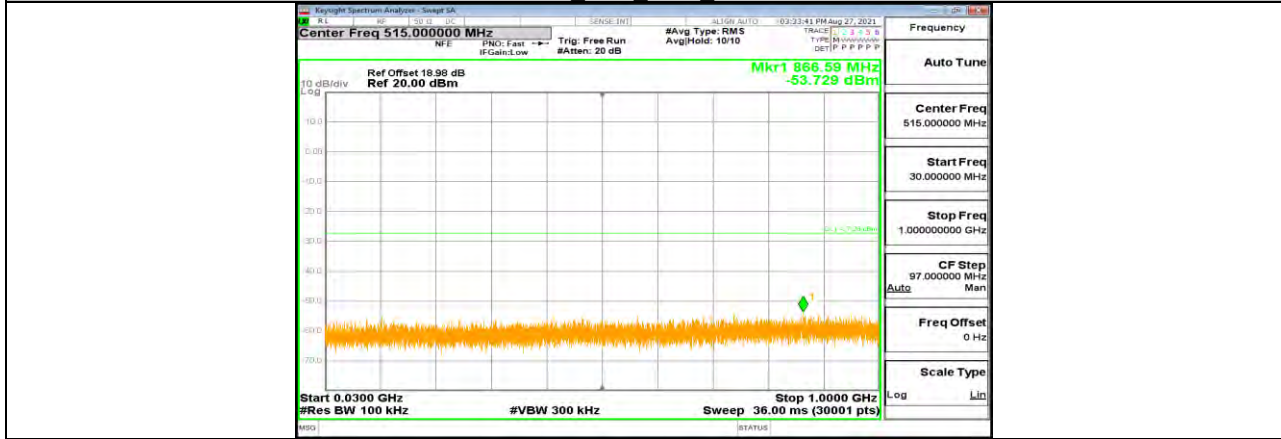


11N20MIMO Ant2 2462 1000~26500

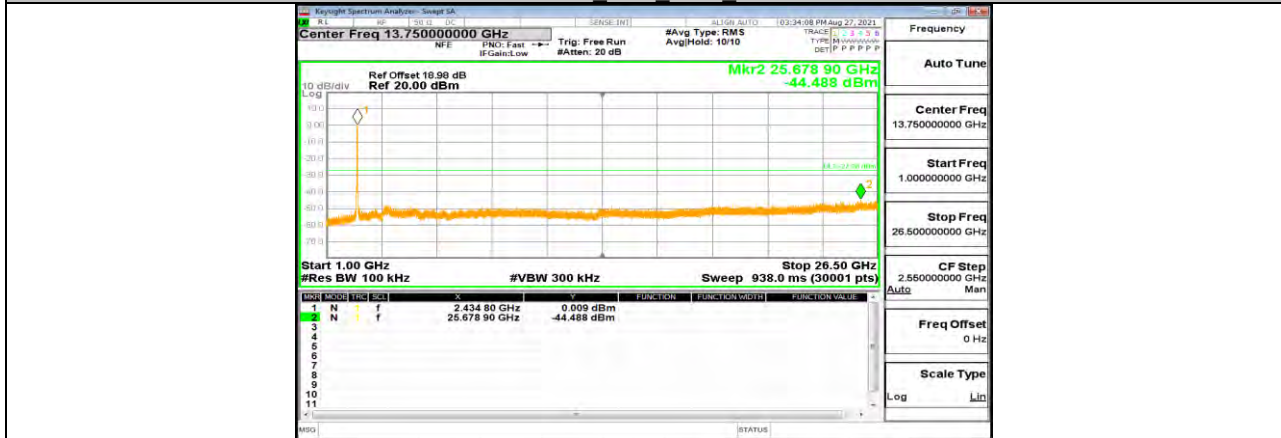




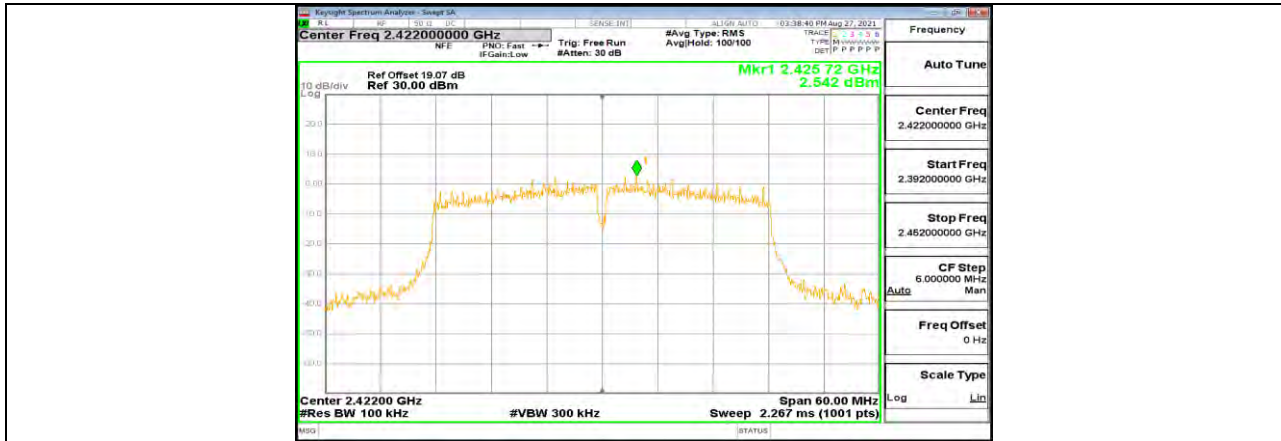
11N40MIMO Ant1 2422 0~Reference



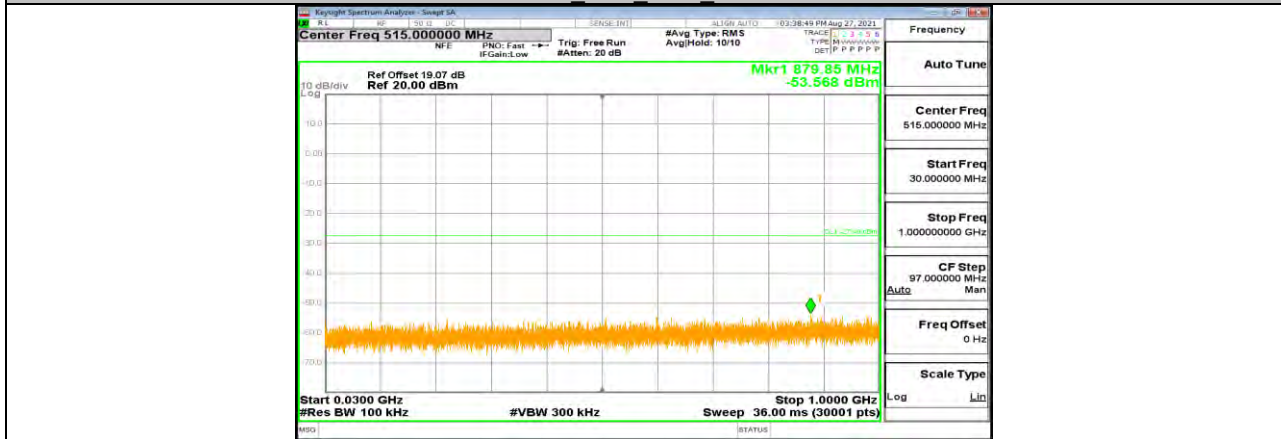
11N40MIMO Ant1 2422 30~1000



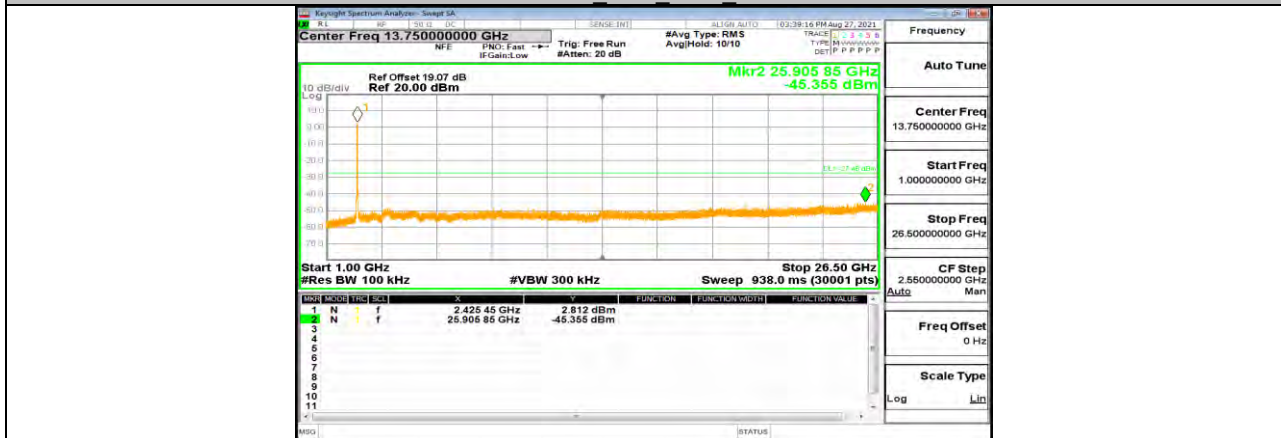
11N40MIMO Ant1 2422 1000~26500



11N40MIMO Ant2 2422 0~Reference



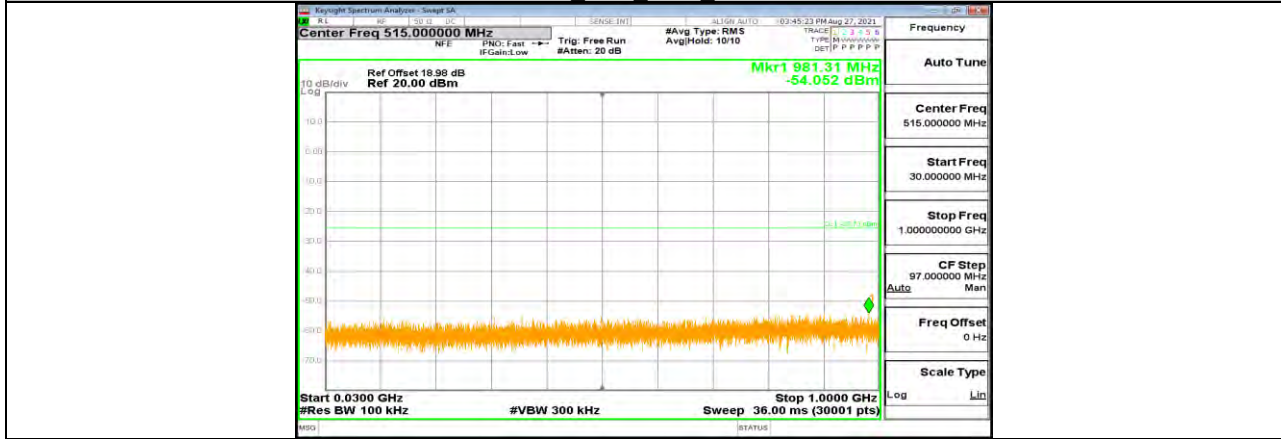
11N40MIMO Ant2 2422 30~1000



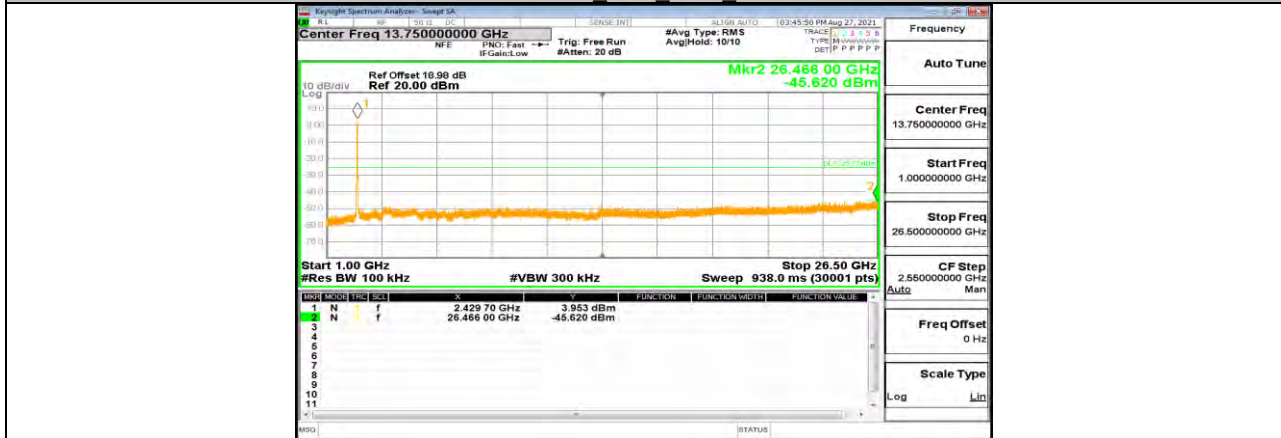
11N40MIMO Ant2 2422 1000~26500



11N40MIMO Ant1 2427 0~Reference

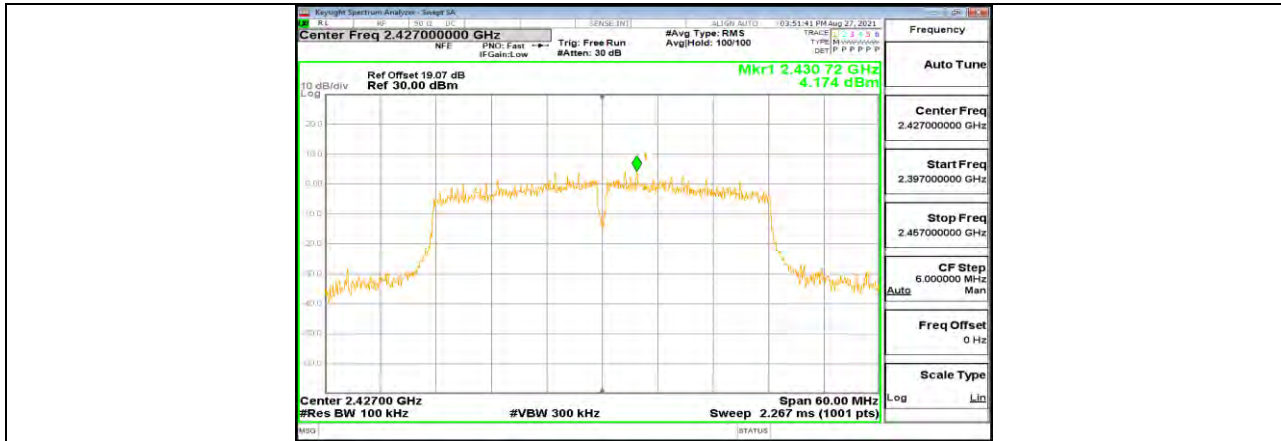


11N40MIMO Ant1 2427 30~1000

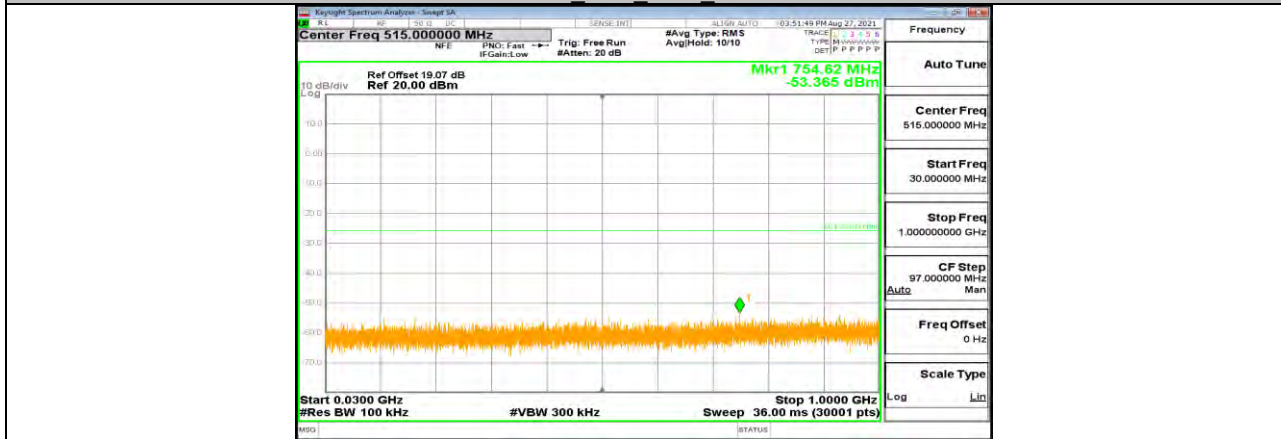


11N40MIMO Ant1 2427 1000~26500

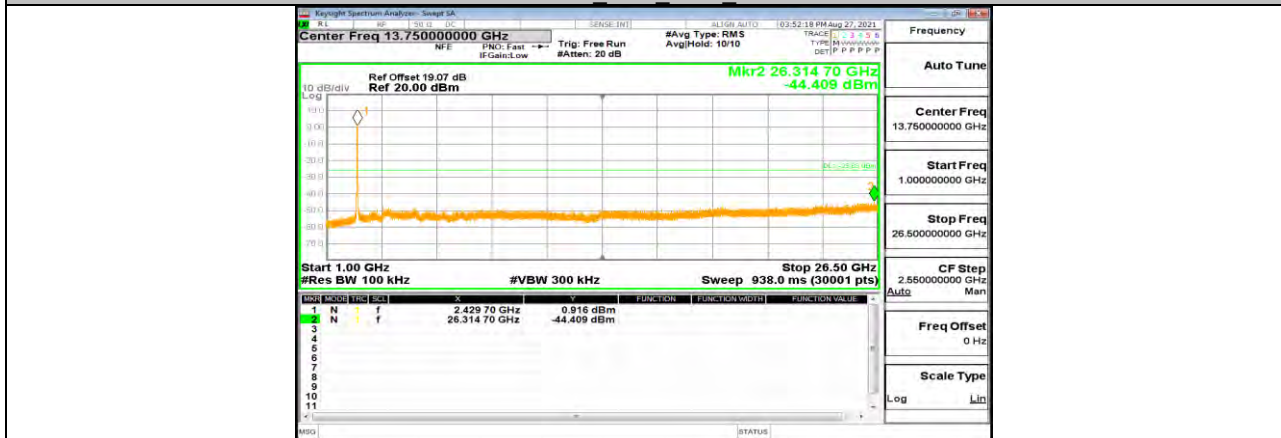




11N40MIMO Ant2 2427 0~Reference



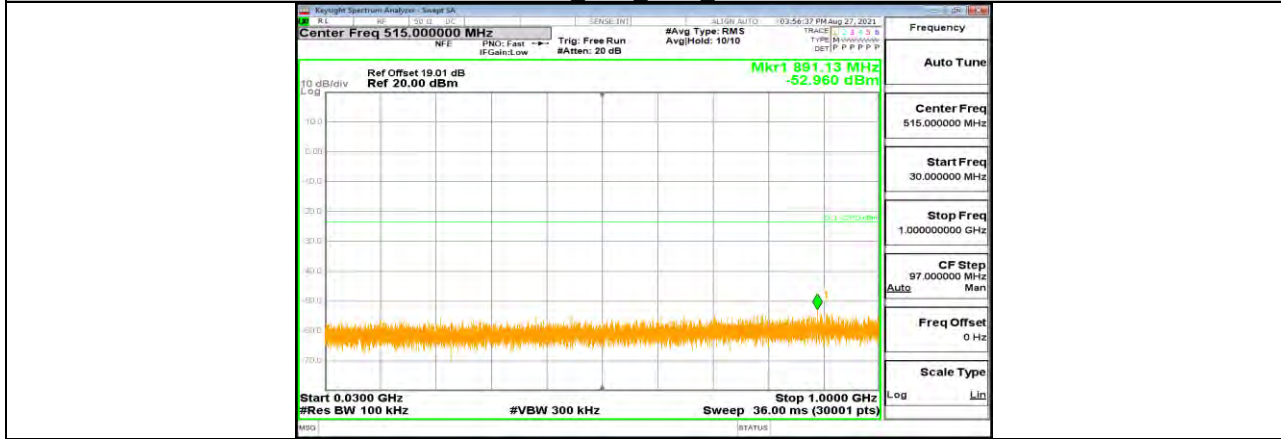
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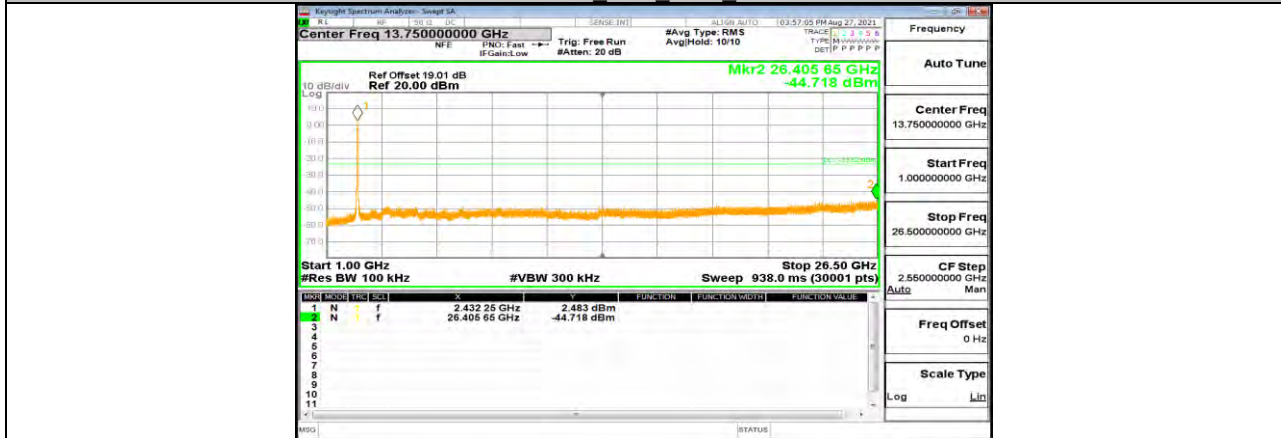
11N40MIMO Ant2 2427 1000~26500



11N40MIMO Ant1 2437 0~Reference



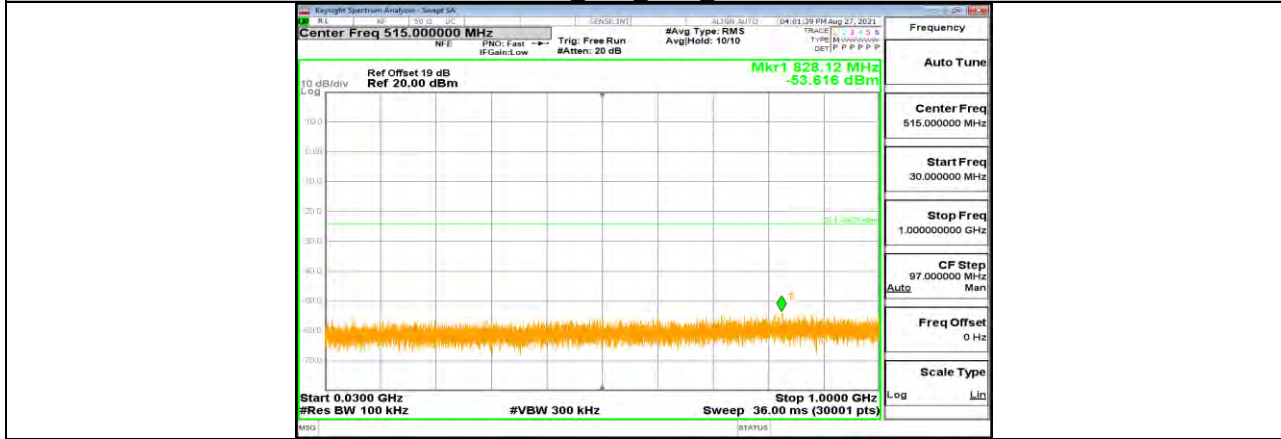
11N40MIMO Ant1 2437 30~1000



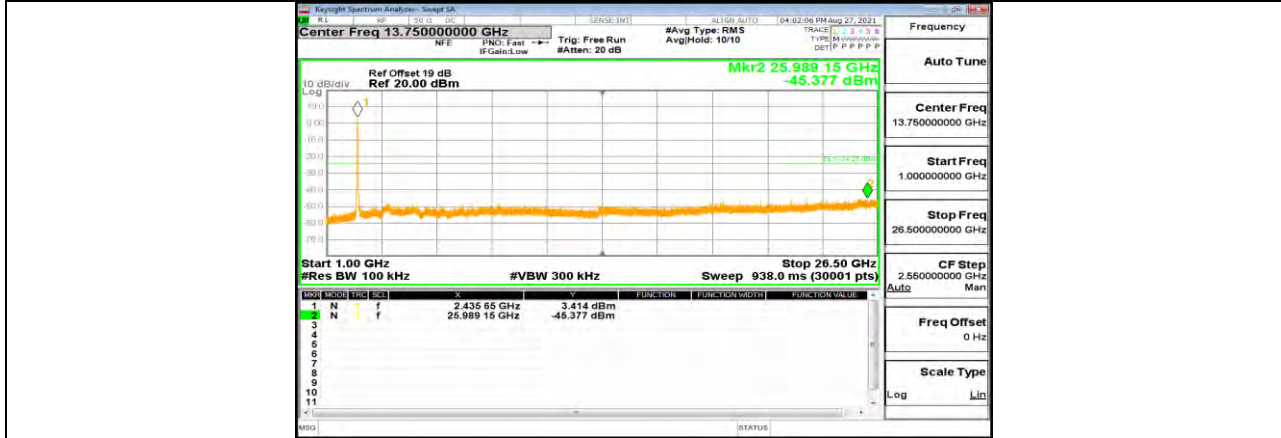
11N40MIMO Ant1 2437 1000~26500



11N40MIMO Ant2 2437 0~Reference



11N40MIMO Ant2 2437 30~1000

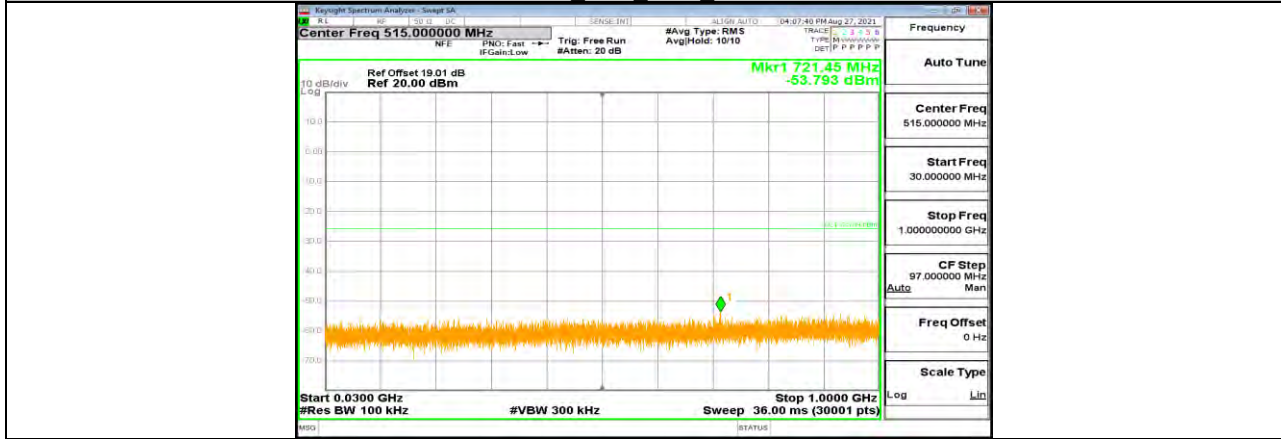


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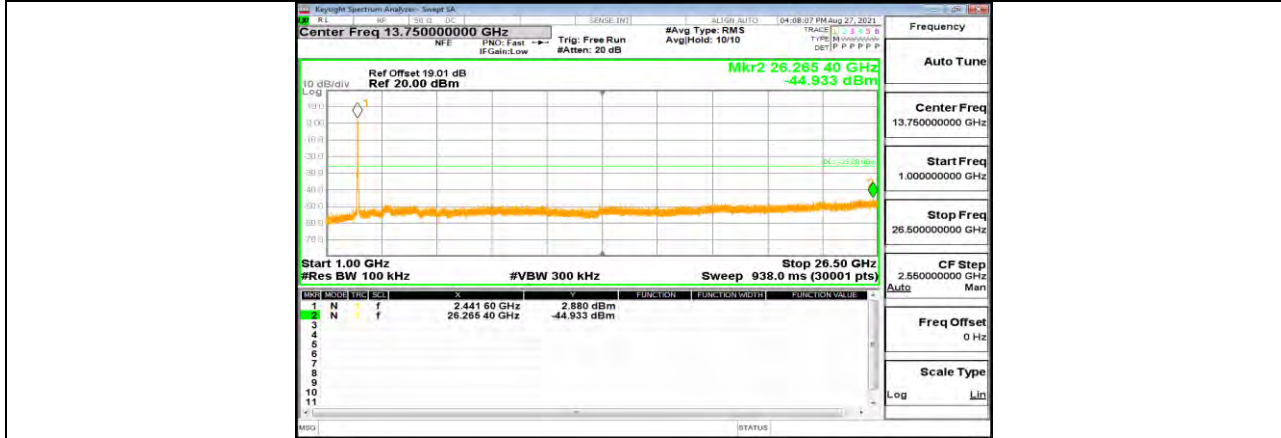




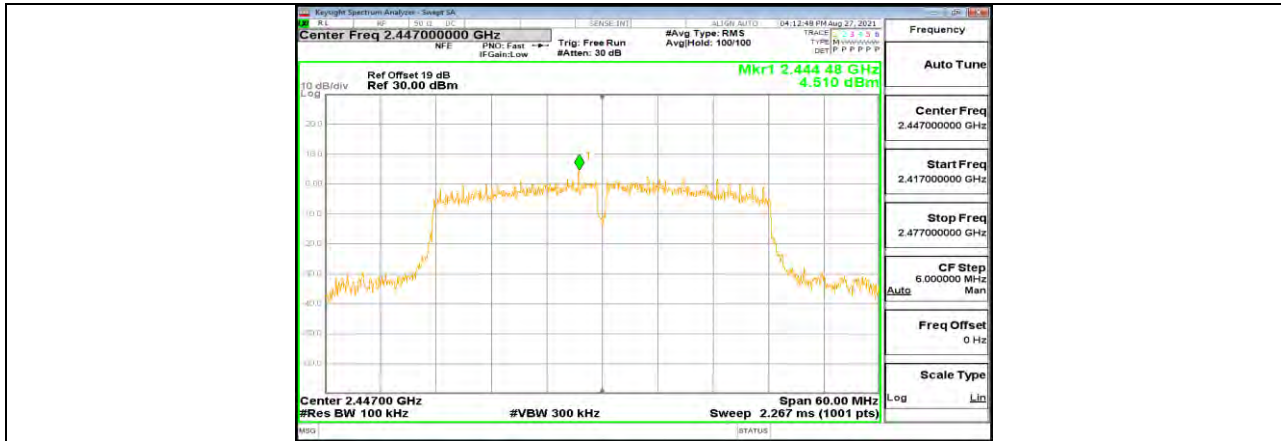
11N40MIMO Ant1 2447 0~Reference



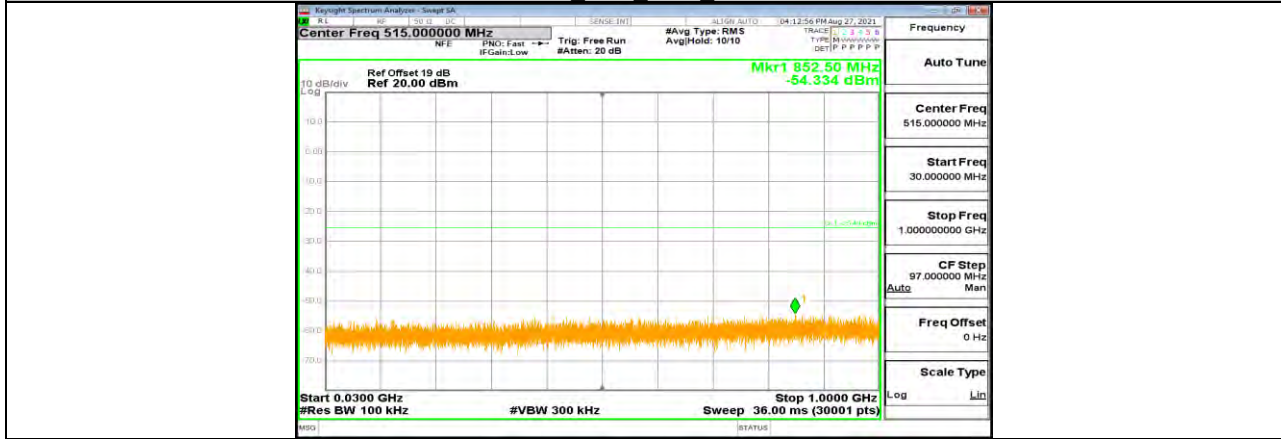
11N40MIMO Ant1 2447 30~1000



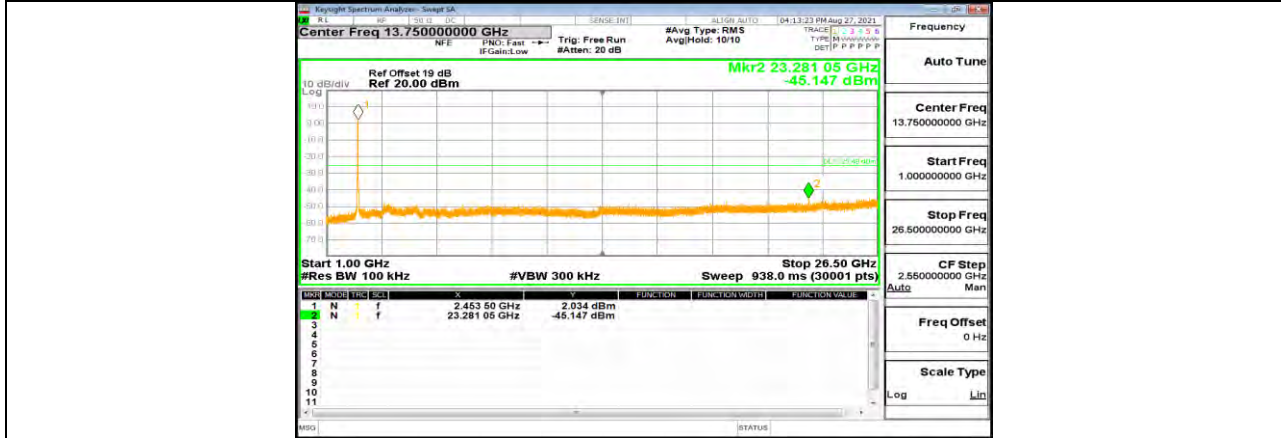
11N40MIMO Ant1 2447 1000~26500



11N40MIMO Ant2 2447 0~Reference



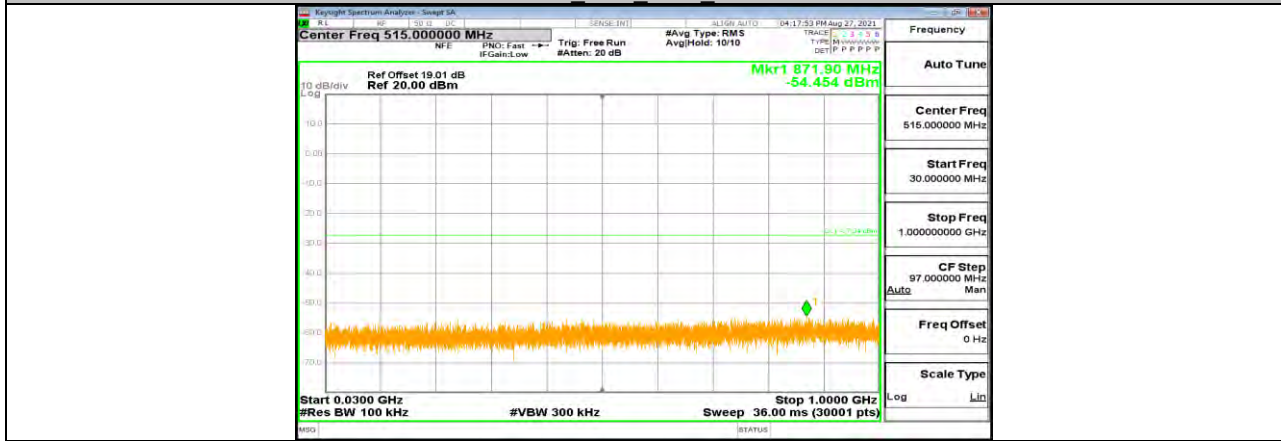
11N40MIMO Ant2 2447 30~1000



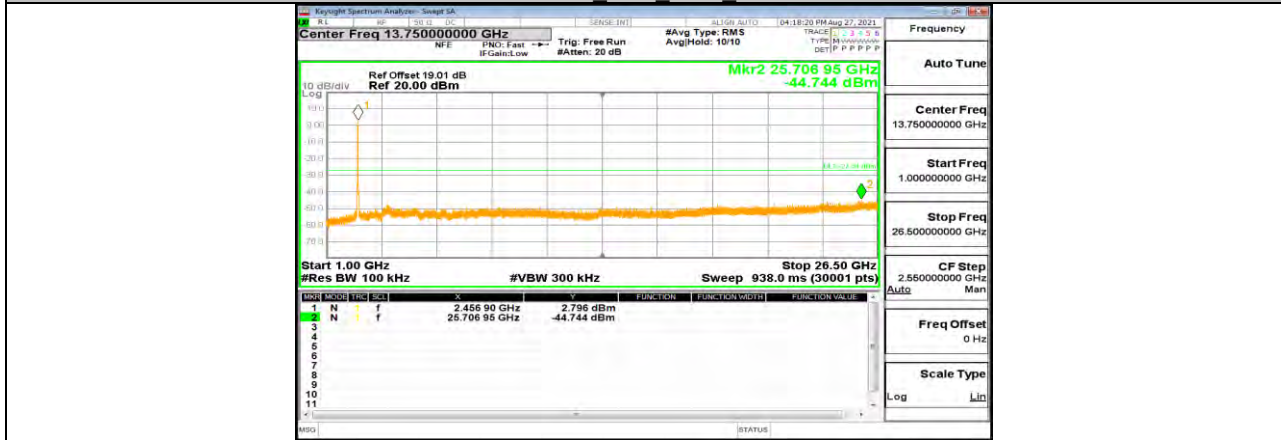
11N40MIMO Ant2 2447 1000~26500



11N40MIMO Ant1 2452 0~Reference



11N40MIMO Ant1 2452 30~1000

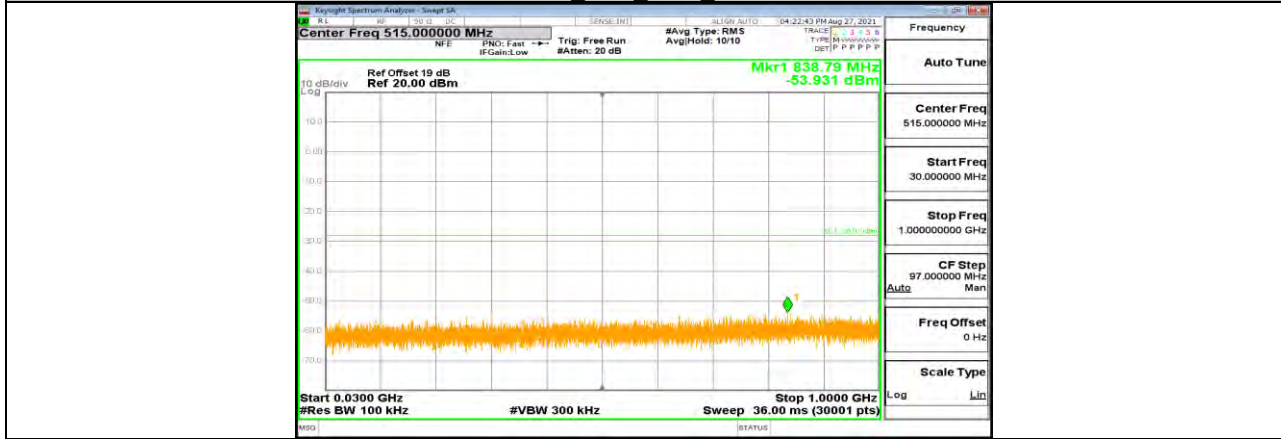


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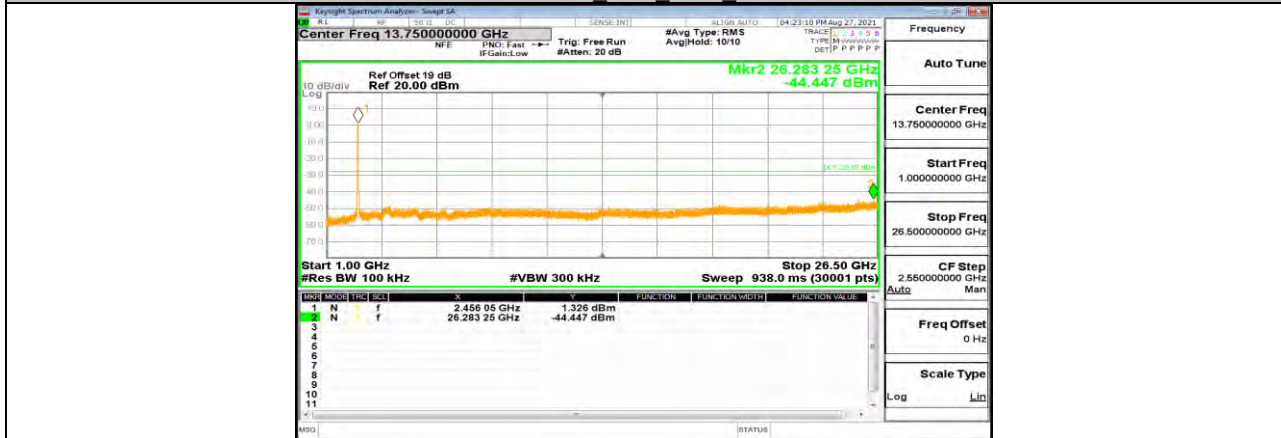




11N40MIMO Ant2 2452 0~Reference



11N40MIMO Ant2 2452 30~1000



11N40MIMO Ant2 2452 1000~26500



## 11.7. Appendix G: Duty Cycle

### 11.7.1. Test Result

Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	8.42	8.55	0.9848	98.48	0.07	0.12	0.01
11G-CDD	1.40	1.54	0.9091	90.91	0.41	0.71	1
11N20MIMO	1.31	1.45	0.9034	90.34	0.44	0.76	1
11N40MIMO	0.65	0.75	0.8667	86.67	0.62	1.54	2

Note:

Duty Cycle Correction Factor= $10\log(1/x)$ .

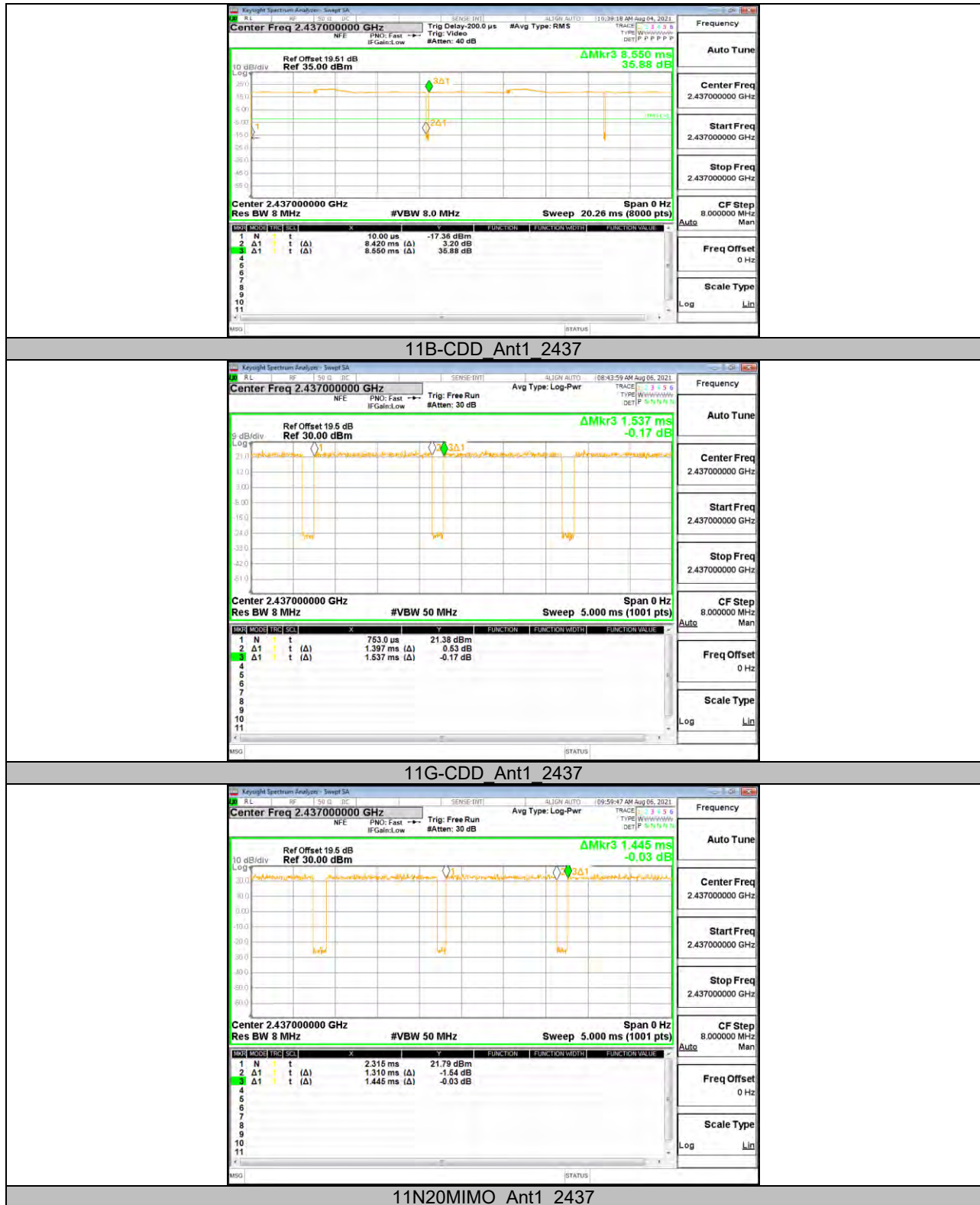
Where: x is Duty Cycle (Linear)

Where: T is On Time

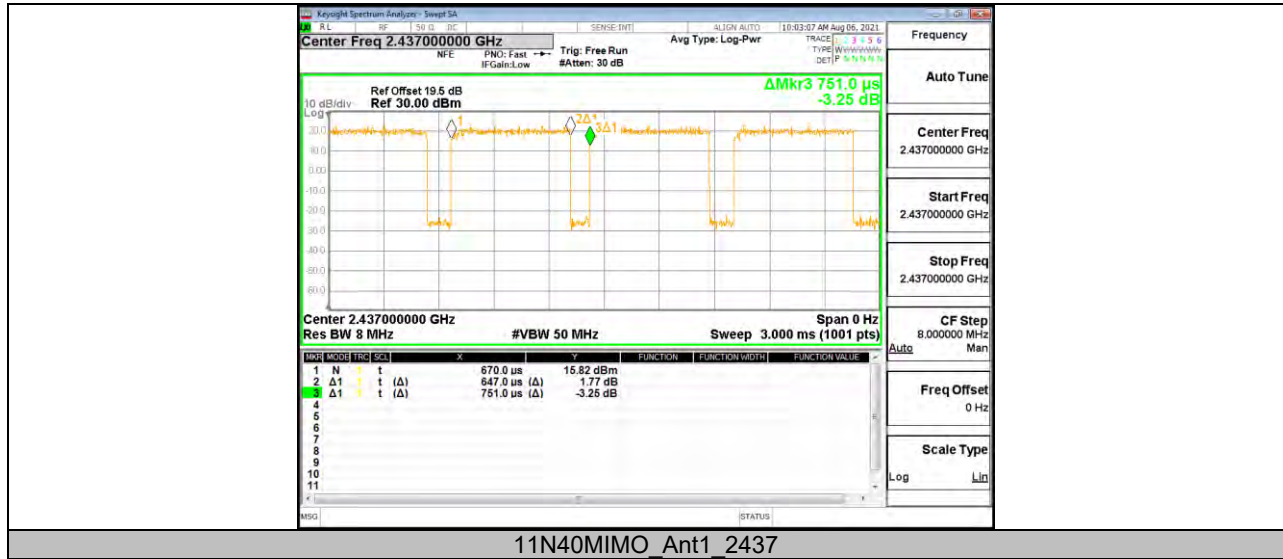
If that calculated VBW is not available on the analyzer then the next higher value should be used.



### 11.7.2. Test Graphs







**END OF REPORT**