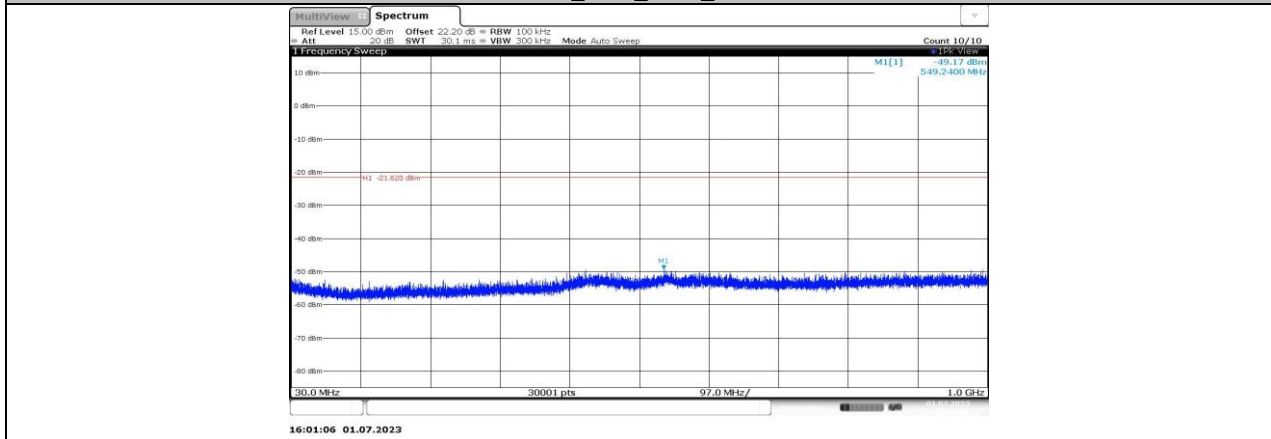
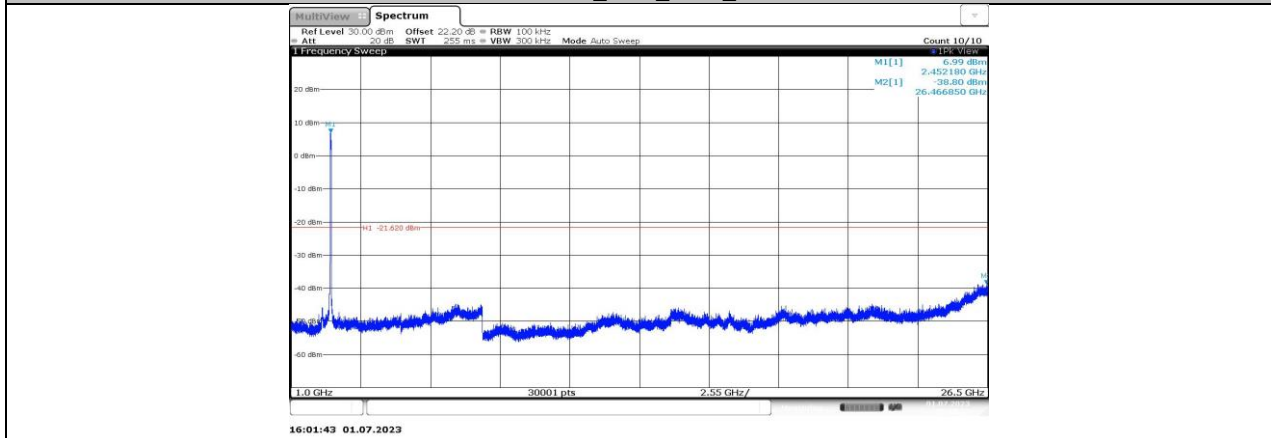


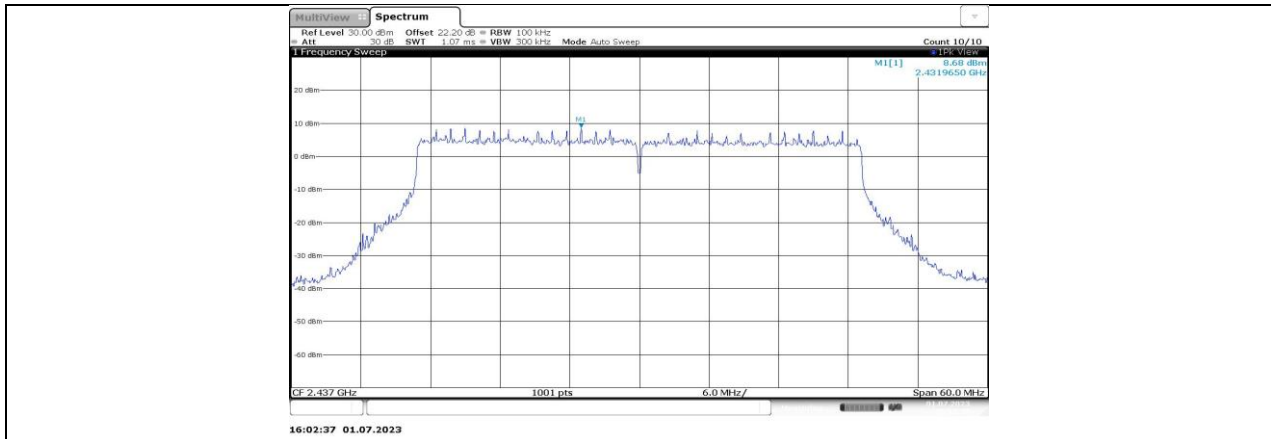
11AX40-CDD_Ant5_2437_0-Reference



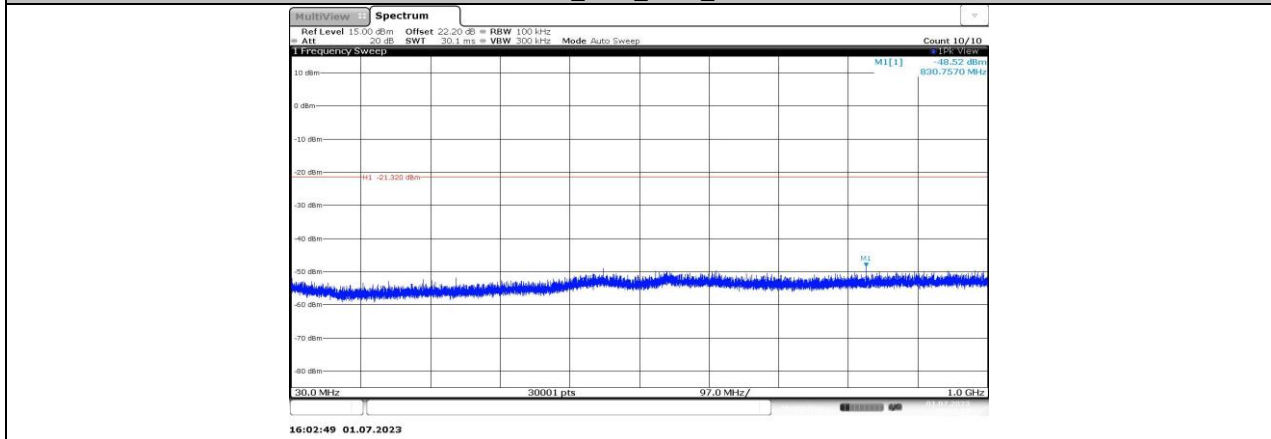
11AX40-CDD_Ant5_2437_30-1000



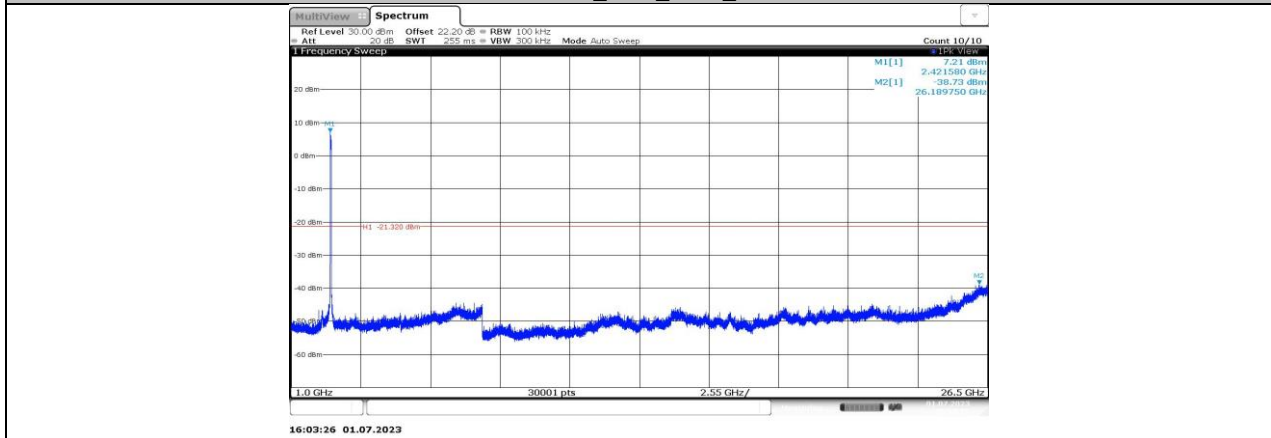
11AX40-CDD_Ant5_2437_1000-26500



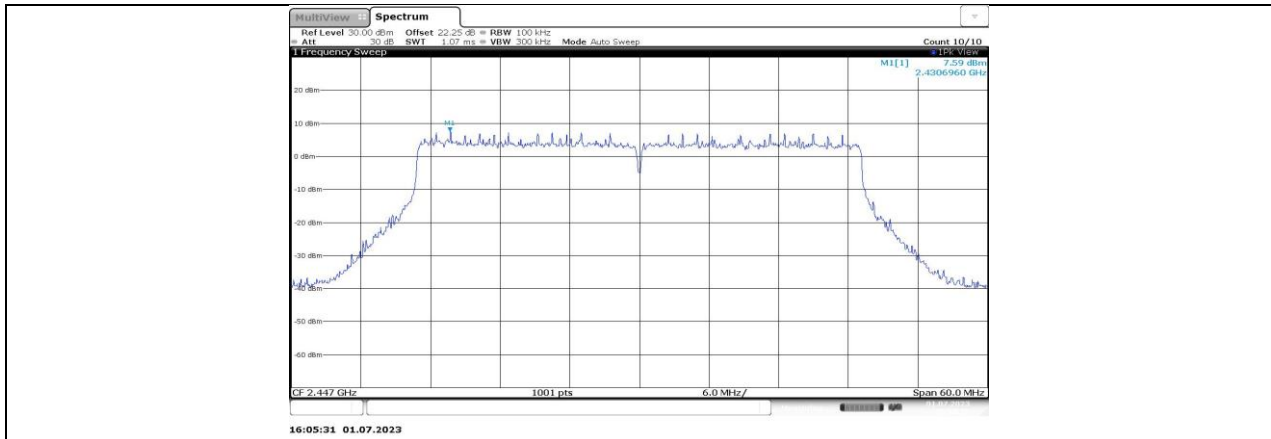
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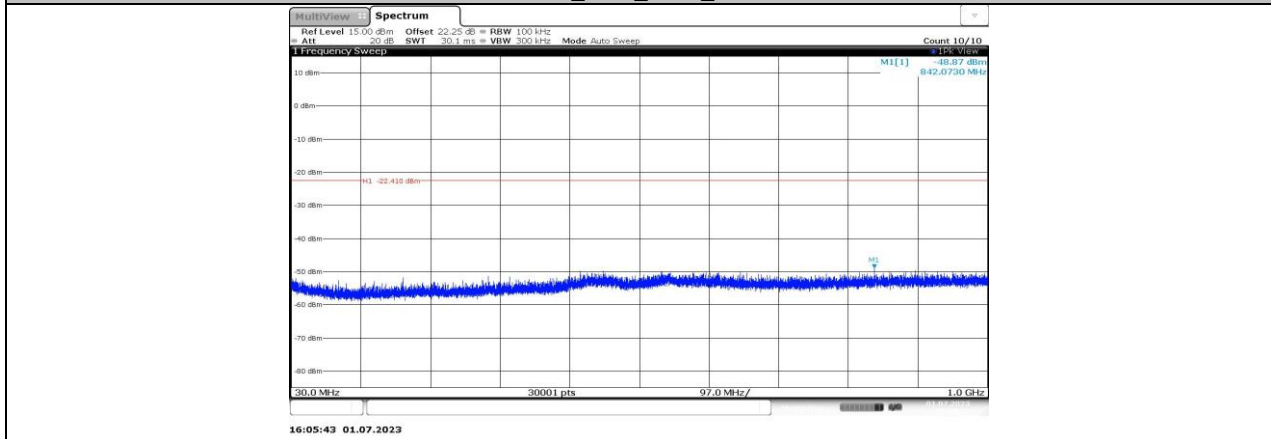
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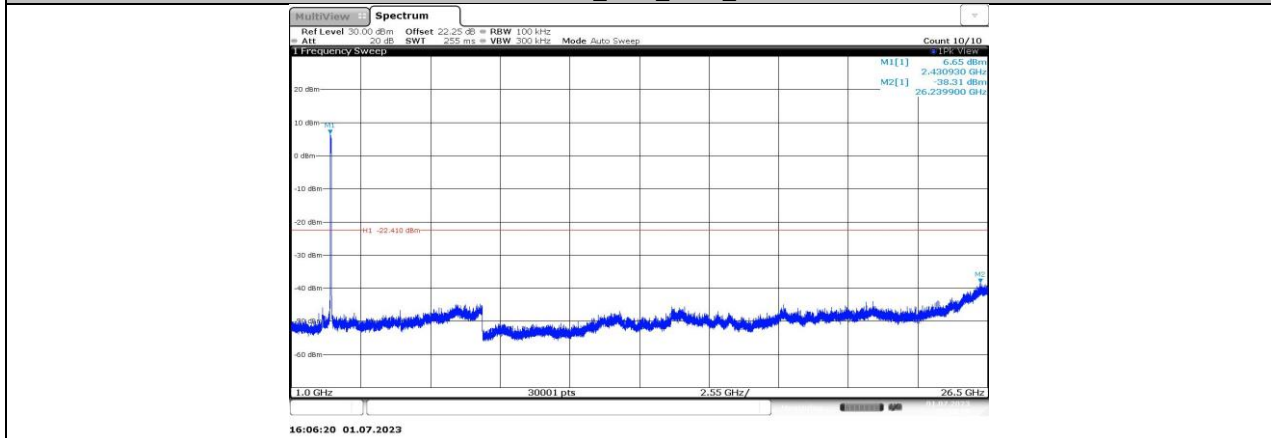
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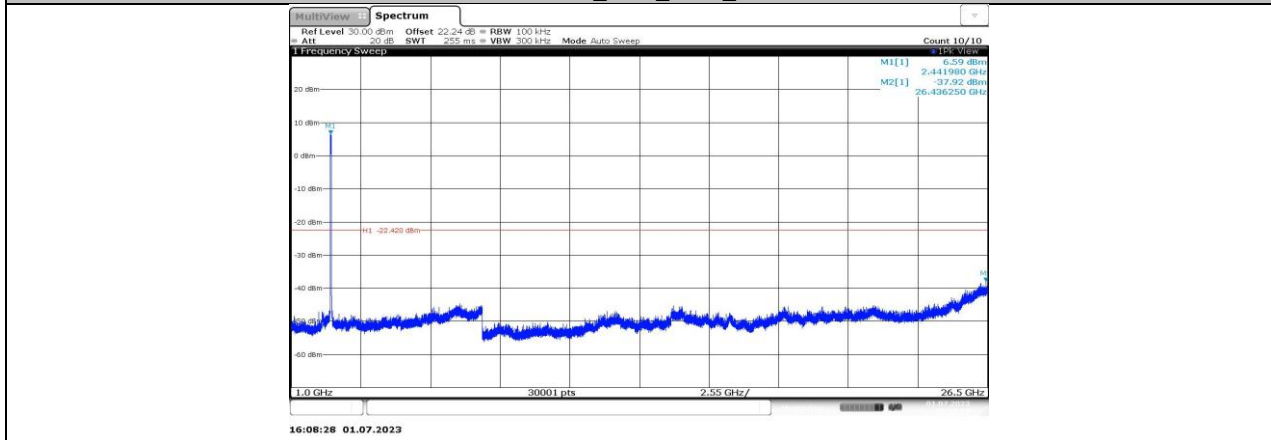
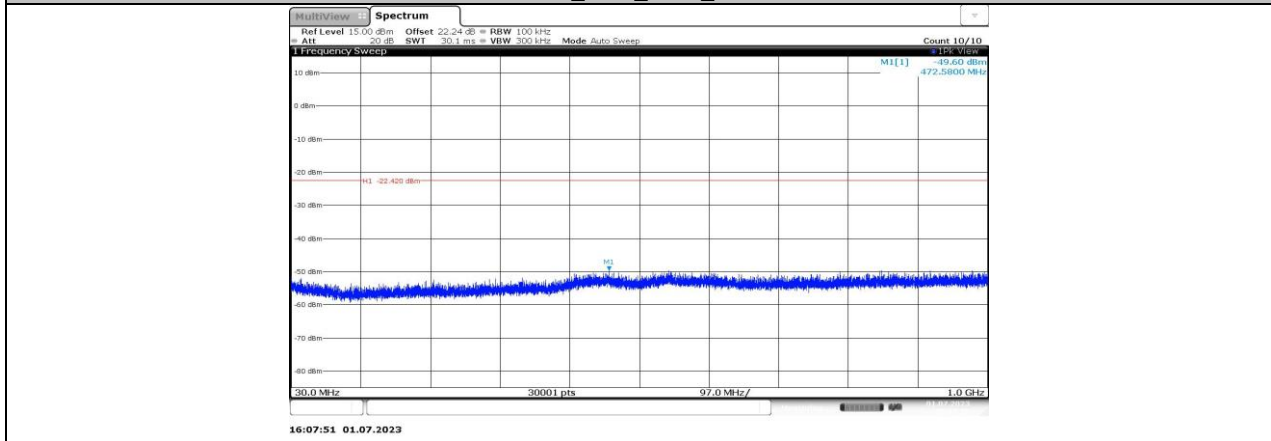
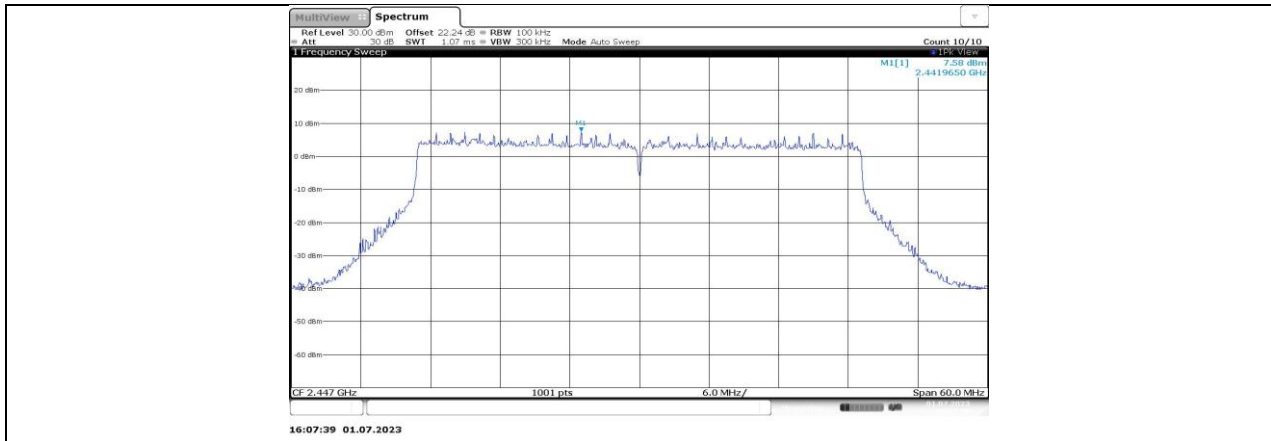
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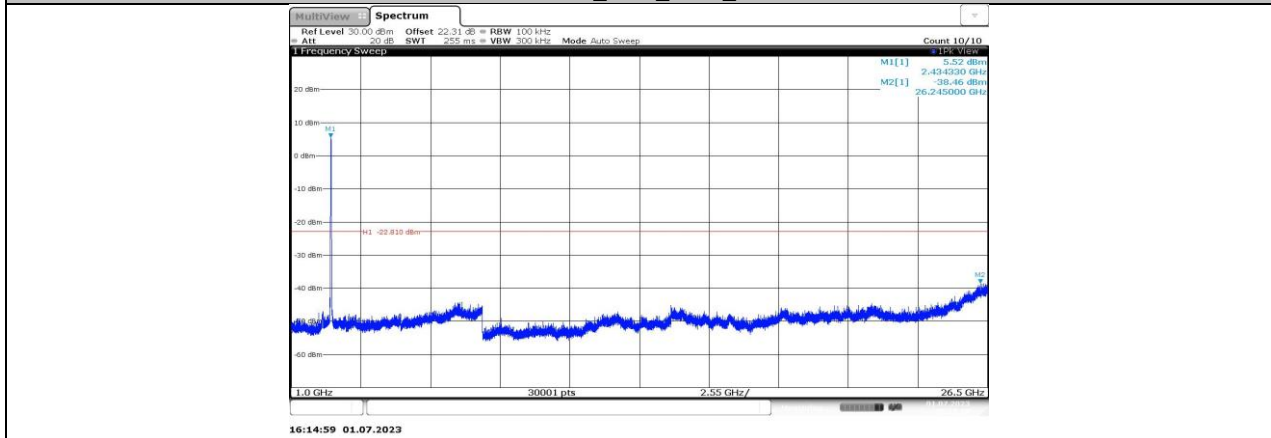
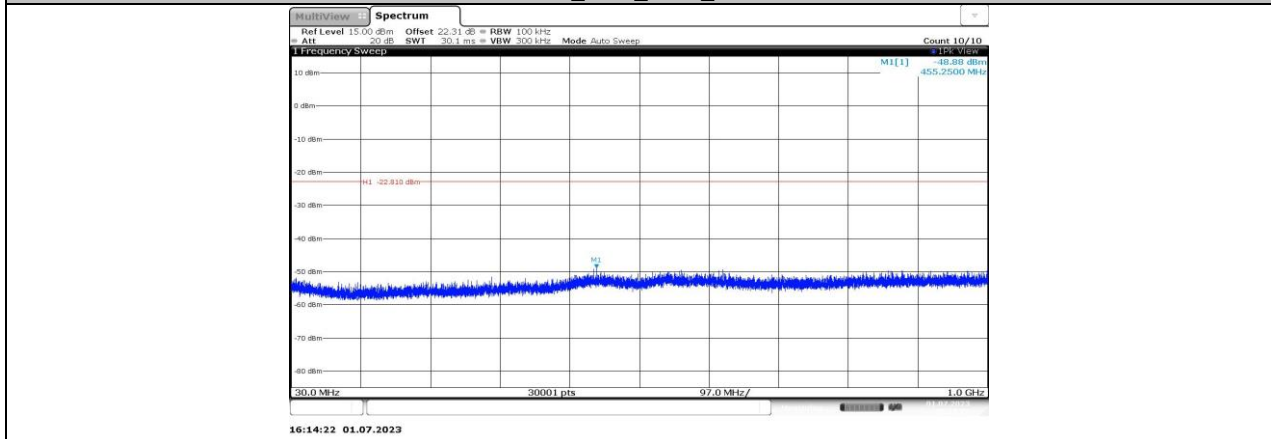
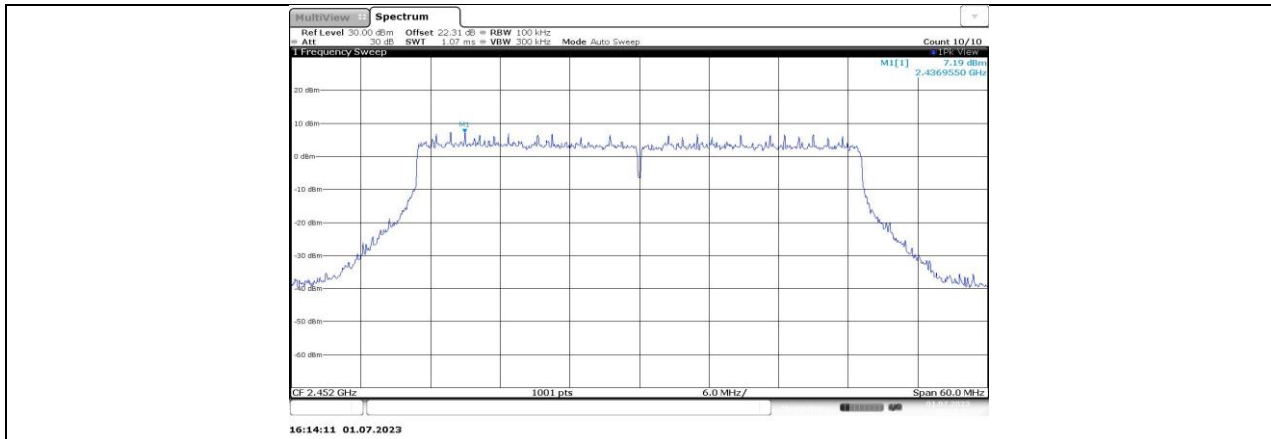


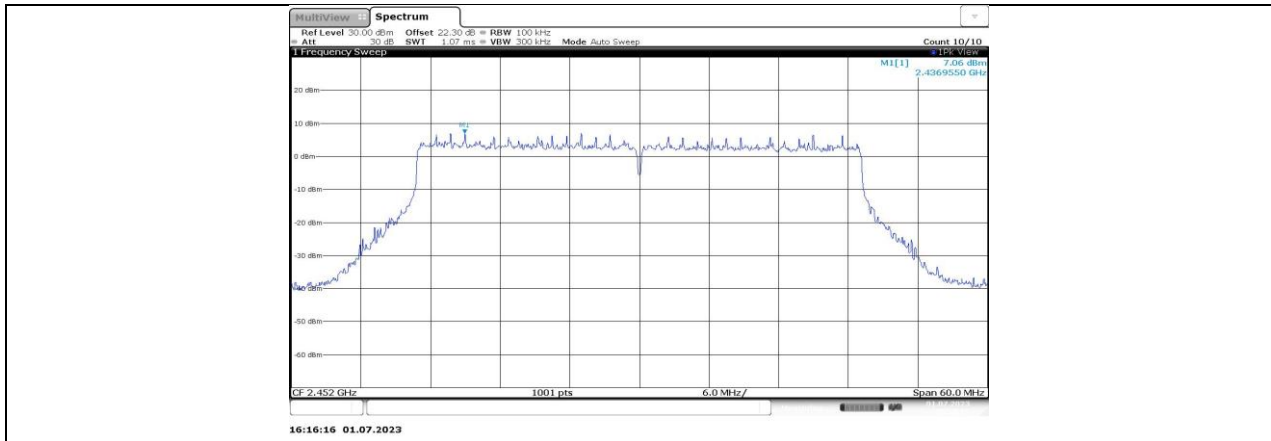
11AX40-CDD_Ant5_2447_30-1000



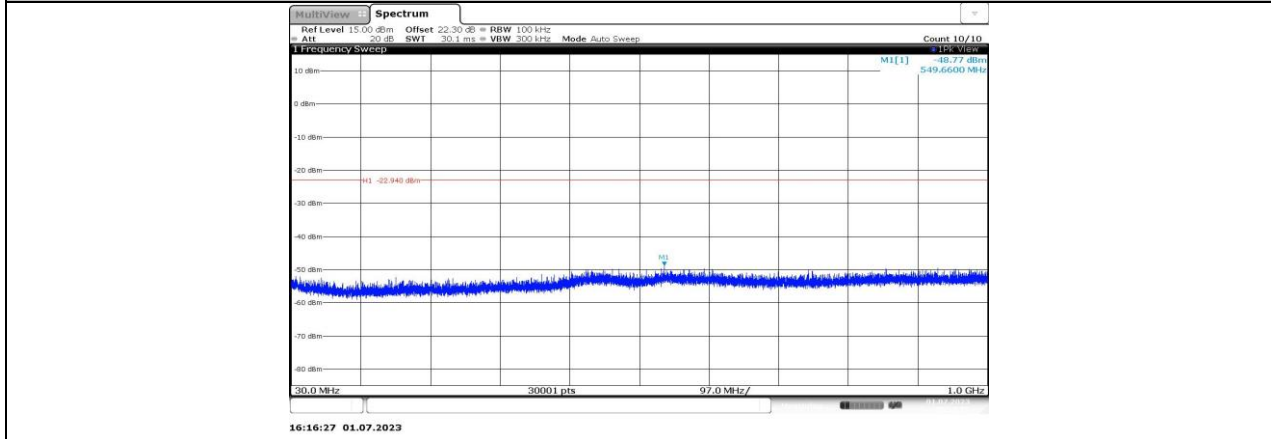
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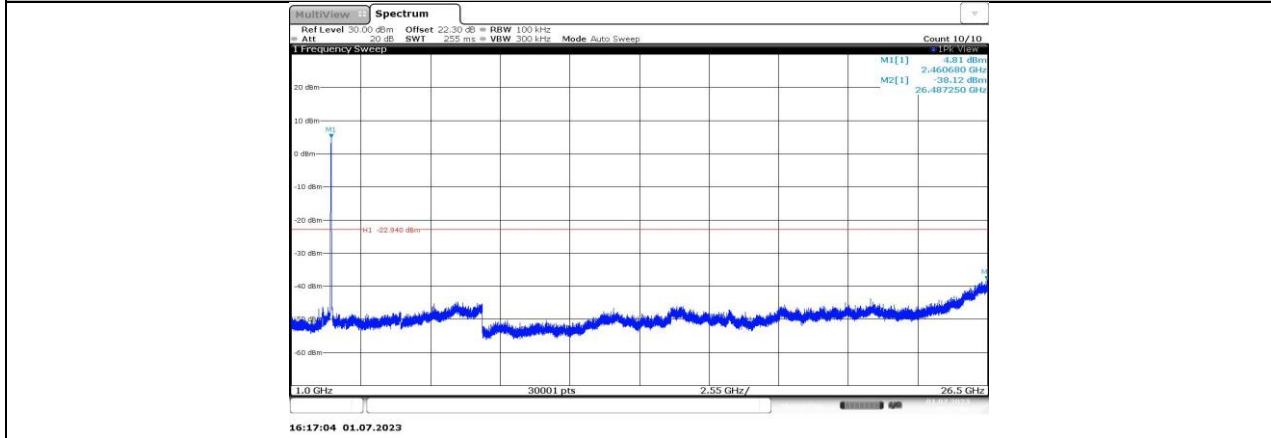




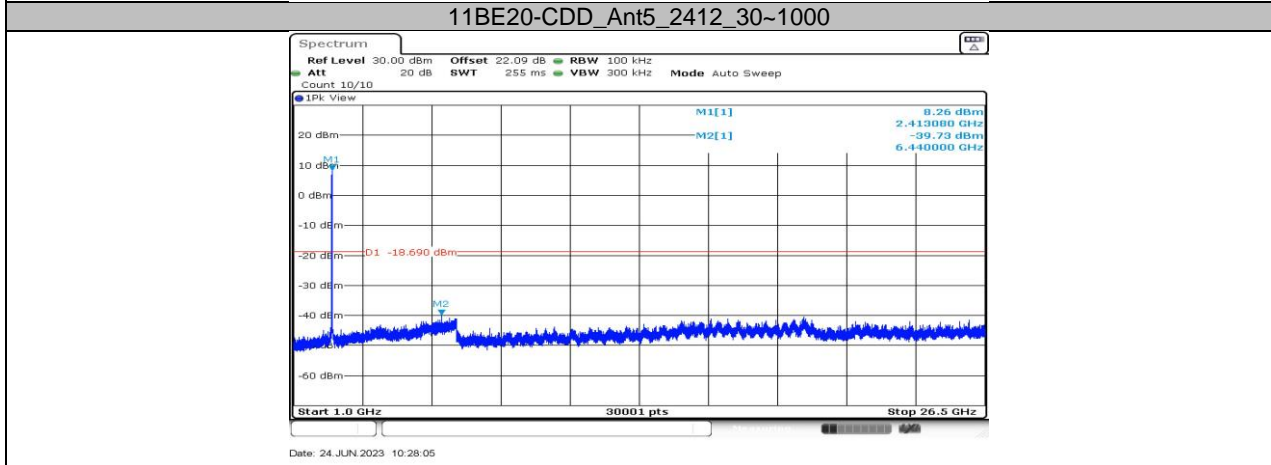
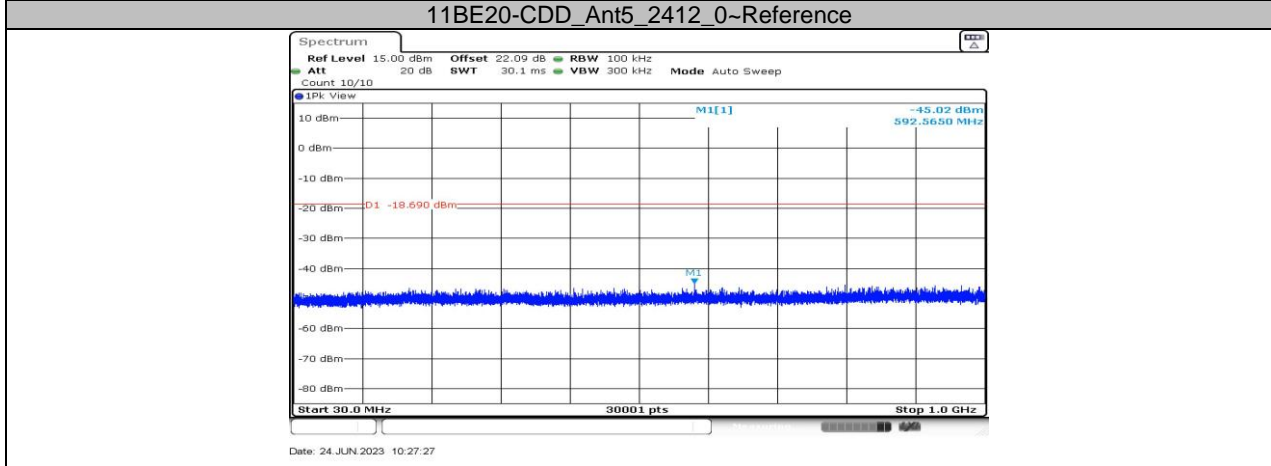
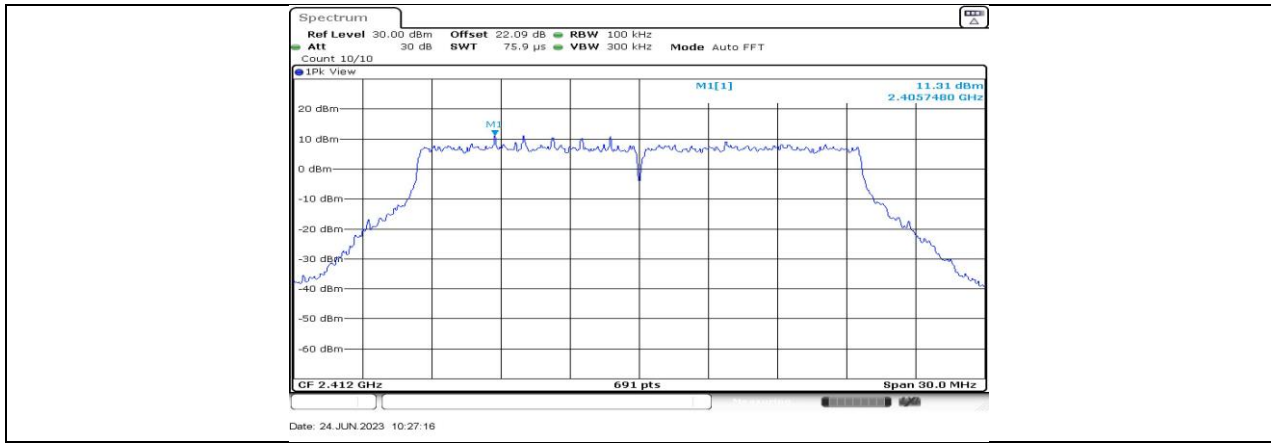
11AX40-CDD_Ant6_2452_0-Reference

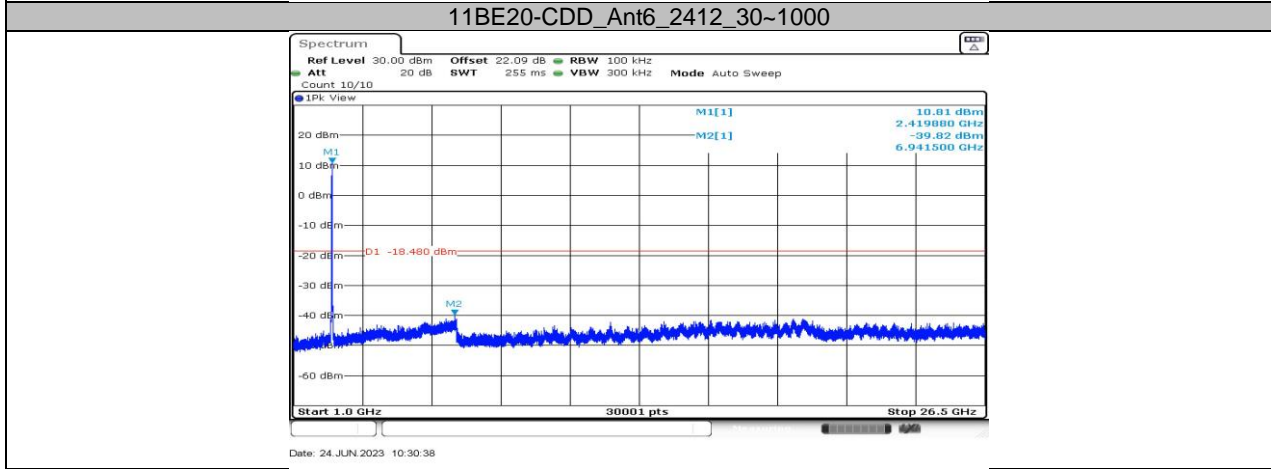
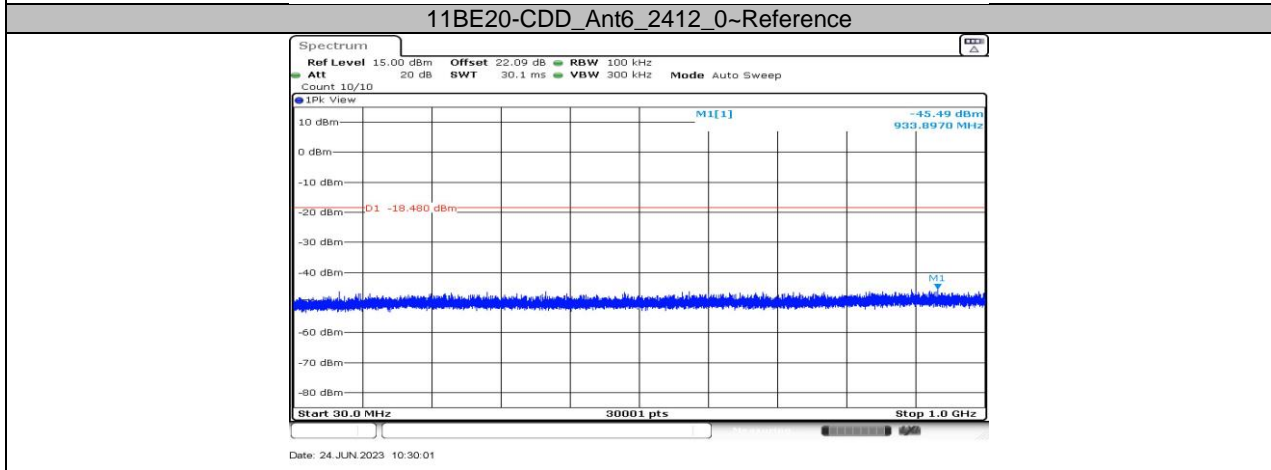
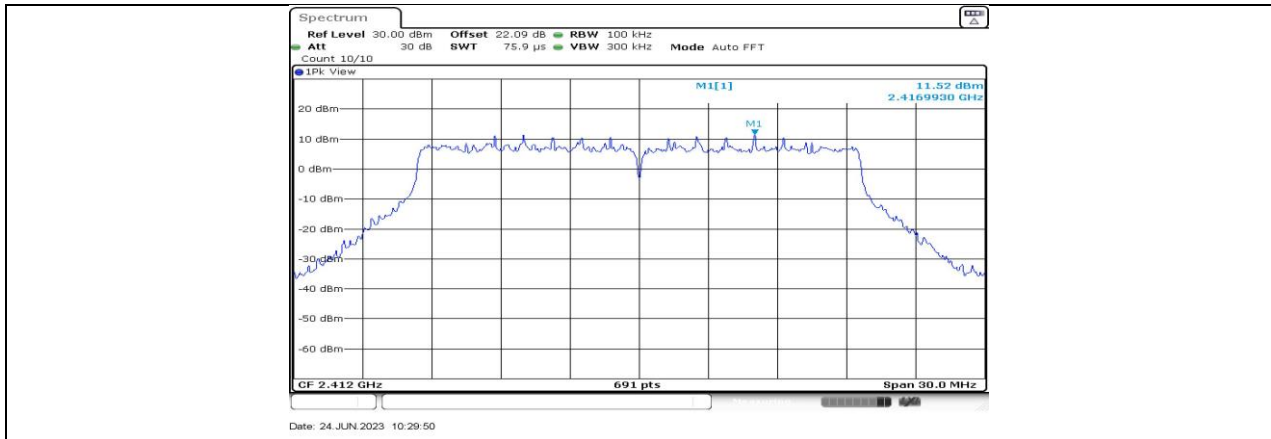


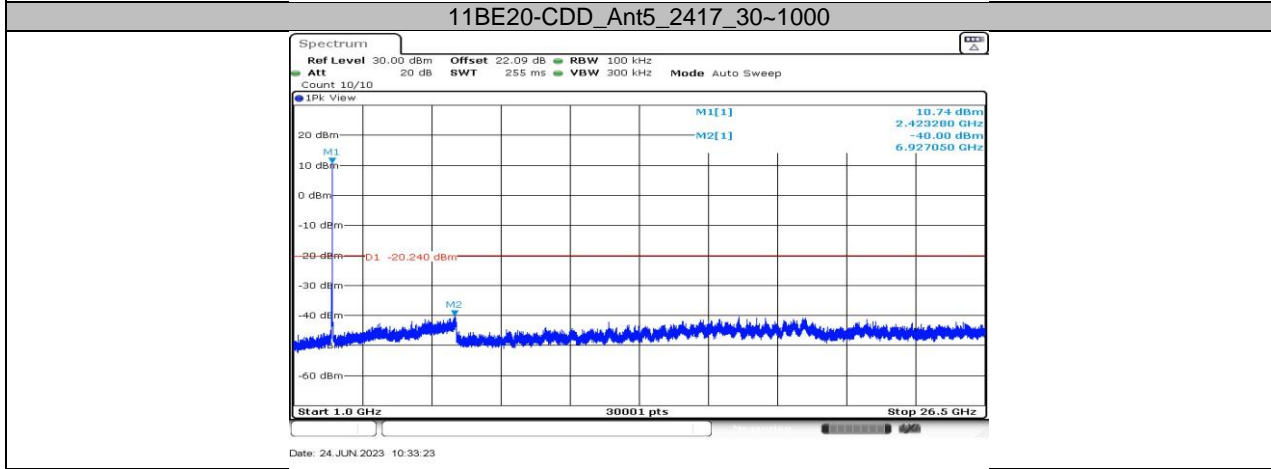
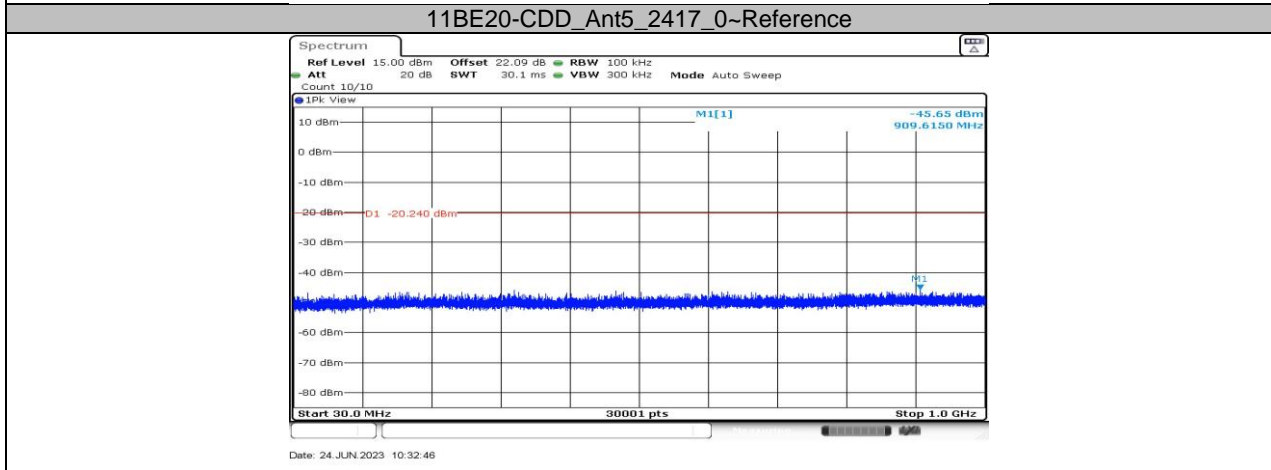
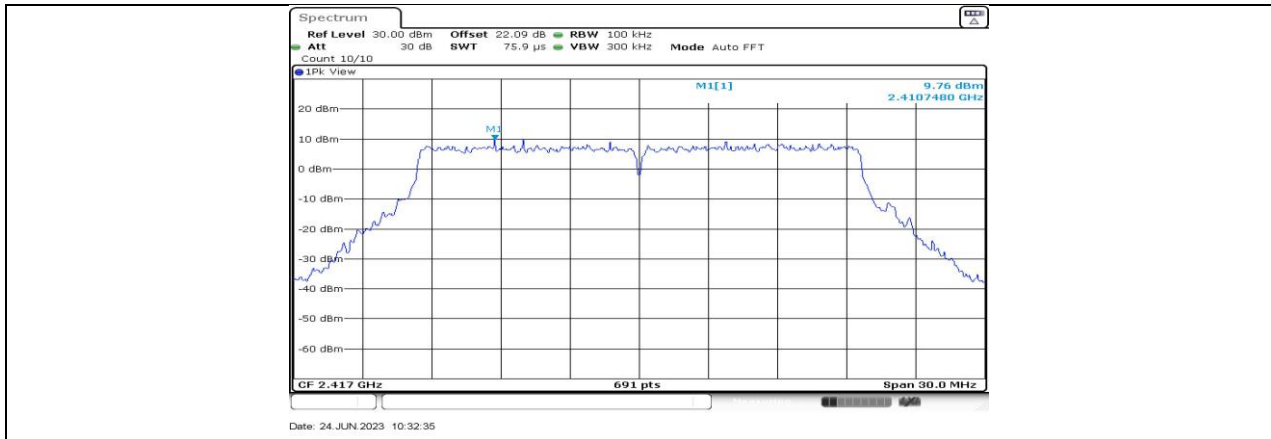
11AX40-CDD_Ant6_2452_30-1000

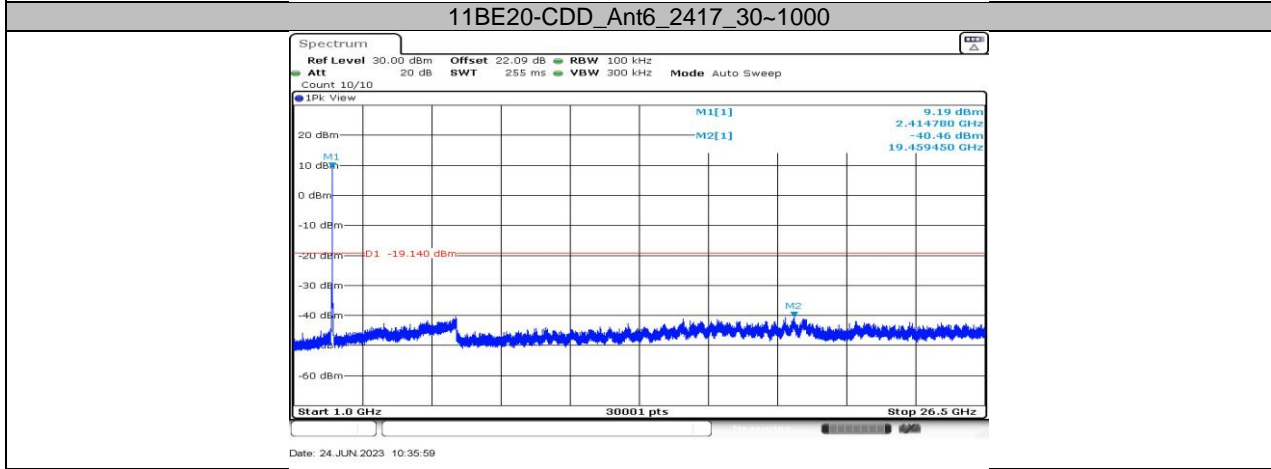
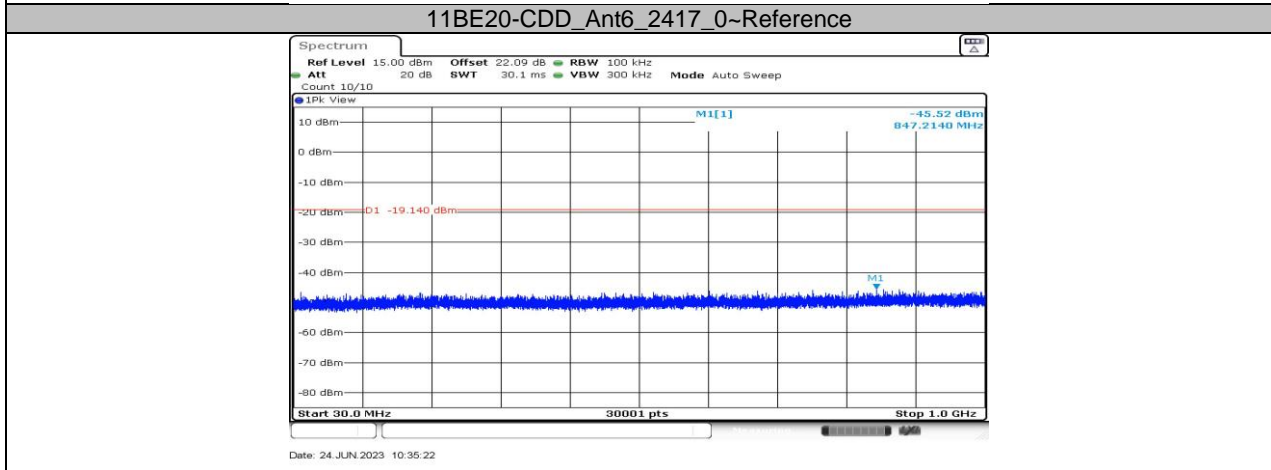
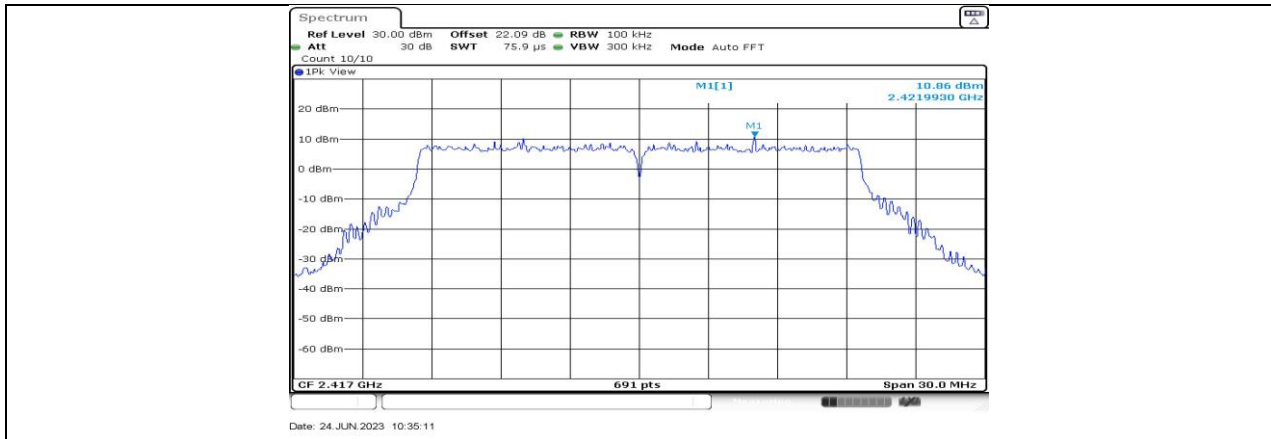


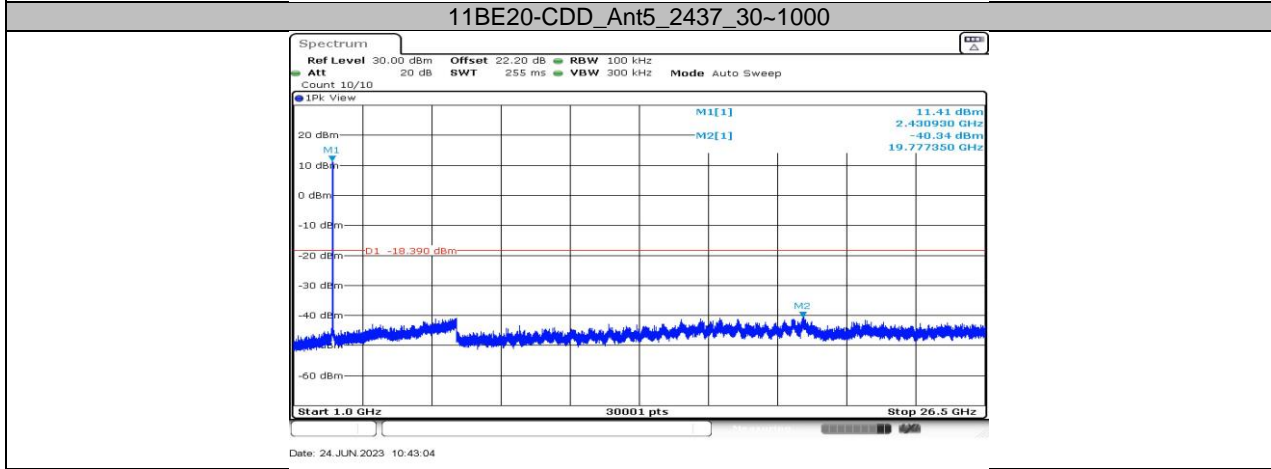
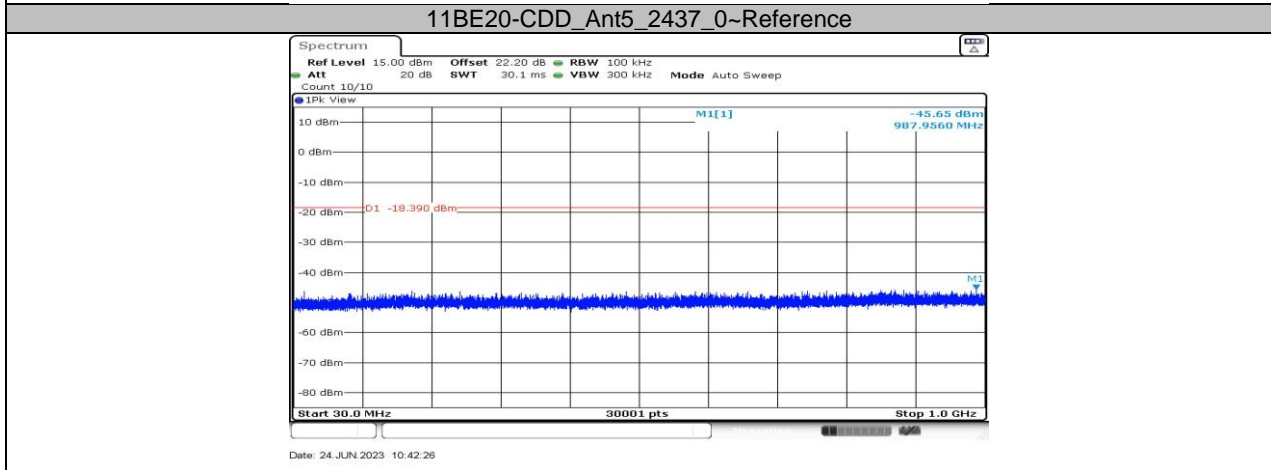
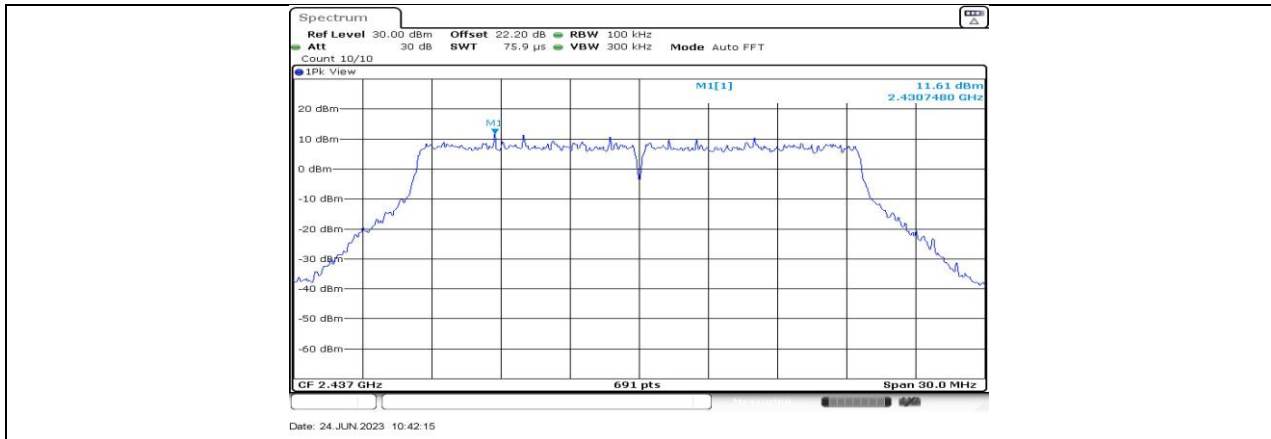
11AX40-CDD_Ant6_2452_1000-26500

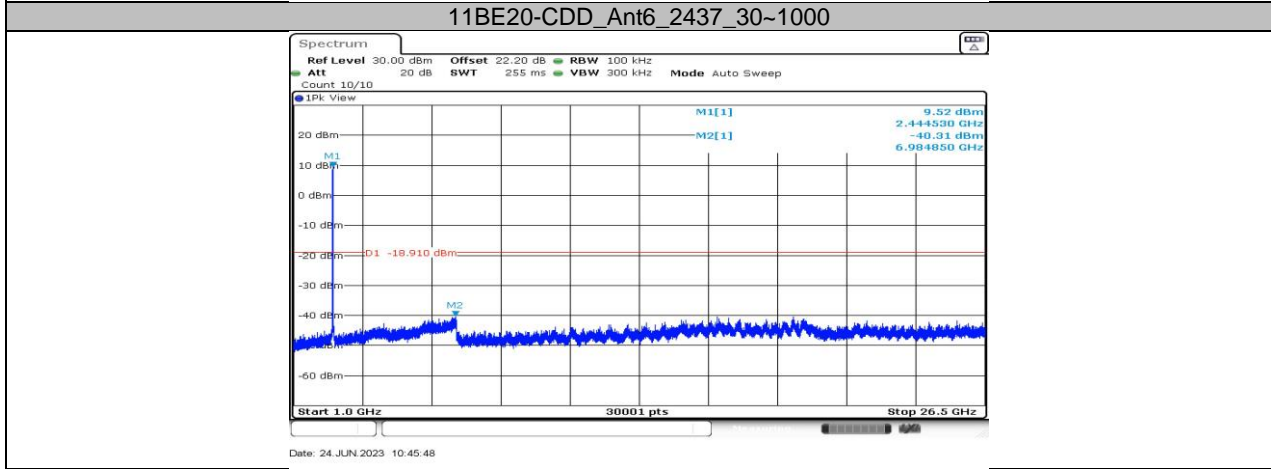
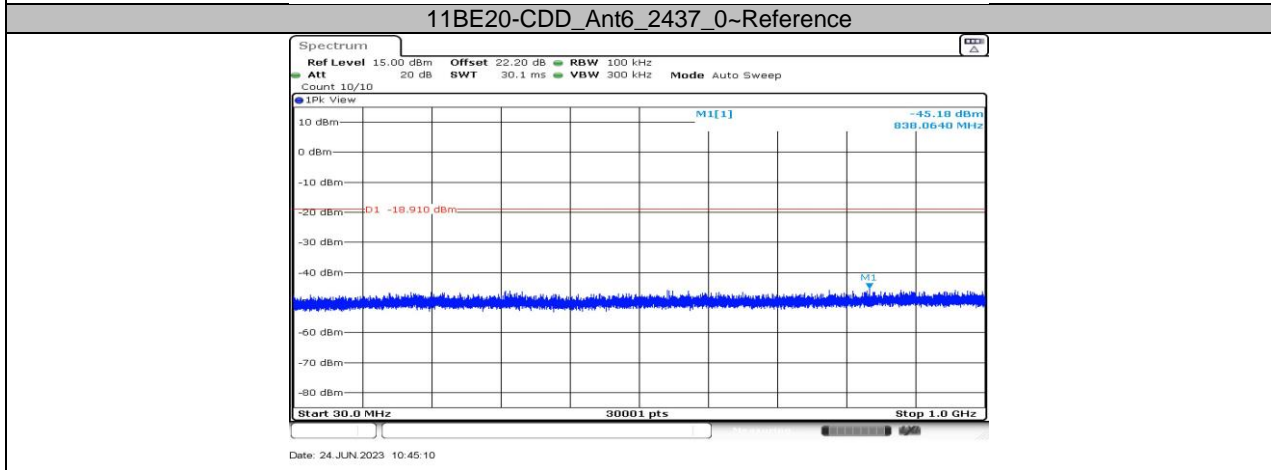
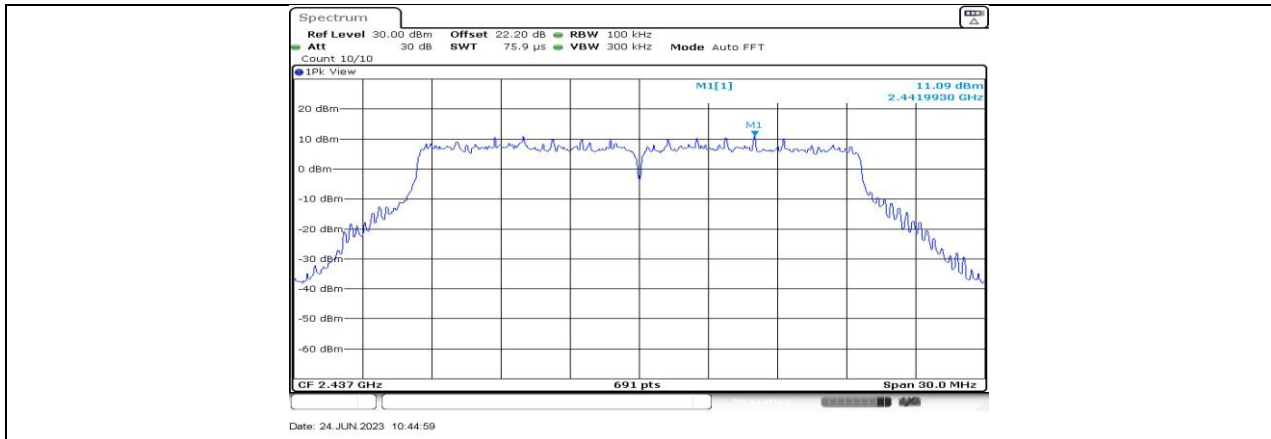


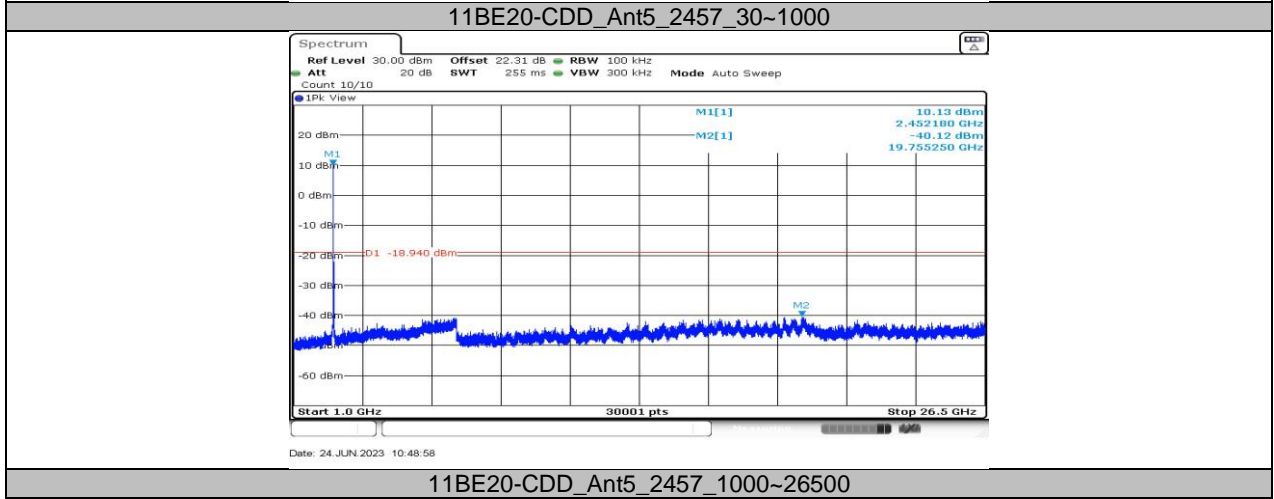
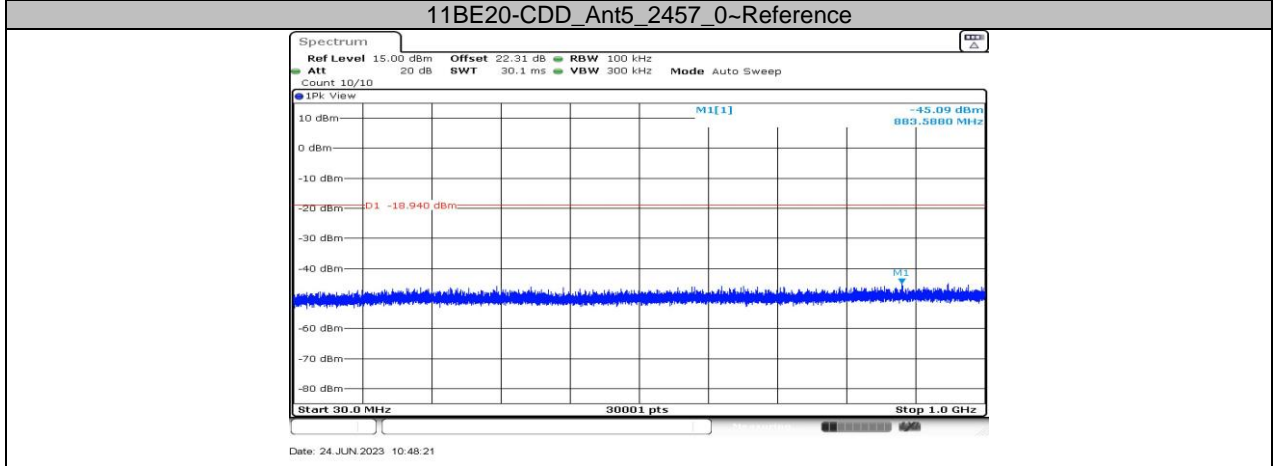
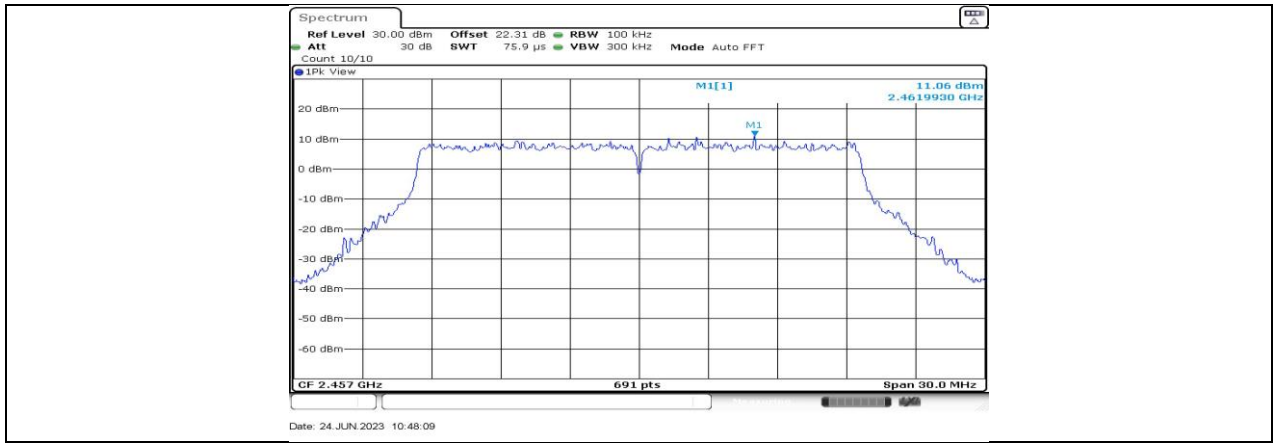


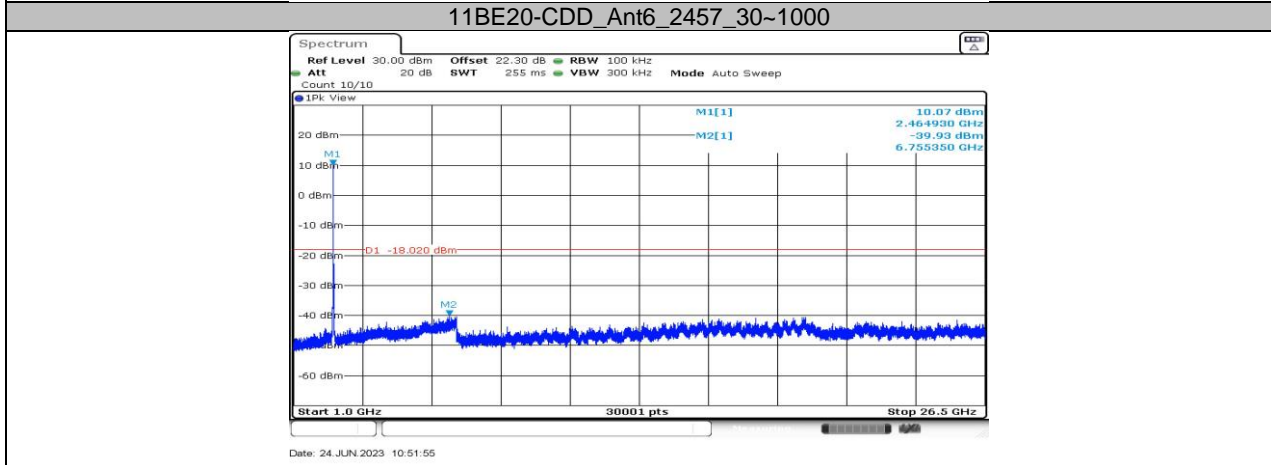
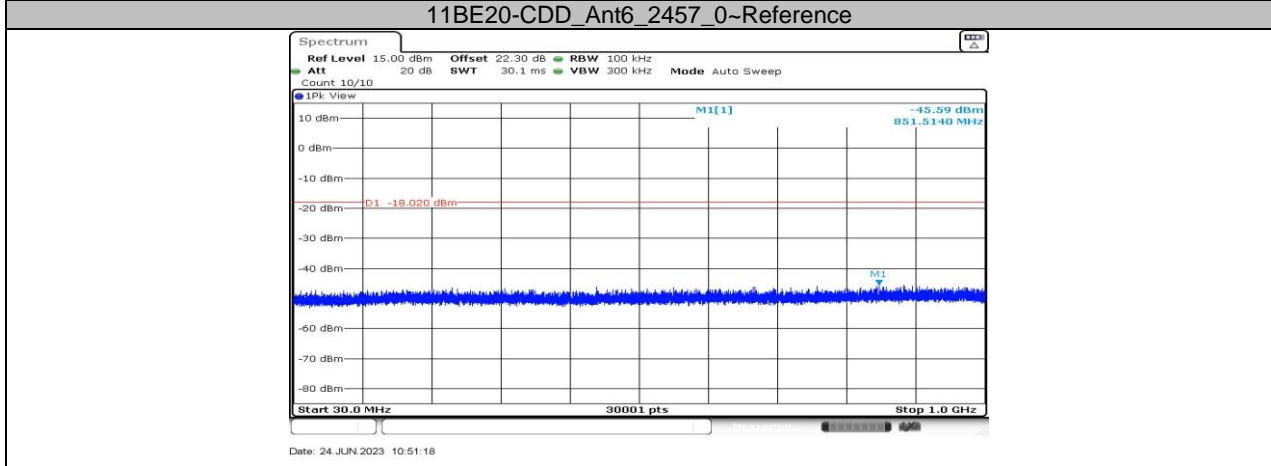
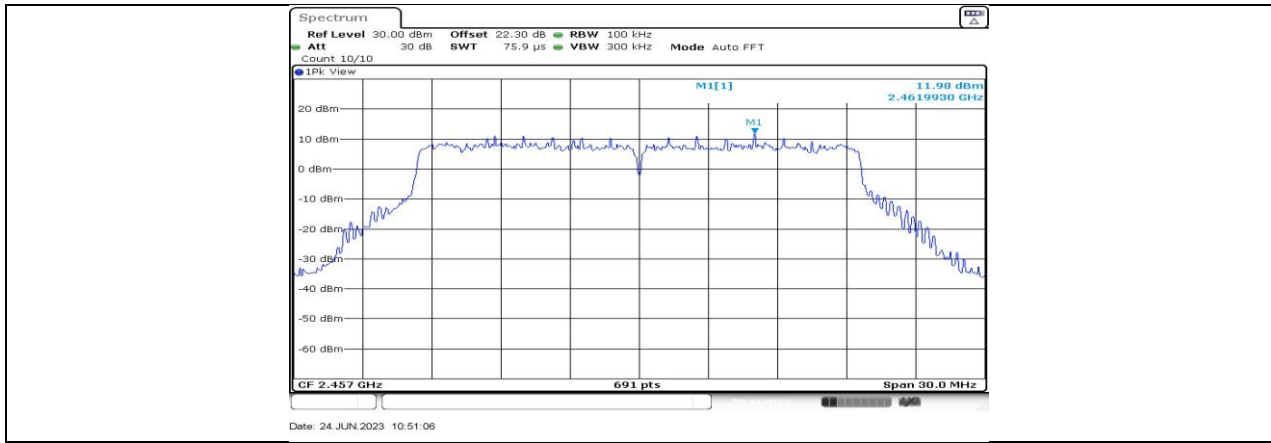


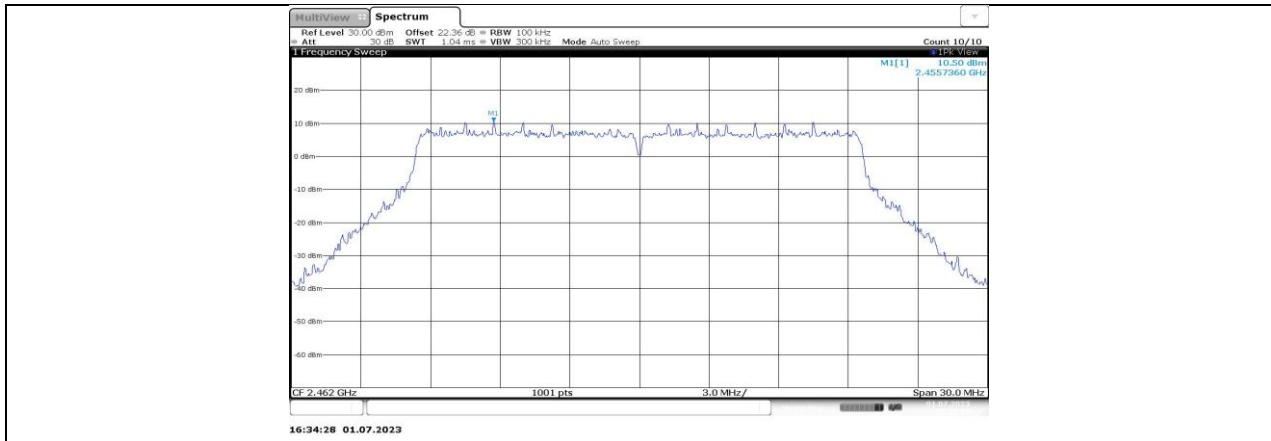




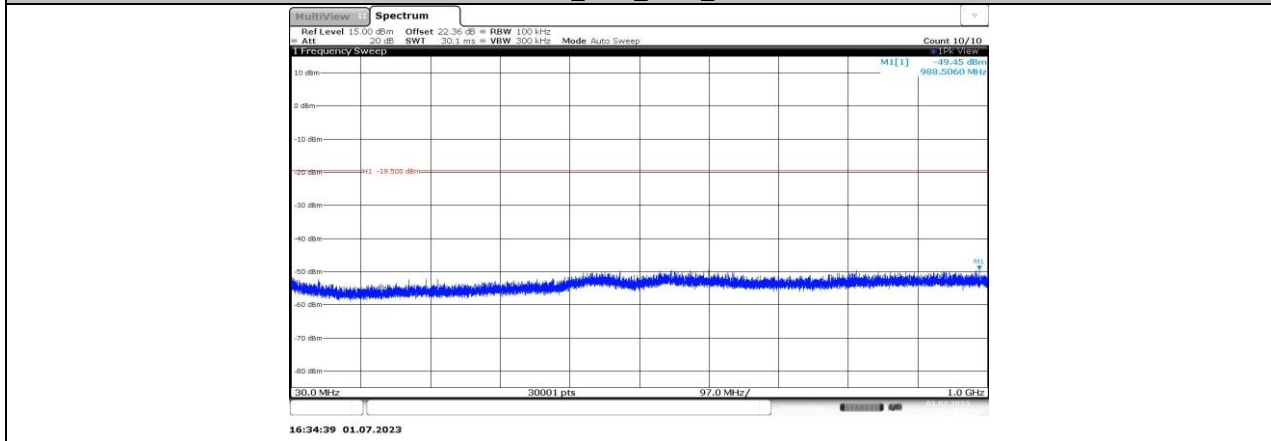




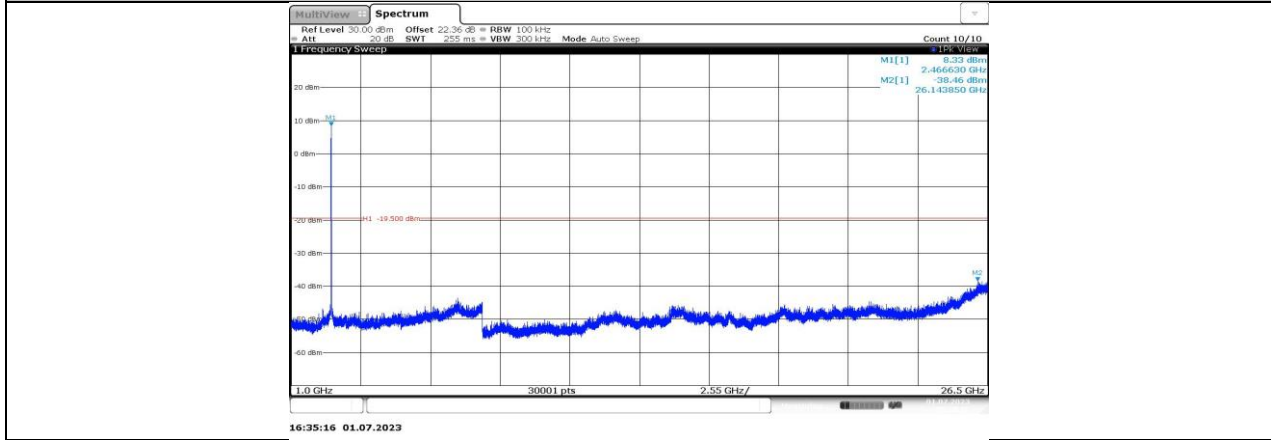




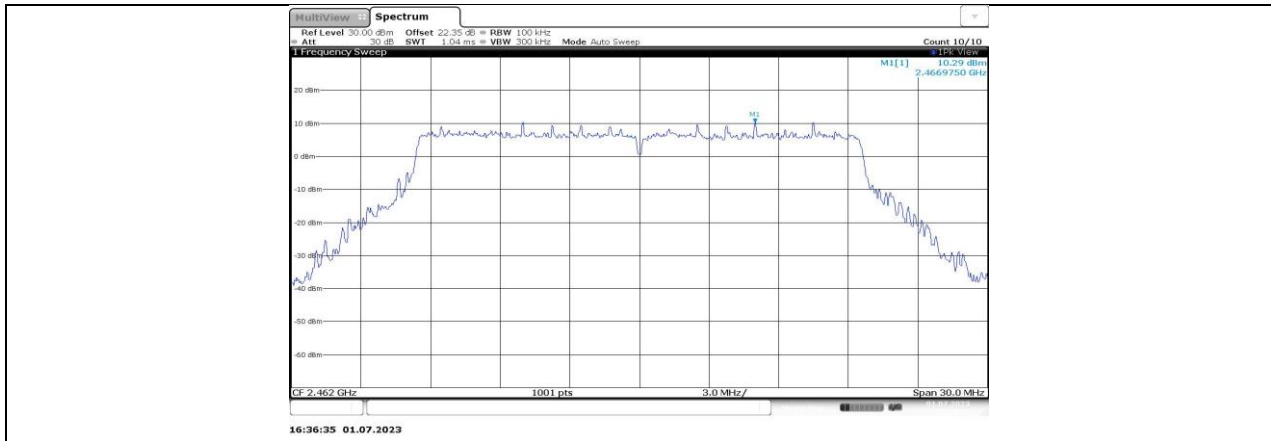
11BE20-CDD_Ant5_2462_0-Reference



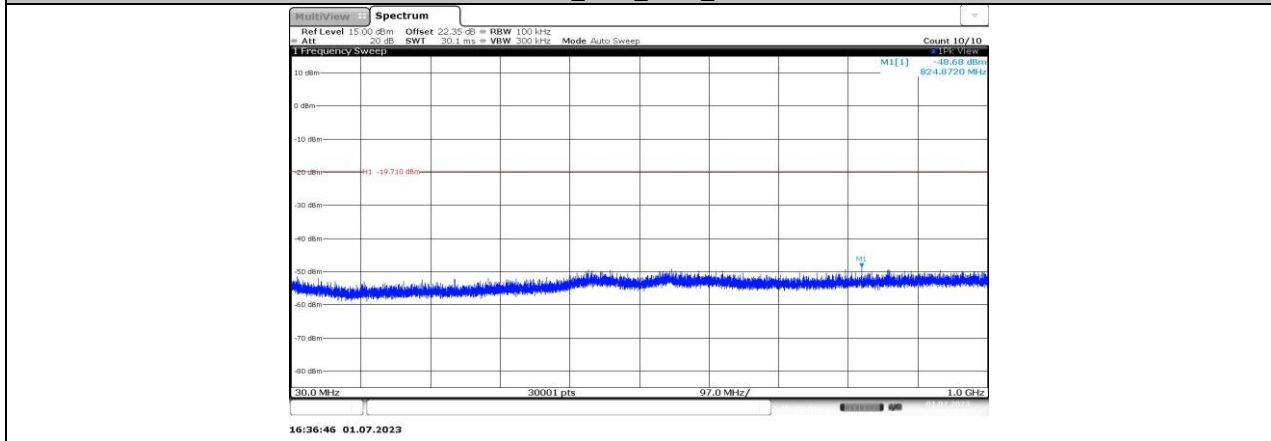
11BE20-CDD_Ant5_2462_30-1000



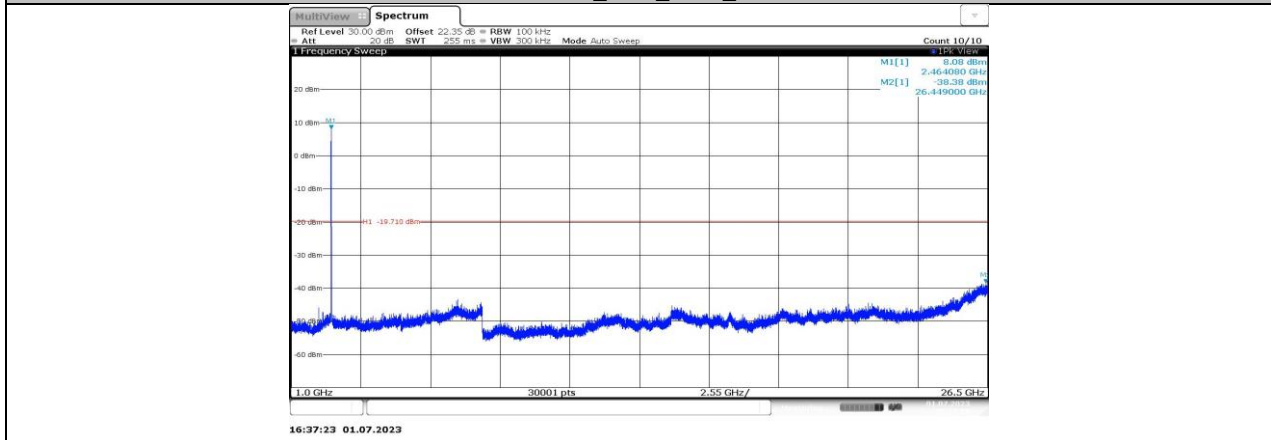
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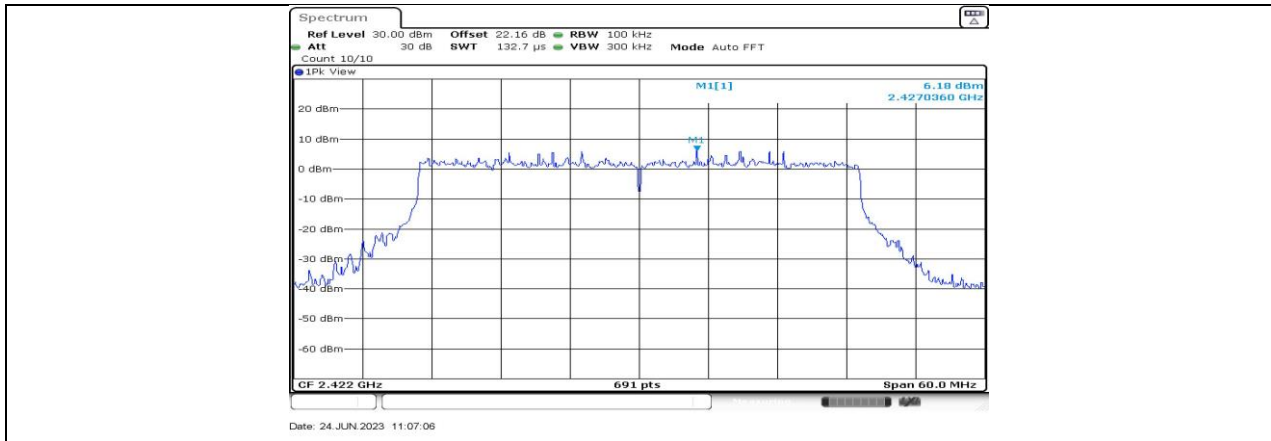
11BE20-CDD_Ant6_2462_0-Reference



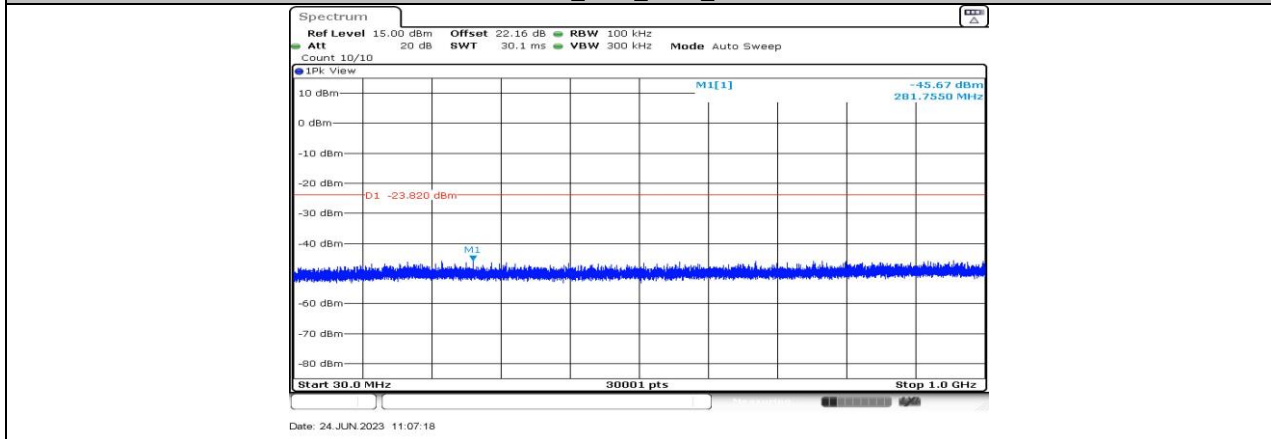
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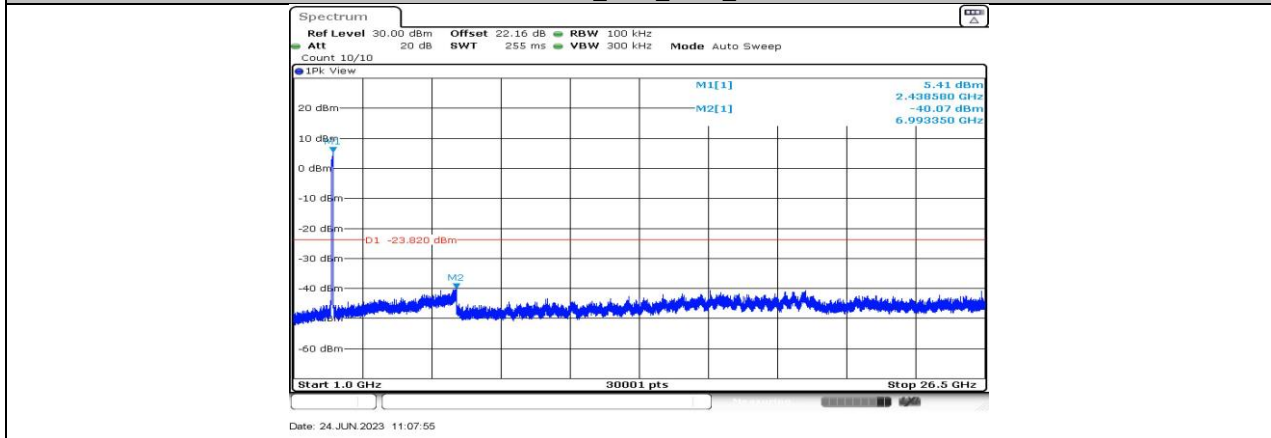
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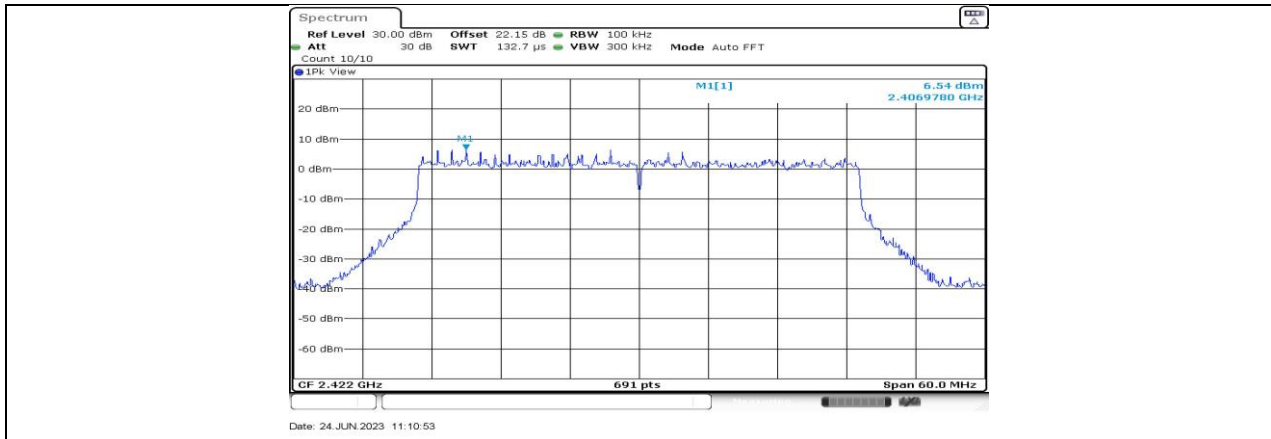
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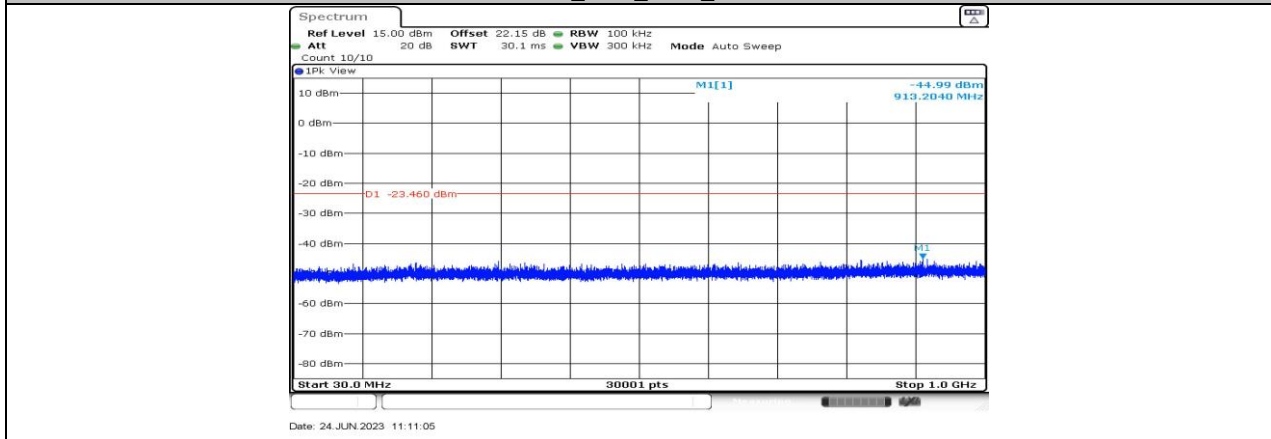
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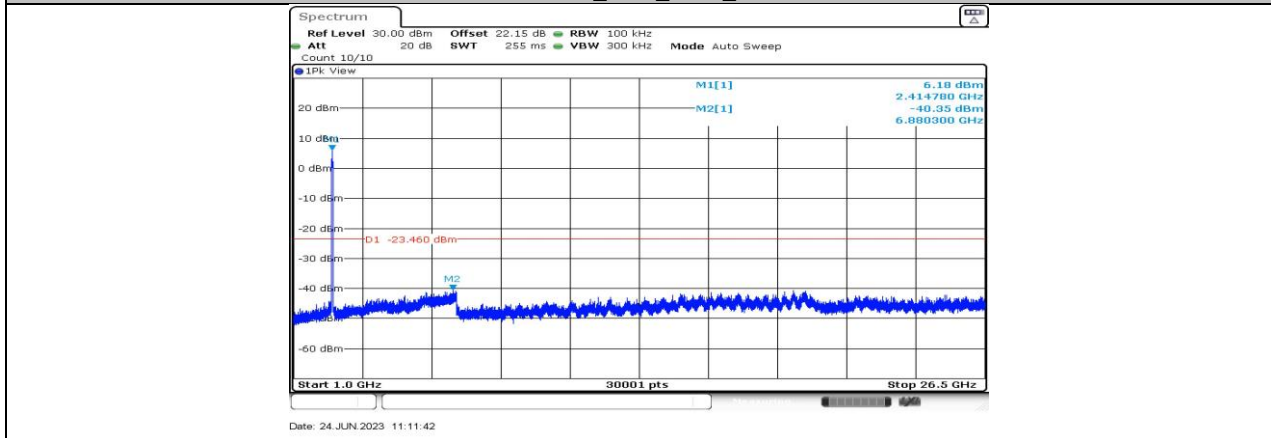
11BE40-CDD_Ant5_2422_1000-26500



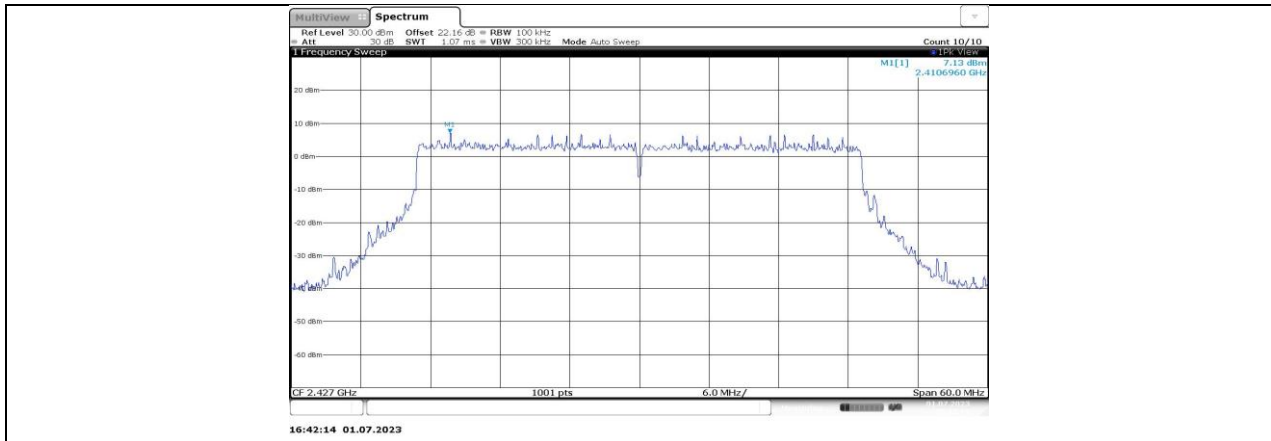
11BE40-CDD_Ant6_2422_0-Reference



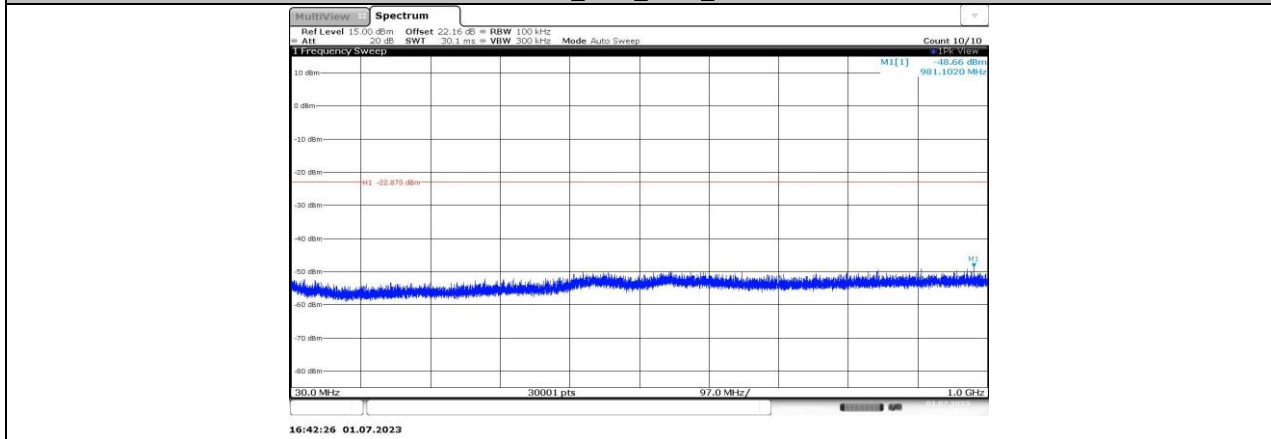
11BE40-CDD_Ant6_2422_30-1000



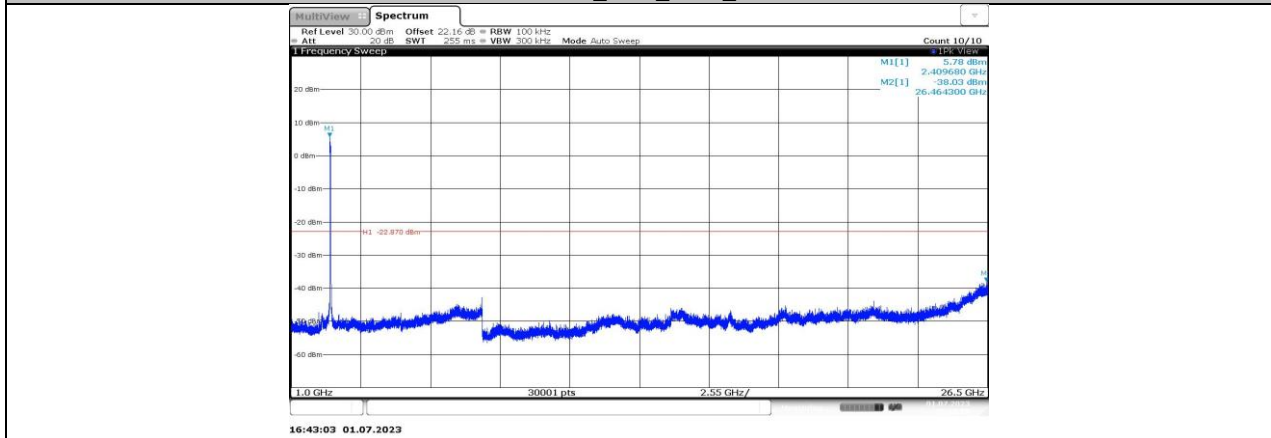
11BE40-CDD_Ant6_2422_1000-26500



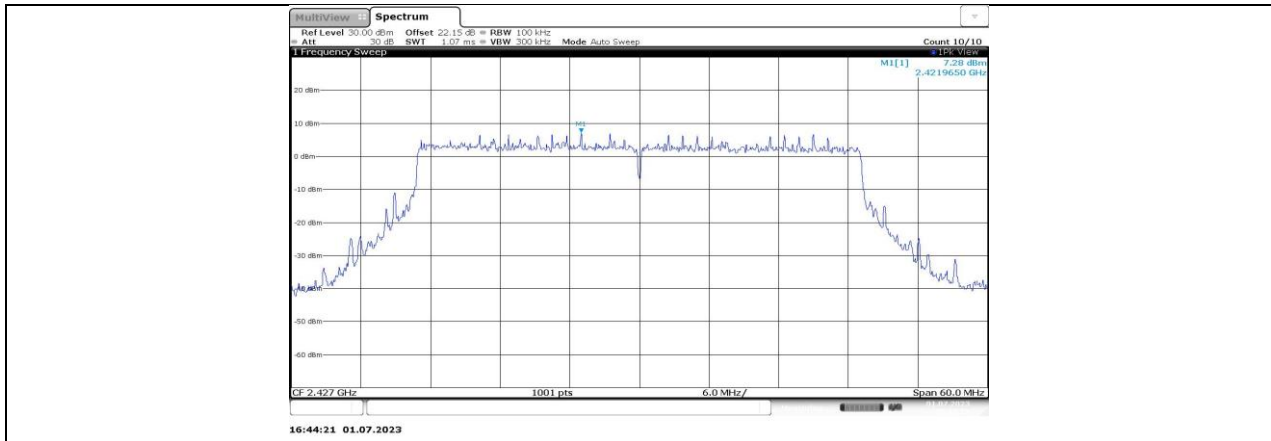
11BE40-CDD_Ant5_2427_0-Reference



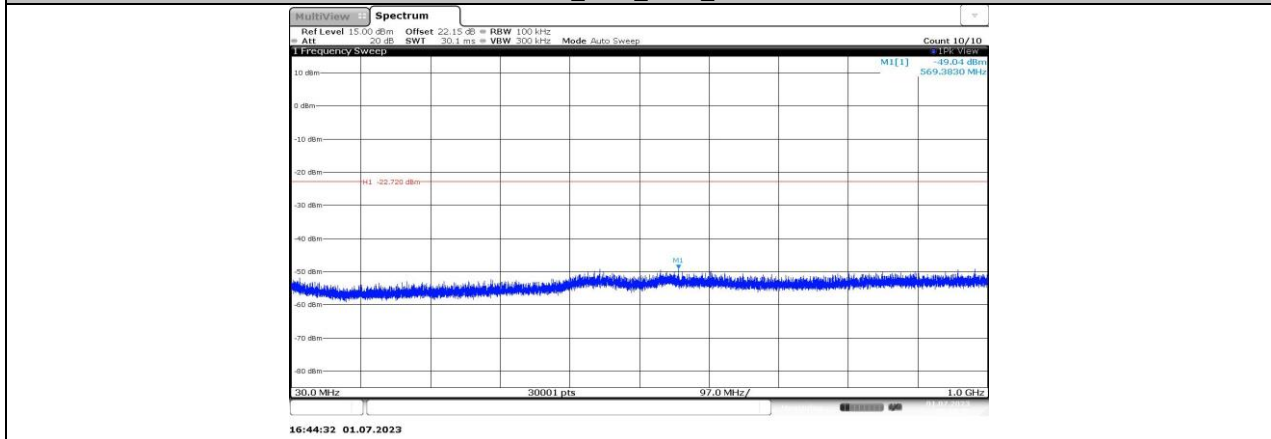
11BE40-CDD_Ant5_2427_30-1000



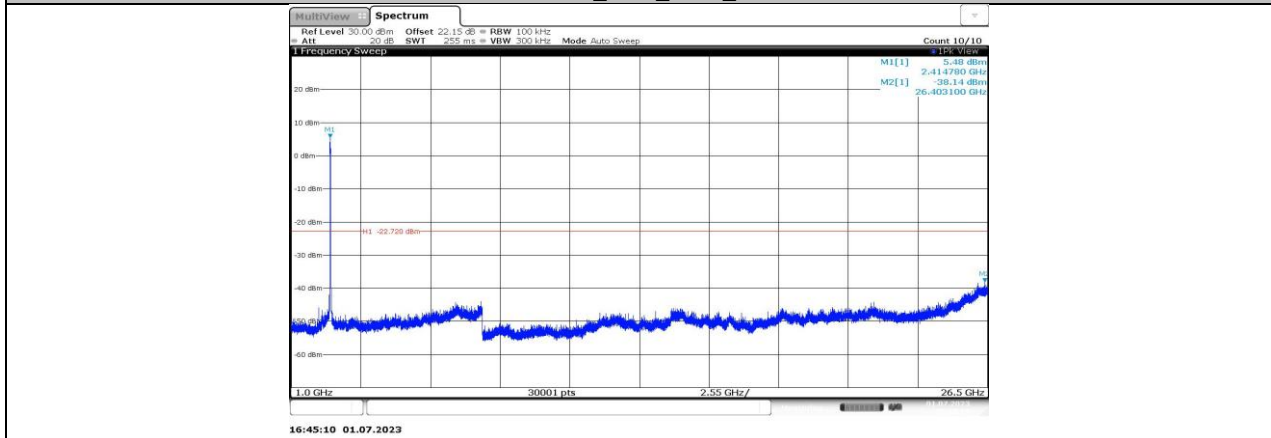
11BE40-CDD_Ant5_2427_1000-26500



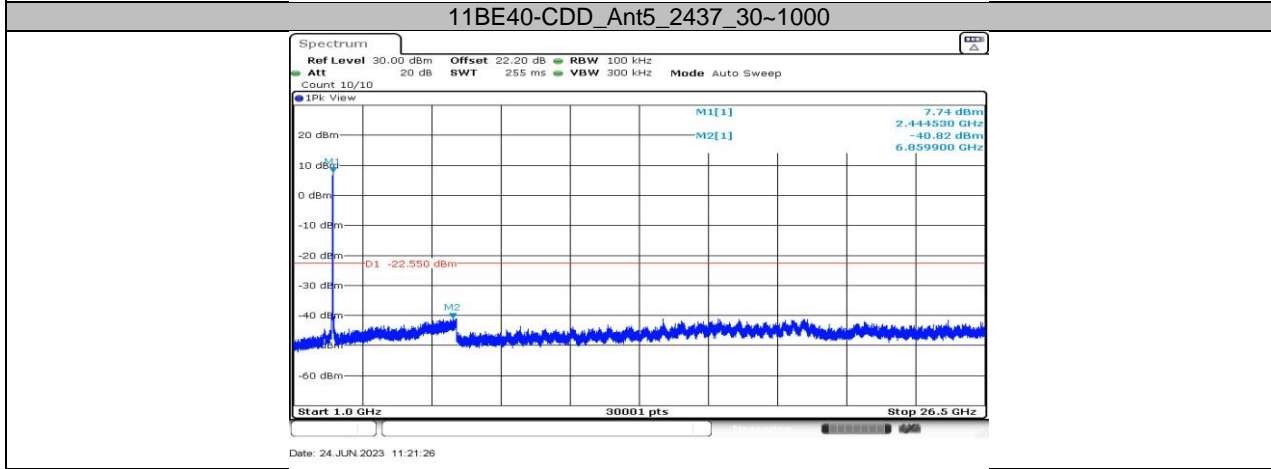
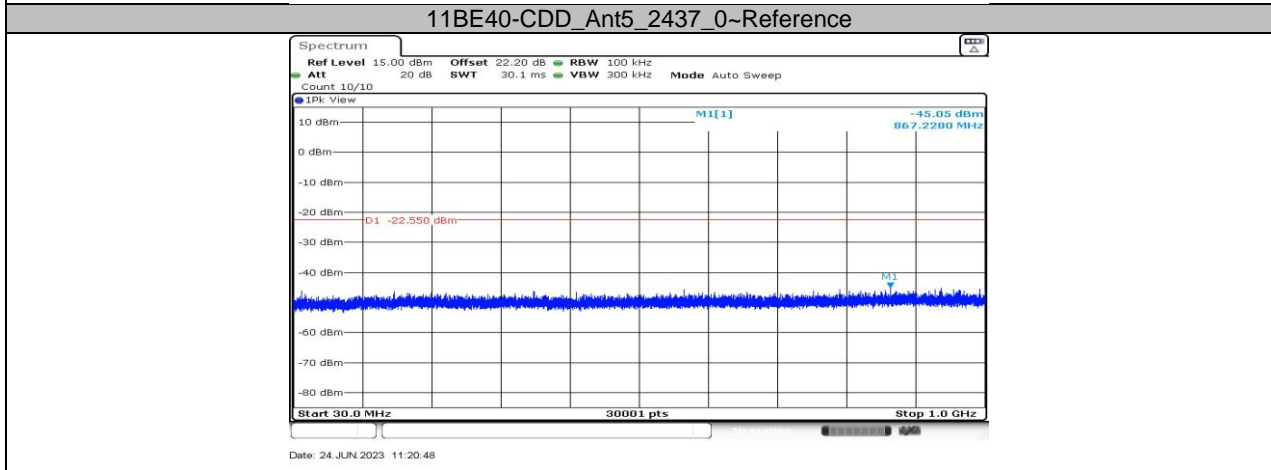
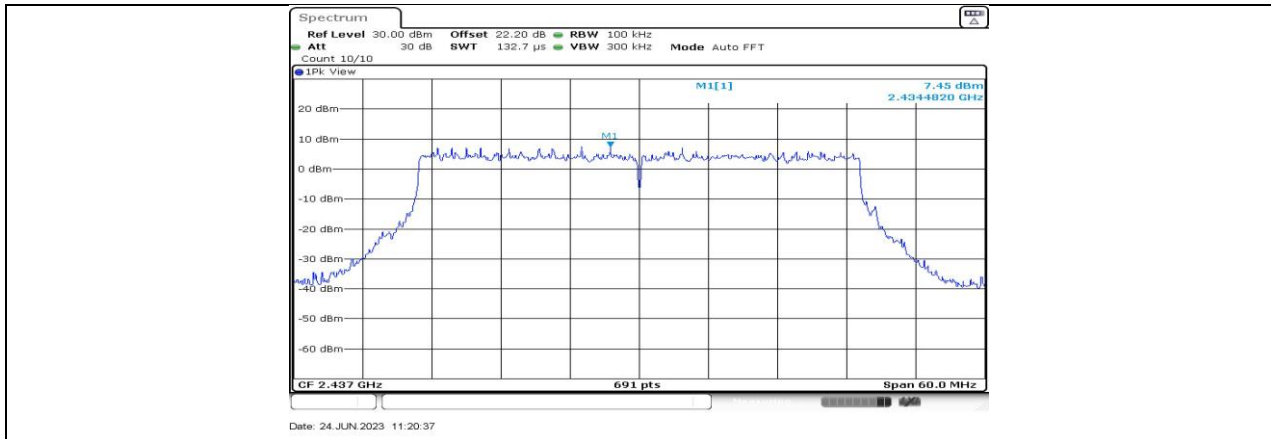
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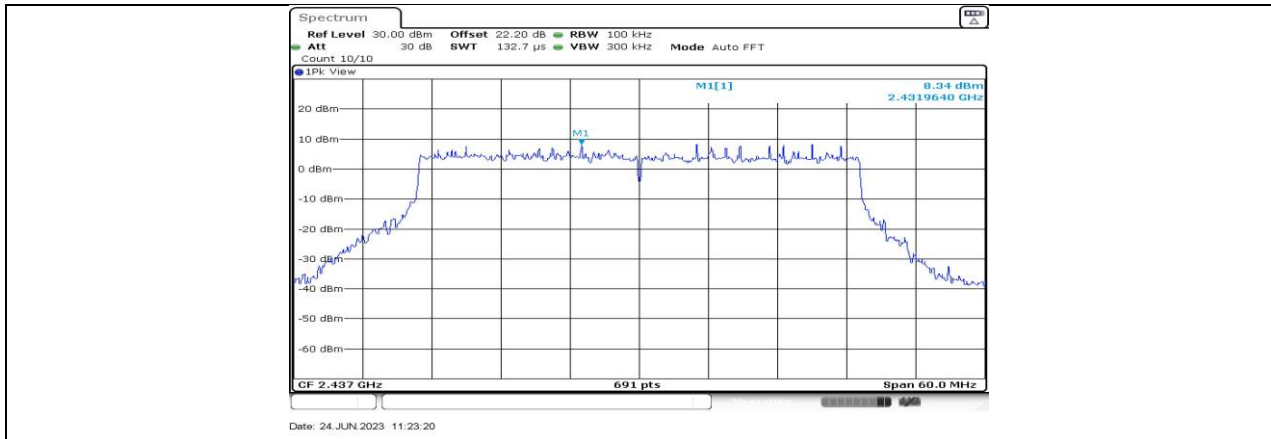


11BE40-CDD_Ant6_2427_30-1000

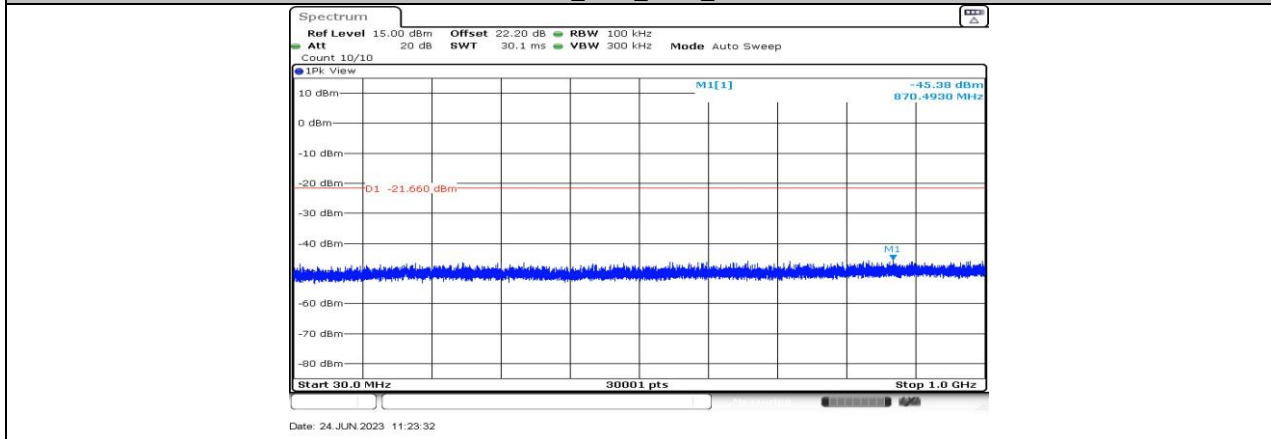


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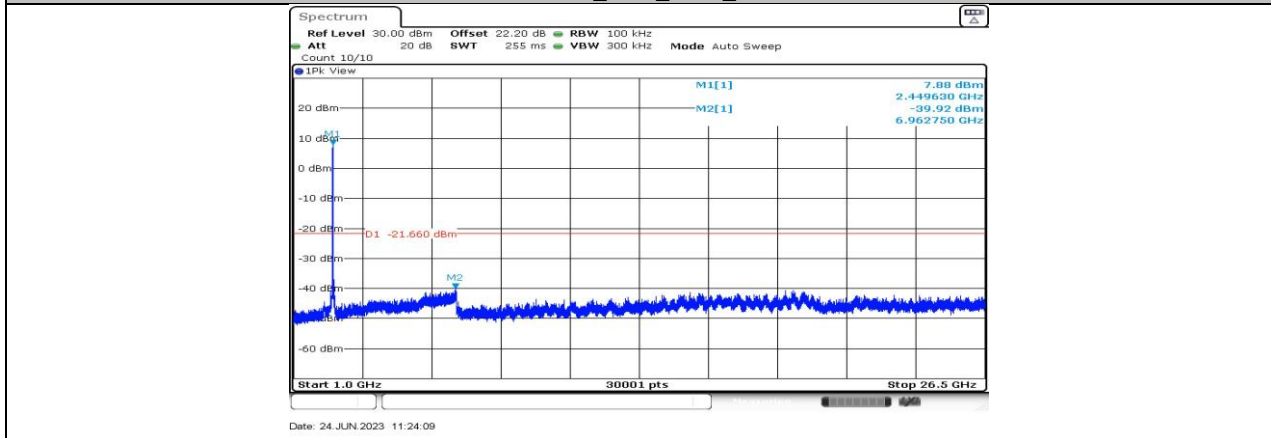




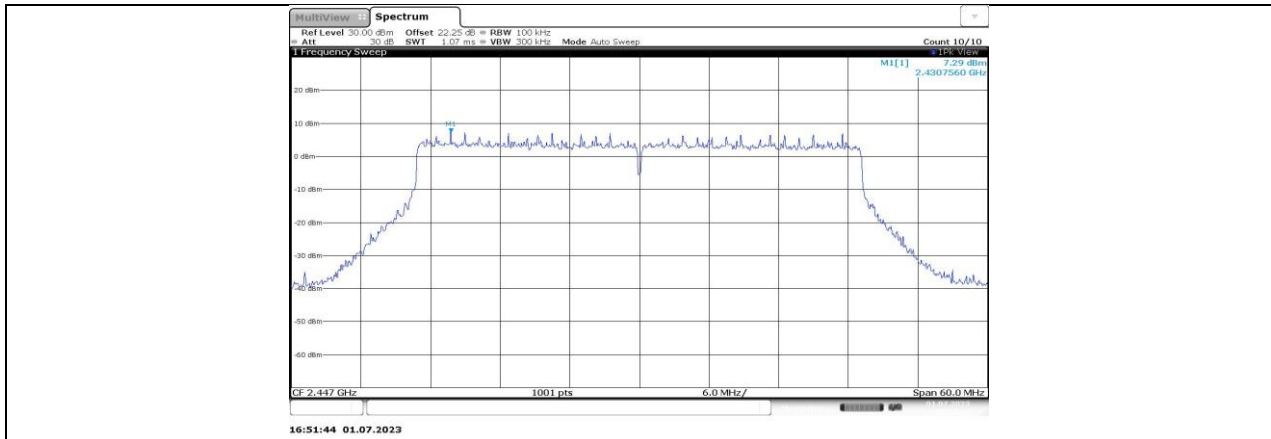
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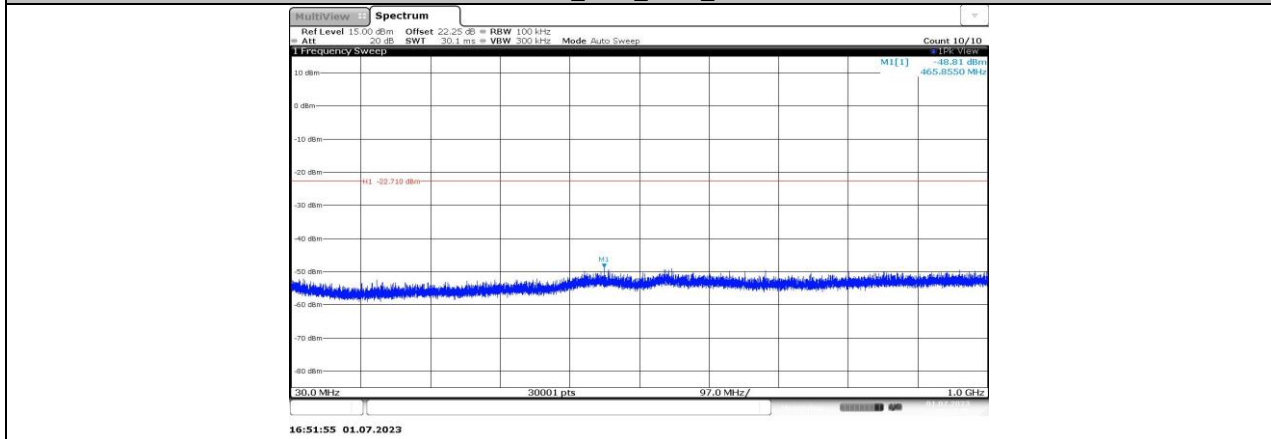
11BE40-CDD_Ant6_2437_30-1000



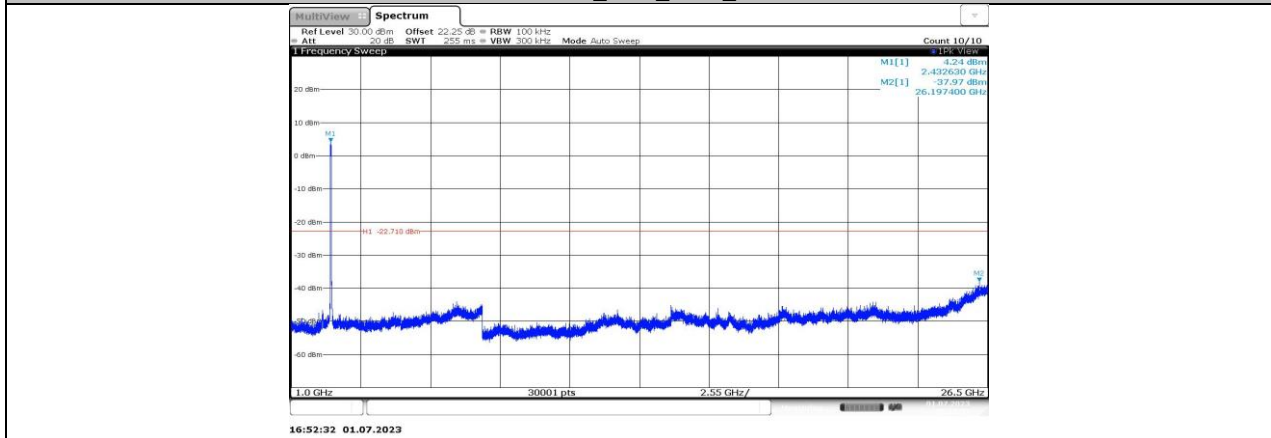
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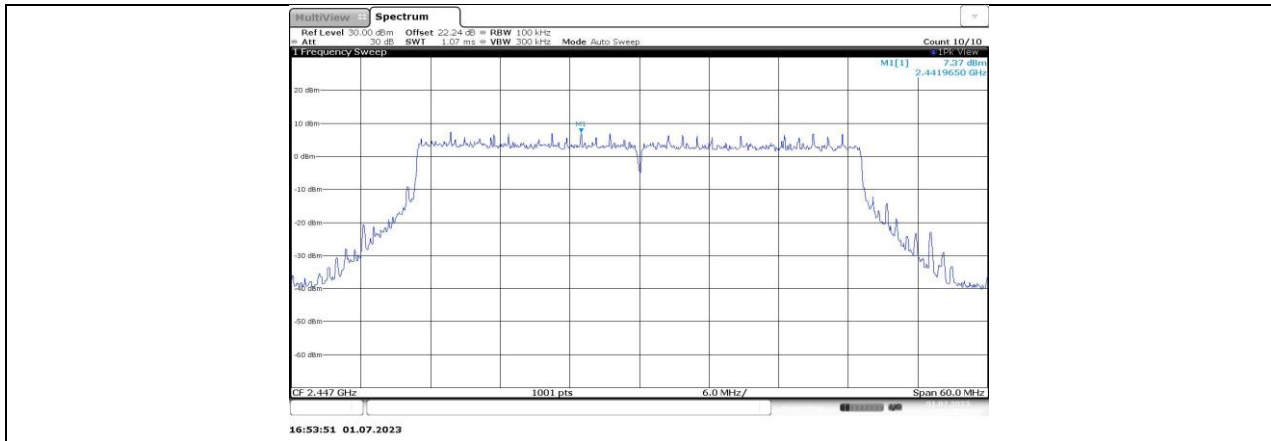
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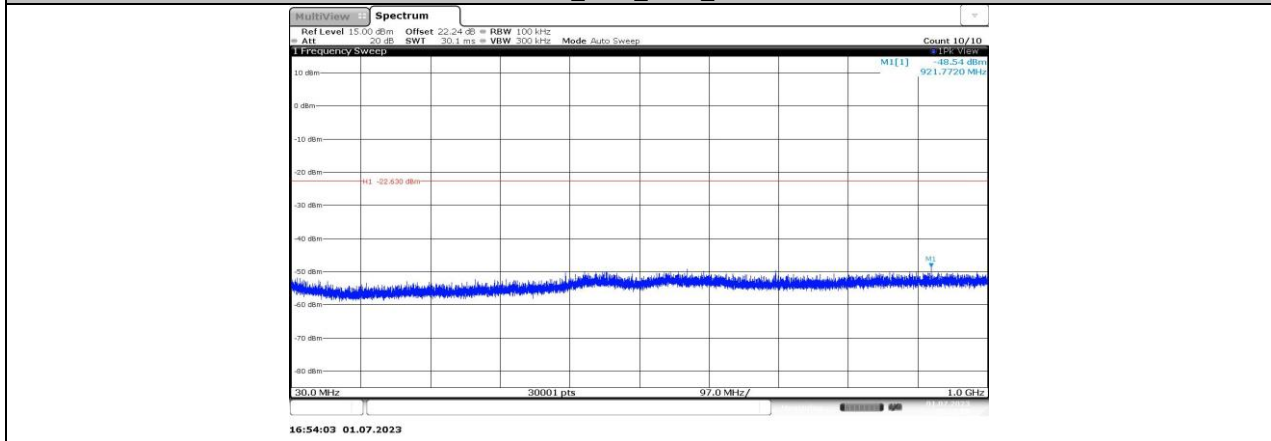
11BE40-CDD_Ant5_2447_30-1000



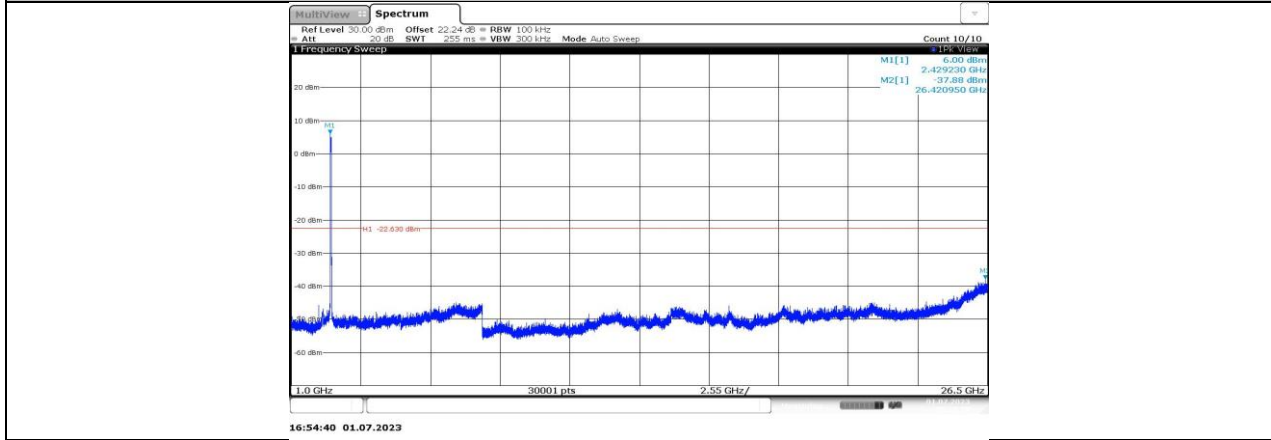
11BE40-CDD_Ant5_2447_1000-26500



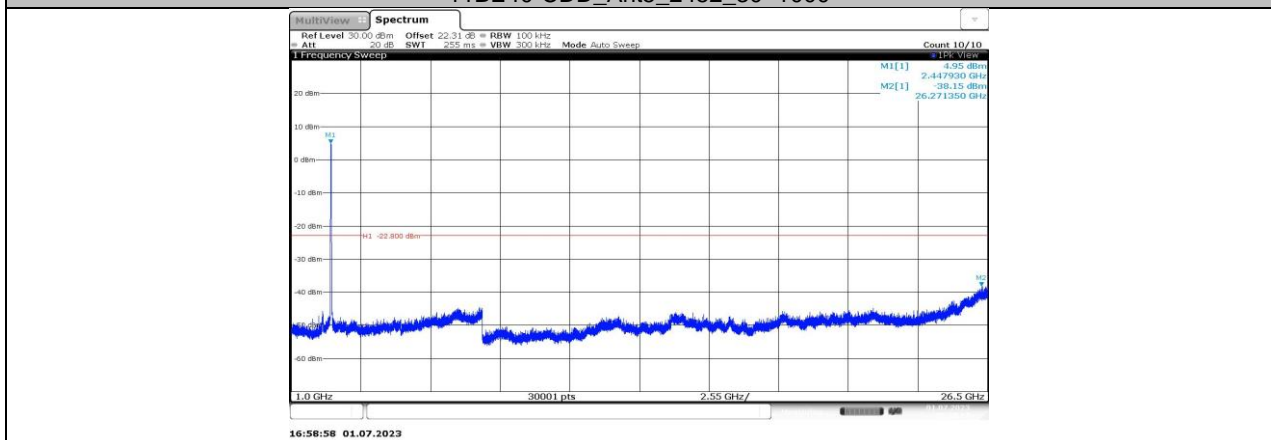
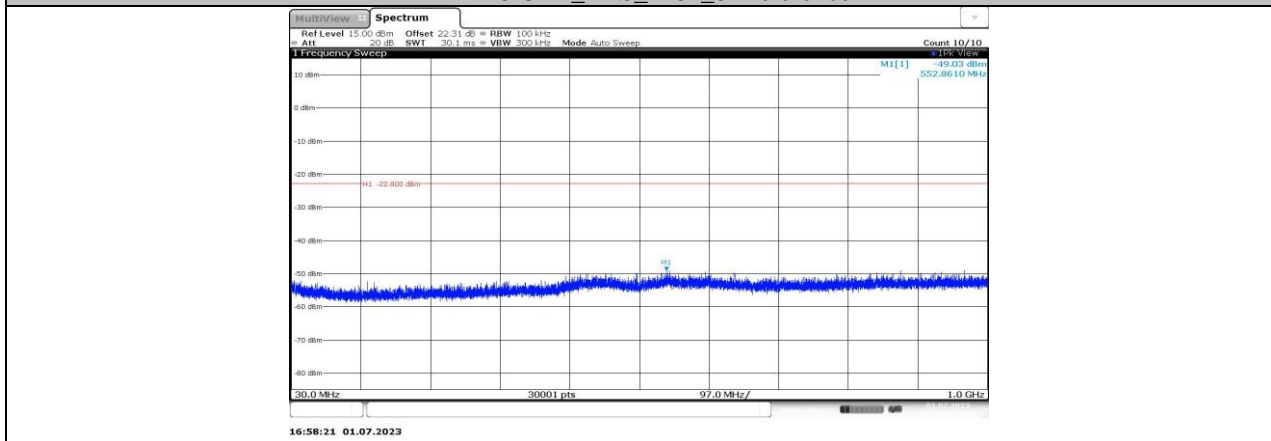
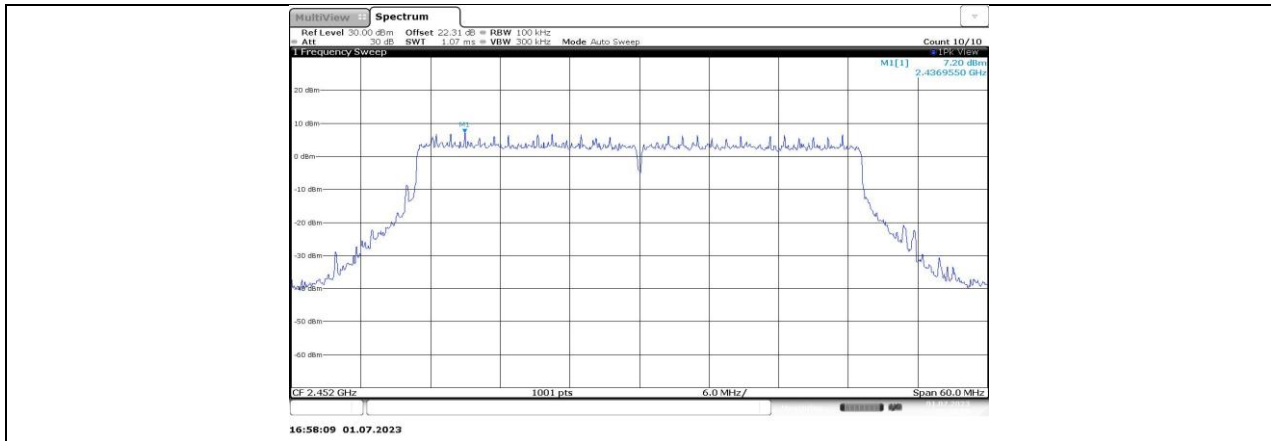
11BE40-CDD_Ant6_2447_0-Reference

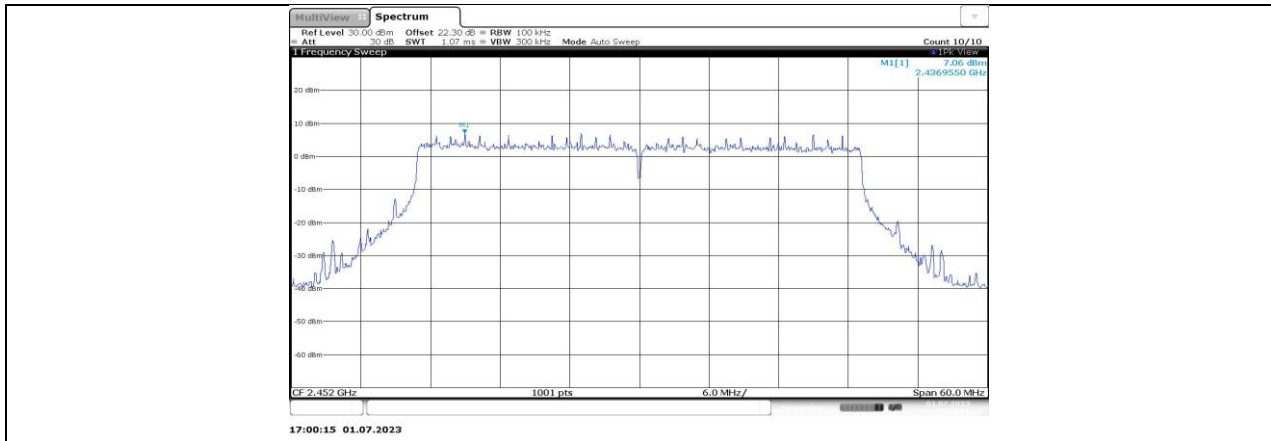


11BE40-CDD_Ant6_2447_30-1000

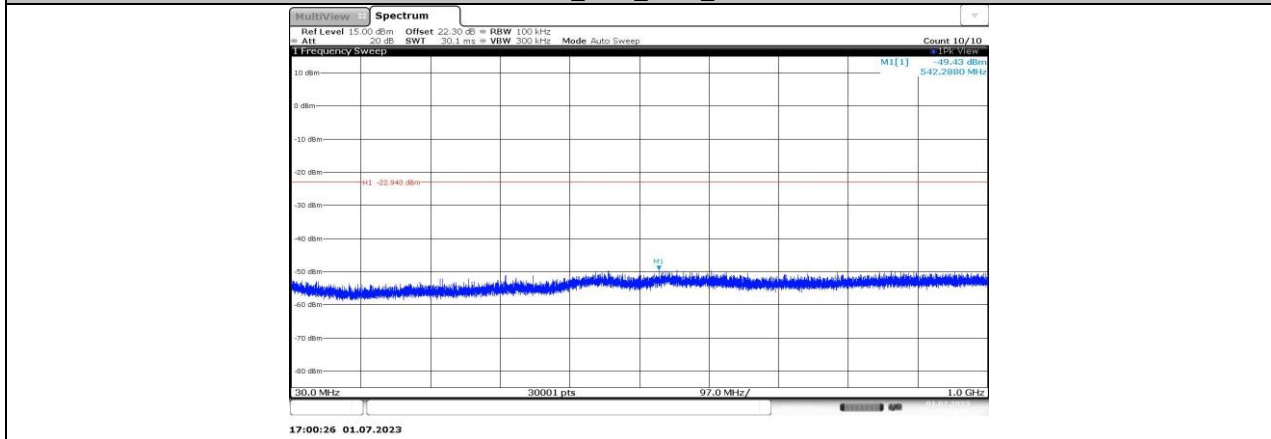


11BE40-CDD_Ant6_2447_1000-26500

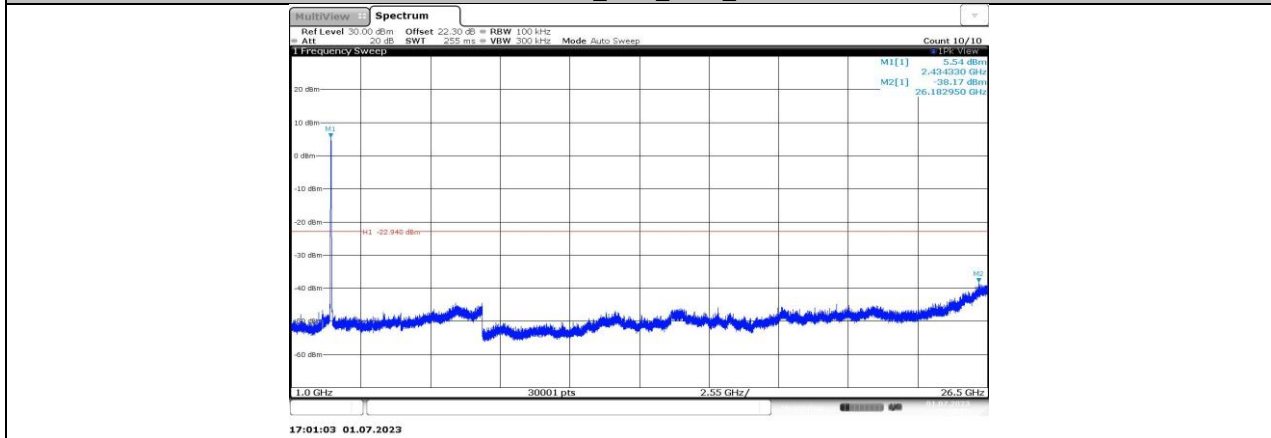




11BE40-CDD_Ant6_2452_0~Reference



11BE40-CDD_Ant6_2452_30~1000



11BE40-CDD_Ant6_2452_1000~26500

Note: All the mode had been test, but only the worst data was recorded in the report.

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	16.25	16.4	0.9909	99.09	0.04	0.06	0.01
11G-CDD	3.02	3.11	0.9711	97.11	0.13	0.33	0.5
11AX20-CDD	3.94	4.03	0.9777	97.77	0.10	0.25	0.5
11AX40-CDD	3.98	4.07	0.9779	97.79	0.10	0.25	0.5
11BE20-CDD	3.95	4.05	0.9753	97.53	0.11	0.25	0.5
11BE40-CDD	3.94	4.03	0.9777	97.77	0.10	0.25	0.5

Note:

Duty Cycle Correction Factor=10log (1/x).

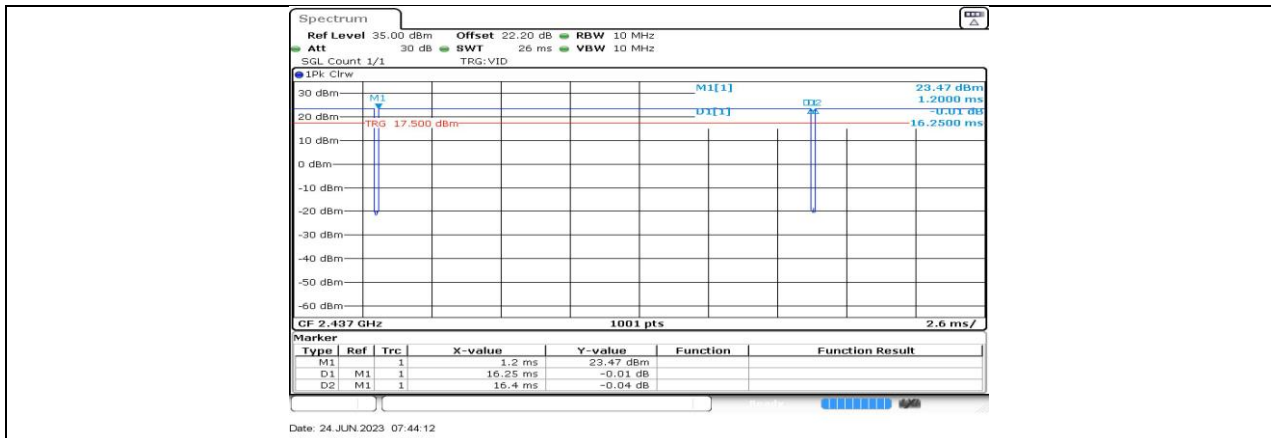
Where: x is Duty Cycle (Linear)

Where: T is On Time

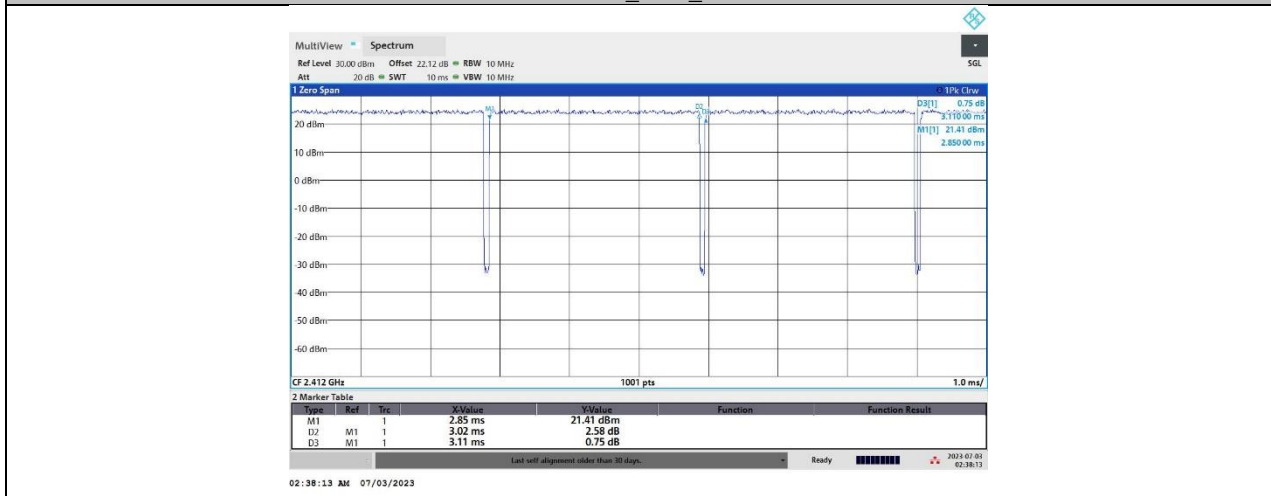
If that calculated VBW is not available on the analyzer then the next higher value should be used.

Note: All the mode had been test, but only the worst data was recorded in the report.

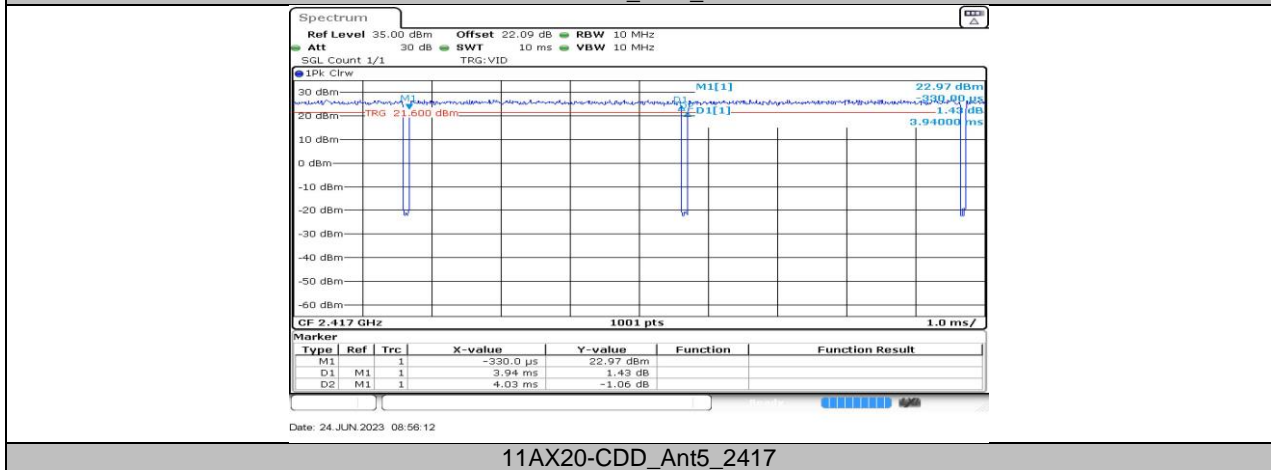
11.7.2. Test Graphs



11B-CDD_Ant5_2437



11G-CDD_Ant5_2412



11AX20-CDD_Ant5_2417