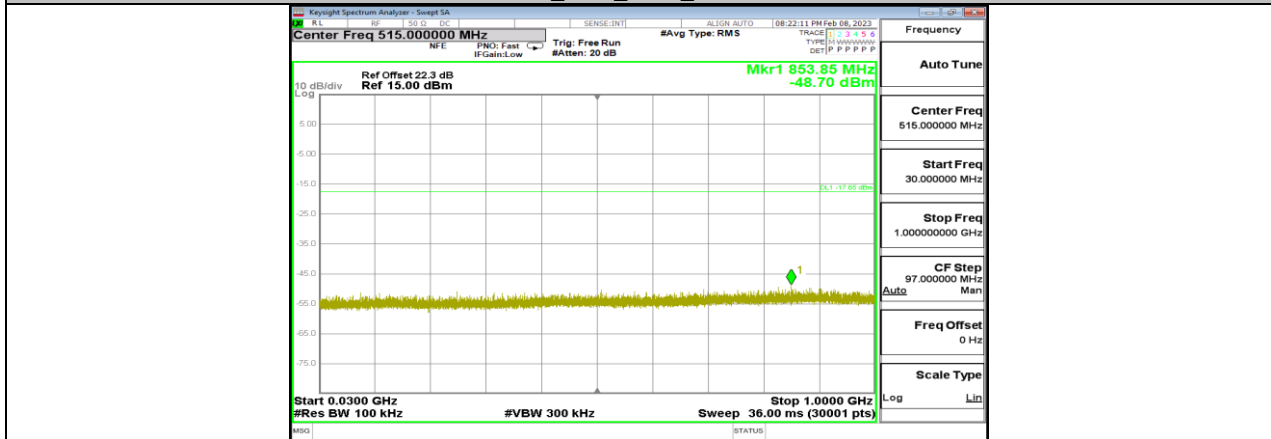
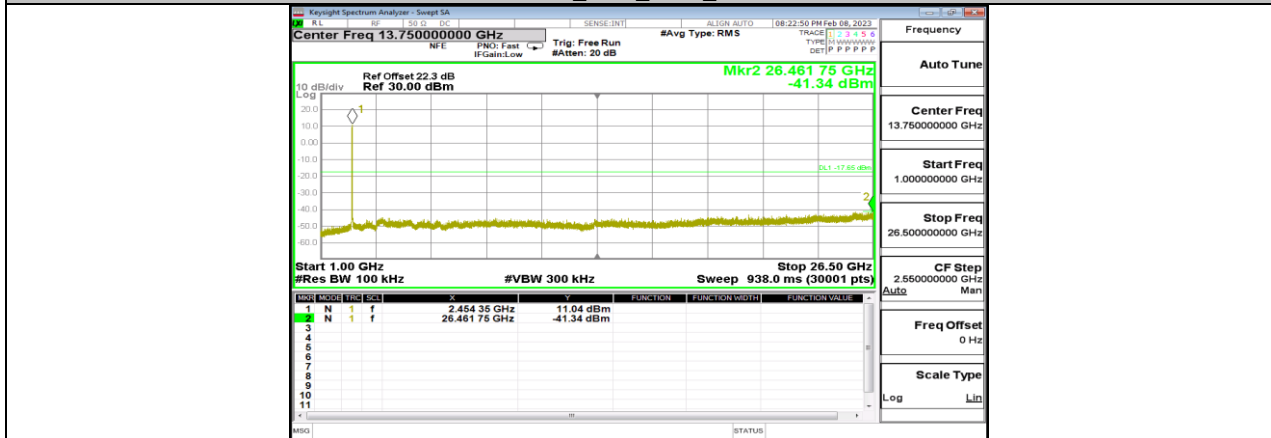


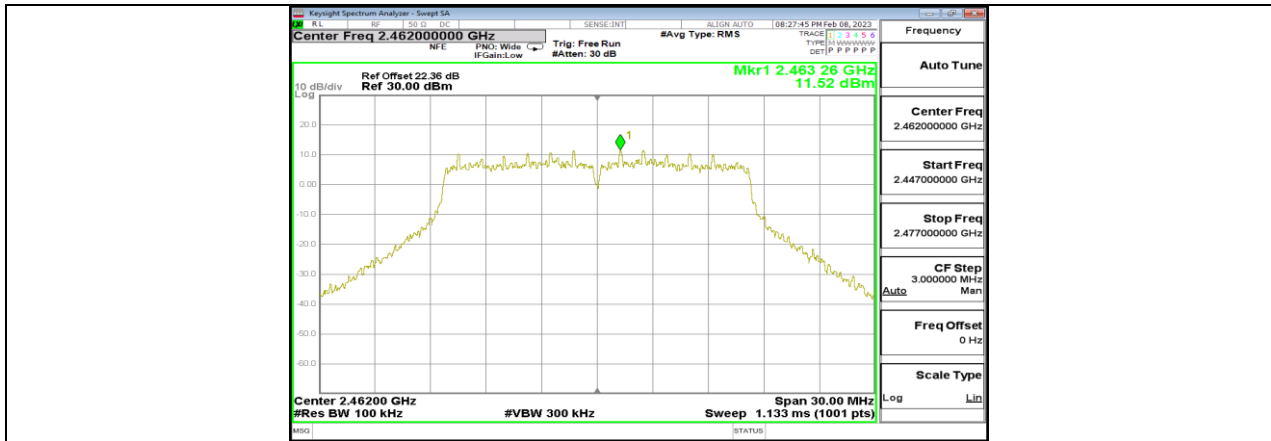
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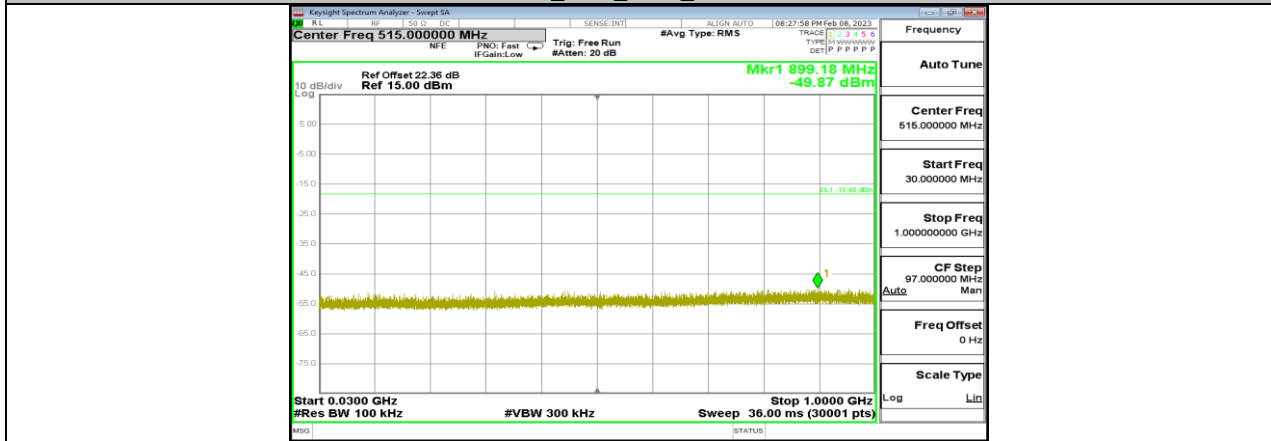
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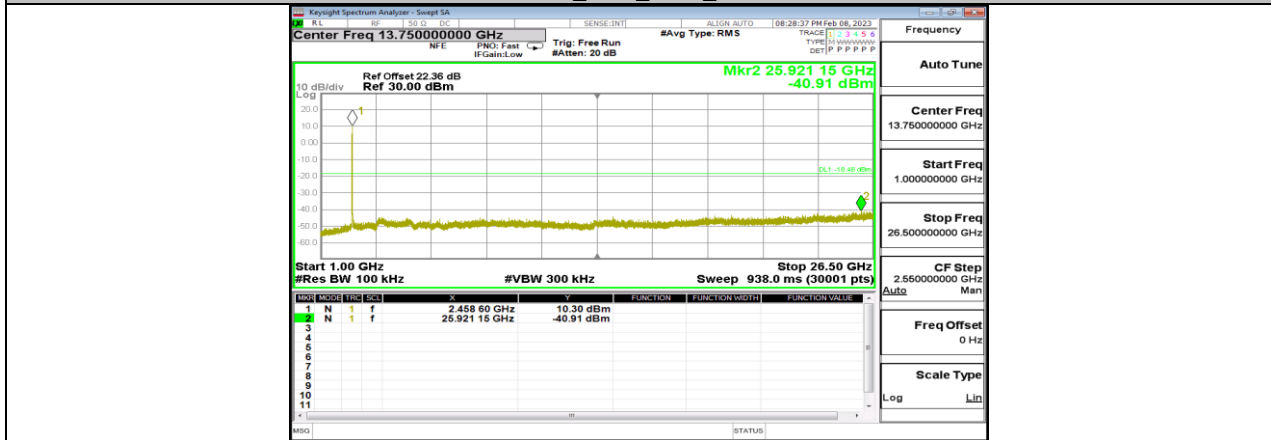
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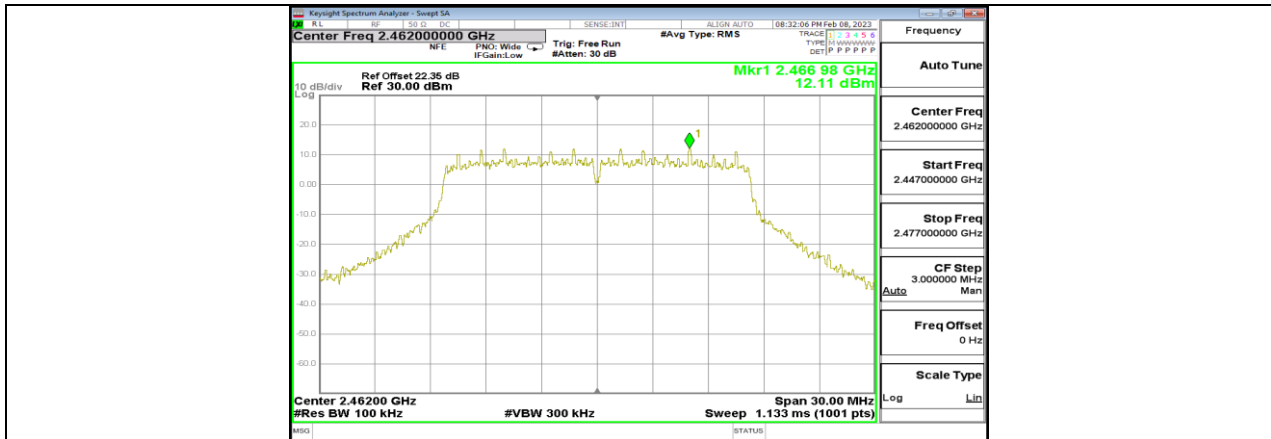
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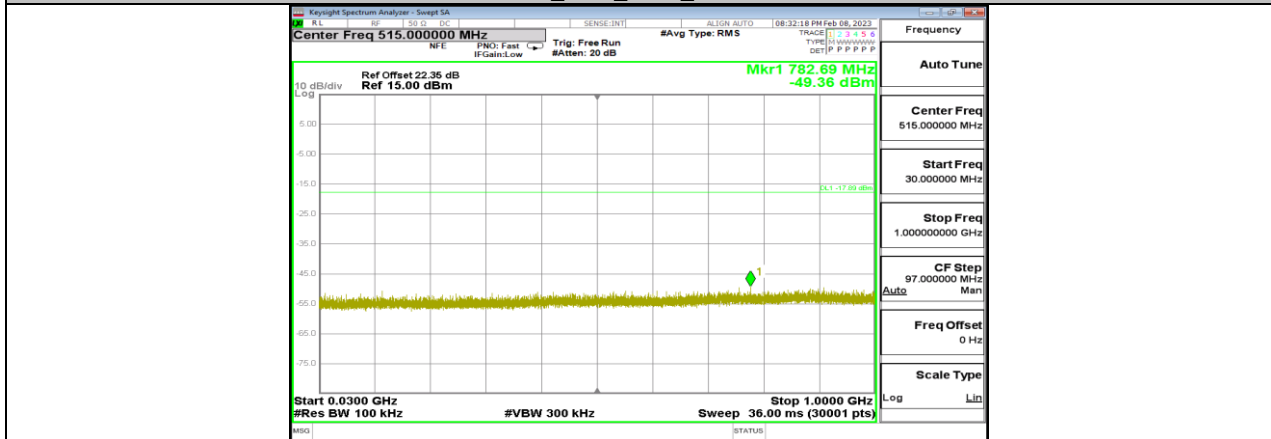
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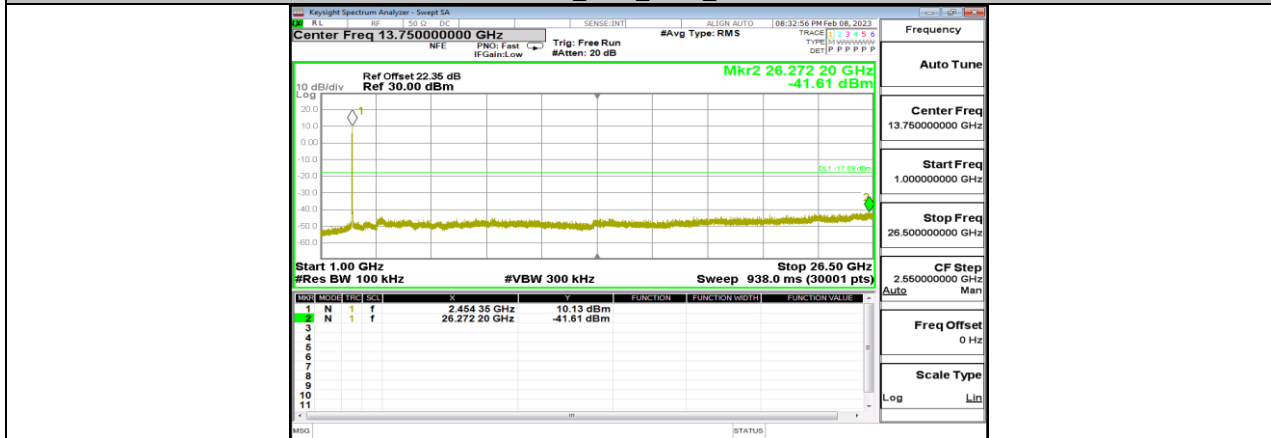
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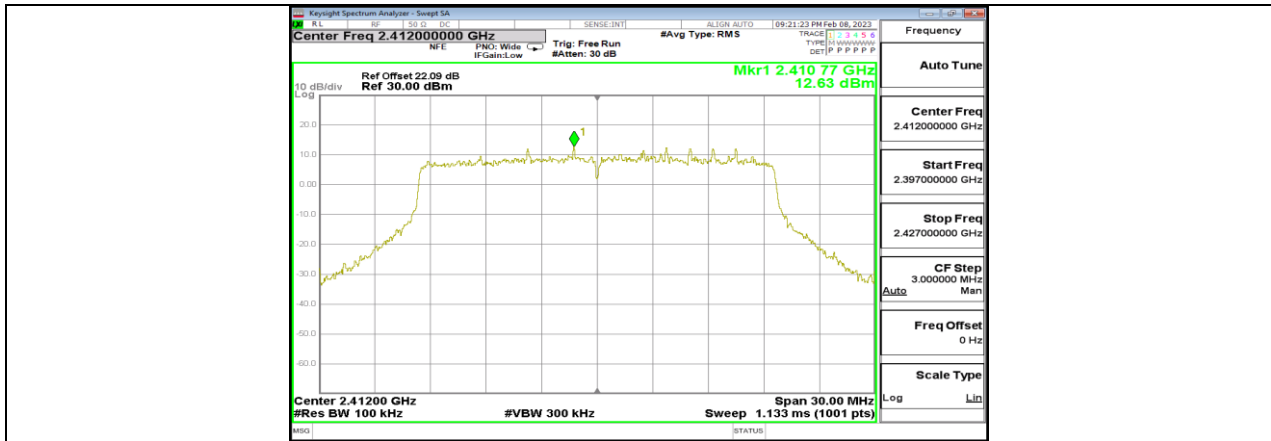
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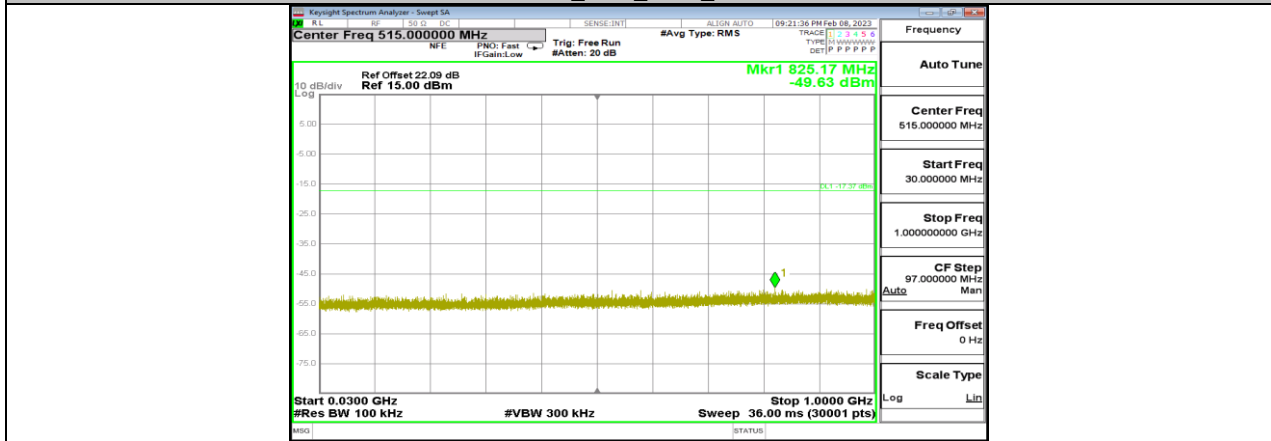
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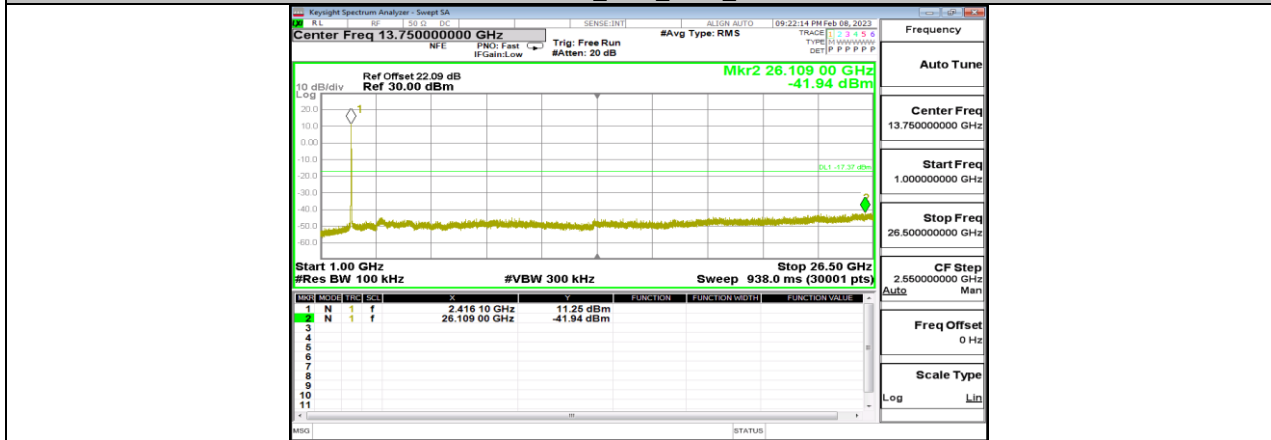
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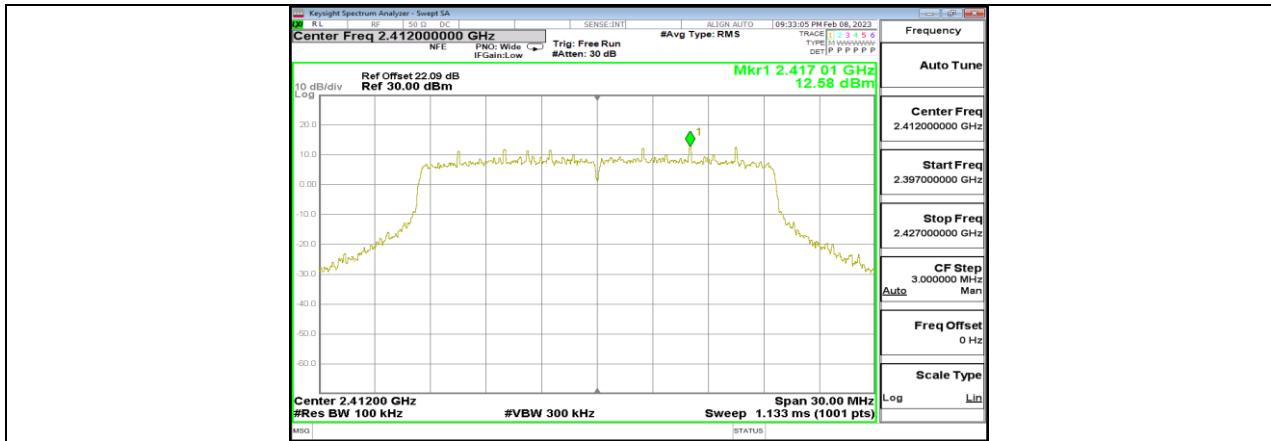
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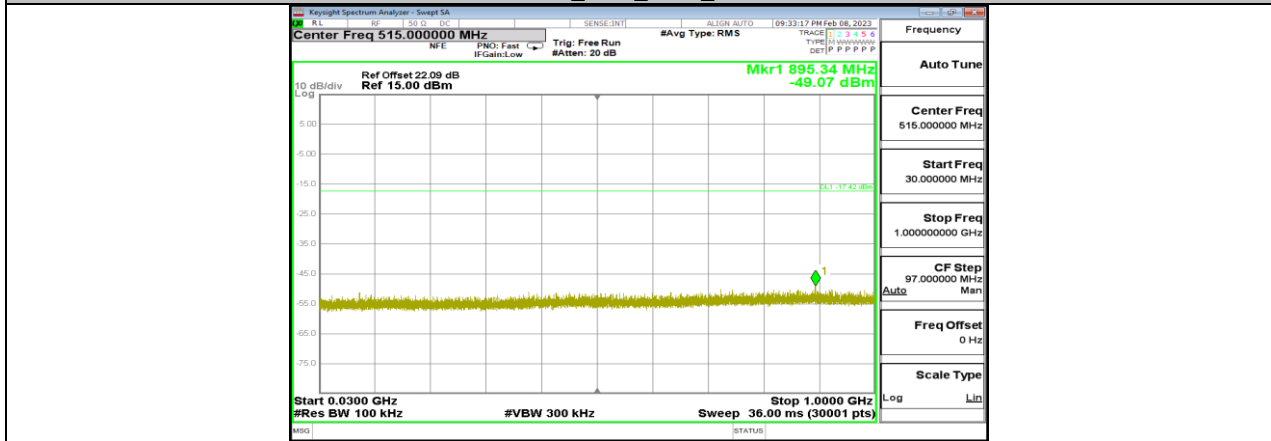
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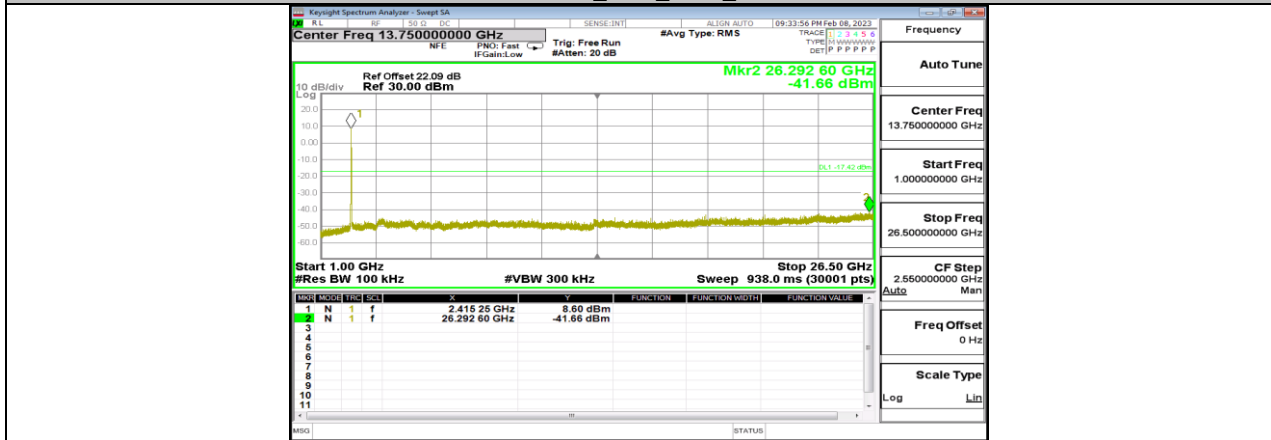
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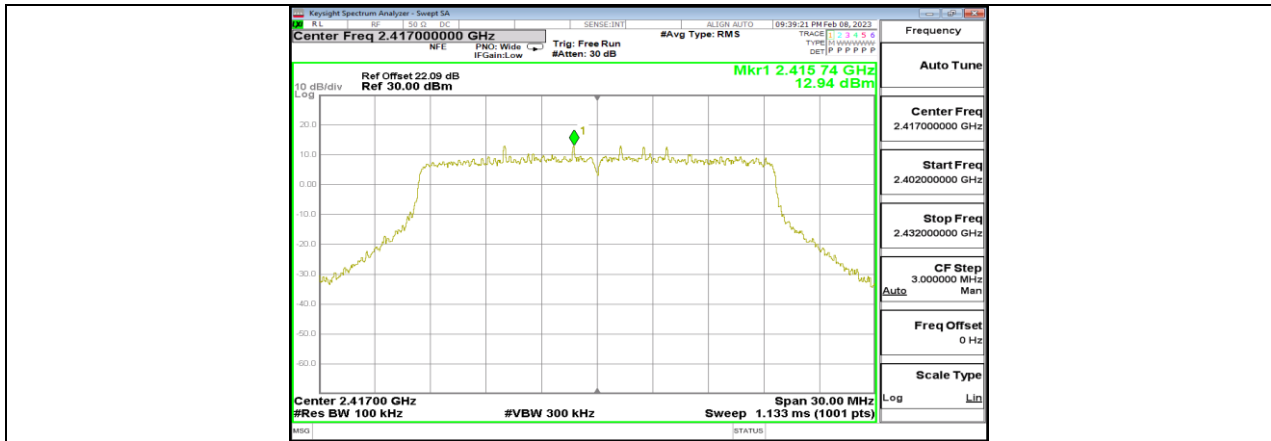
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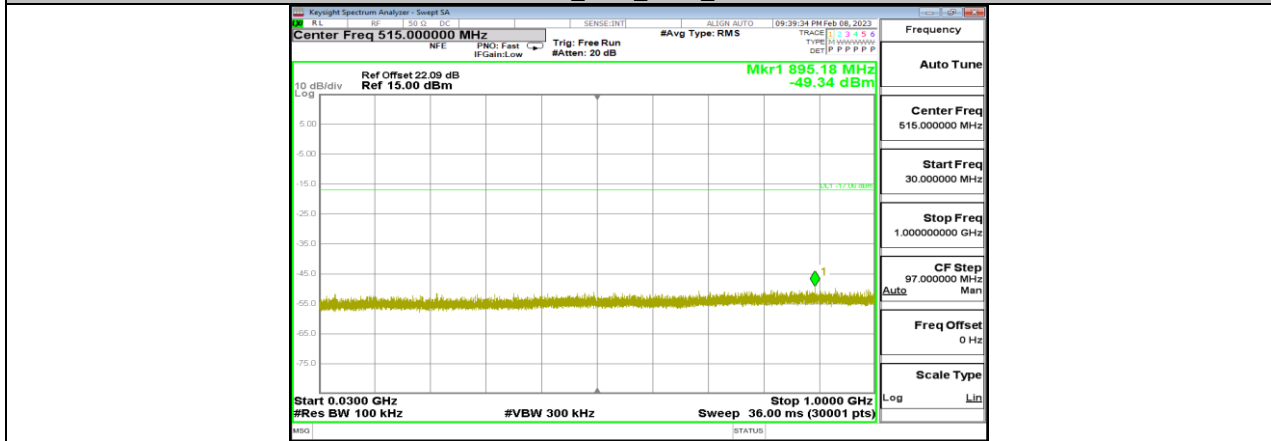
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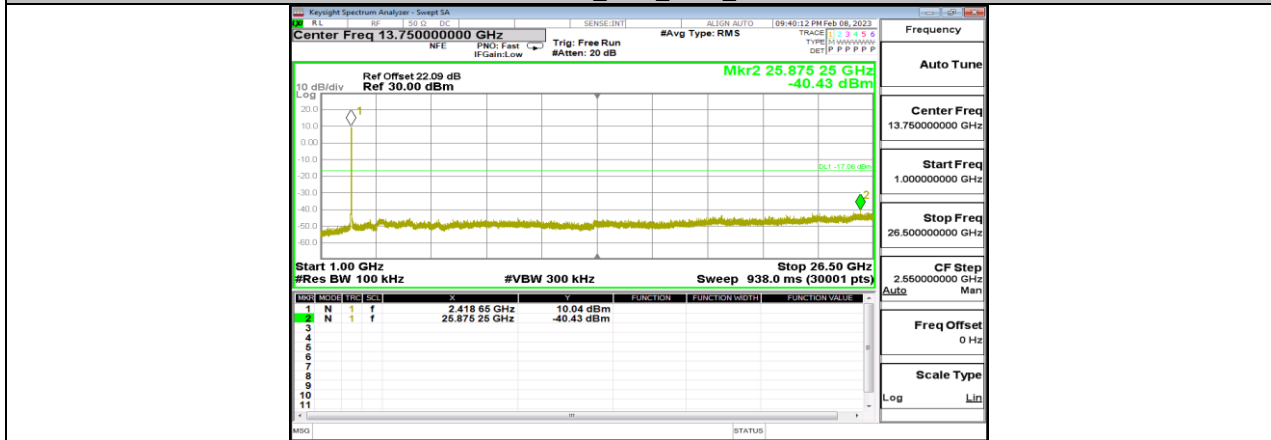
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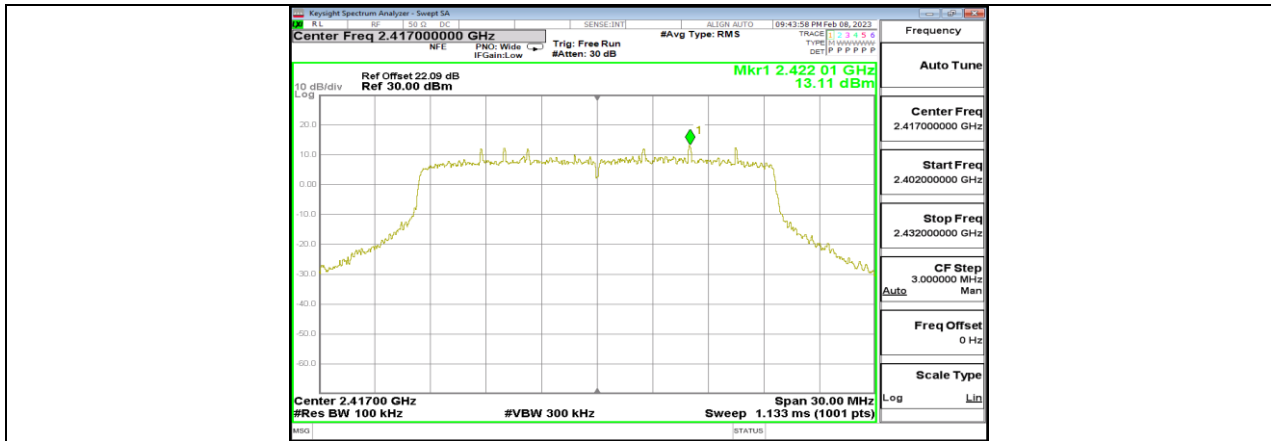
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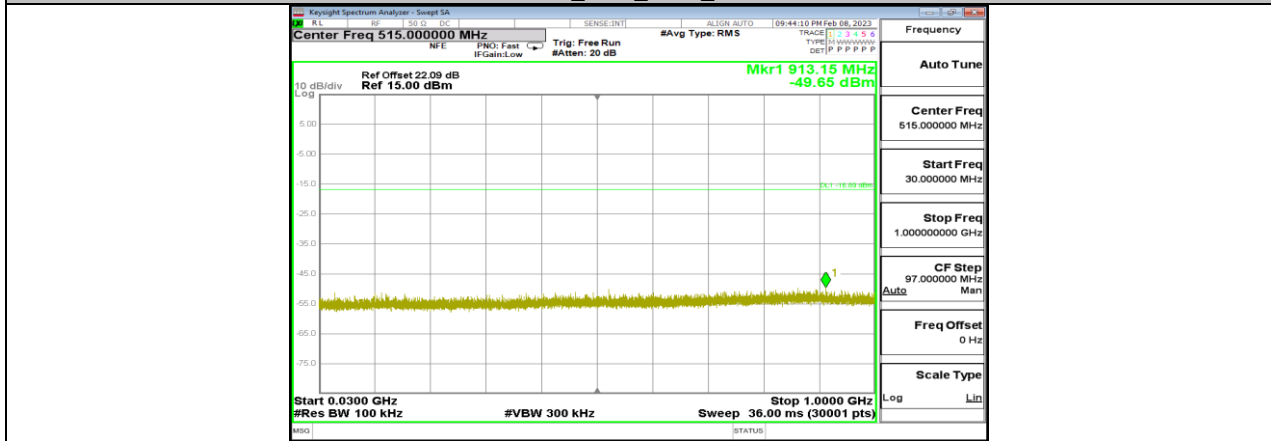
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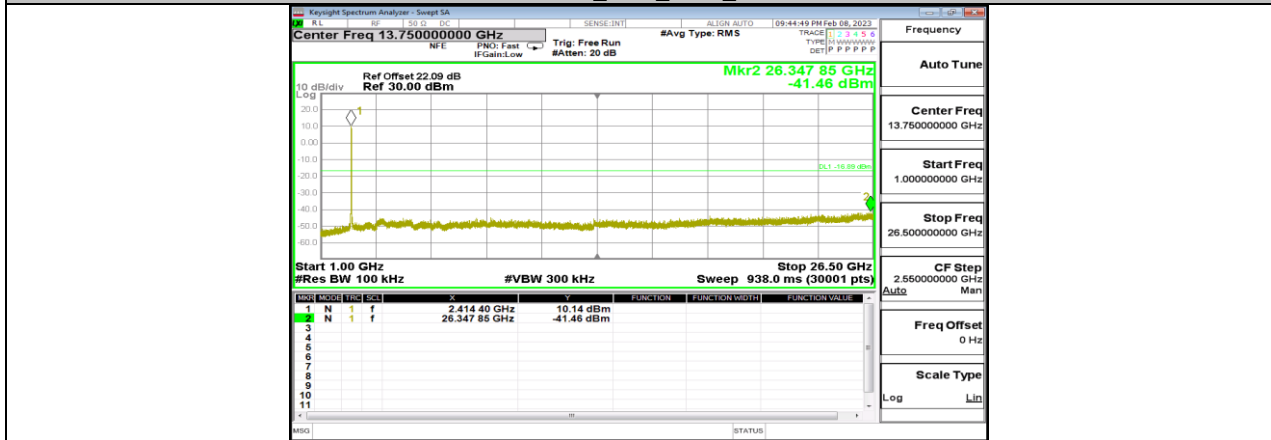
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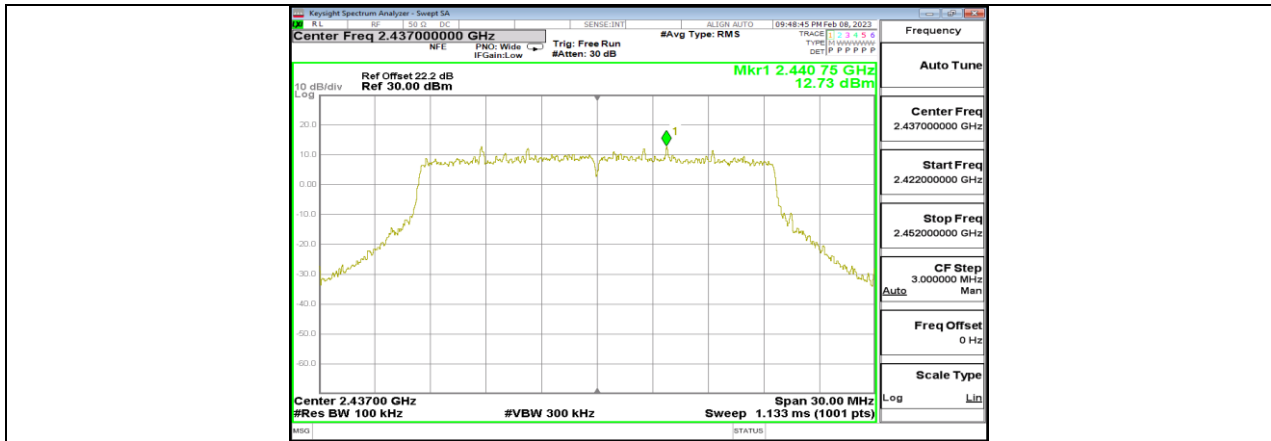
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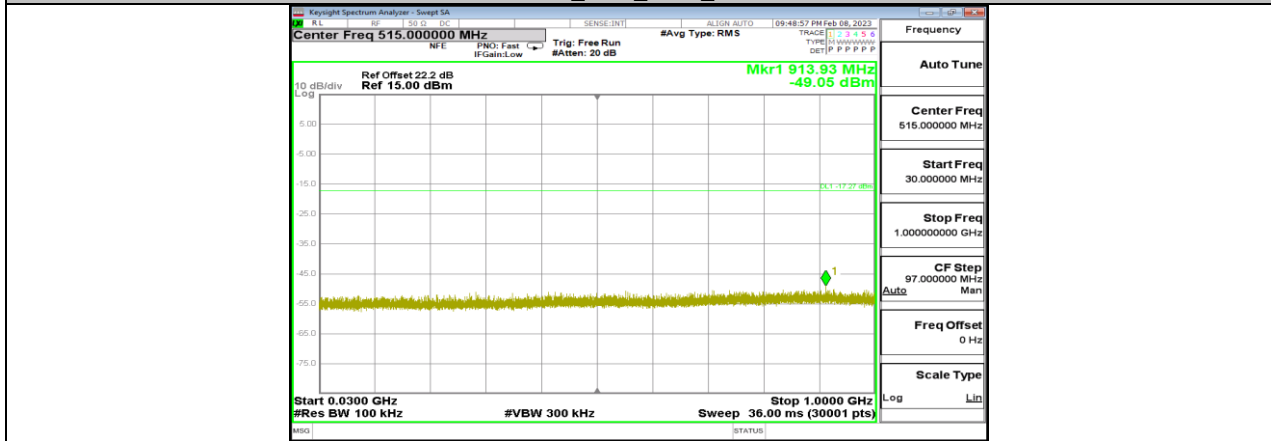
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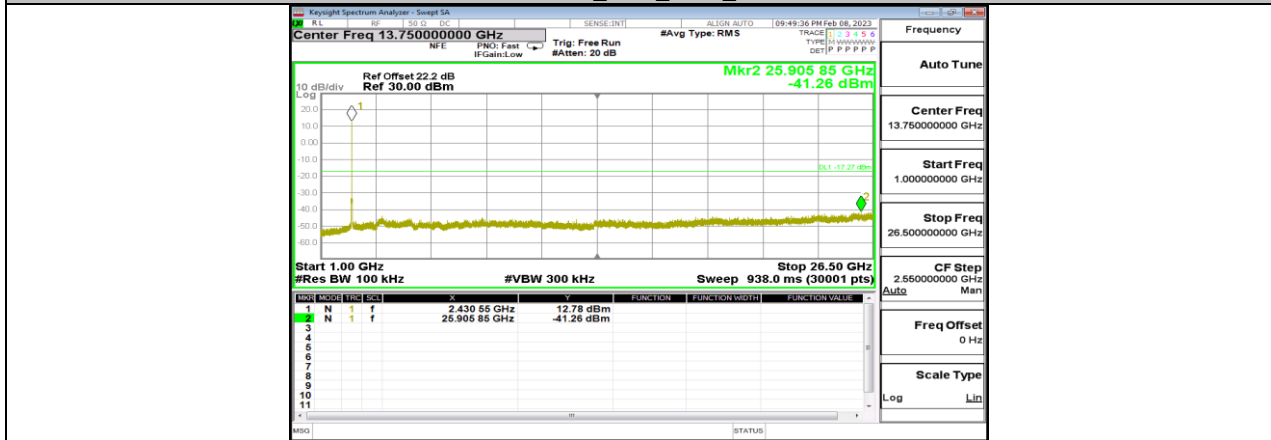
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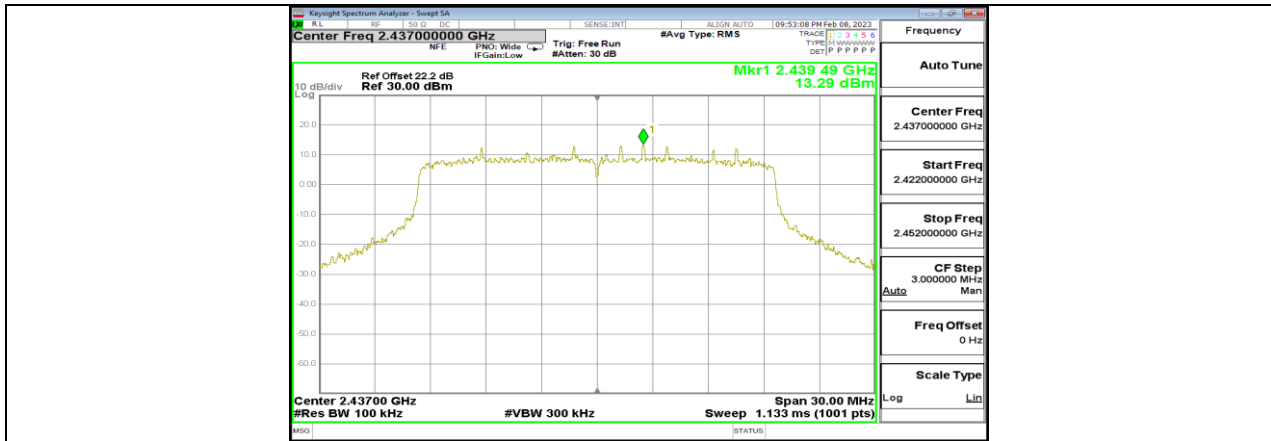
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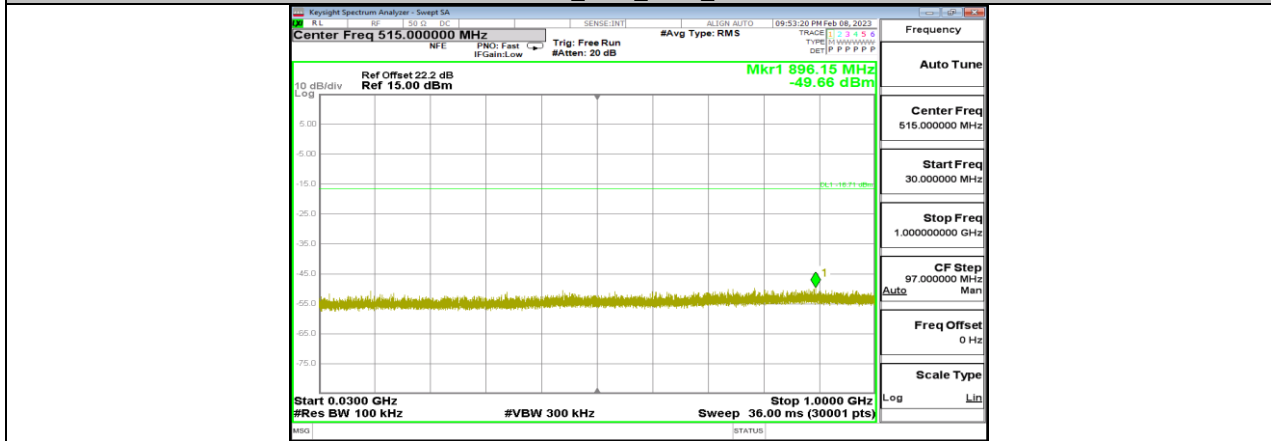
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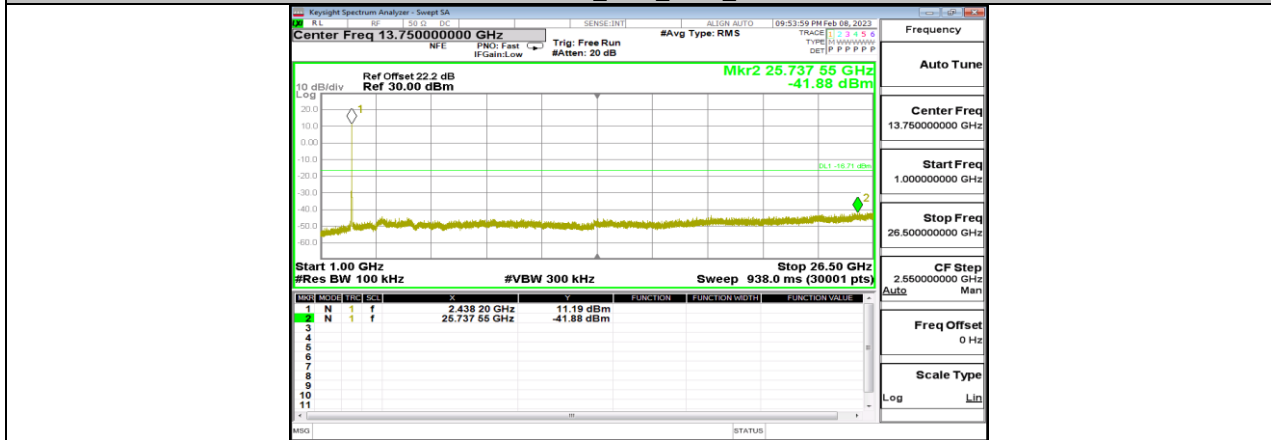
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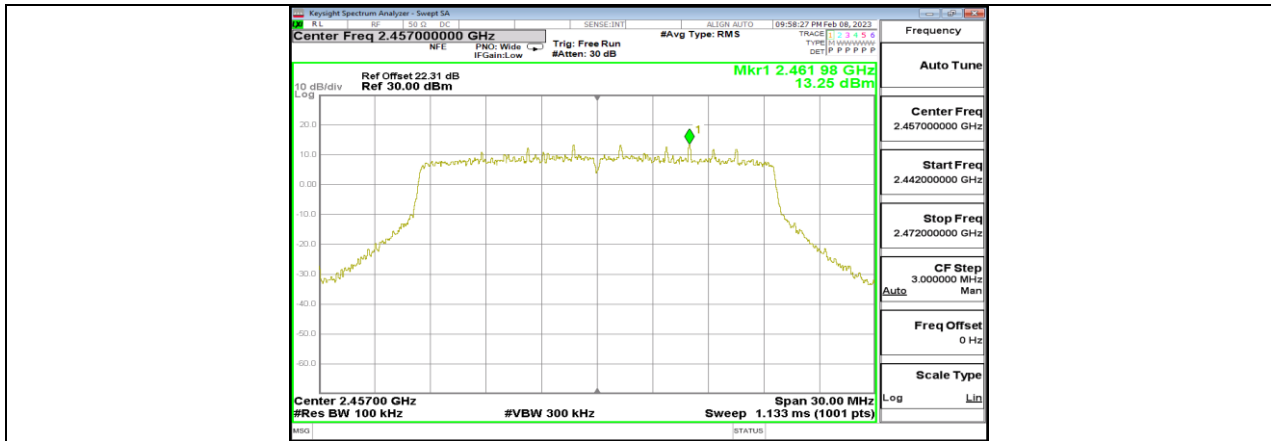
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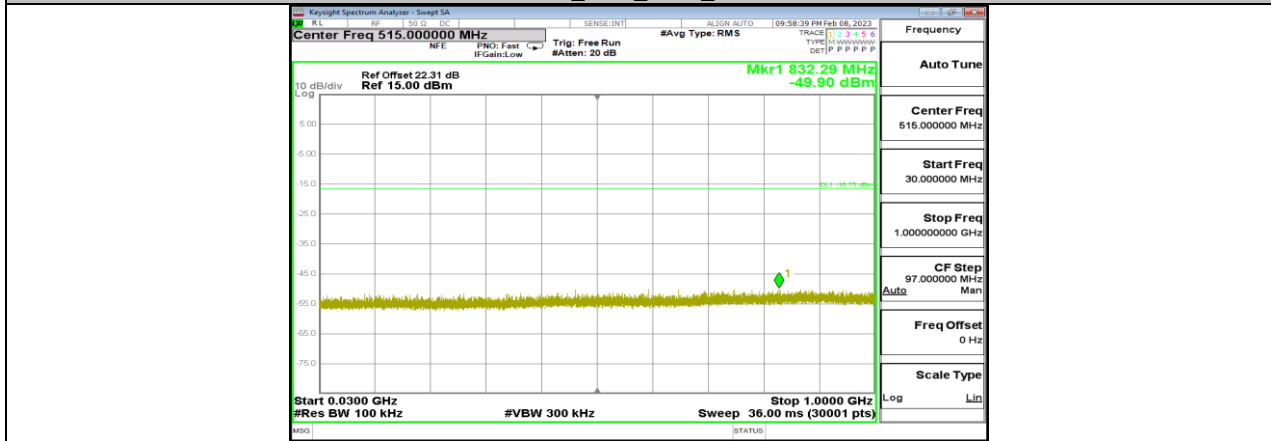
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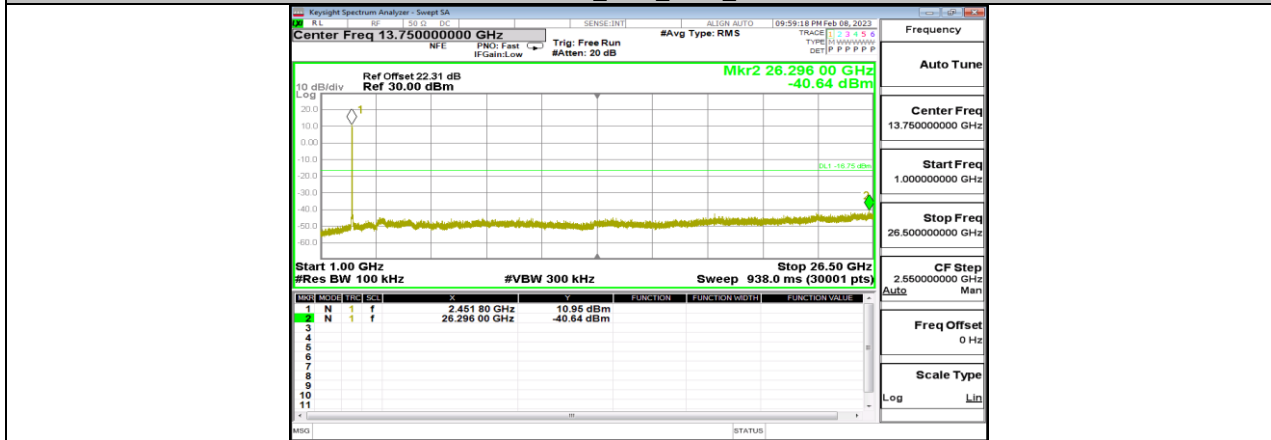
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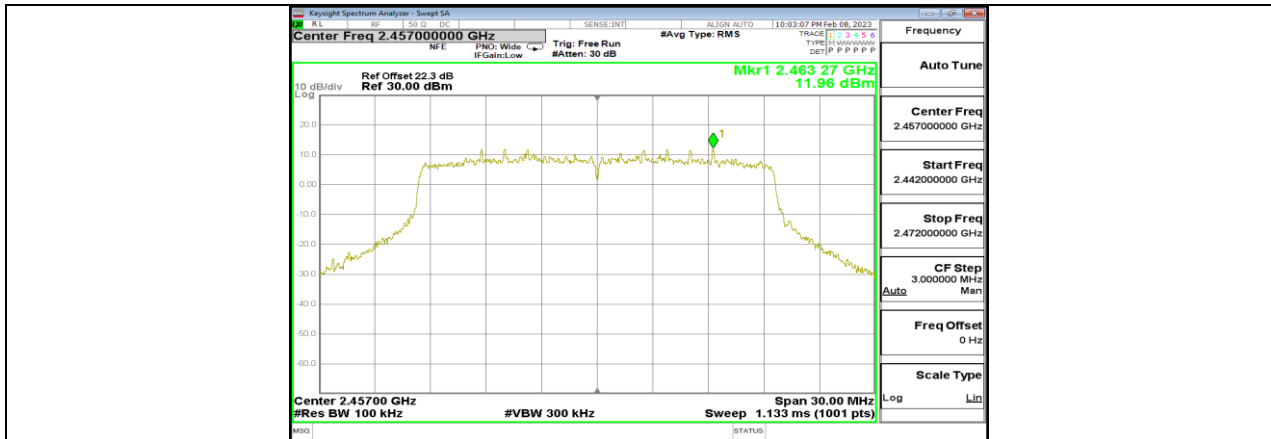
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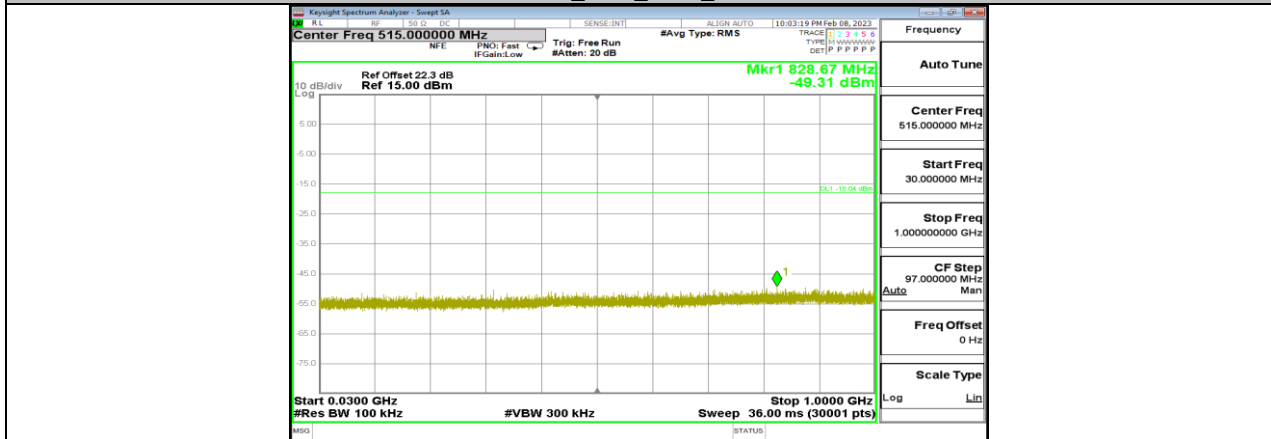
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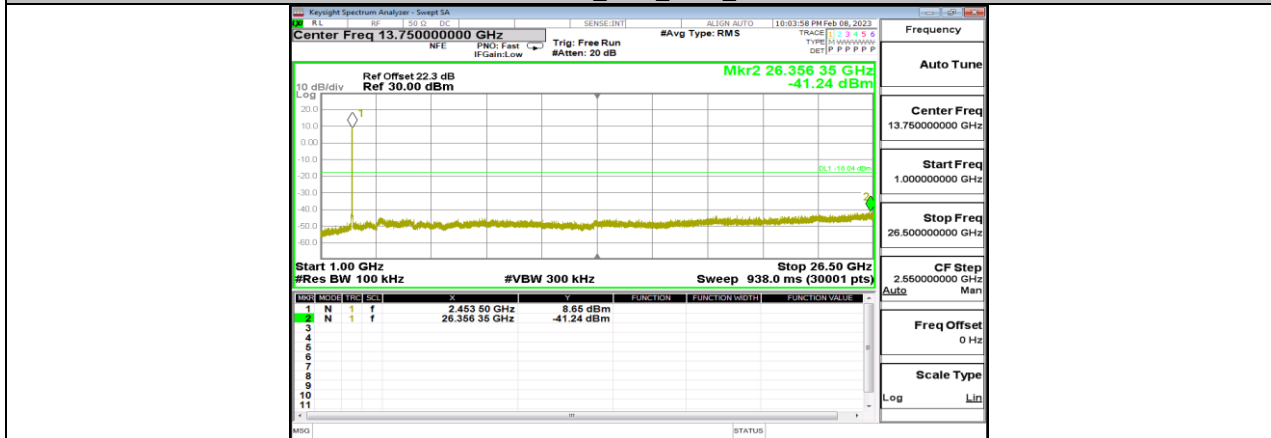
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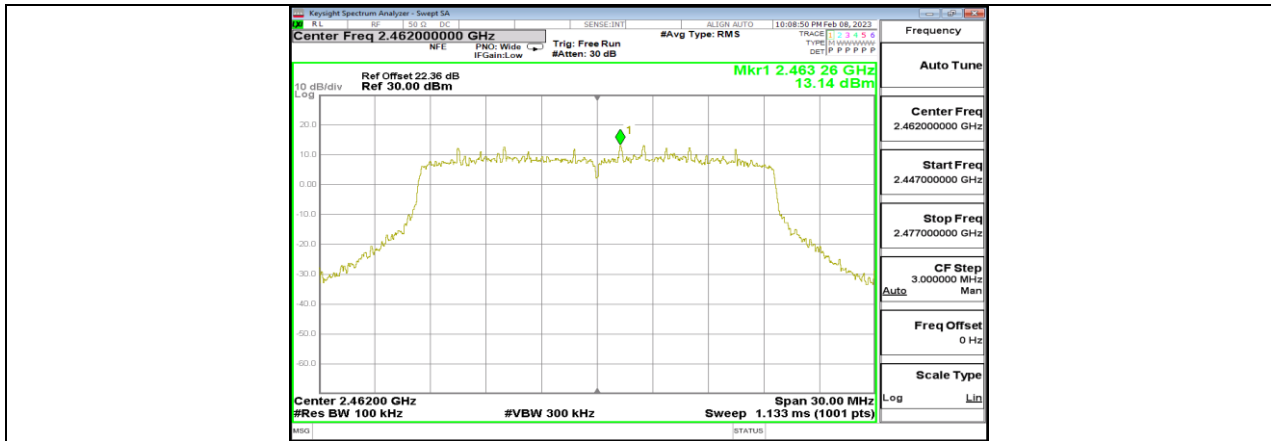
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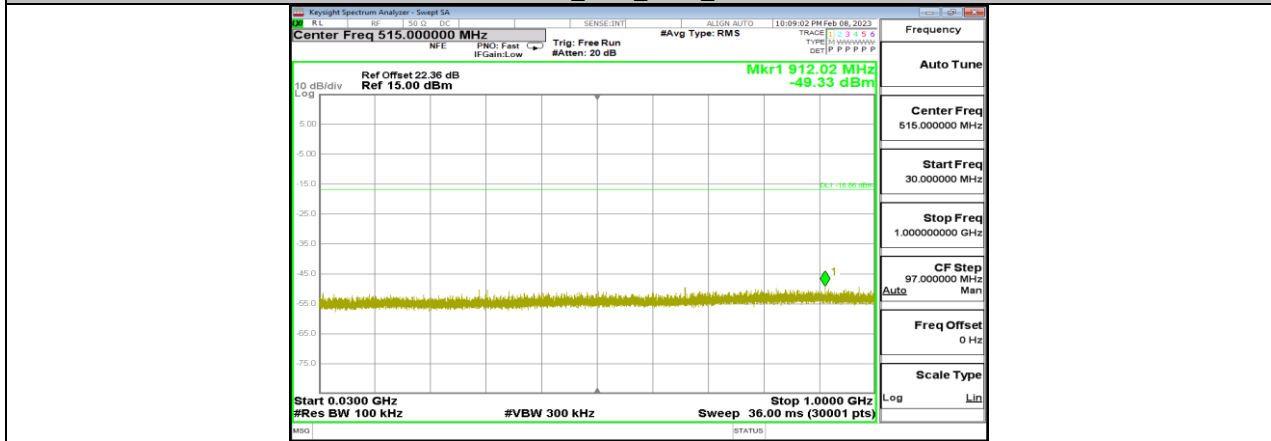
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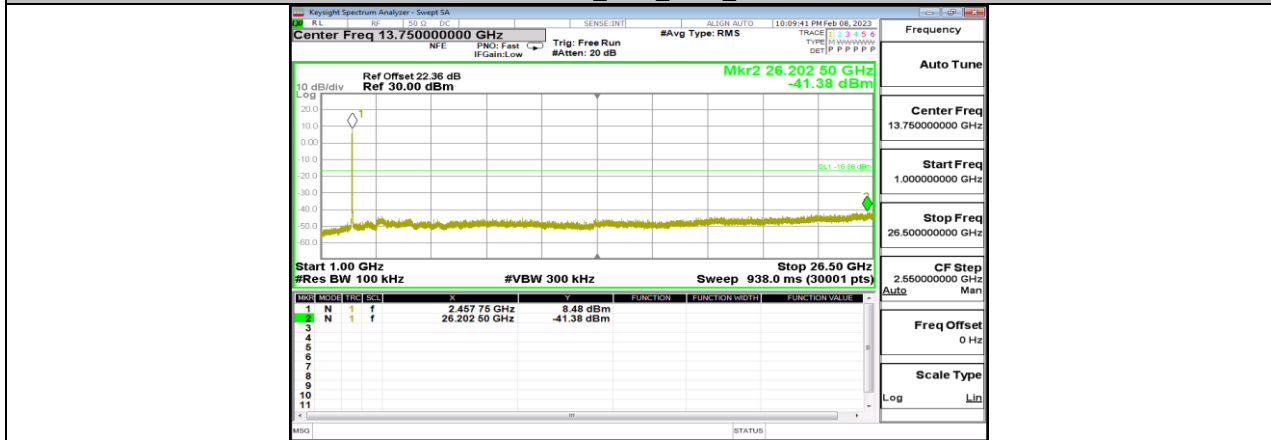
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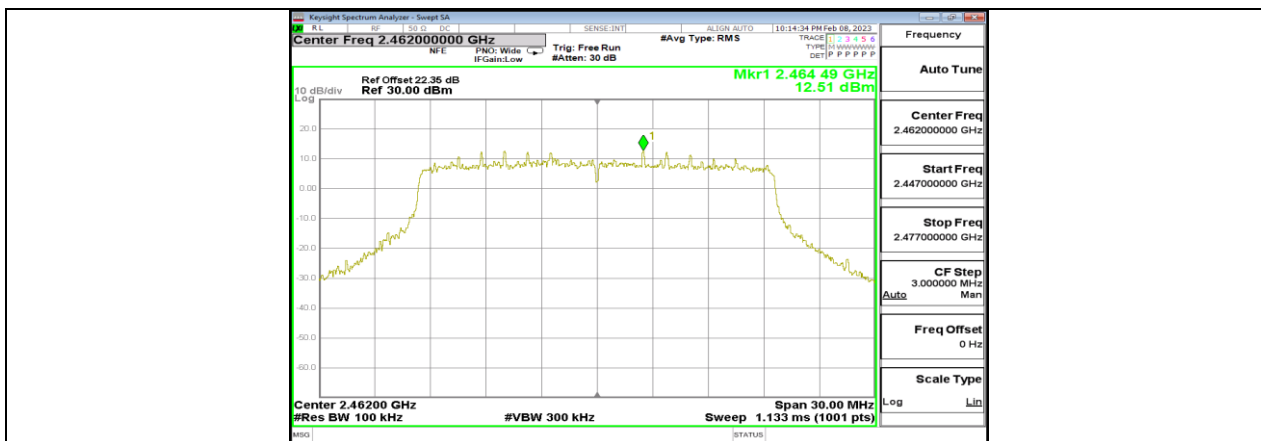
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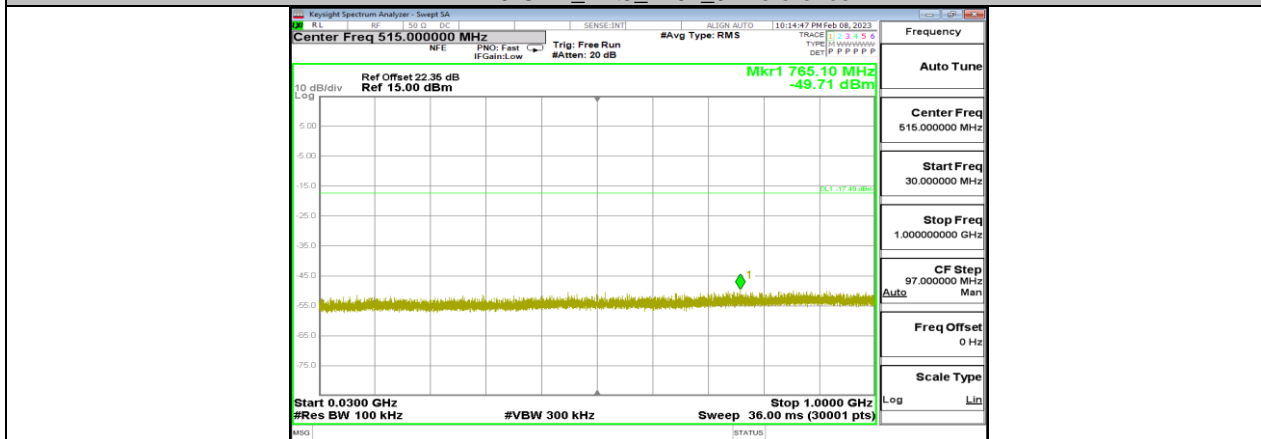
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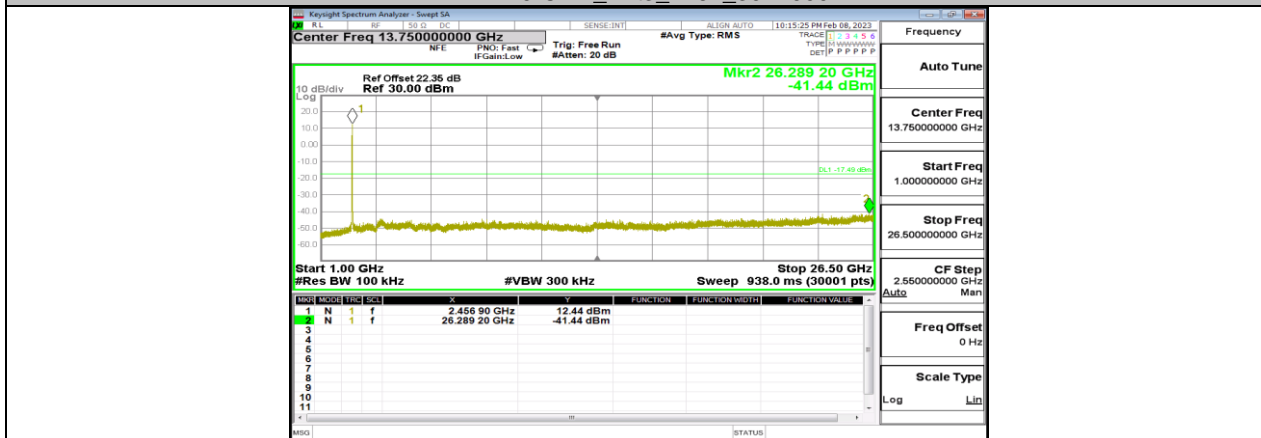
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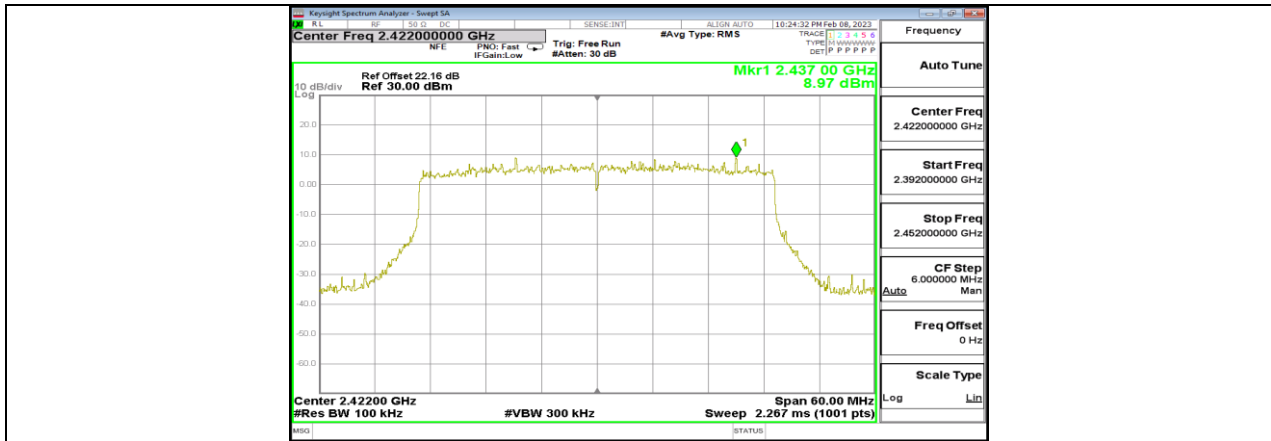
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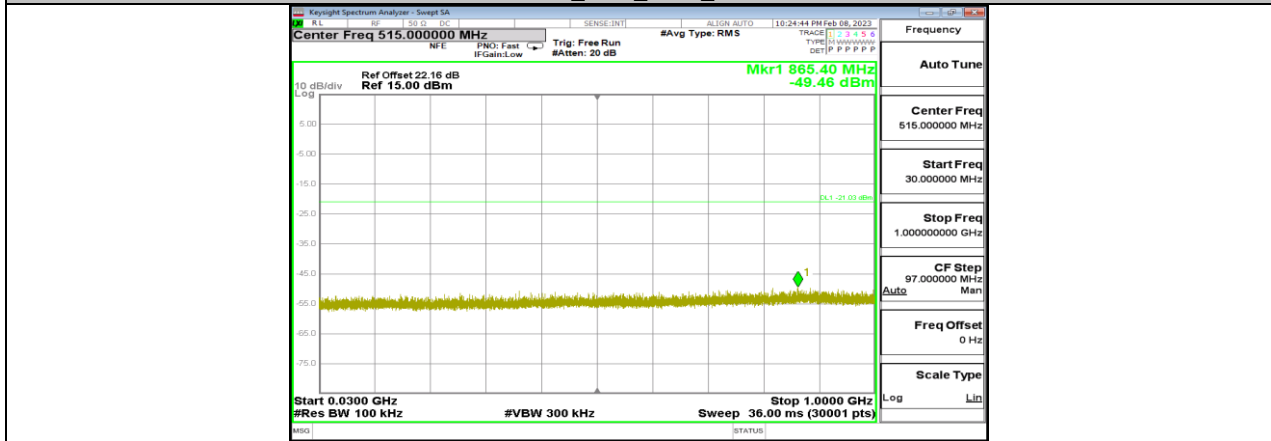
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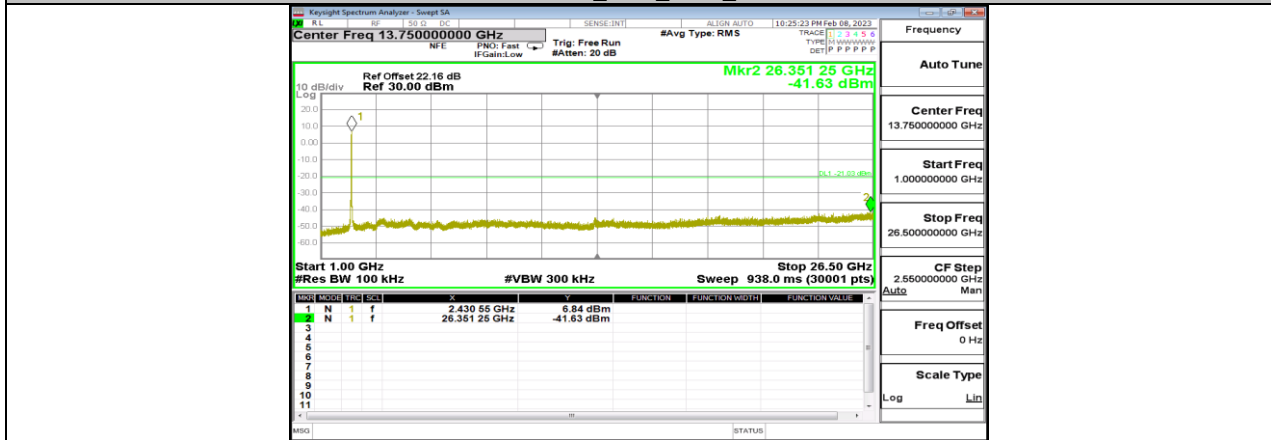
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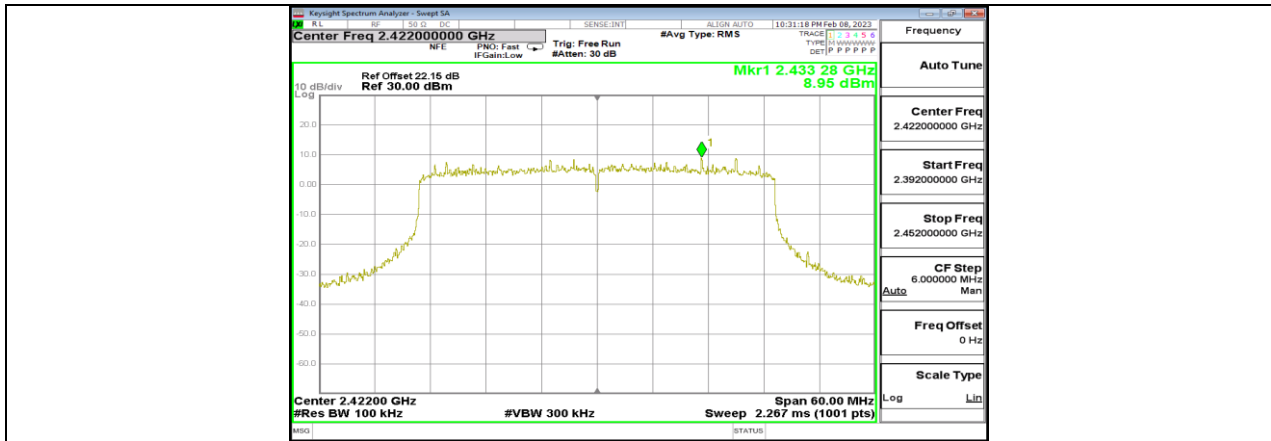
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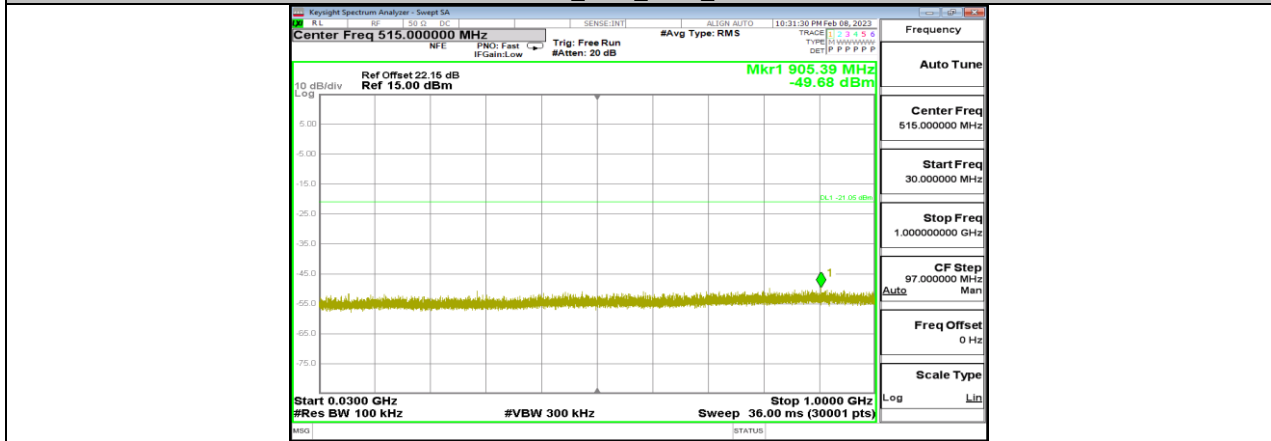
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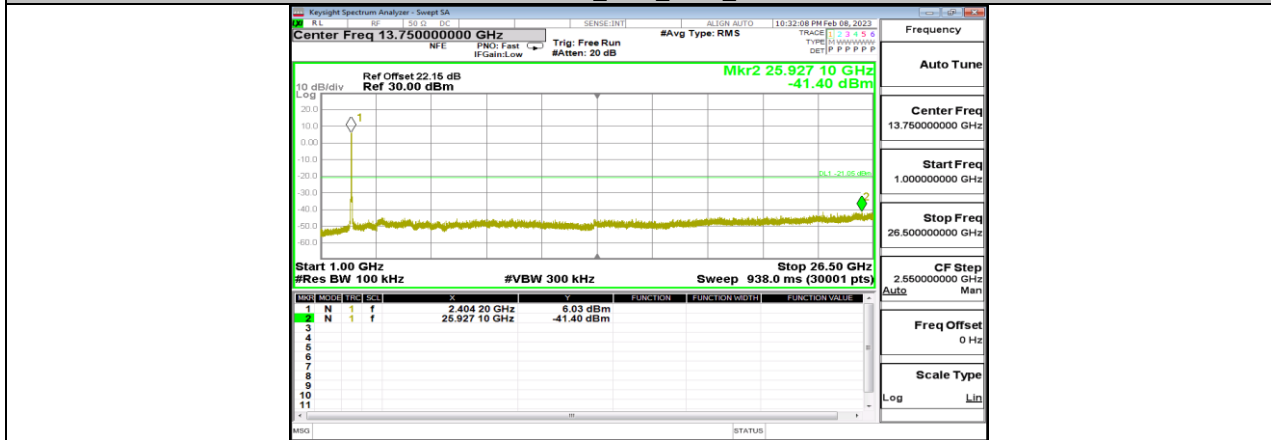
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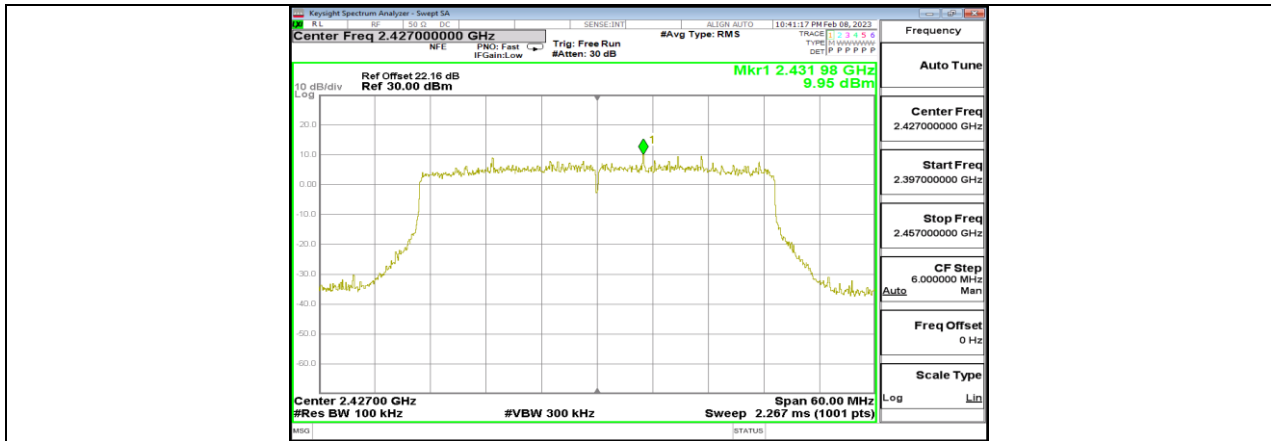
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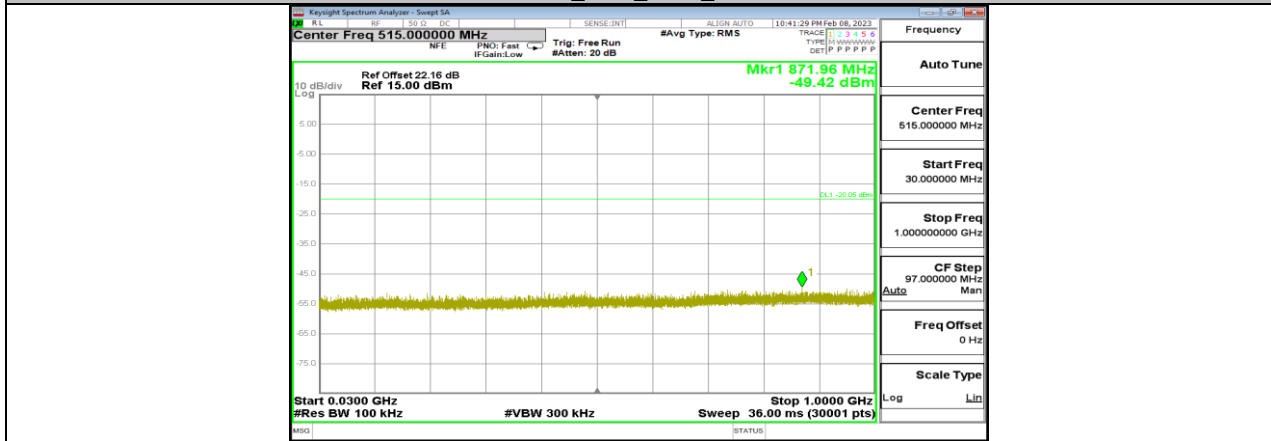
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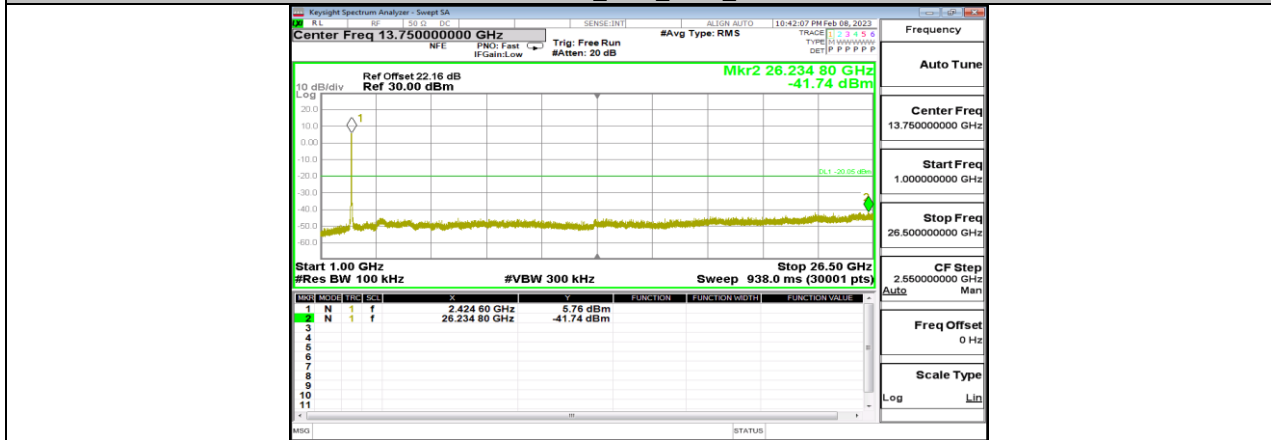
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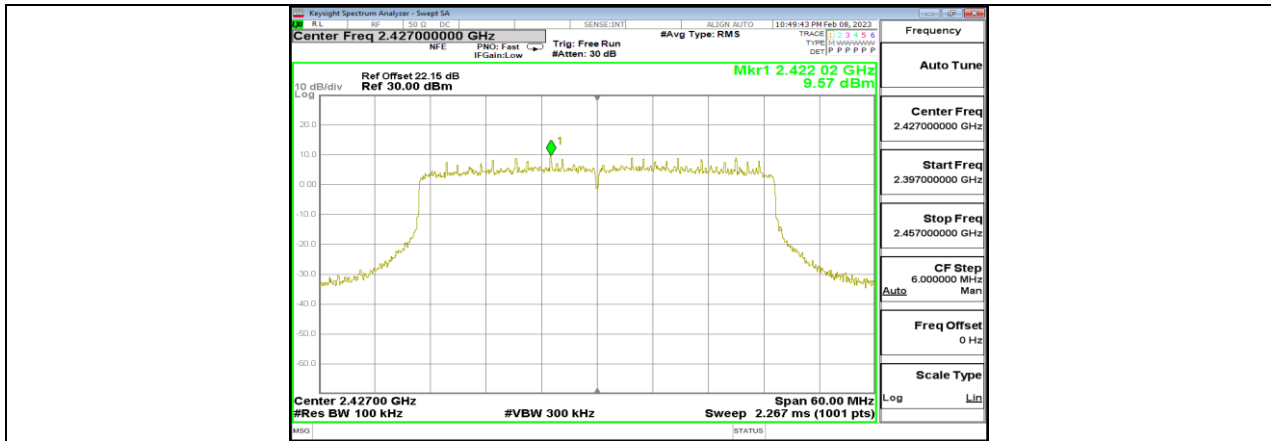
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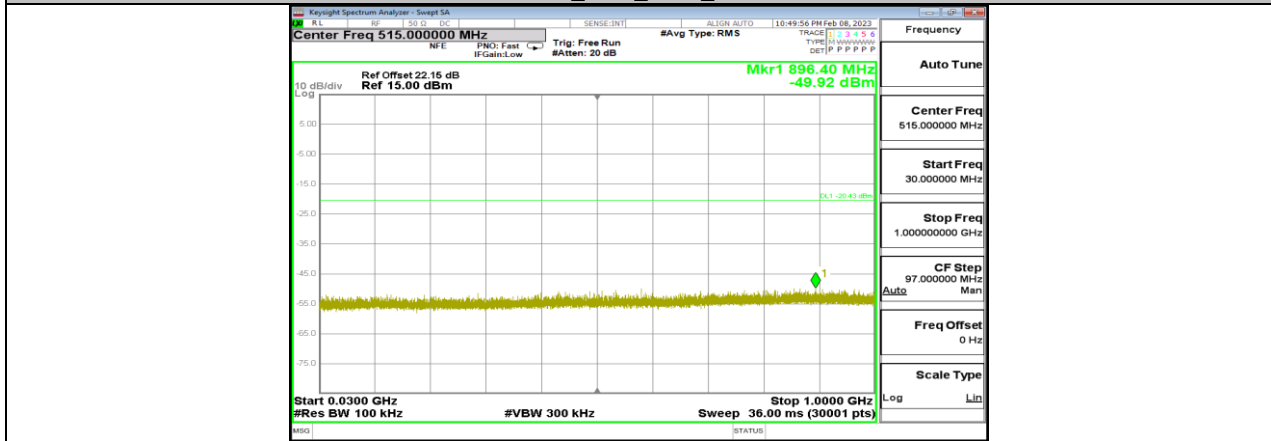
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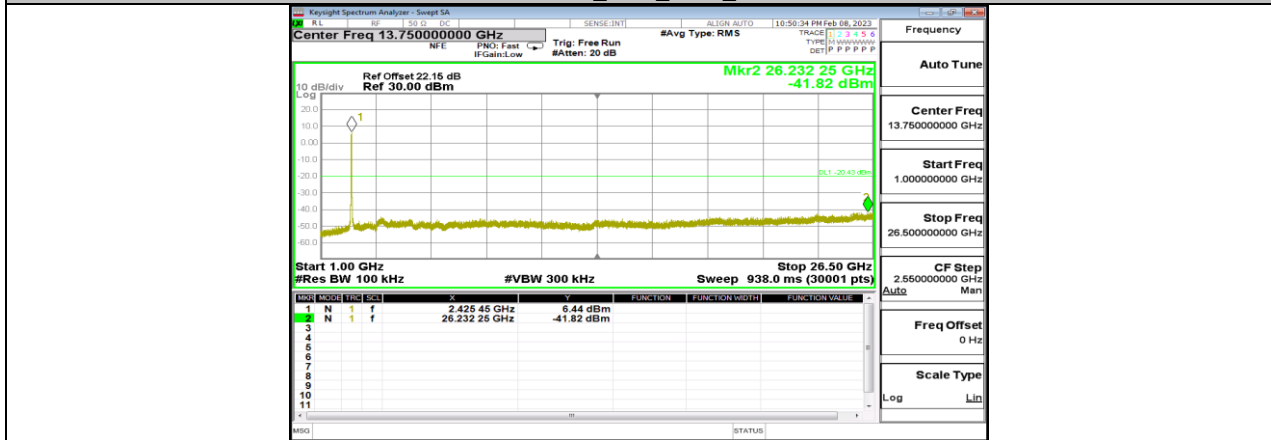
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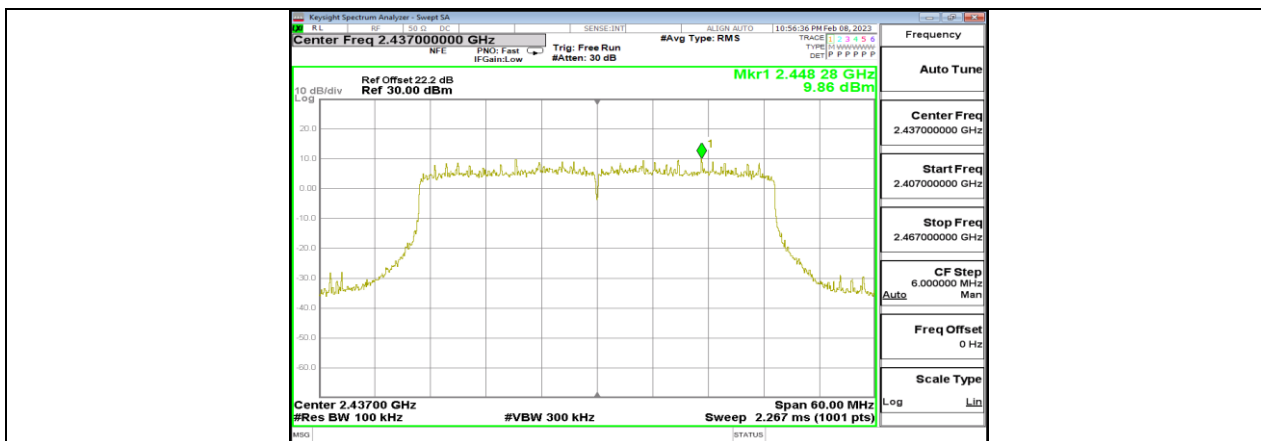
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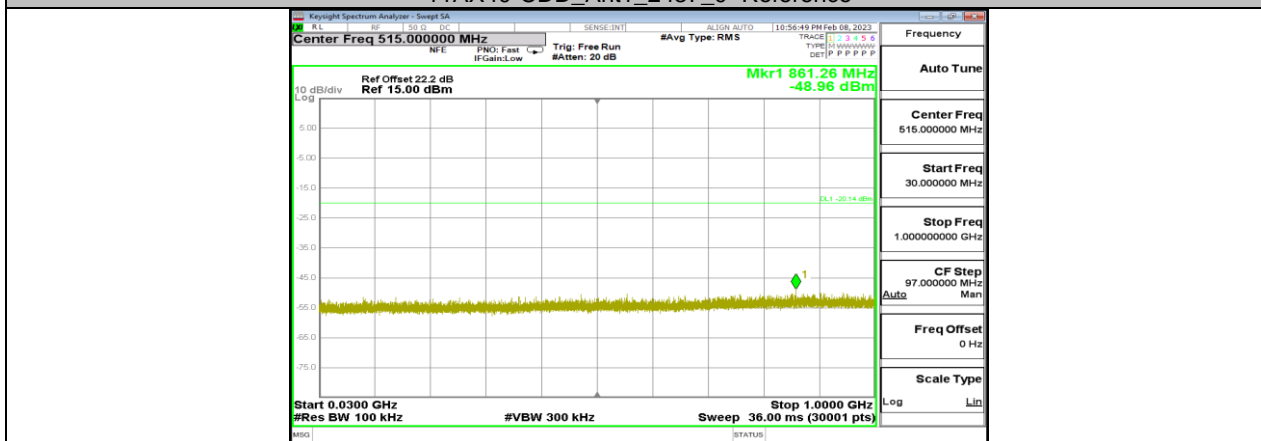
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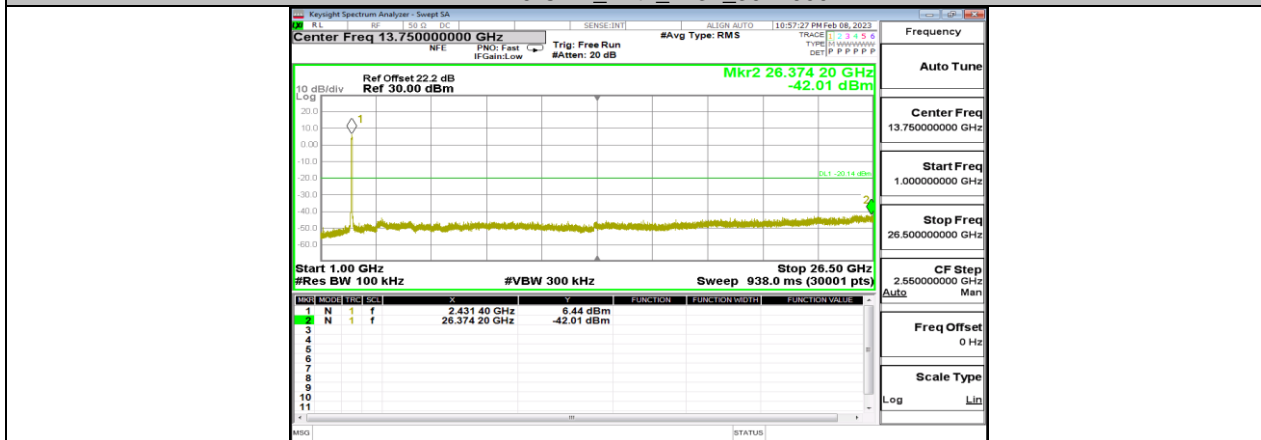
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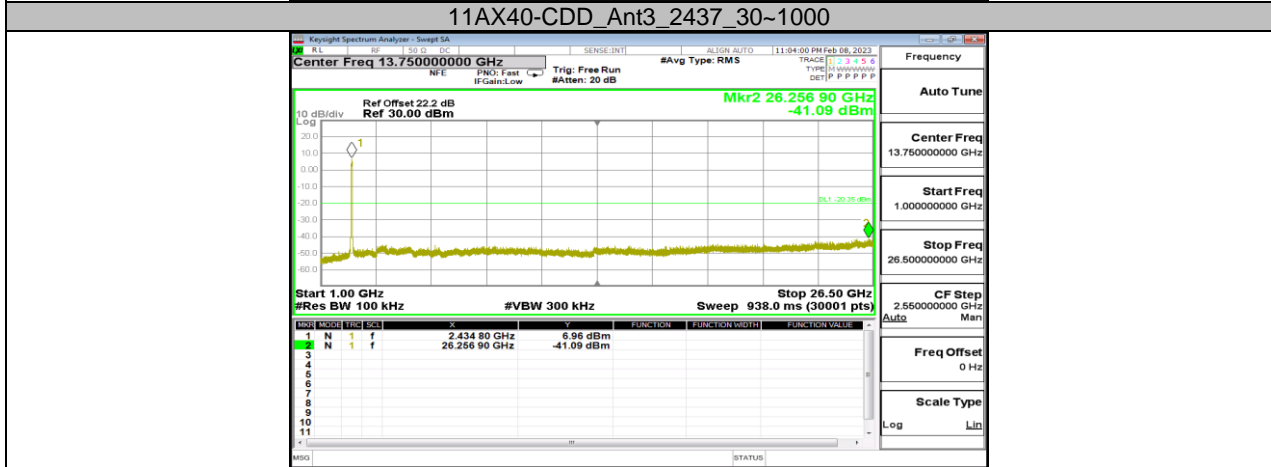
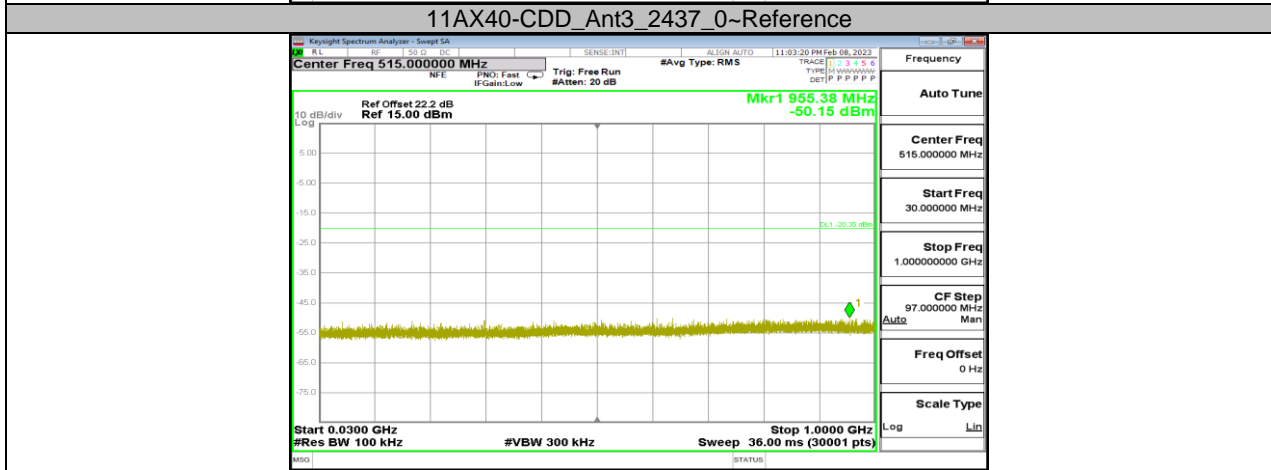
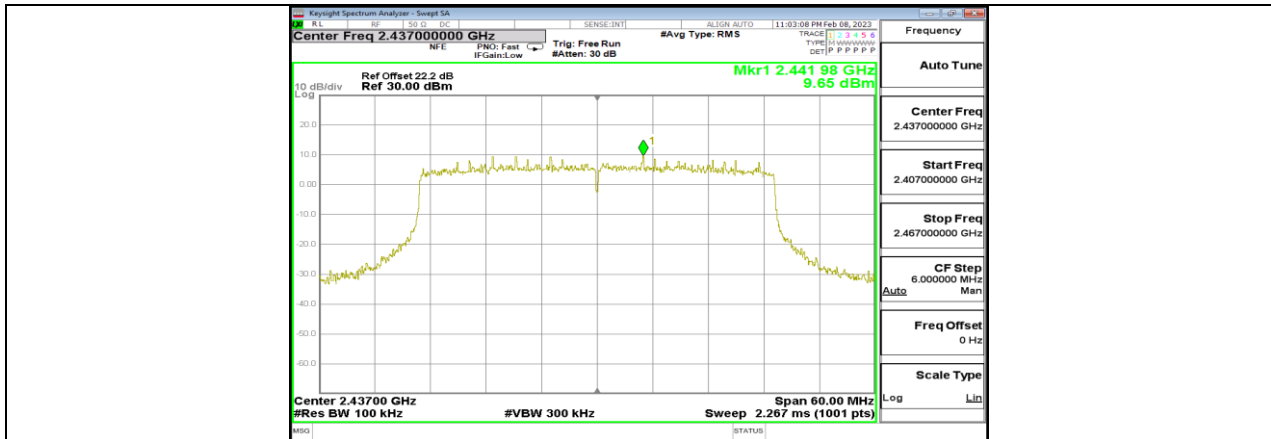
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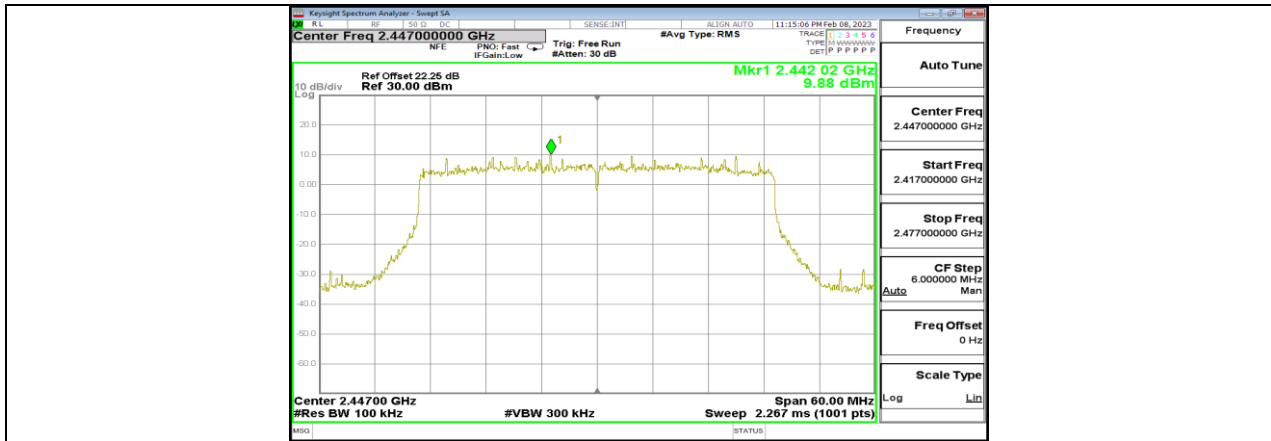


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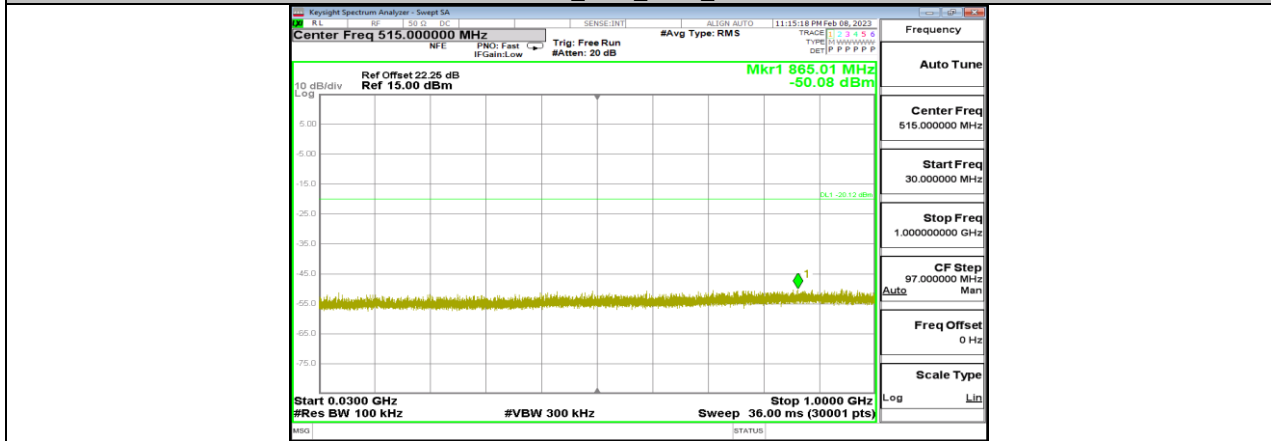


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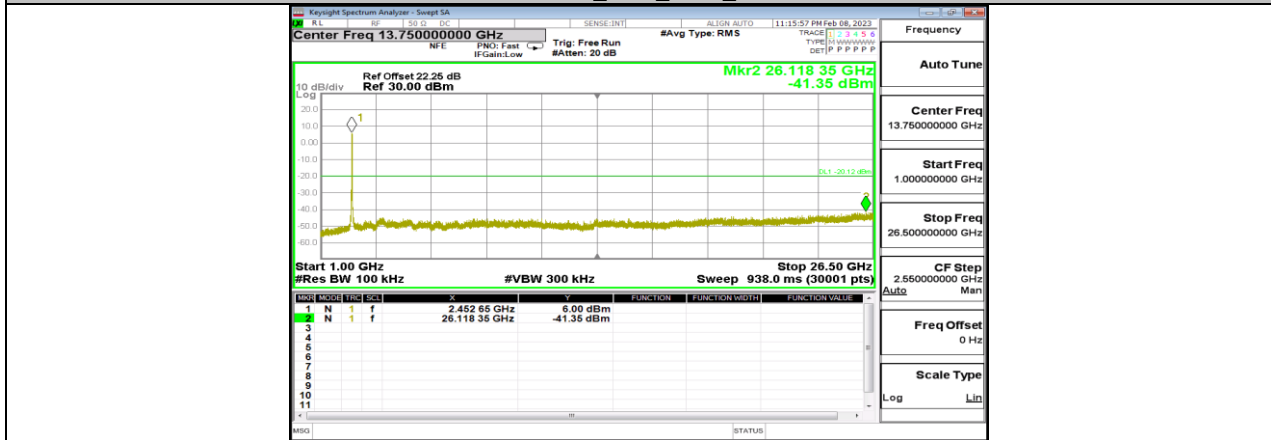




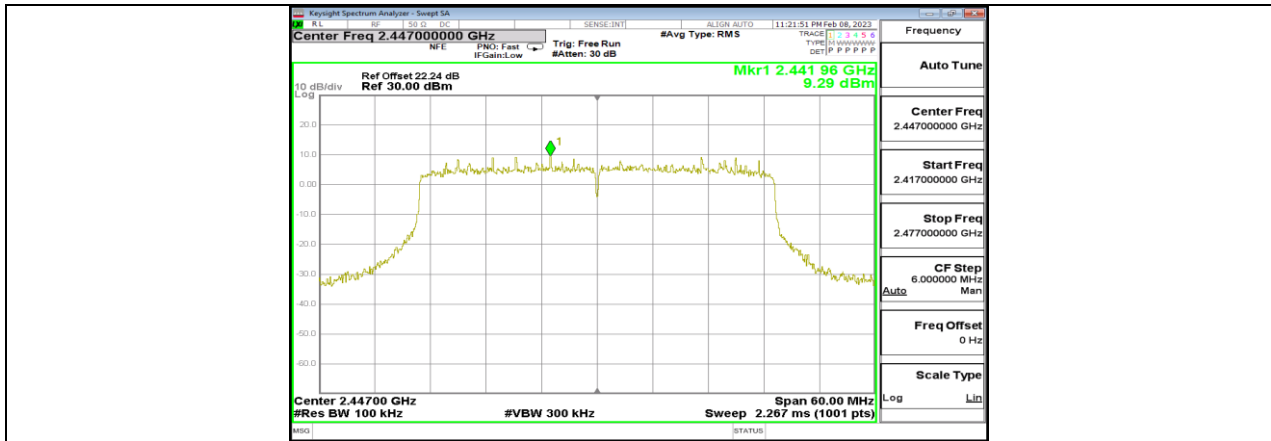
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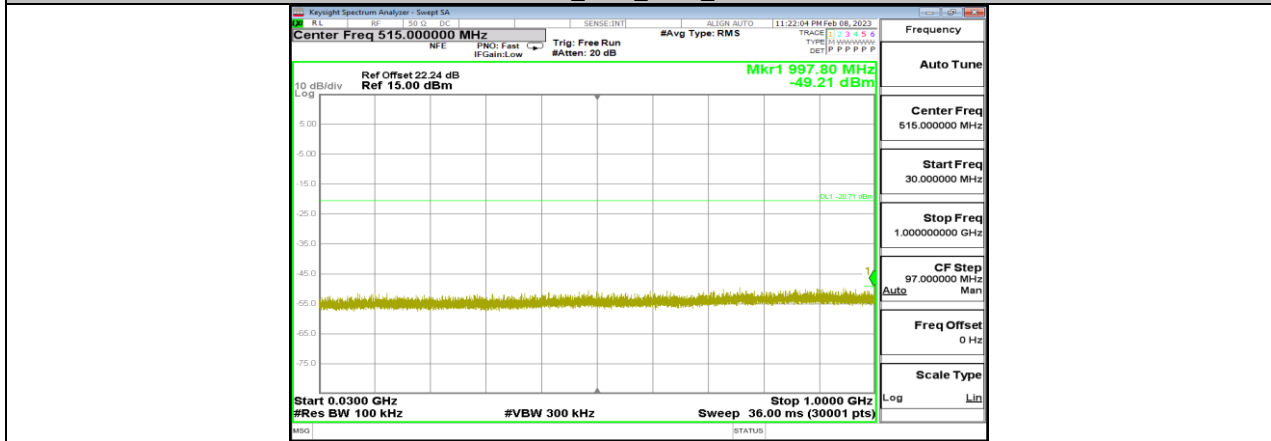
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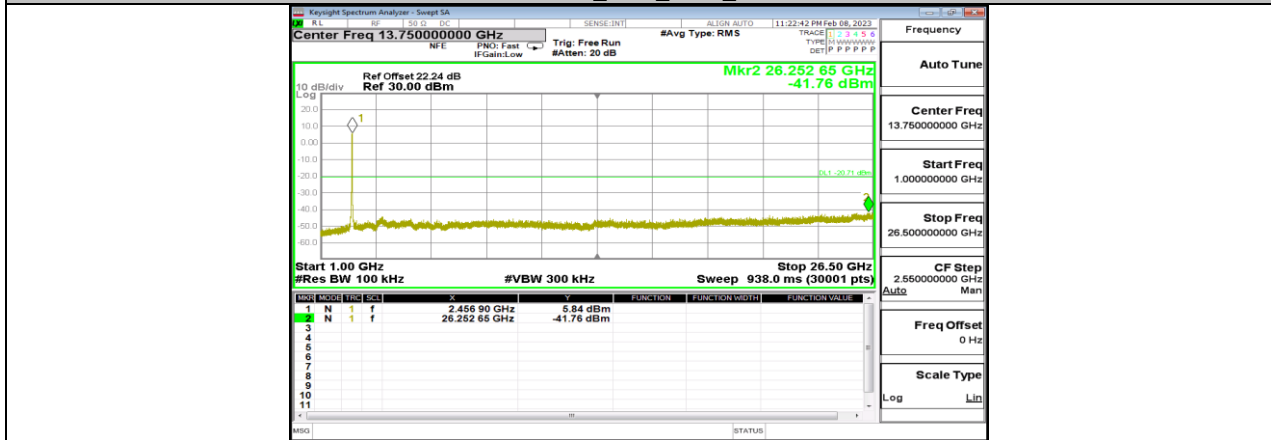
11AX40-CDD_Ant1_2447_1000-26500



11AX40-CDD_Ant3_2447_0-Reference



11AX40-CDD_Ant3_2447_30~1000



11AX40-CDD_Ant3_2447_1000~26500