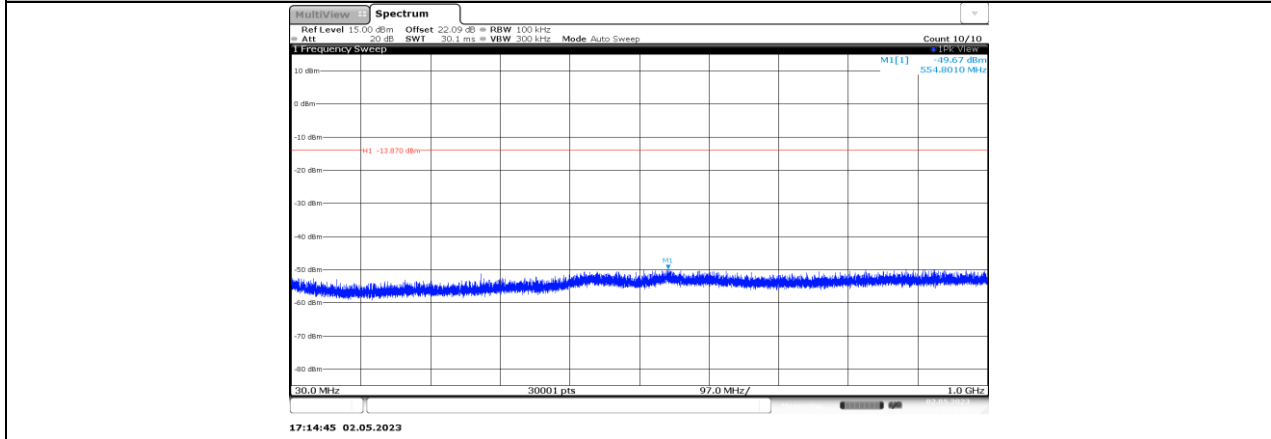
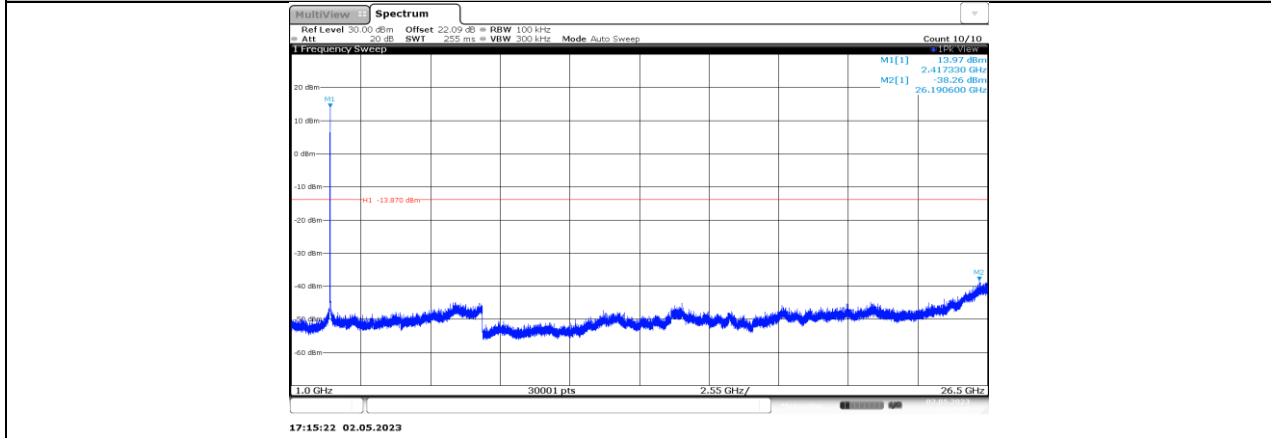


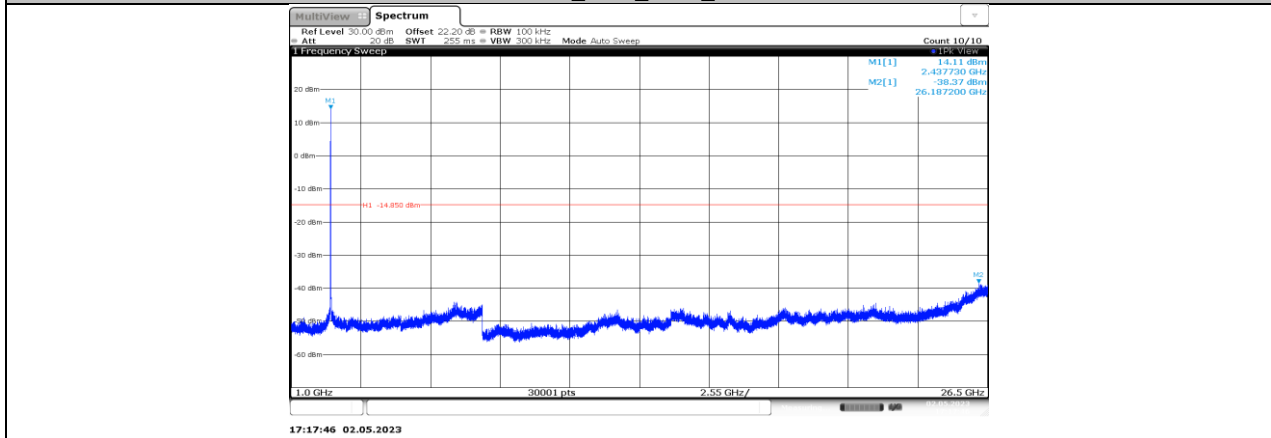
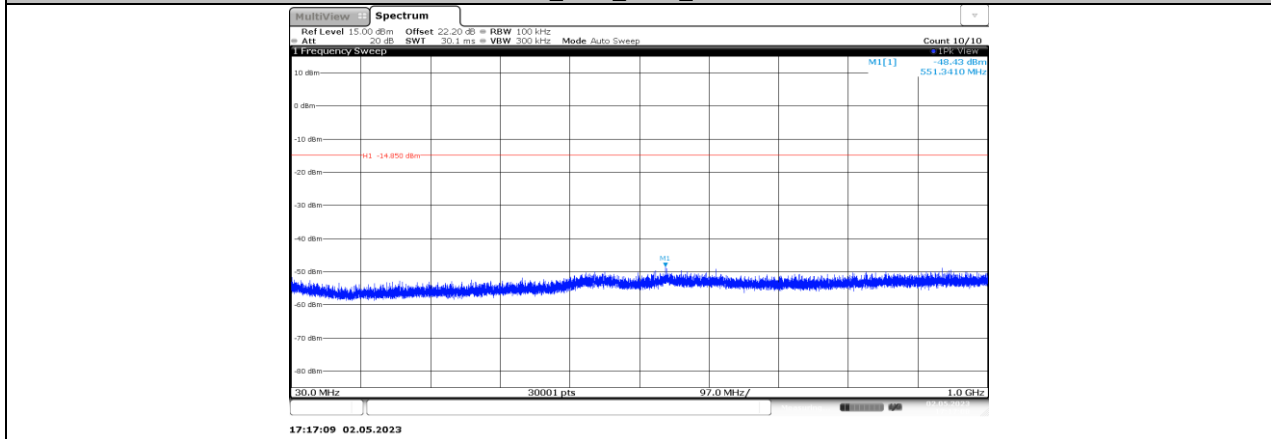
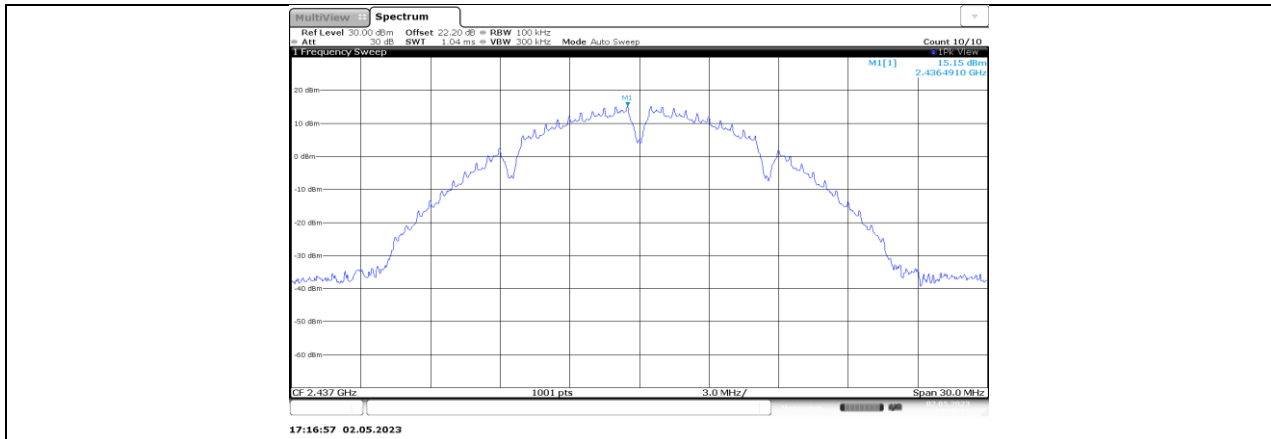
11B-CDD\_Ant5\_2417\_0~Reference

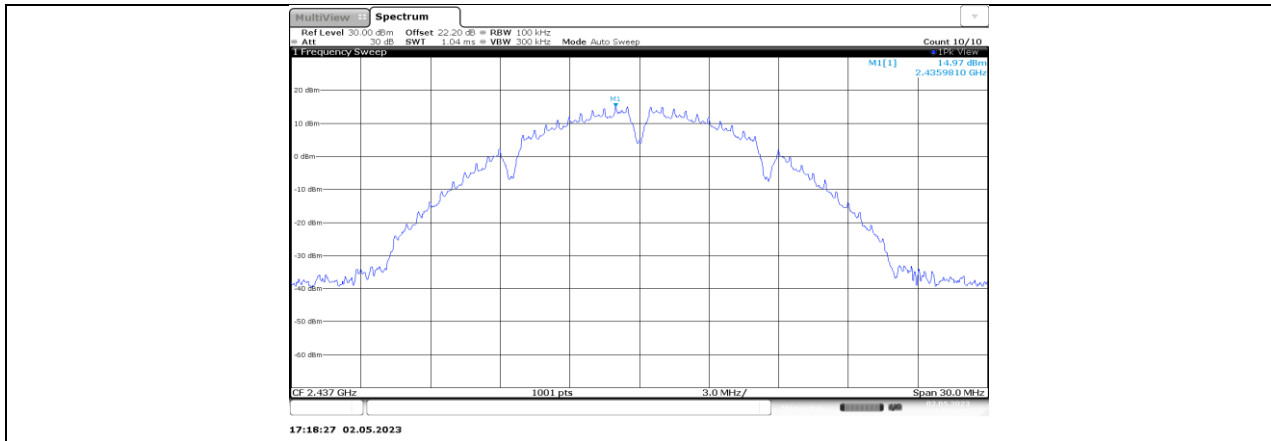


11B-CDD\_Ant5\_2417\_30~1000

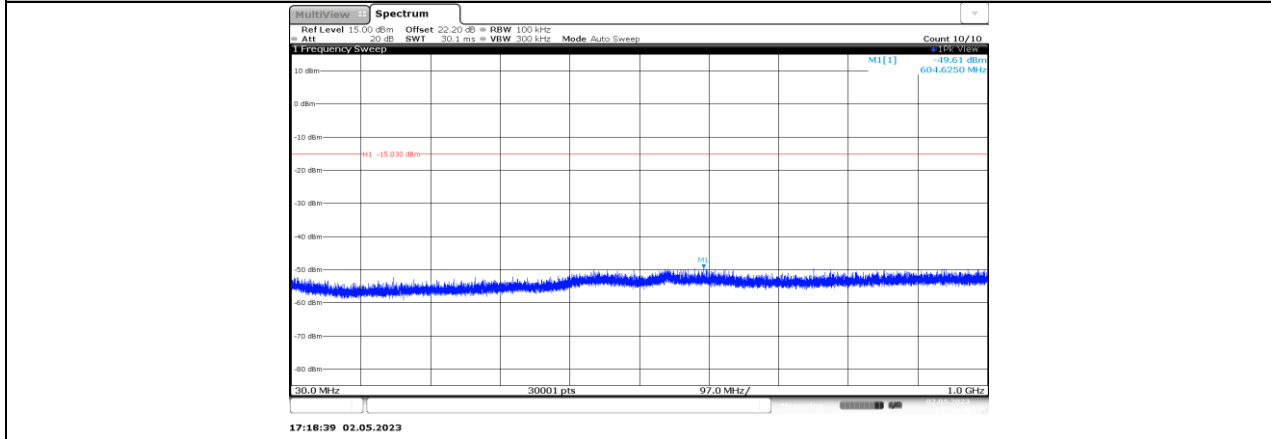


11B-CDD\_Ant5\_2417\_1000~26500

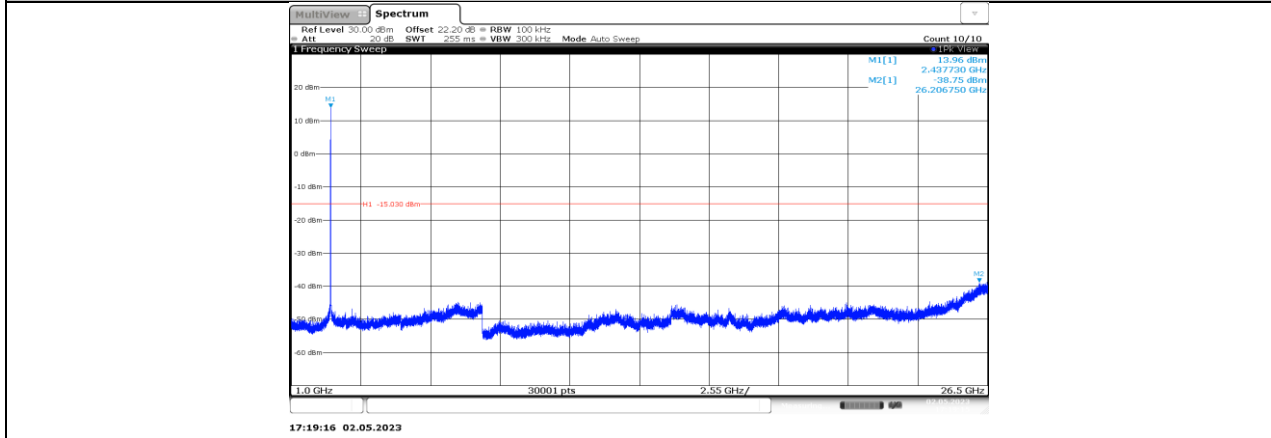




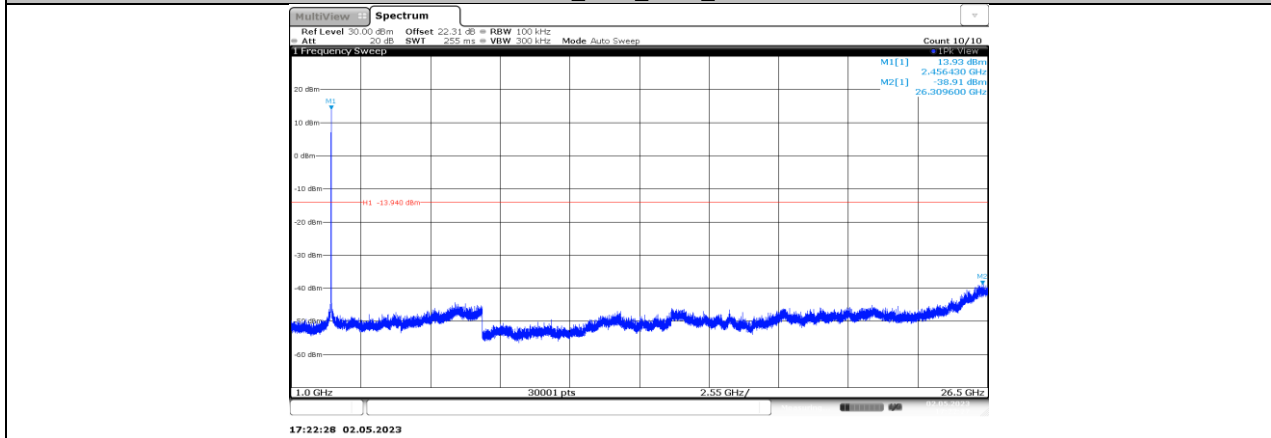
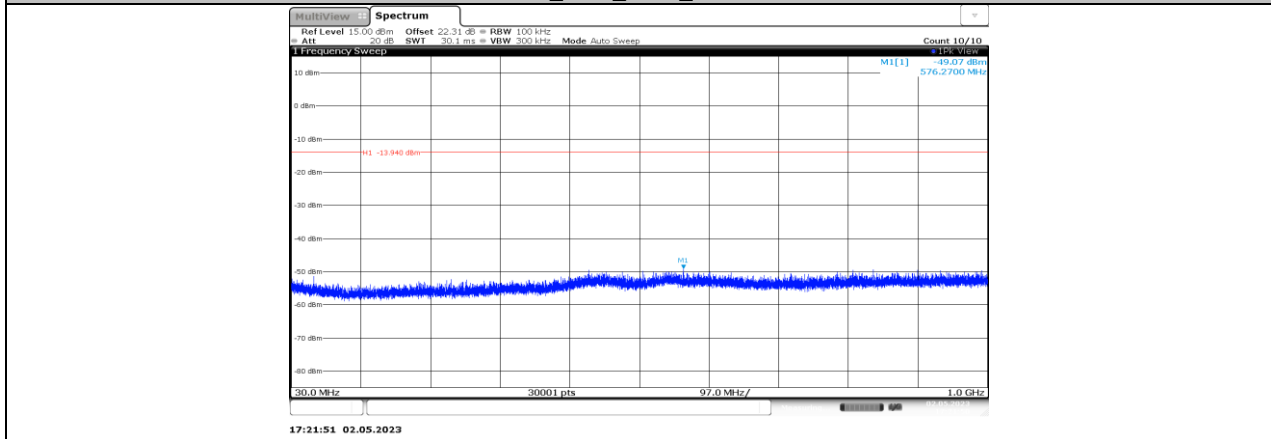
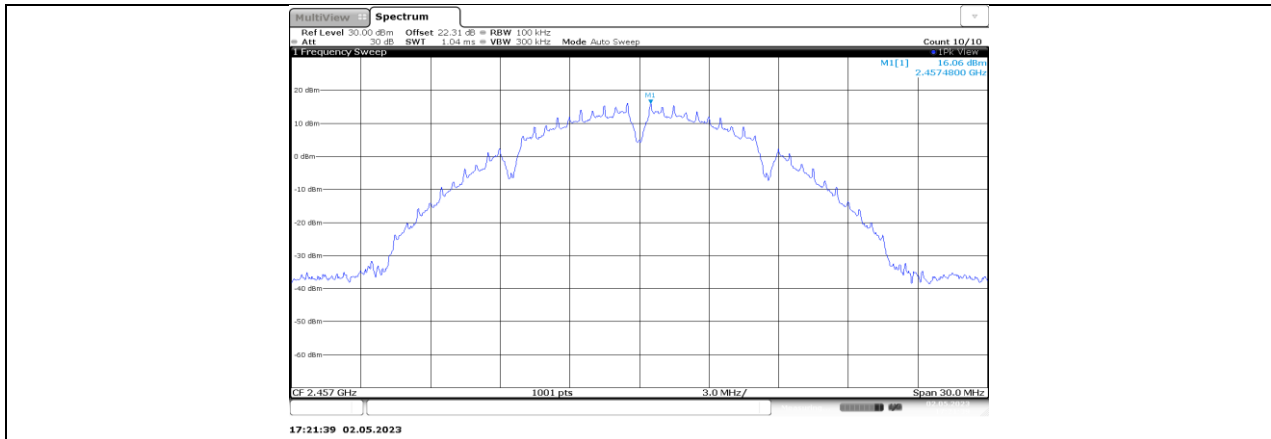
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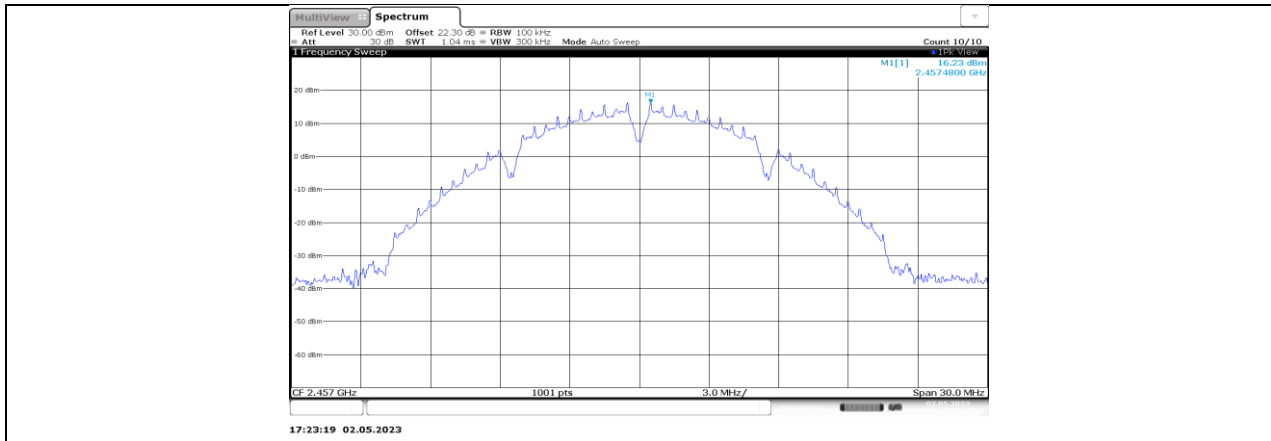


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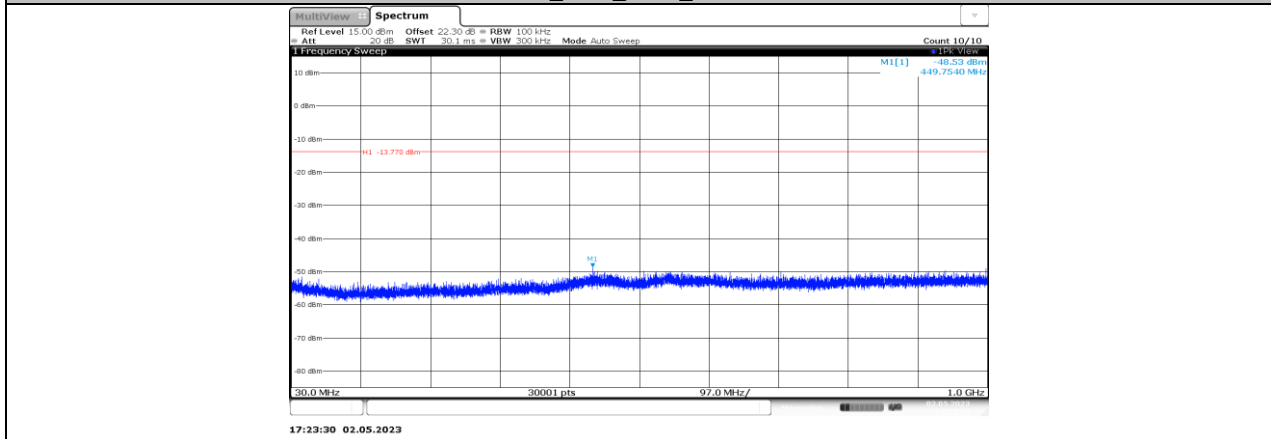


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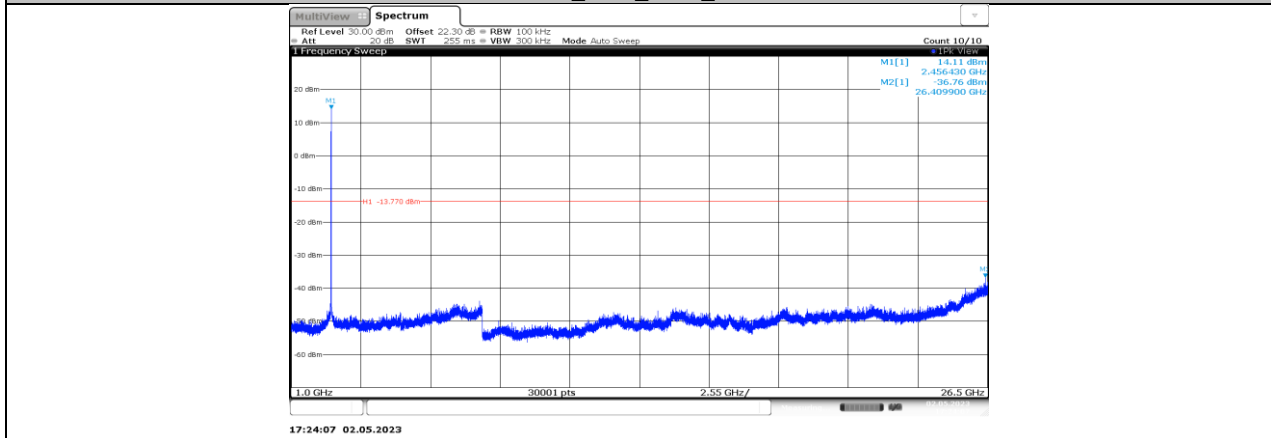




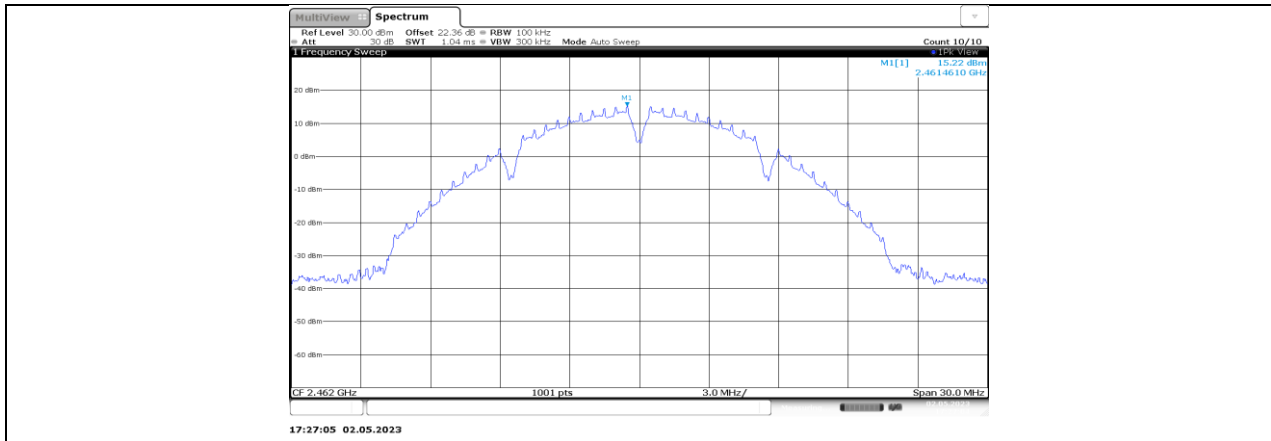
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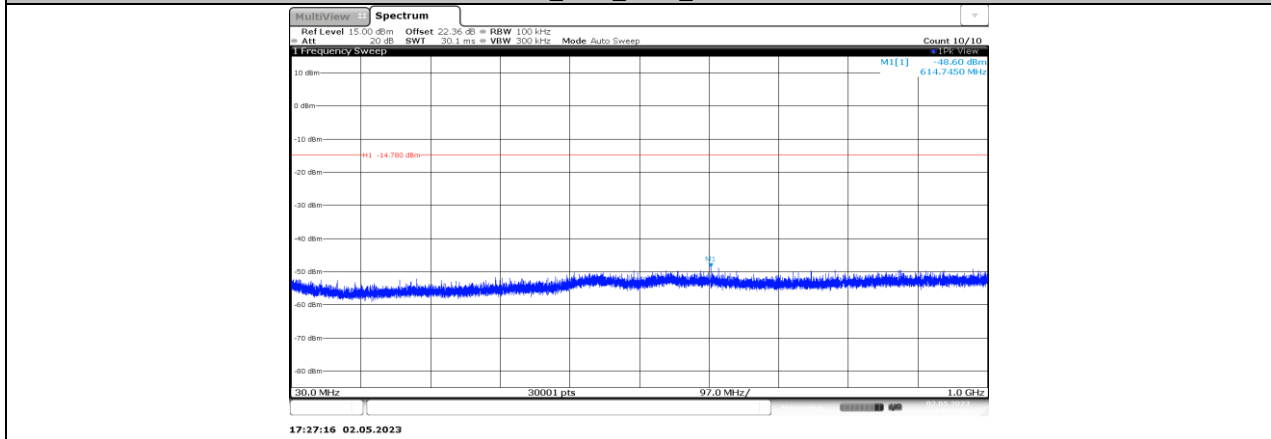
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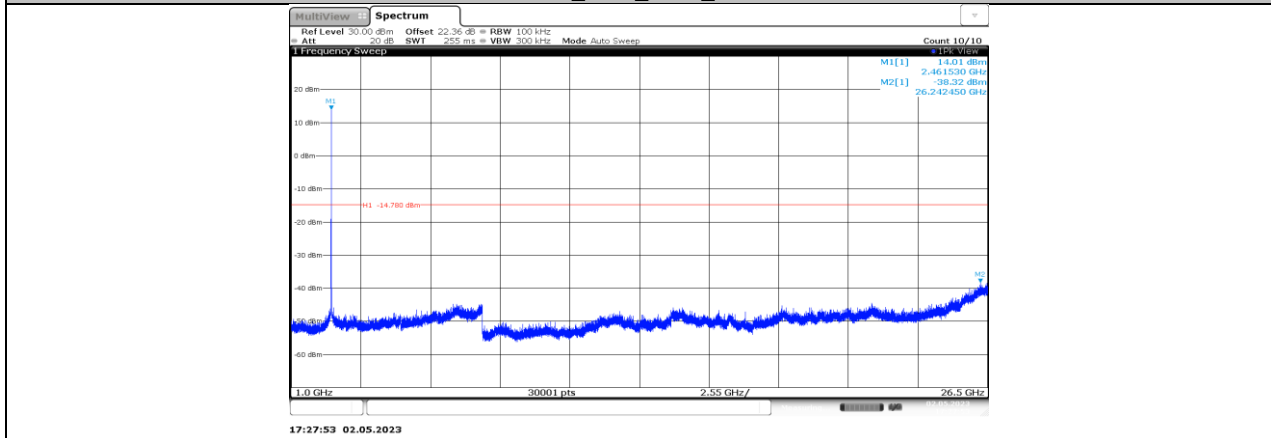
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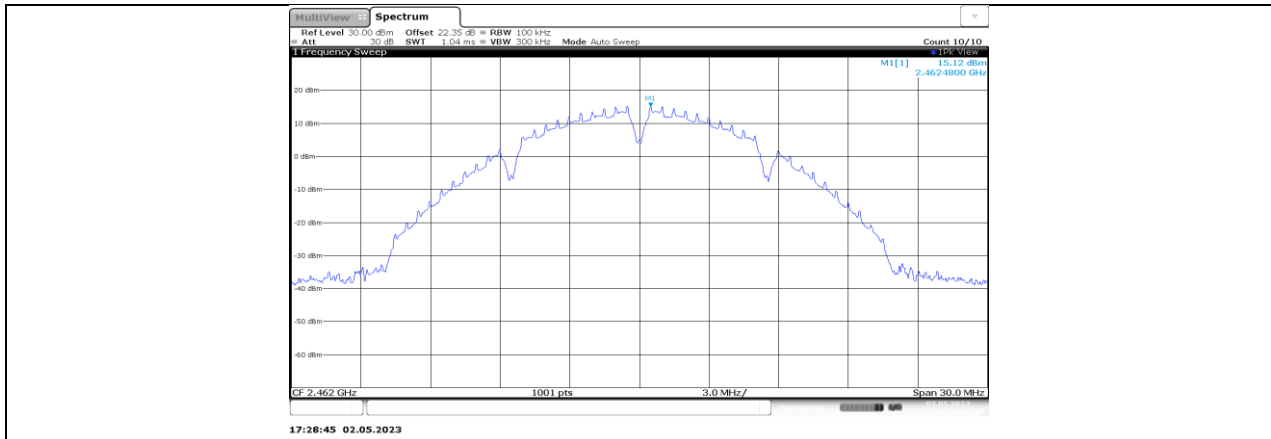
11B-CDD\_Ant2\_2462\_0~Reference



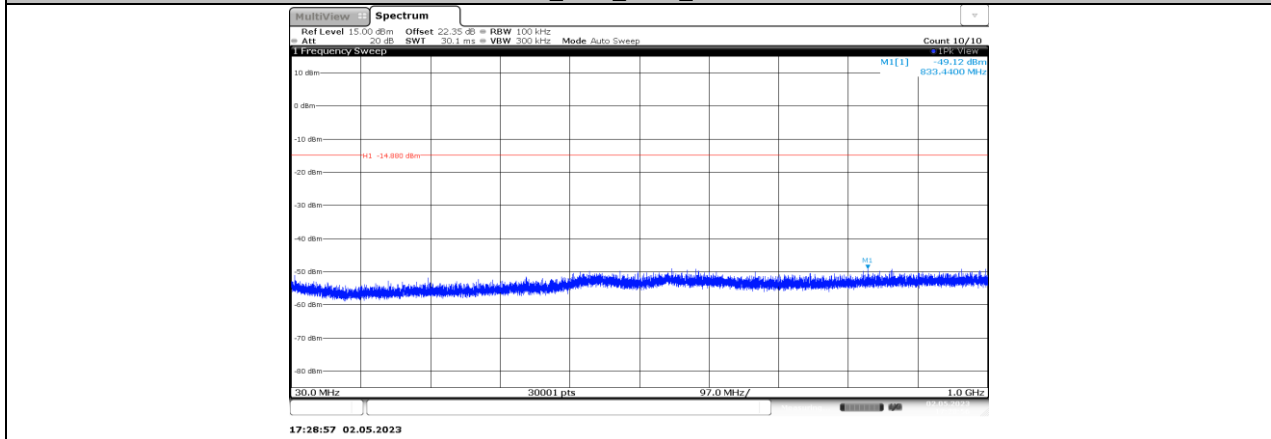
11B-CDD\_Ant2\_2462\_30~1000



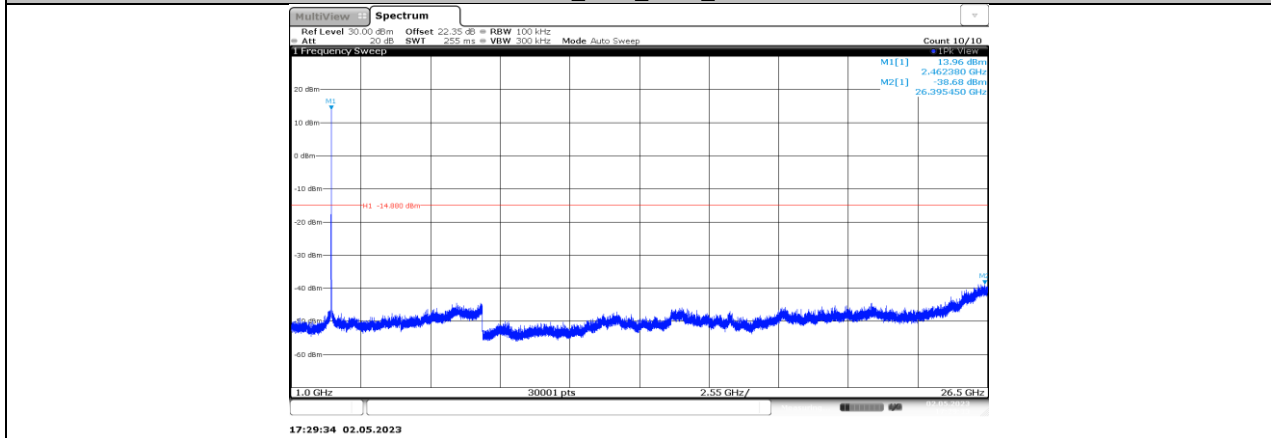
11B-CDD\_Ant2\_2462\_1000~26500



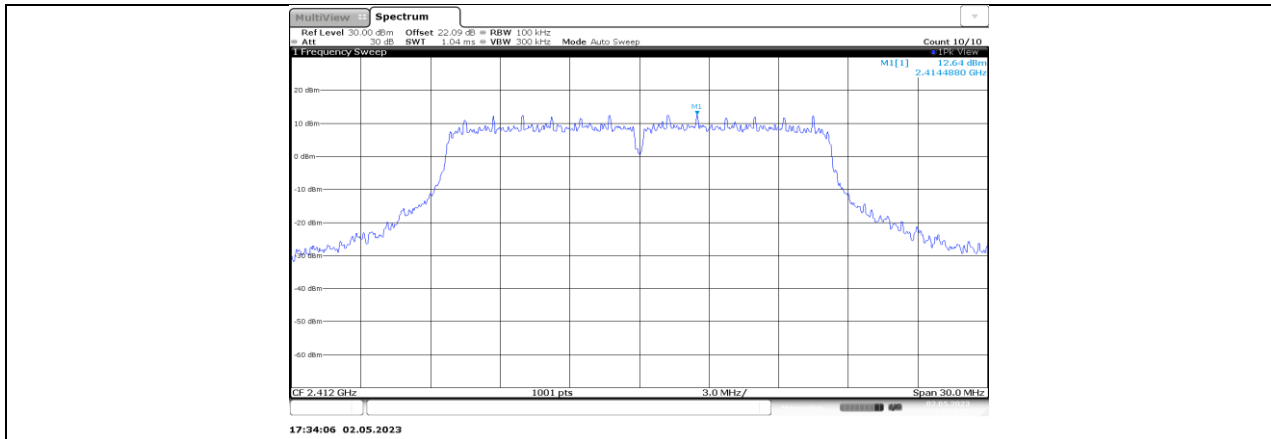
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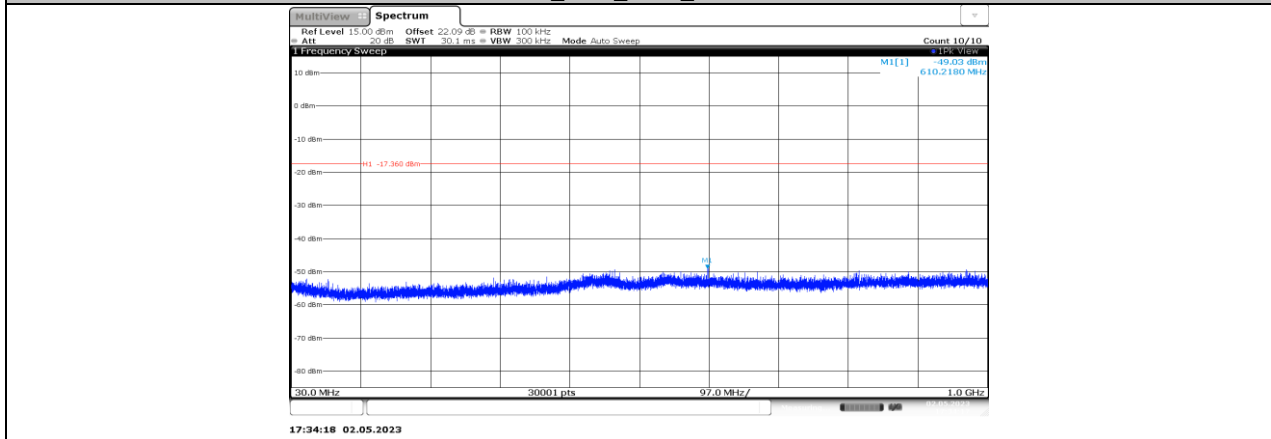
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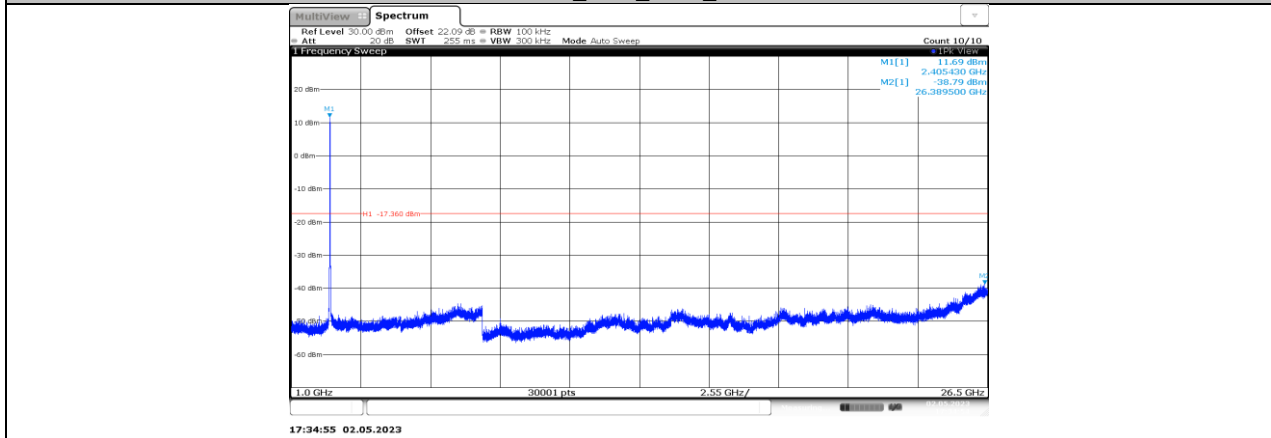
11B-CDD\_Ant5\_2462\_1000~26500



11G-CDD\_Ant2\_2412\_0~Reference

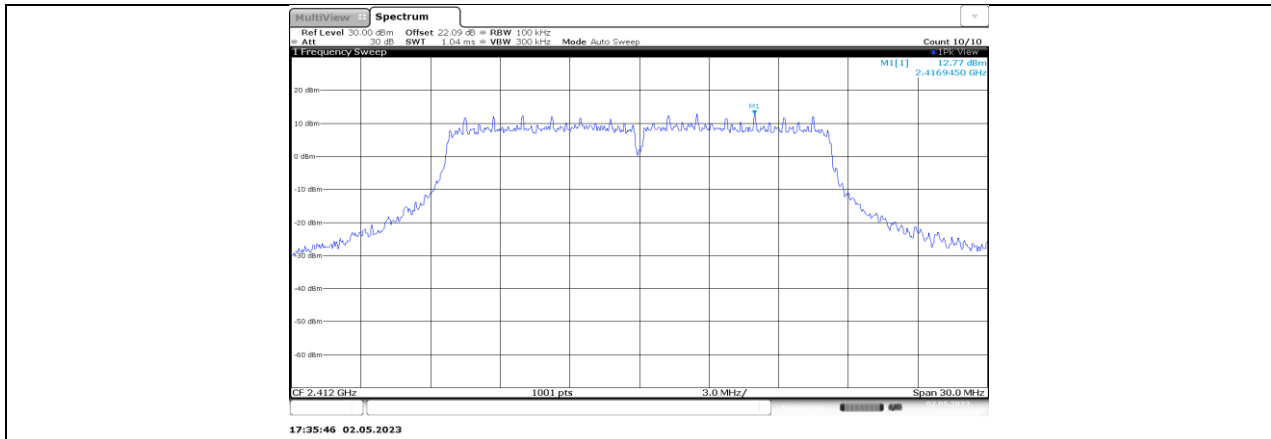


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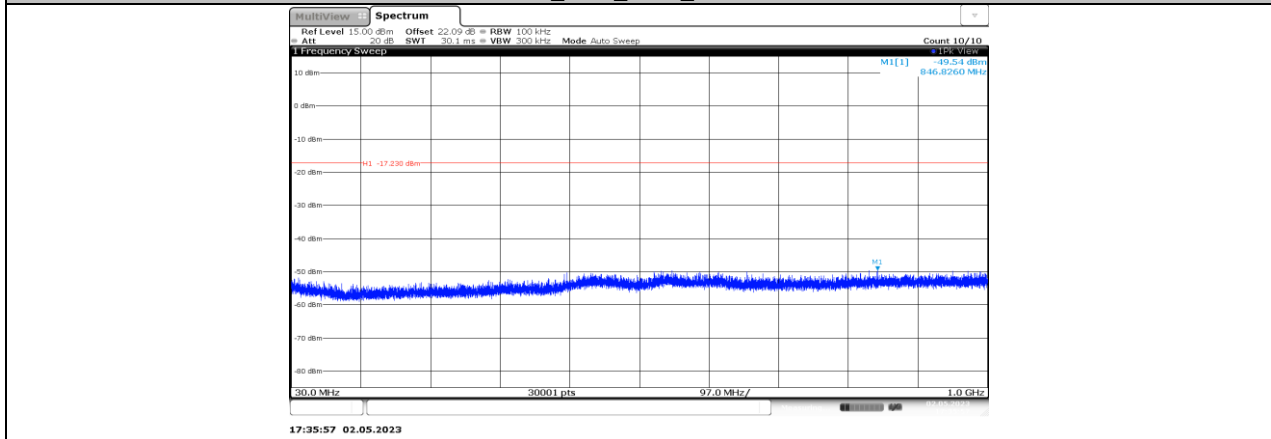


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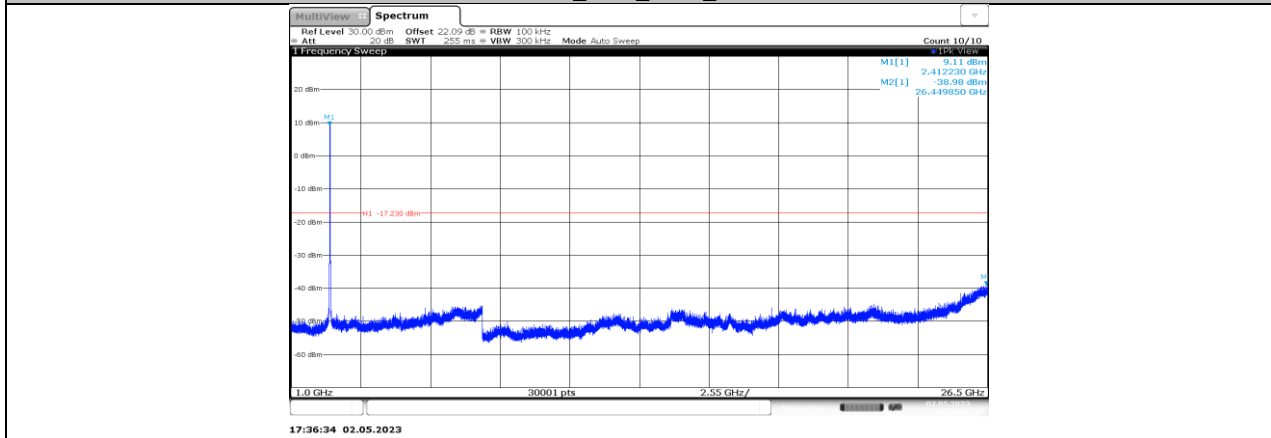




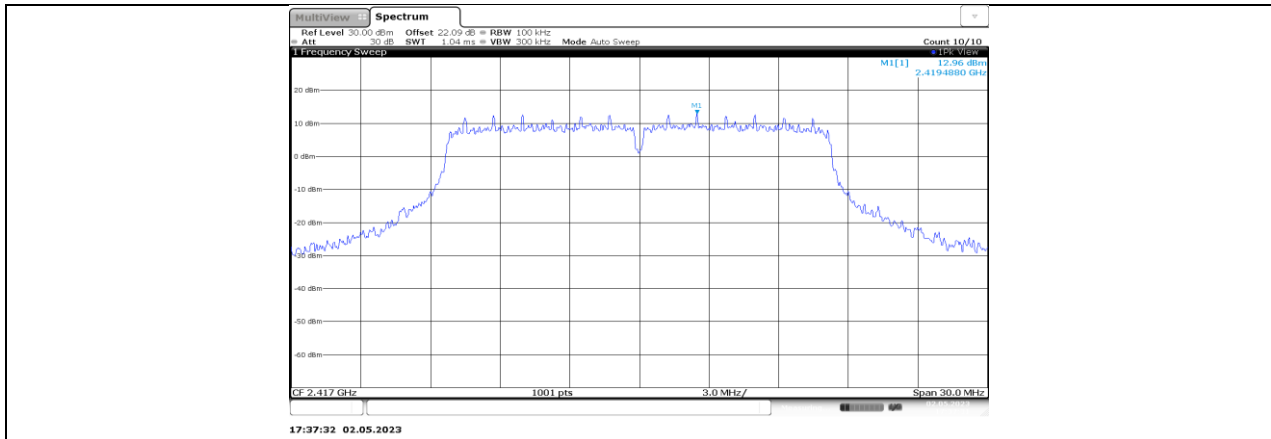
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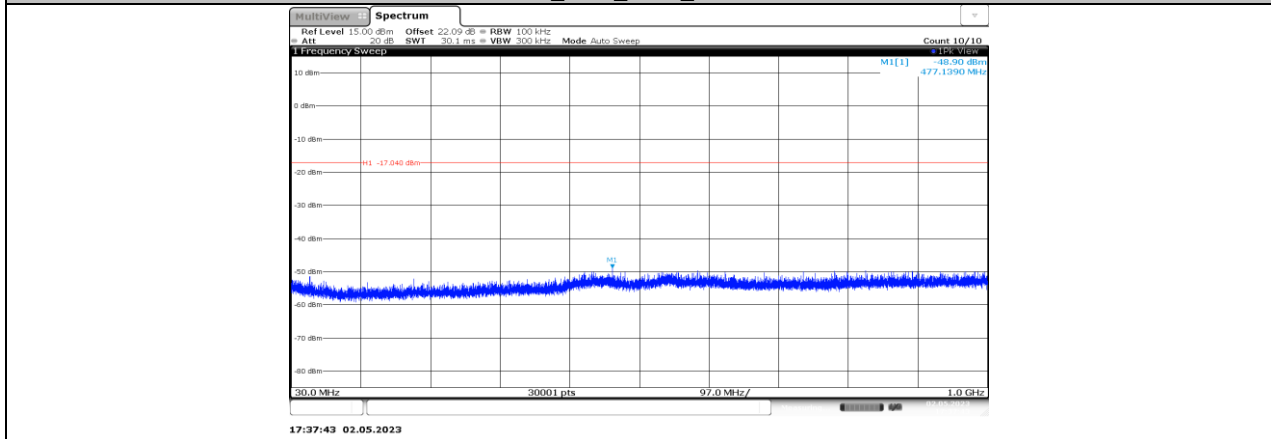
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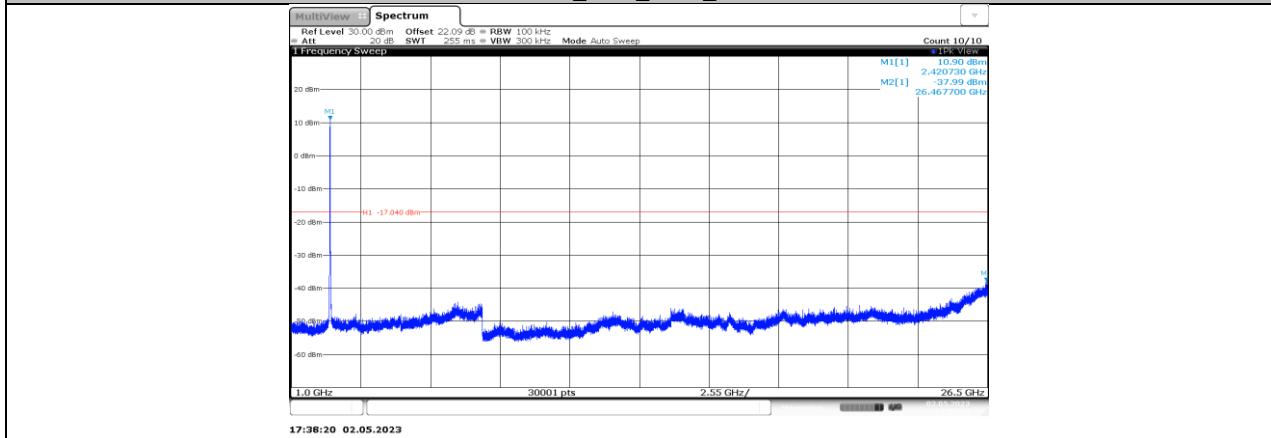
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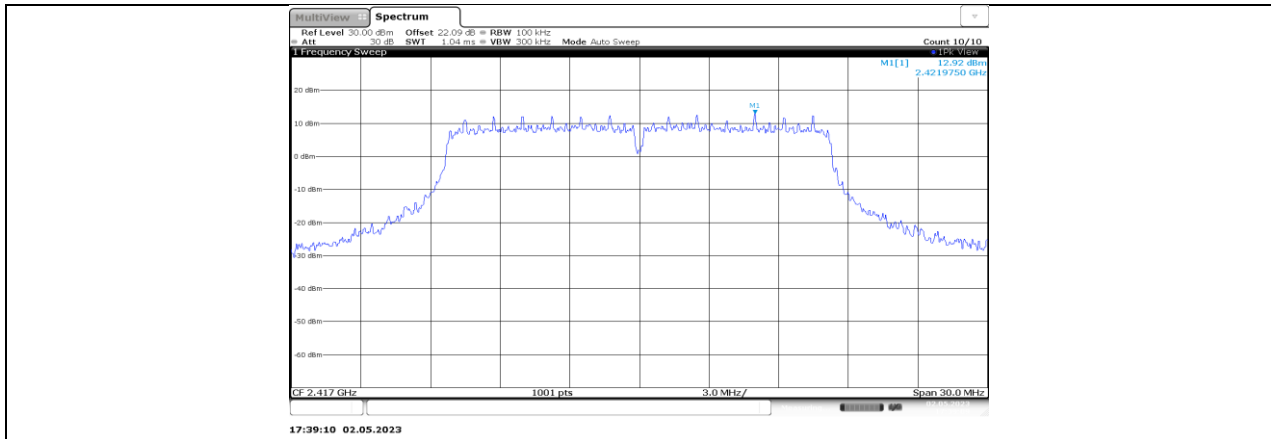
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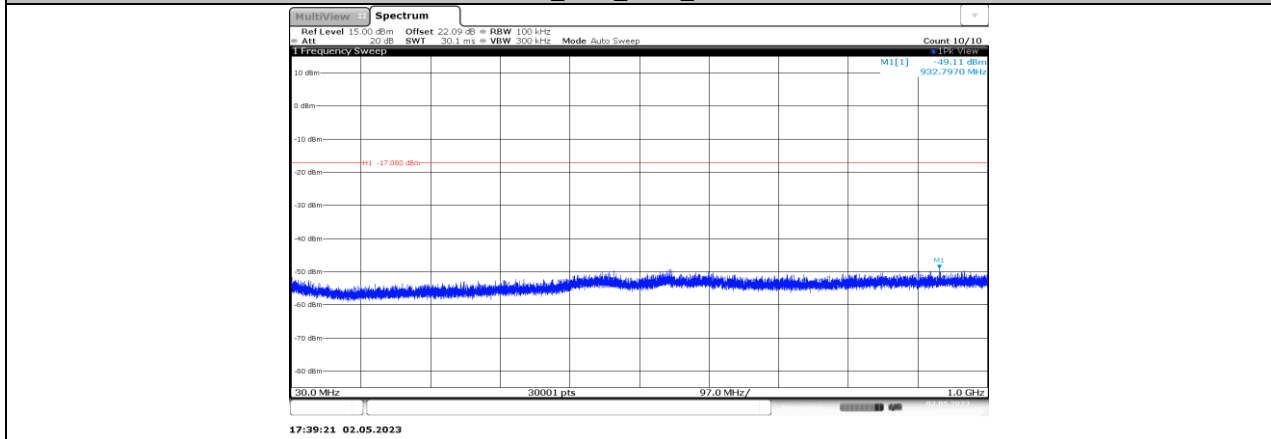
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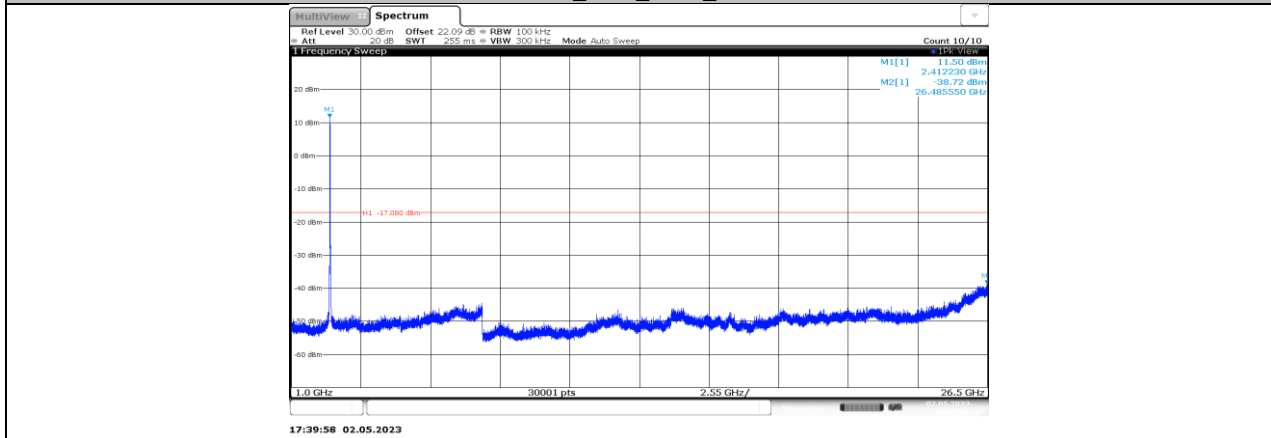
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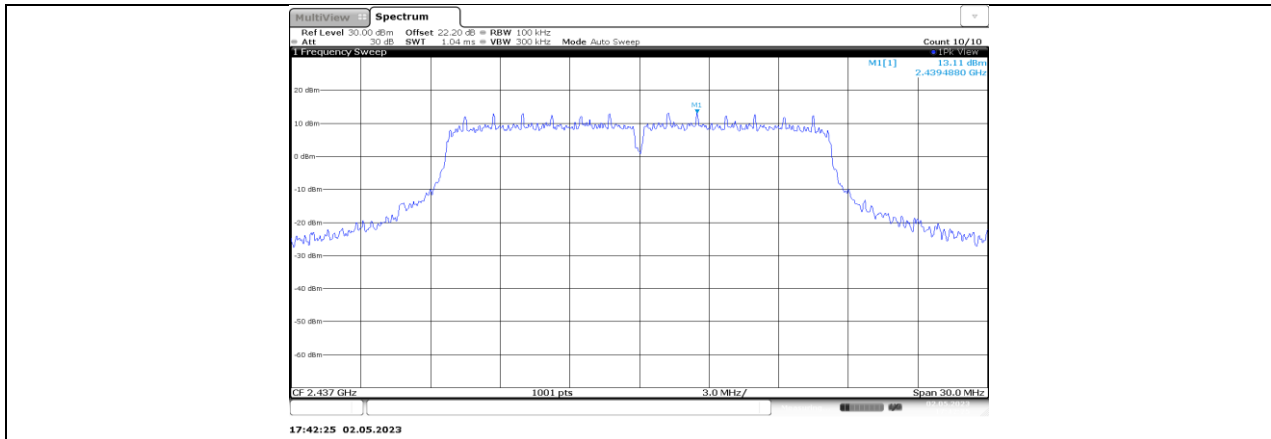
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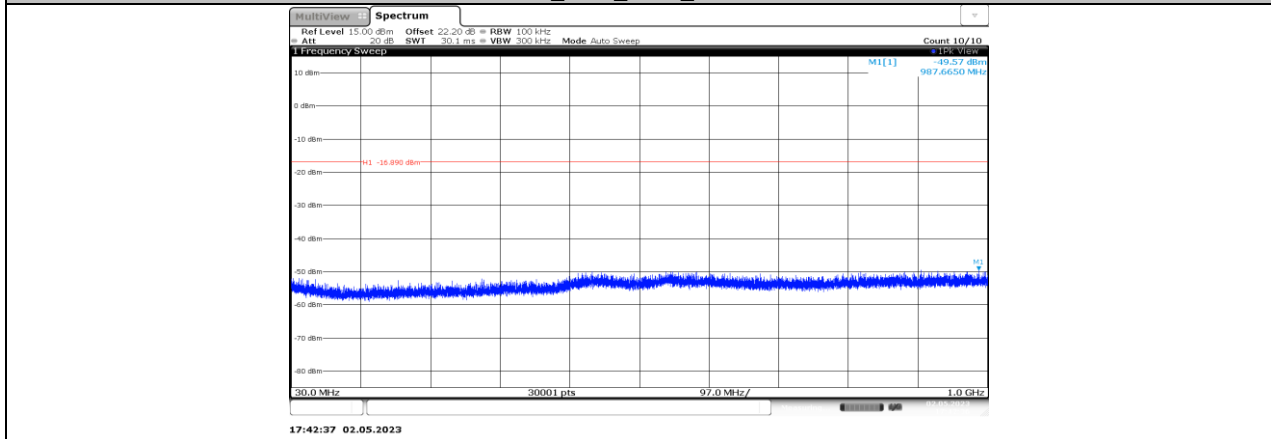
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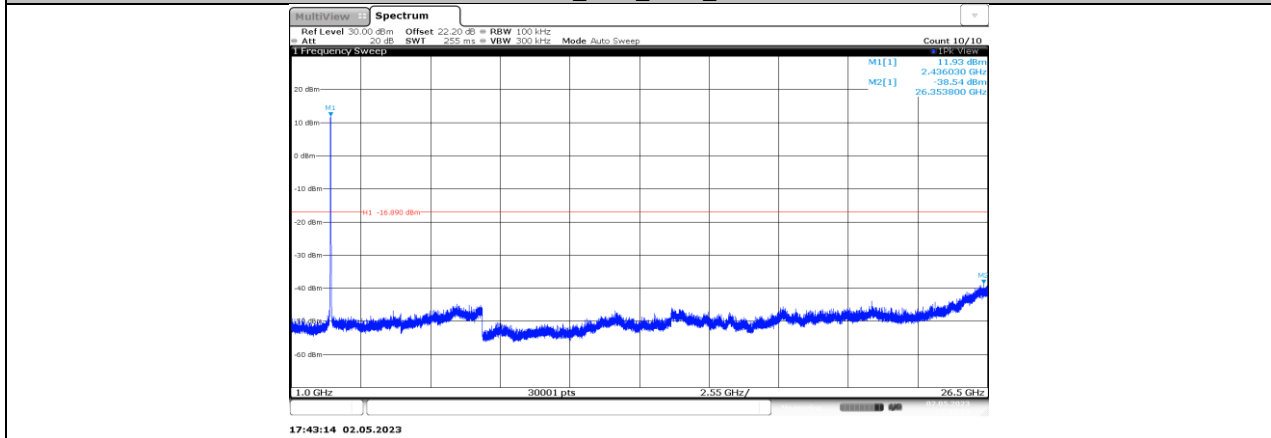
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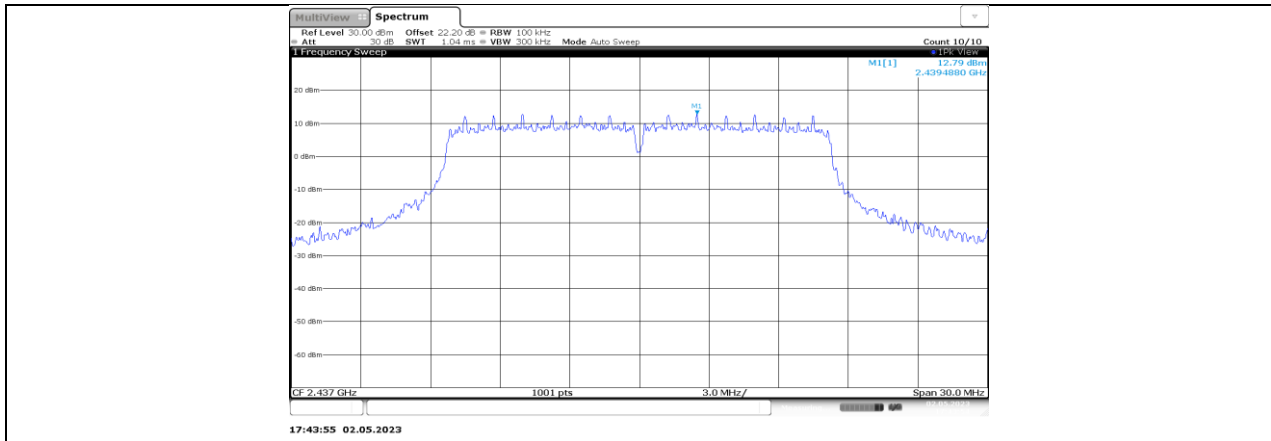
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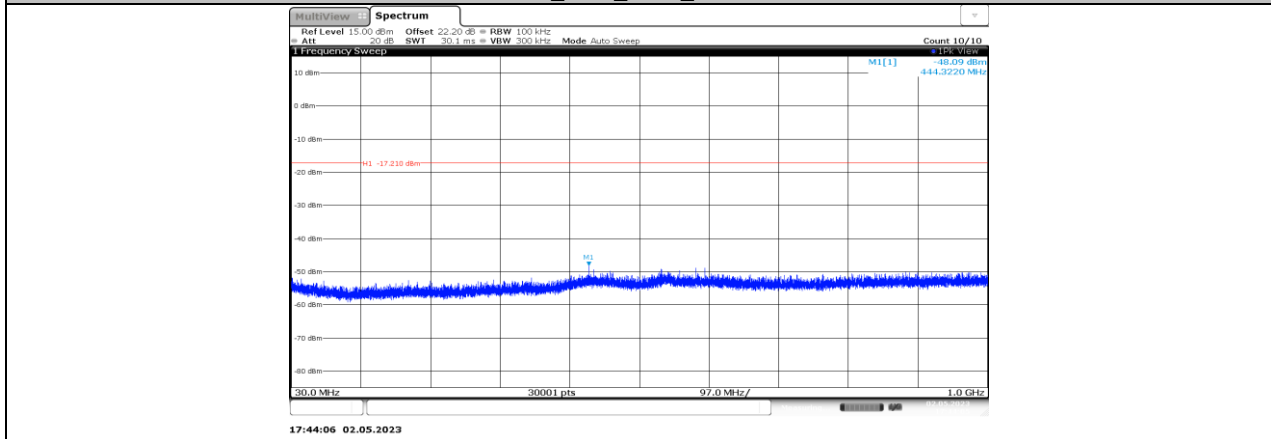
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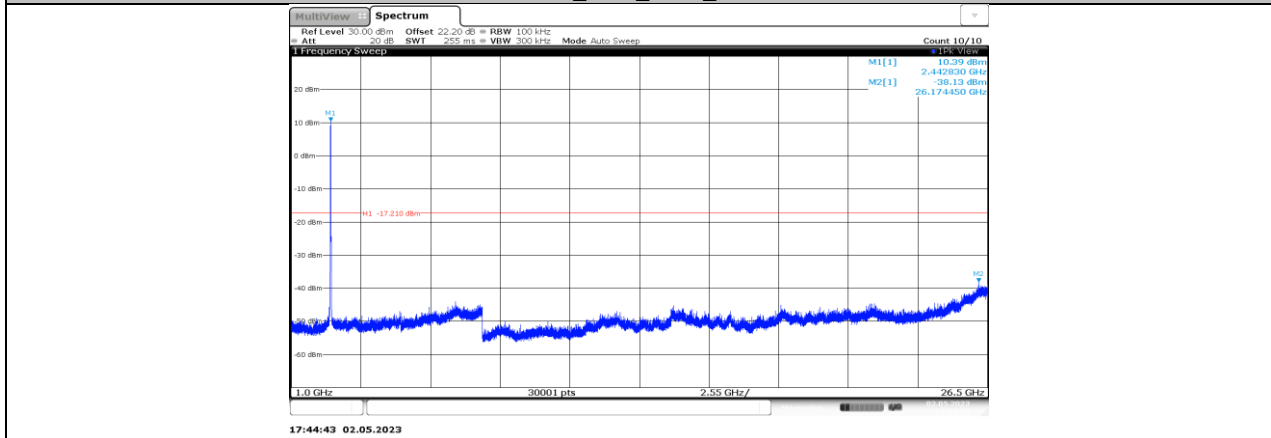
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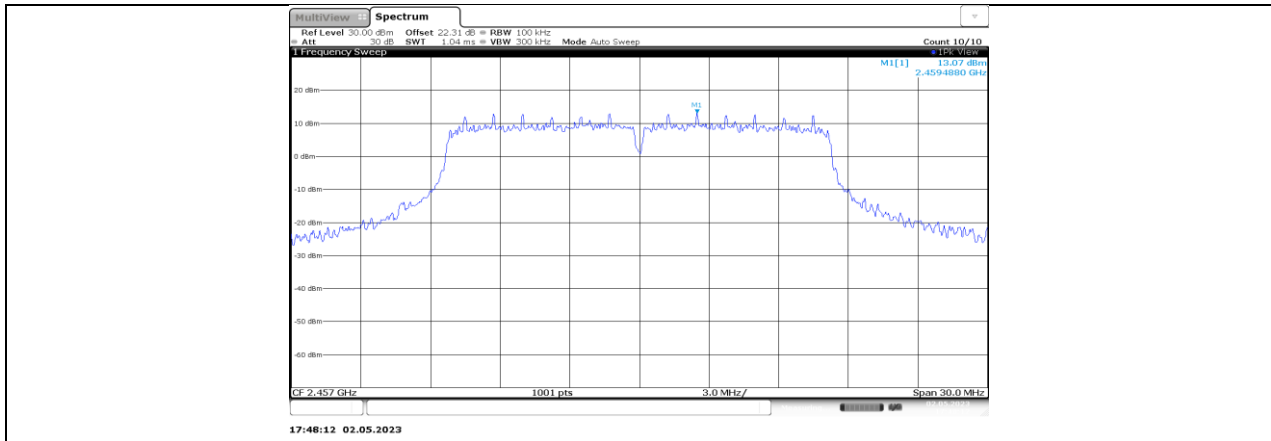
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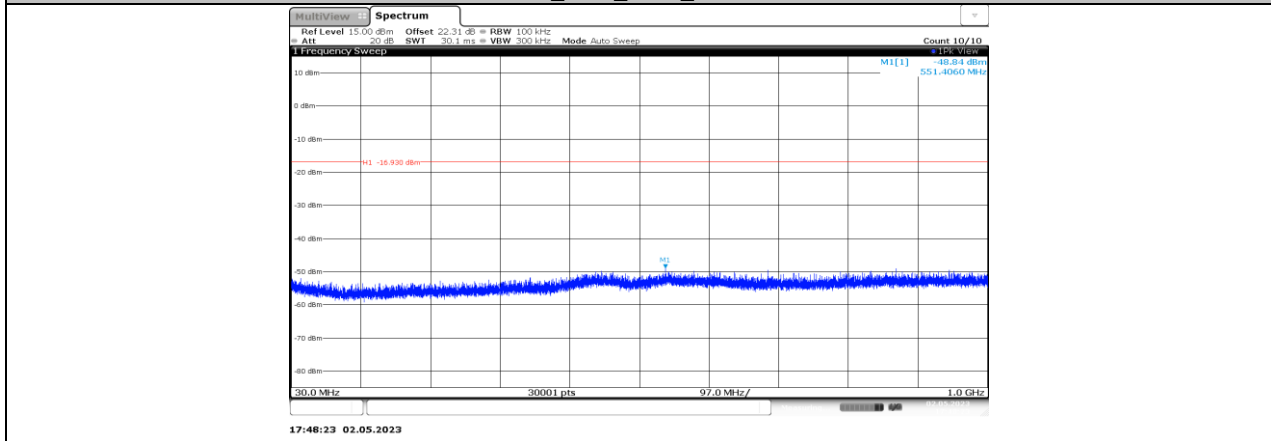
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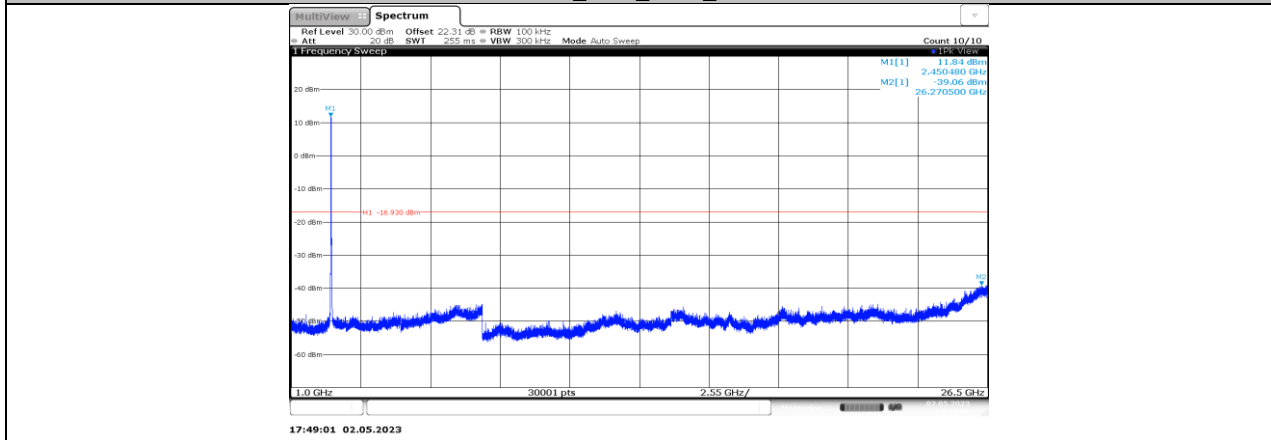
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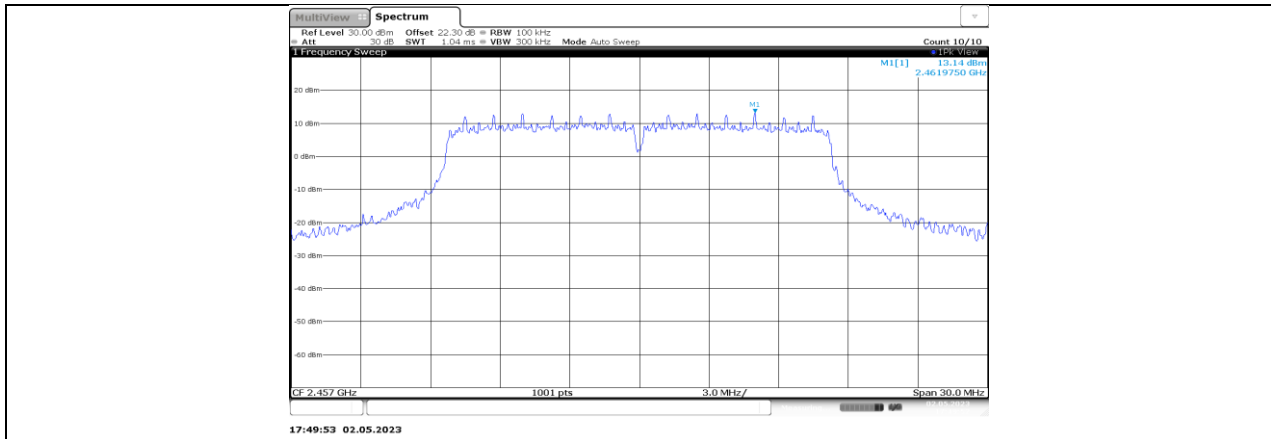
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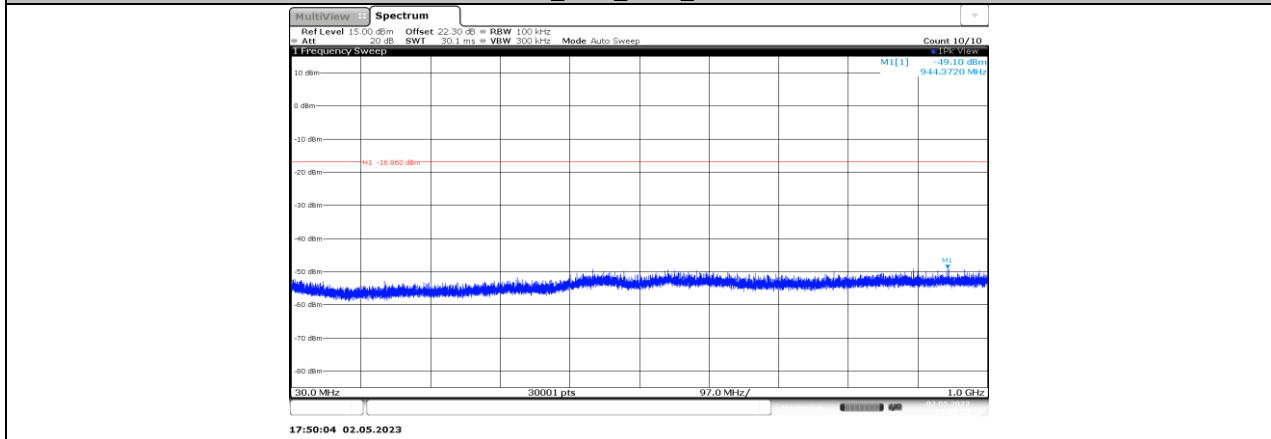
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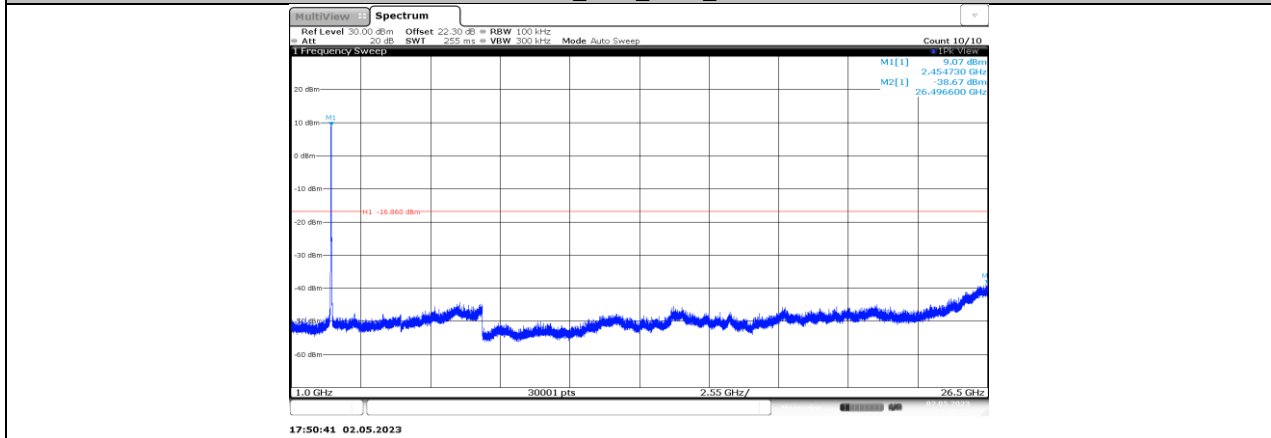
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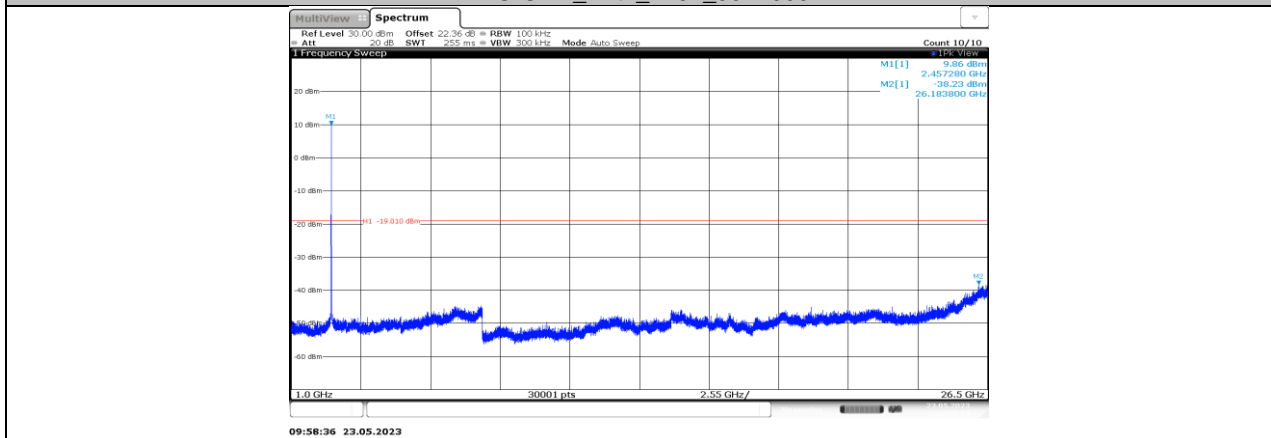
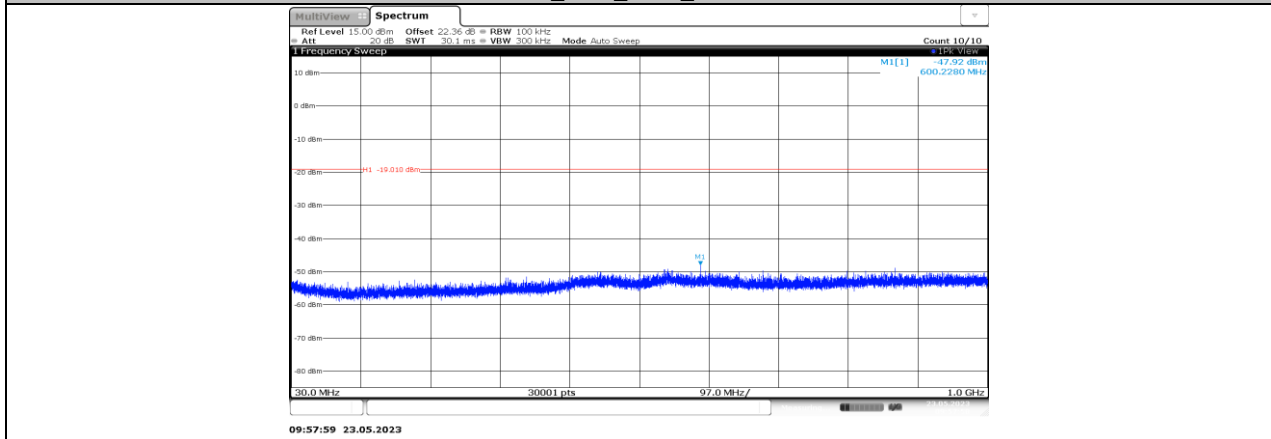
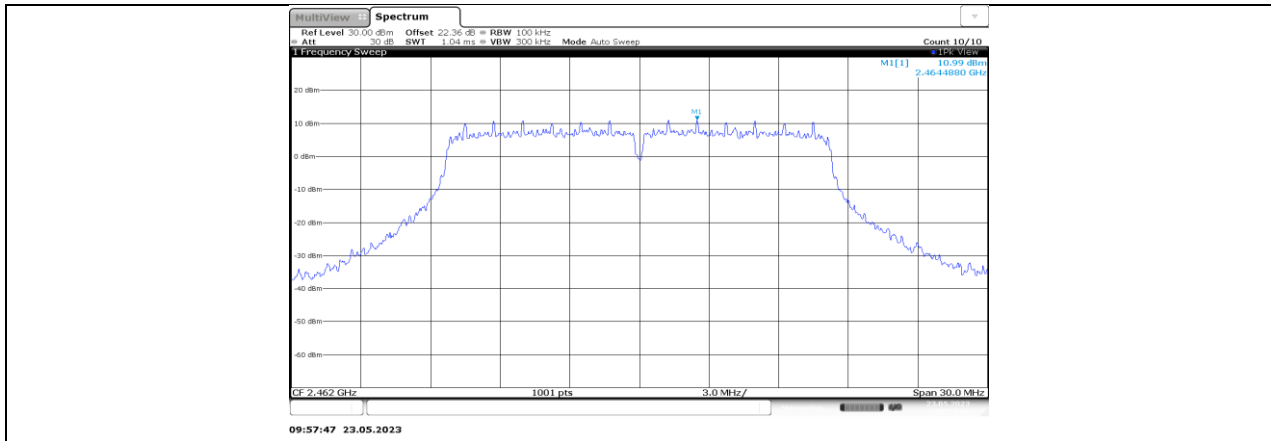
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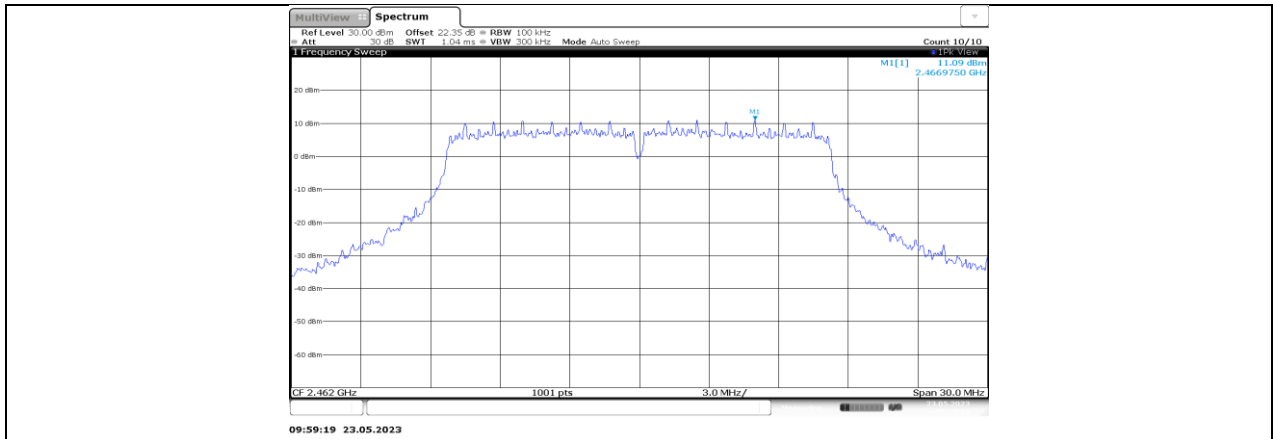
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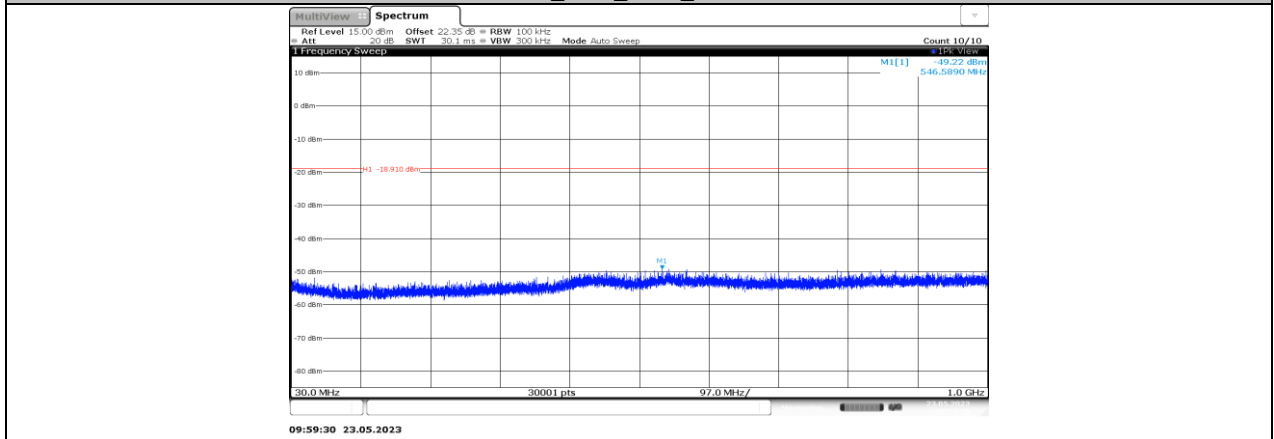
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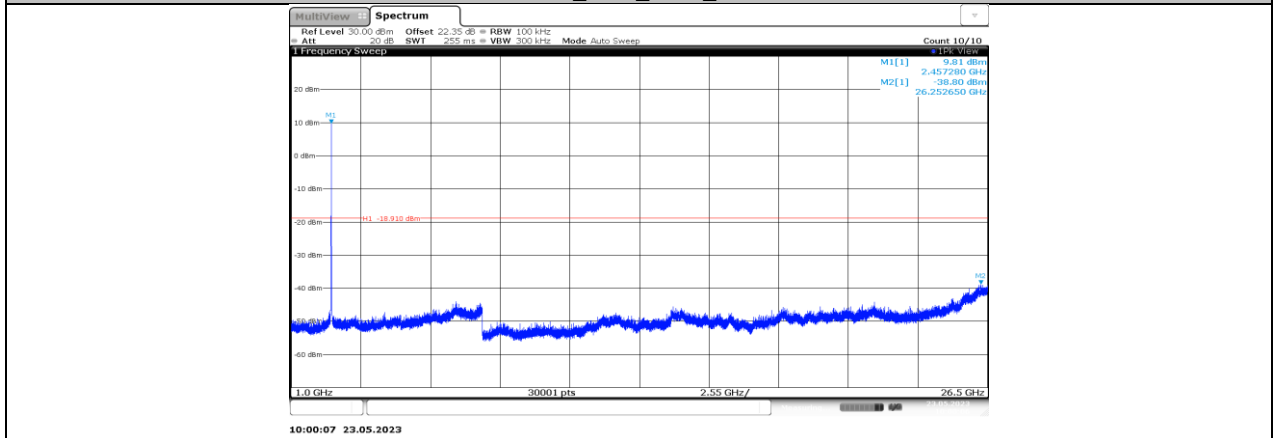




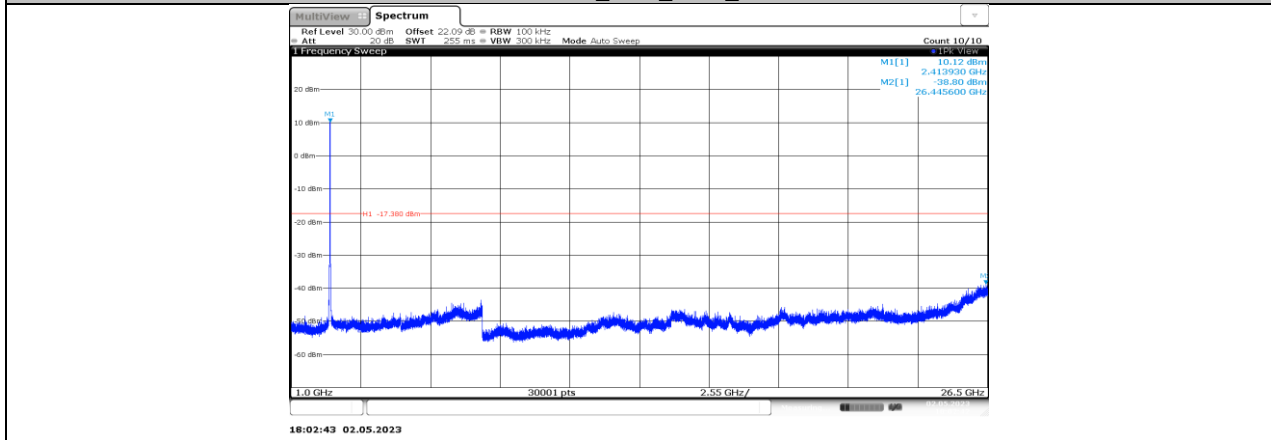
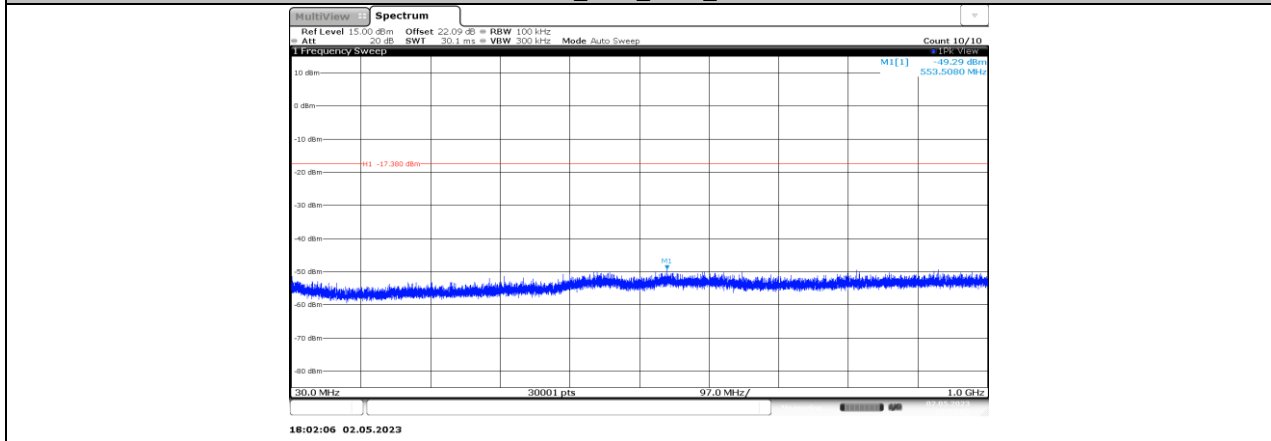
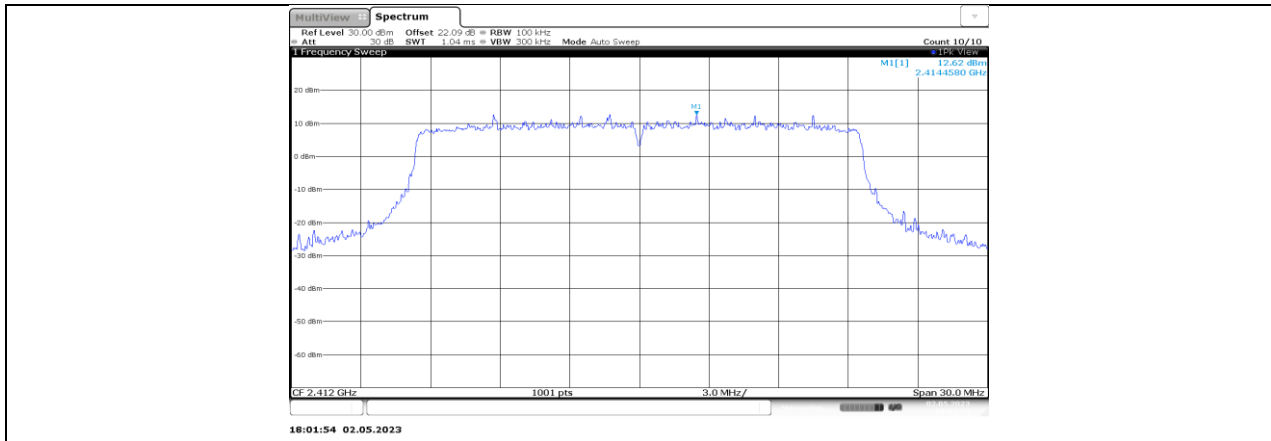
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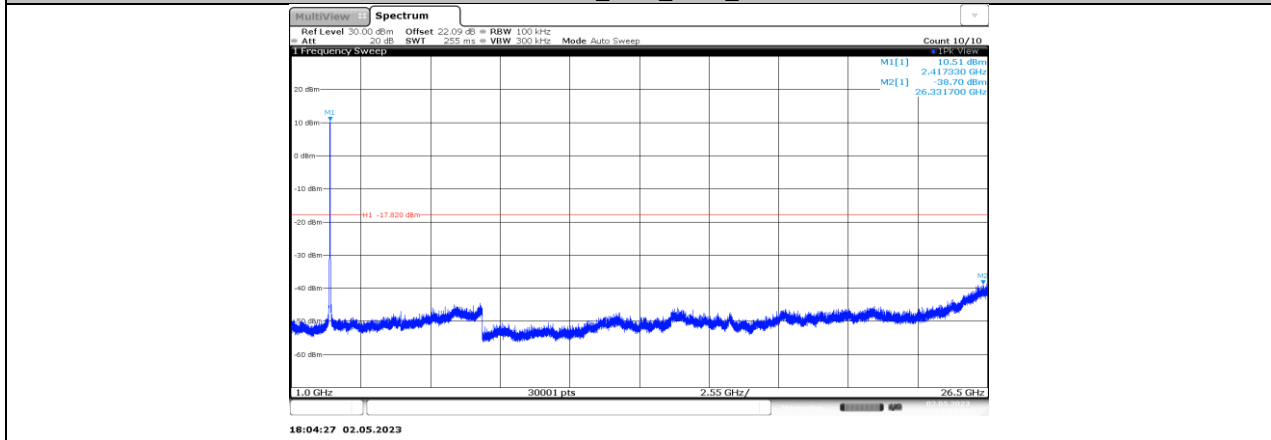
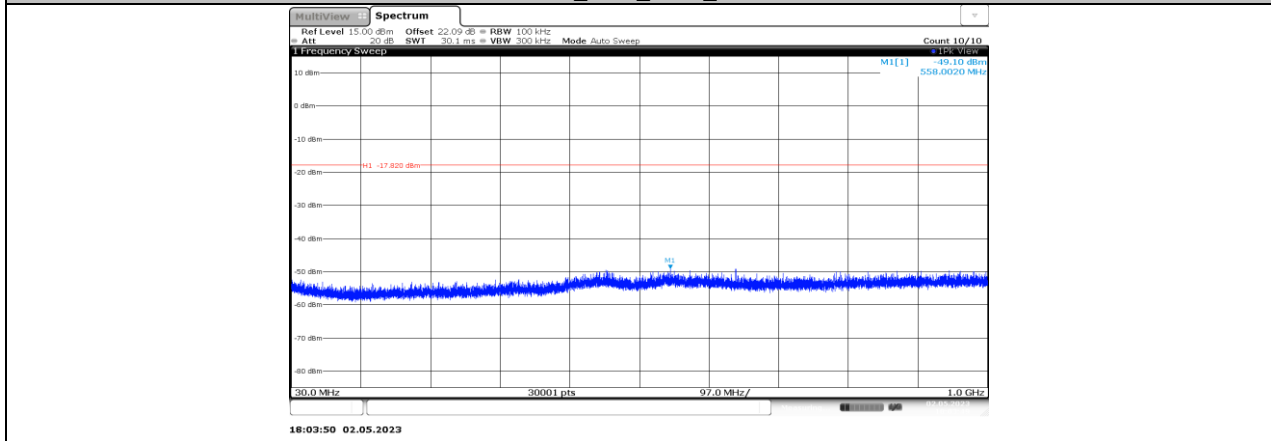
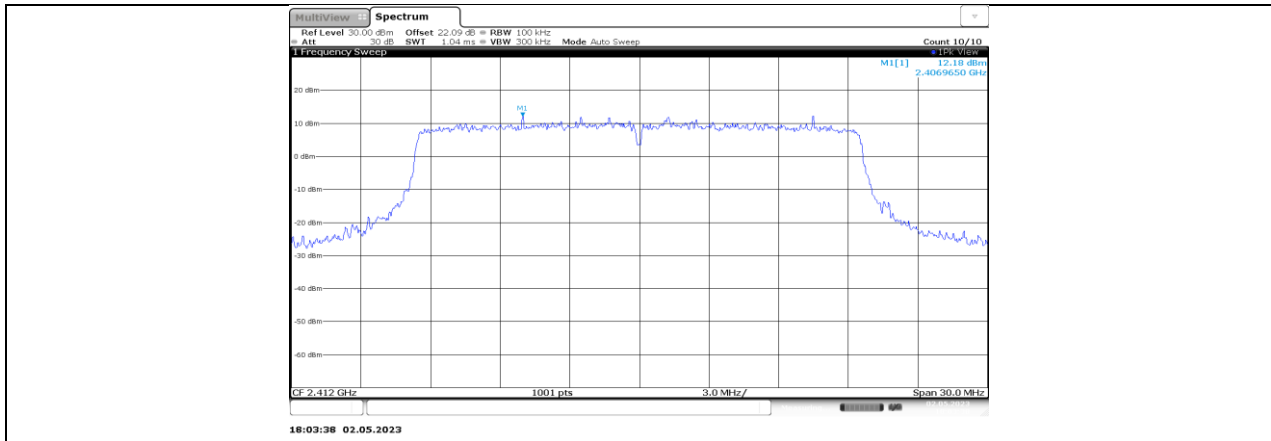


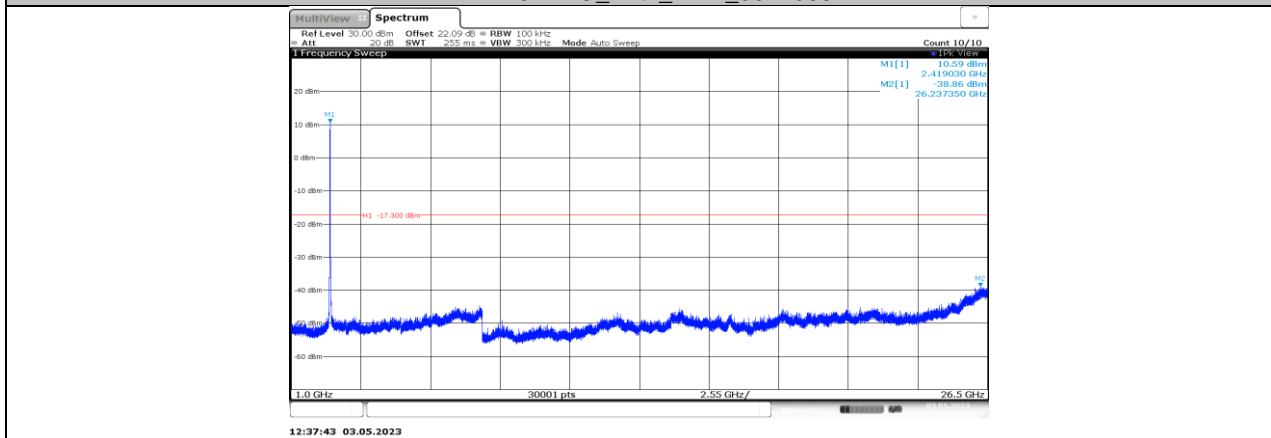
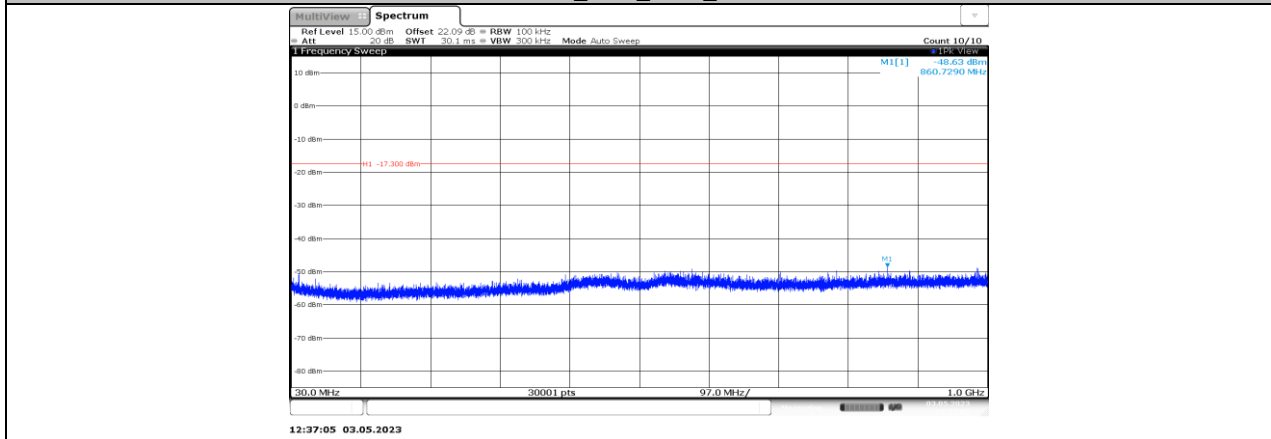
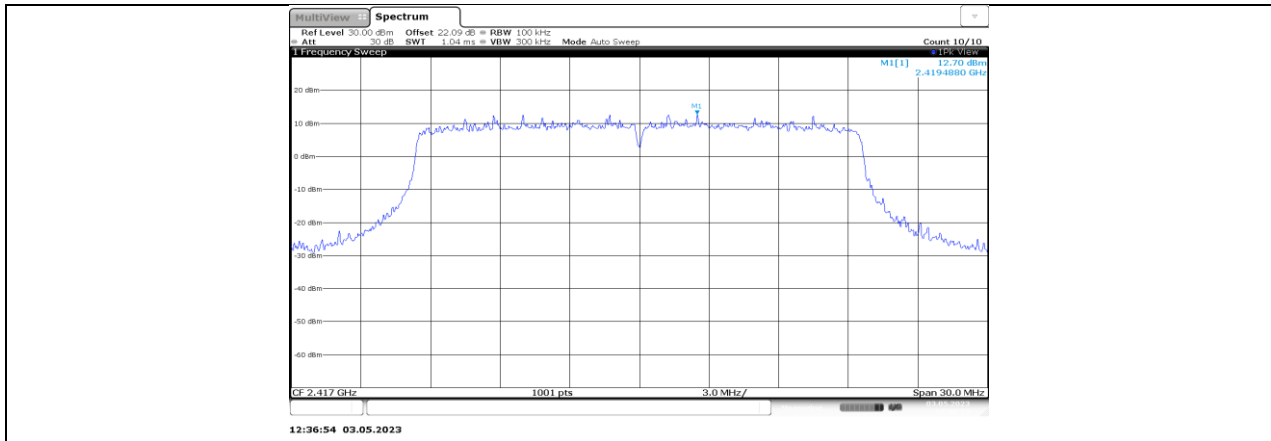
11G-CDD\_Ant5\_2462\_30~1000

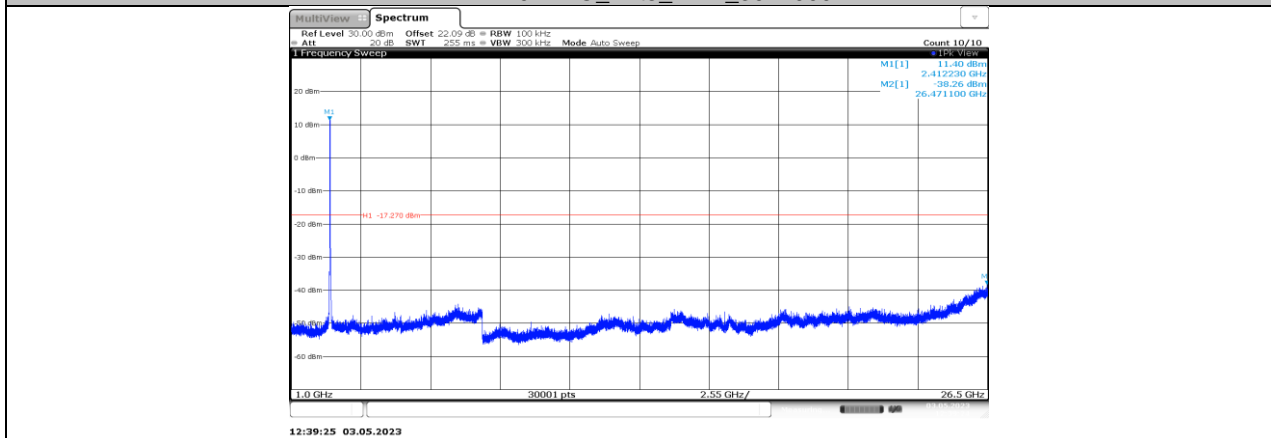
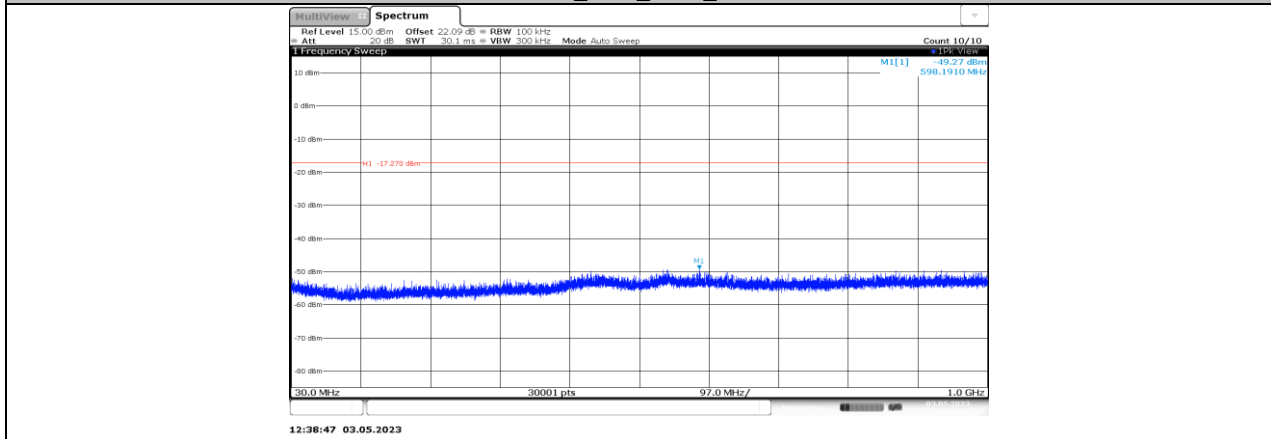
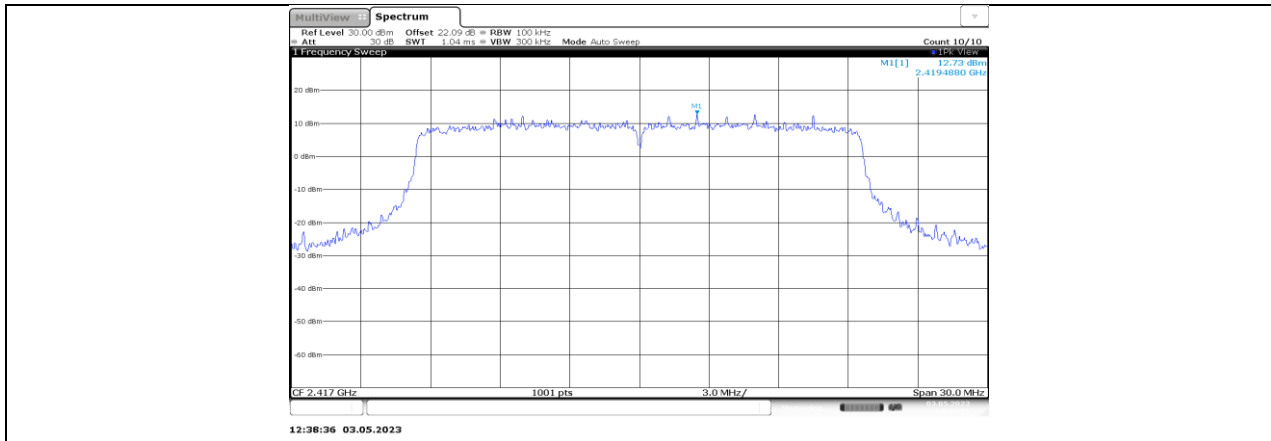


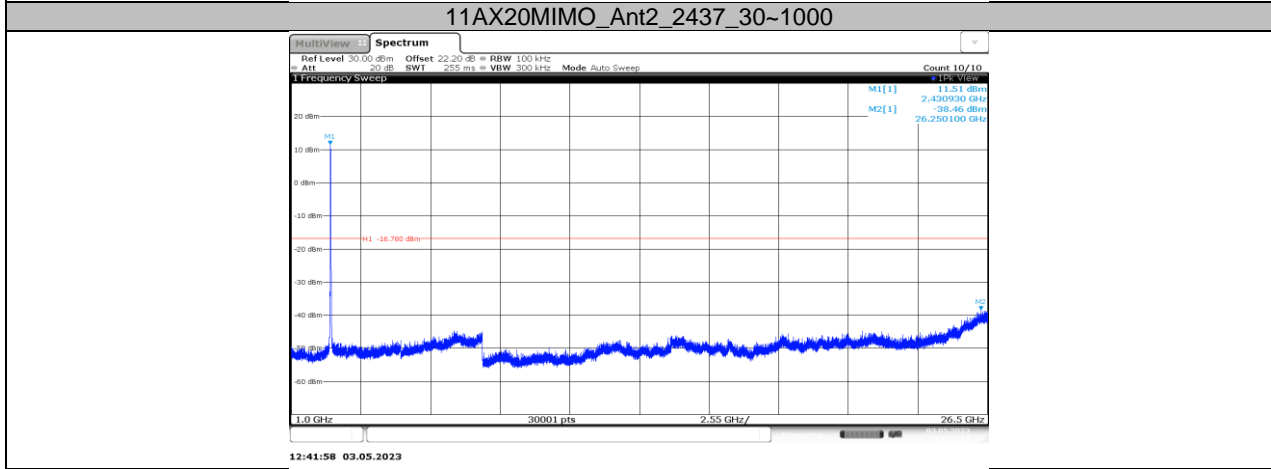
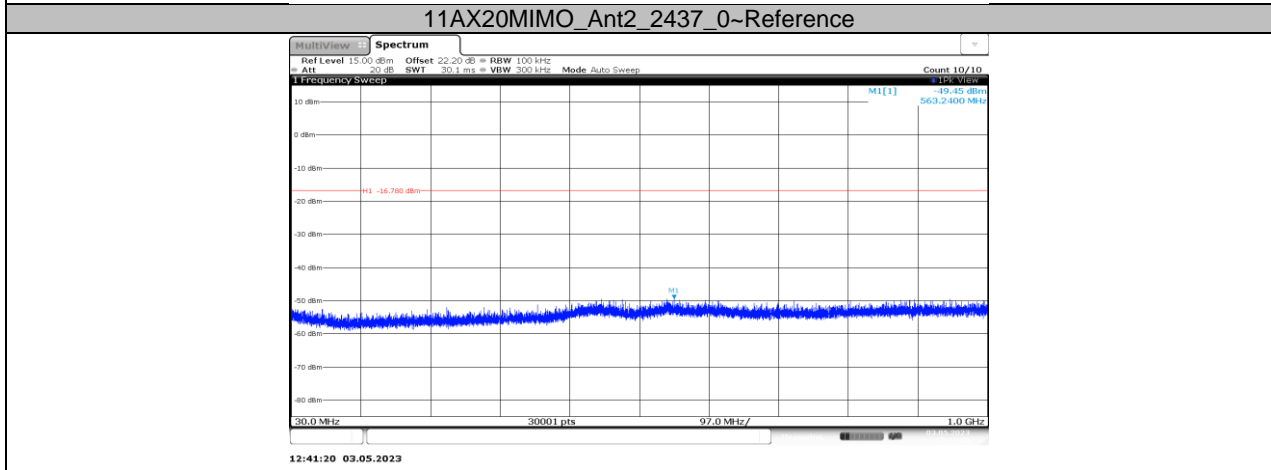
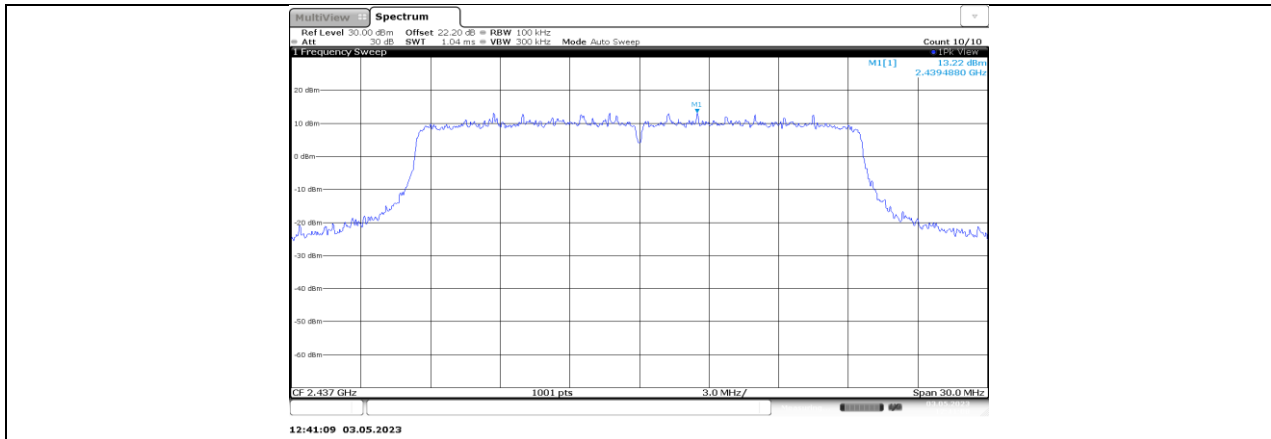
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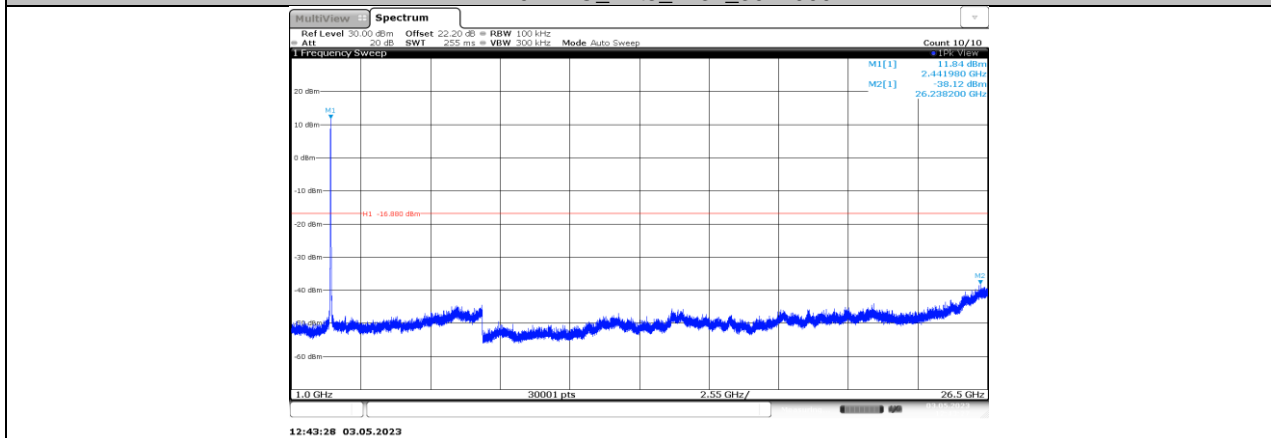
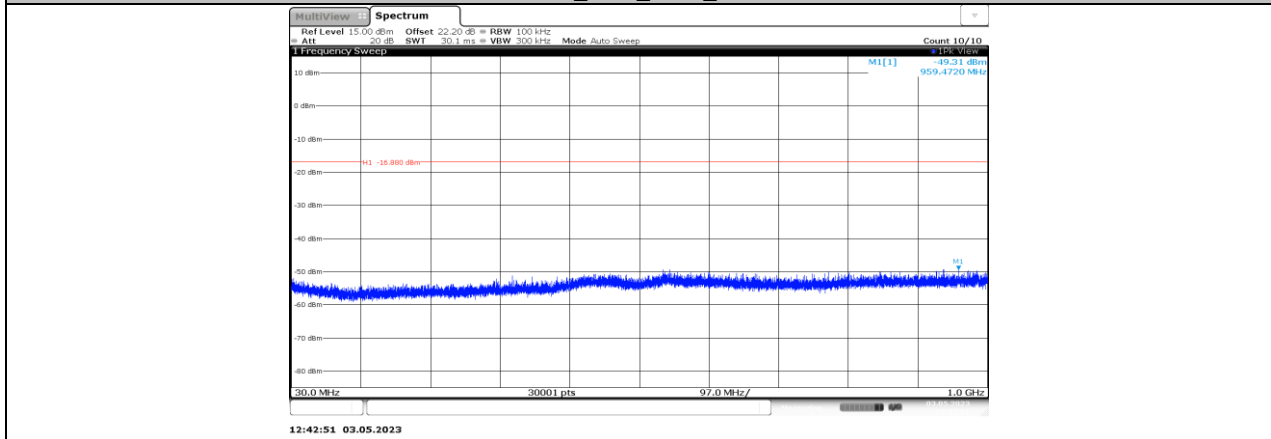
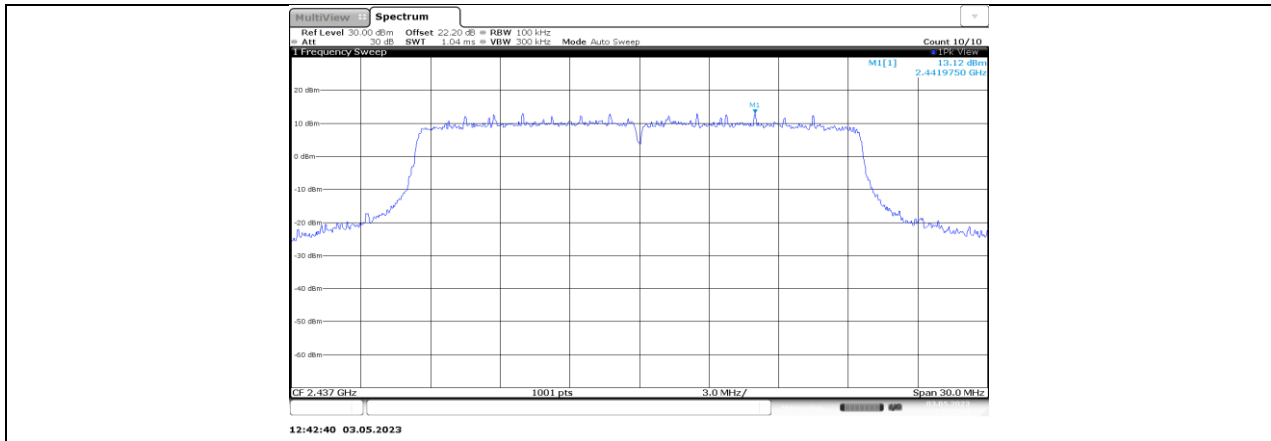


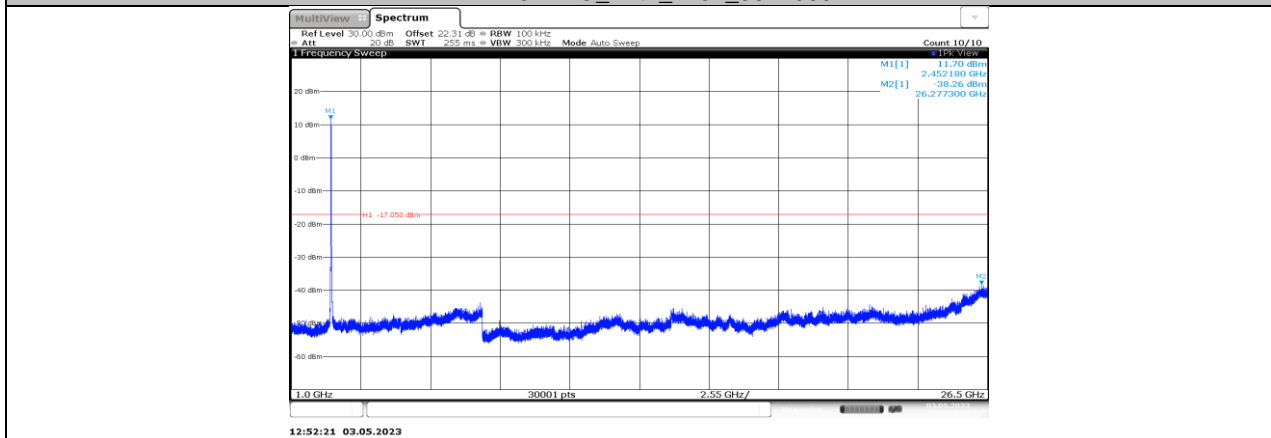
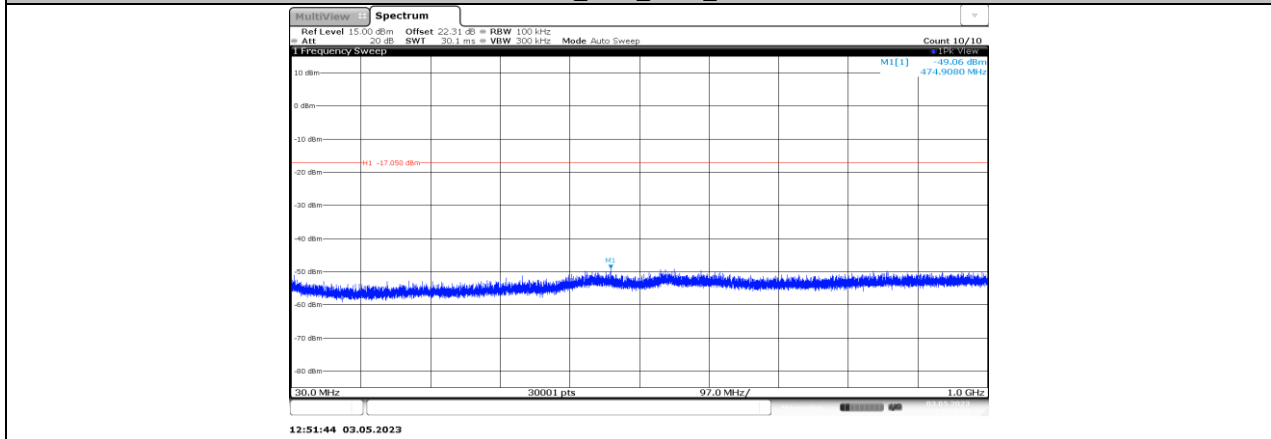
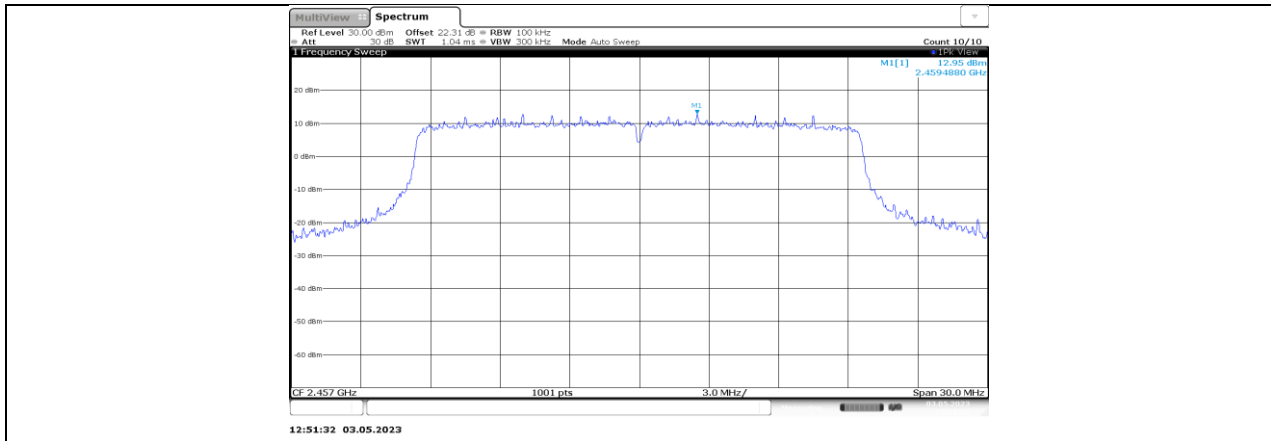




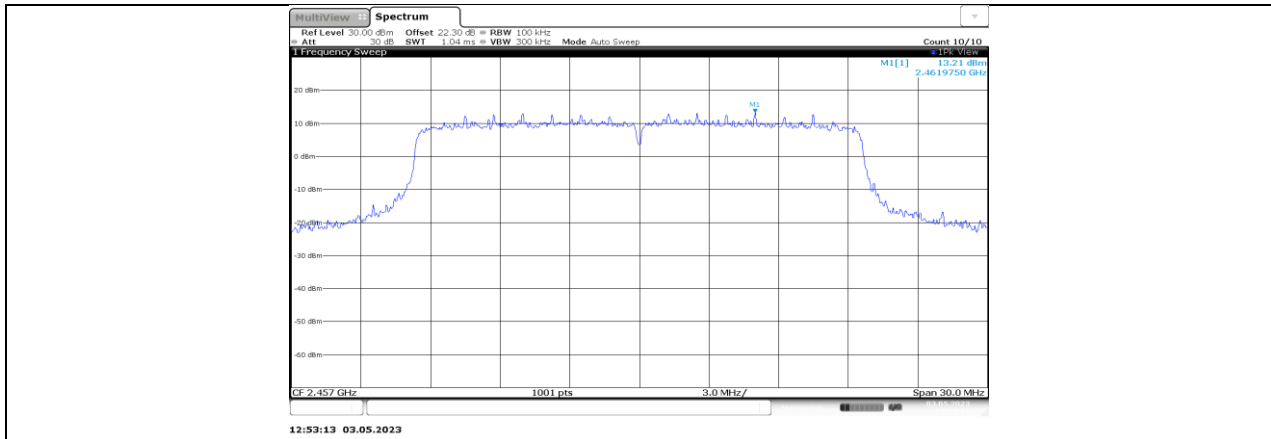




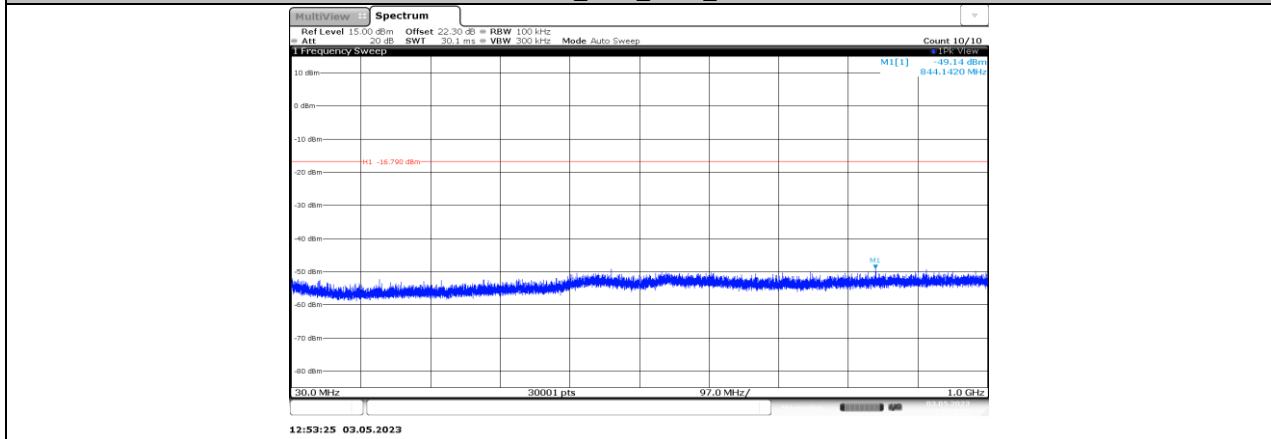




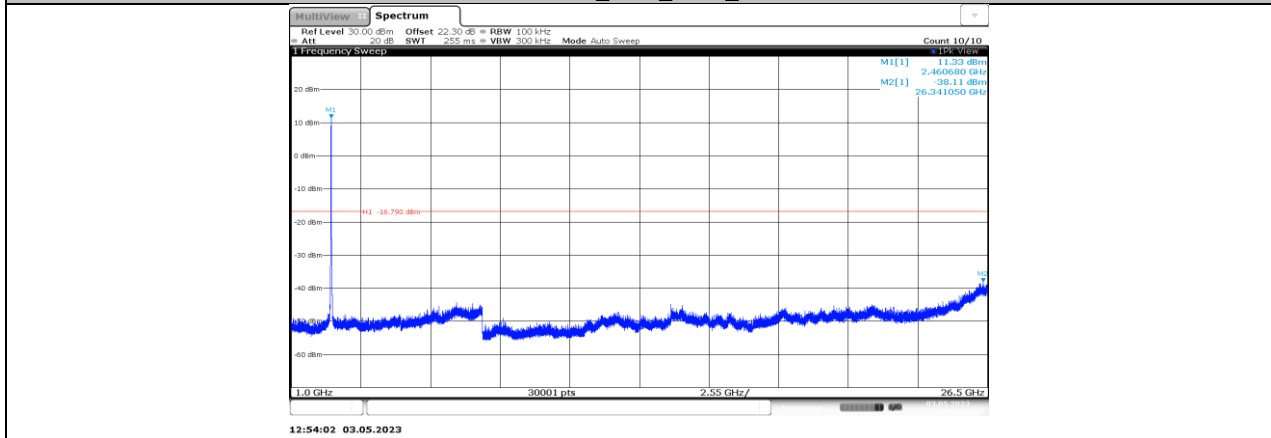




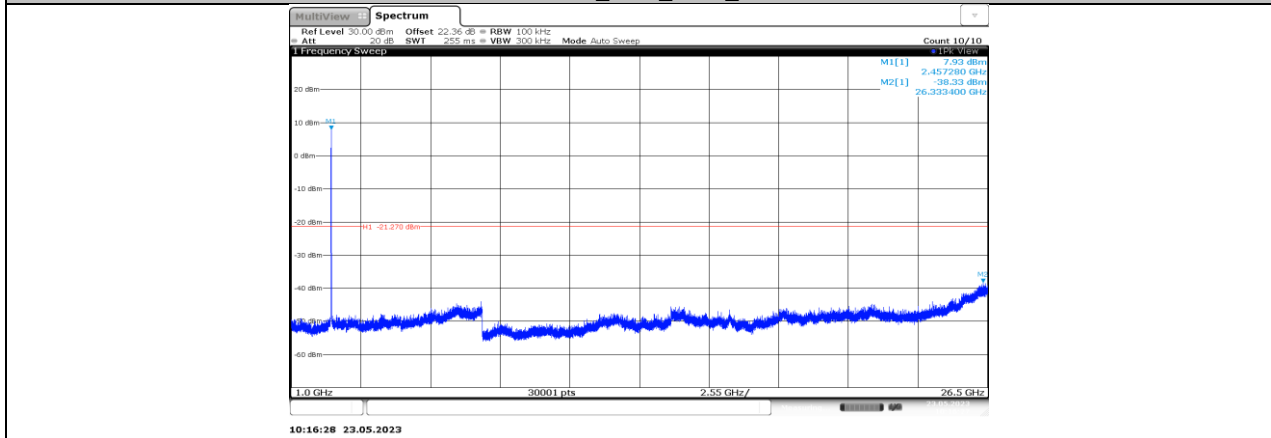
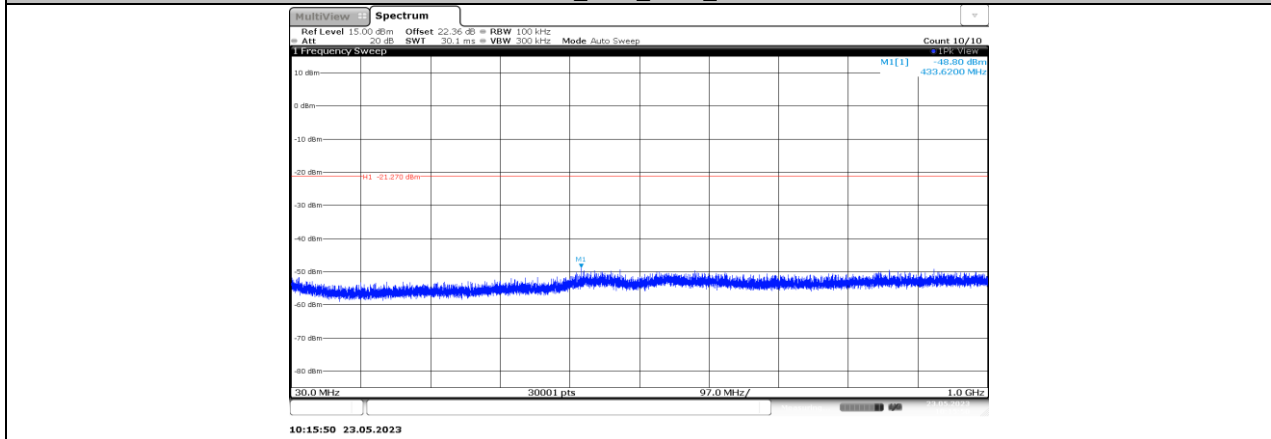
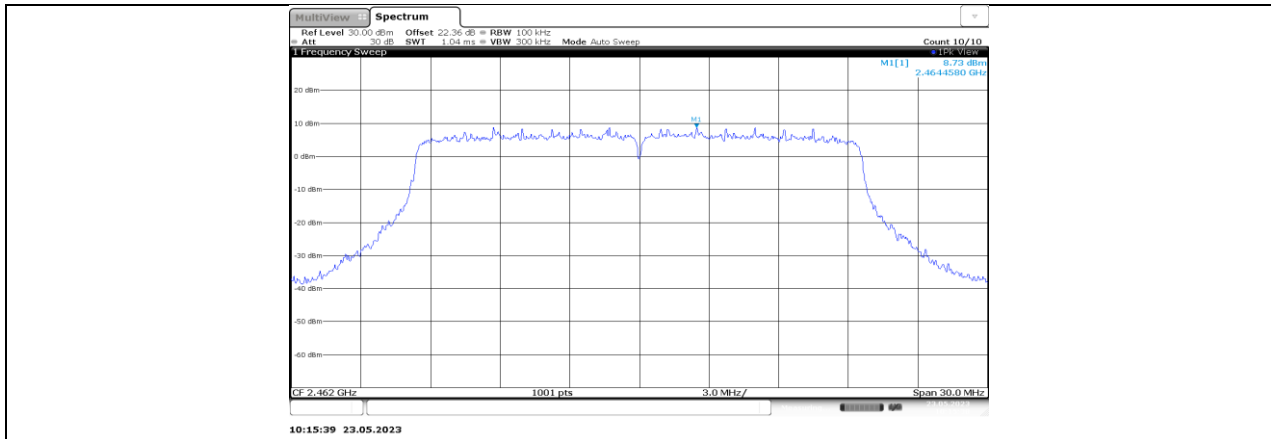
11AX20MIMO\_Ant5\_2457\_0-Reference

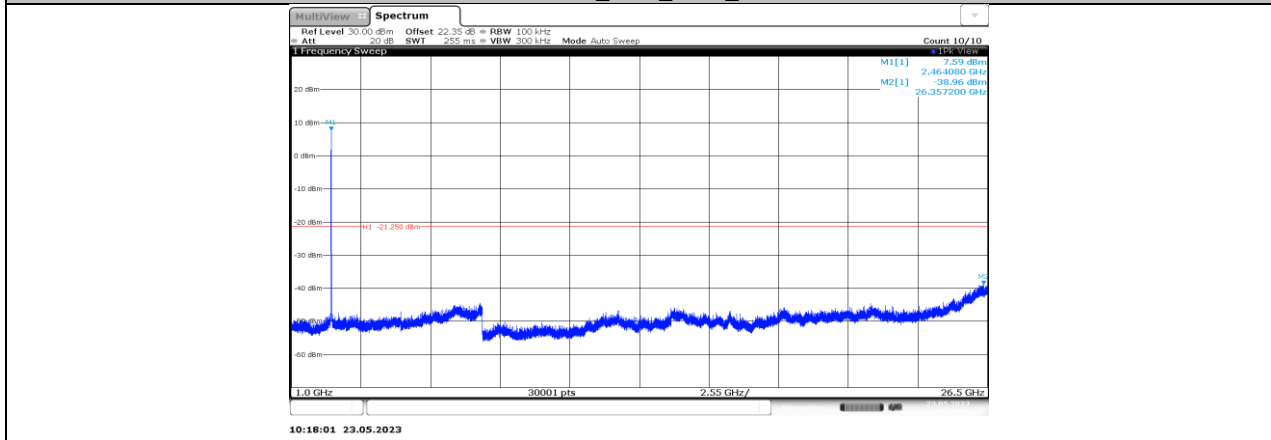
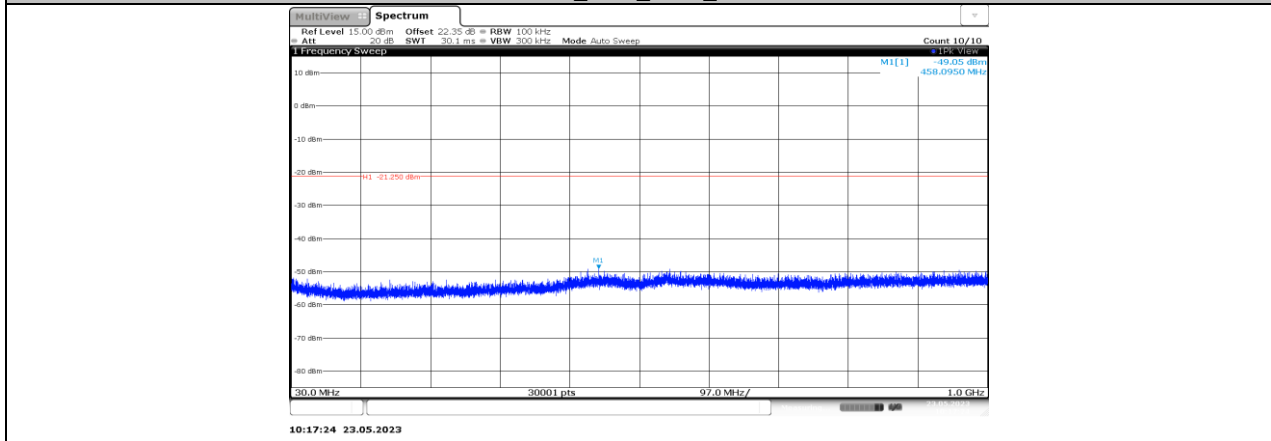
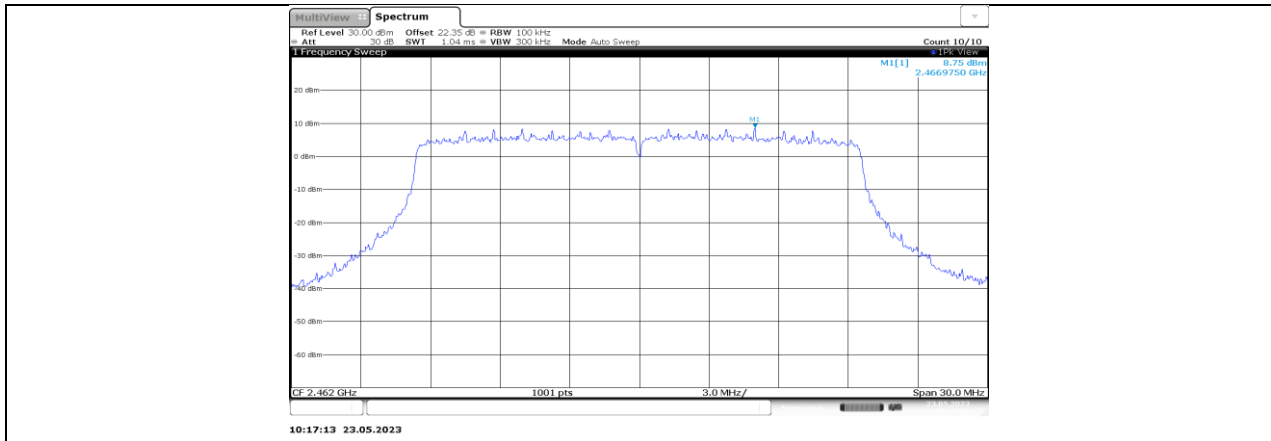


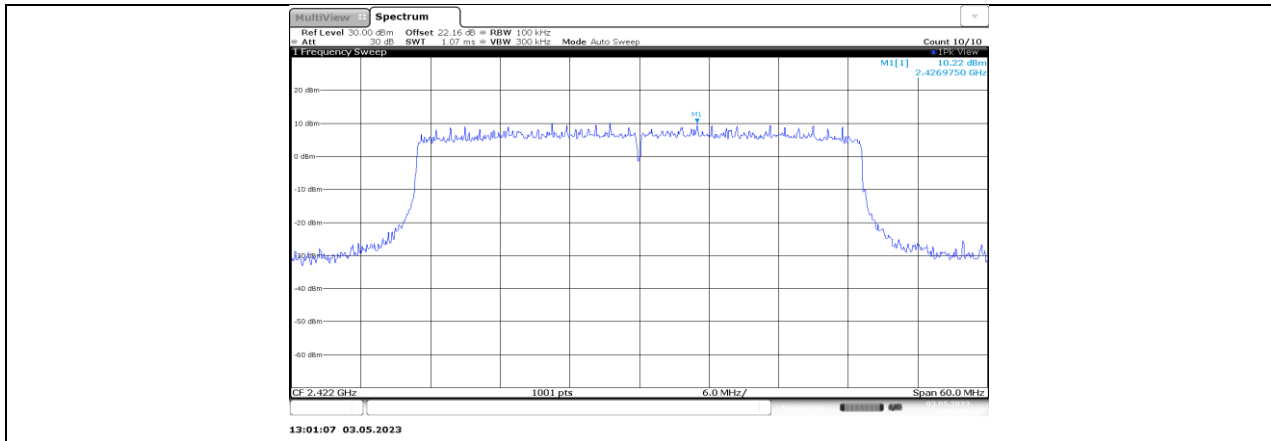
11AX20MIMO\_Ant5\_2457\_30~1000



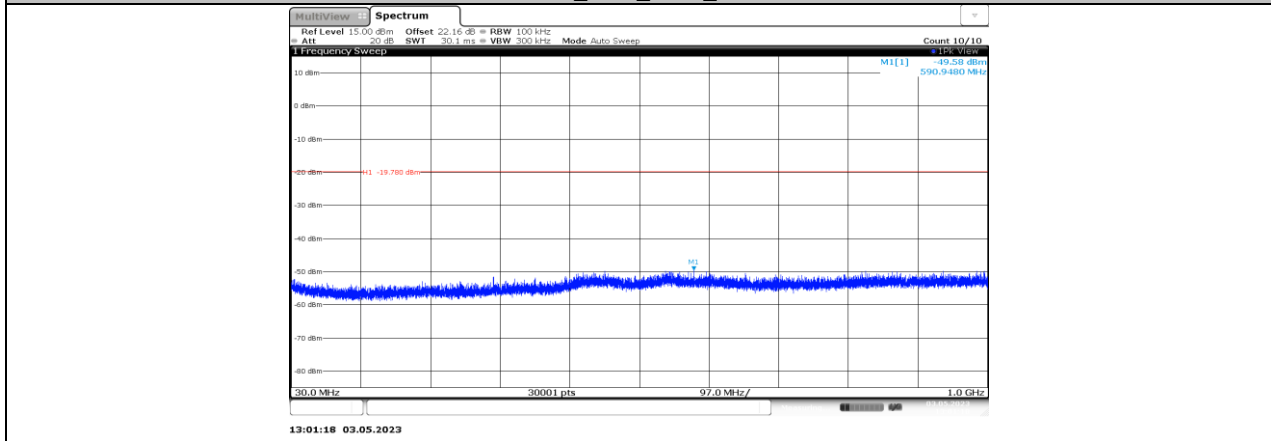
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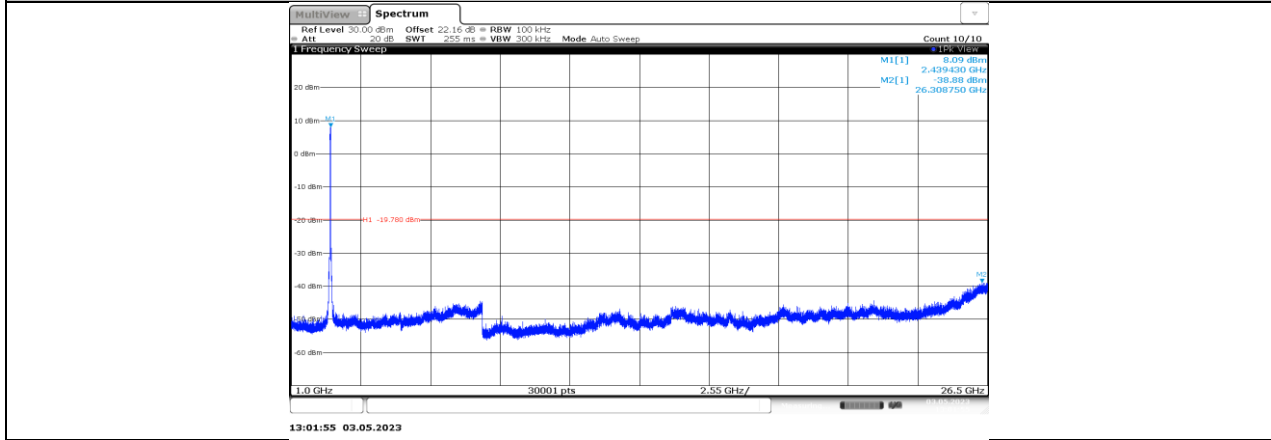




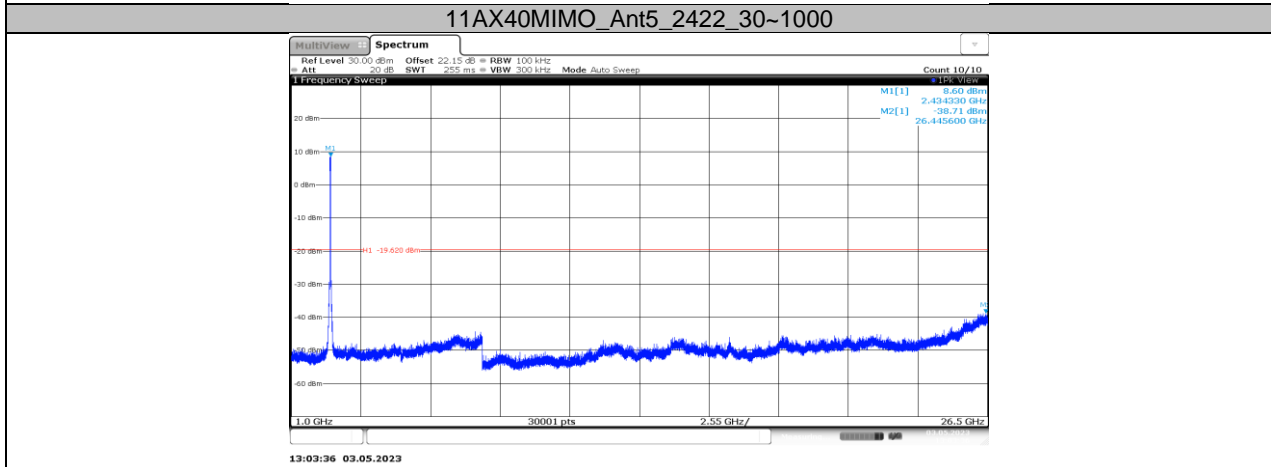
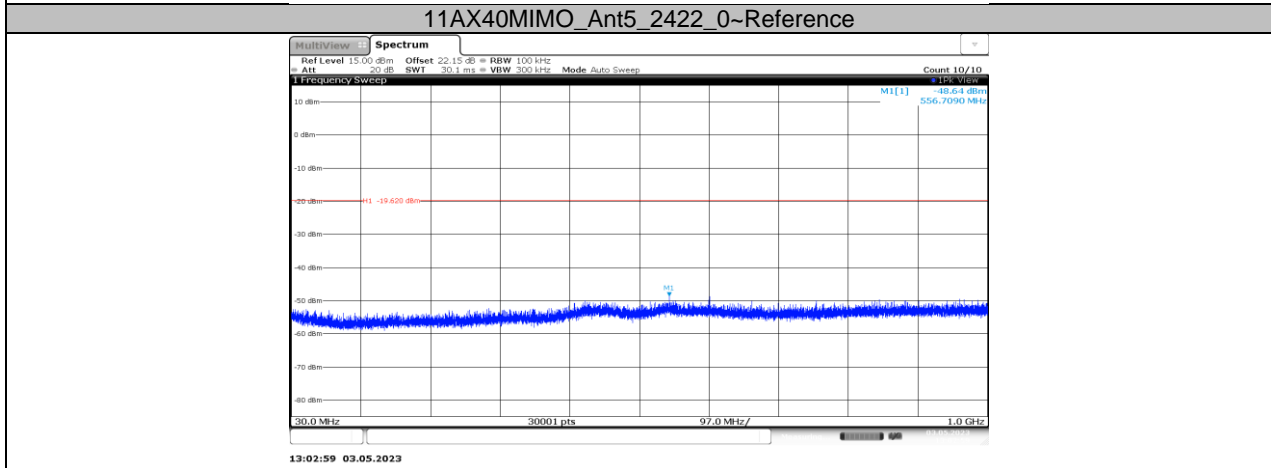
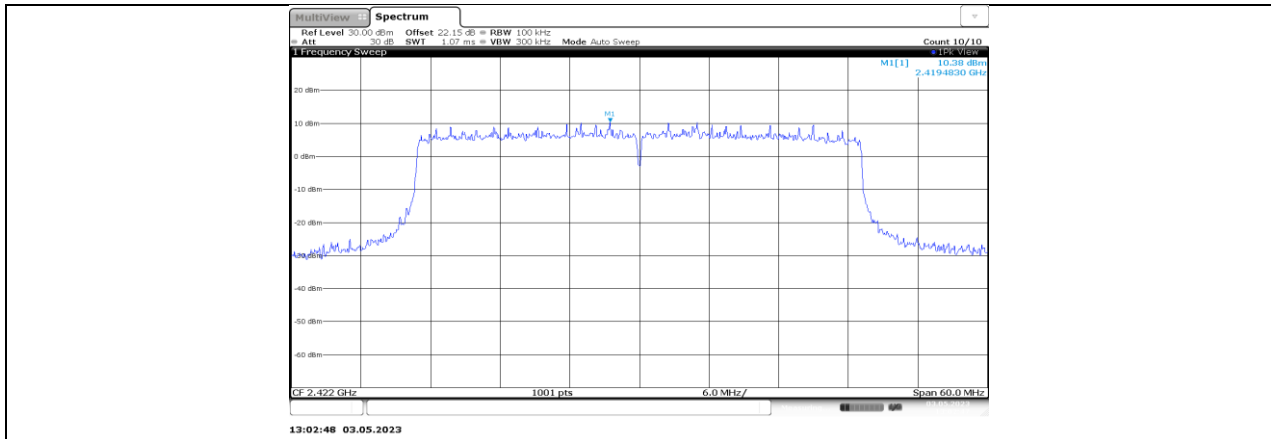
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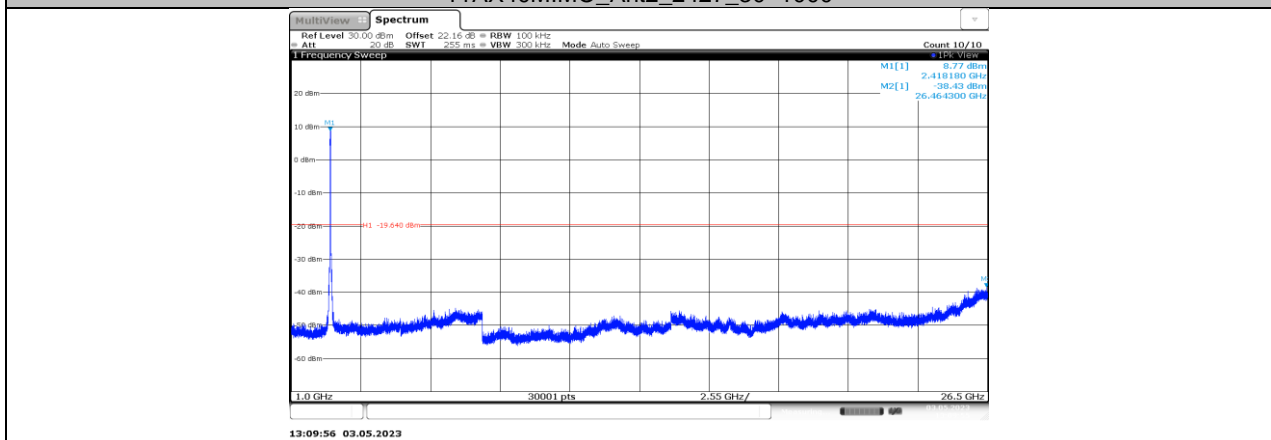
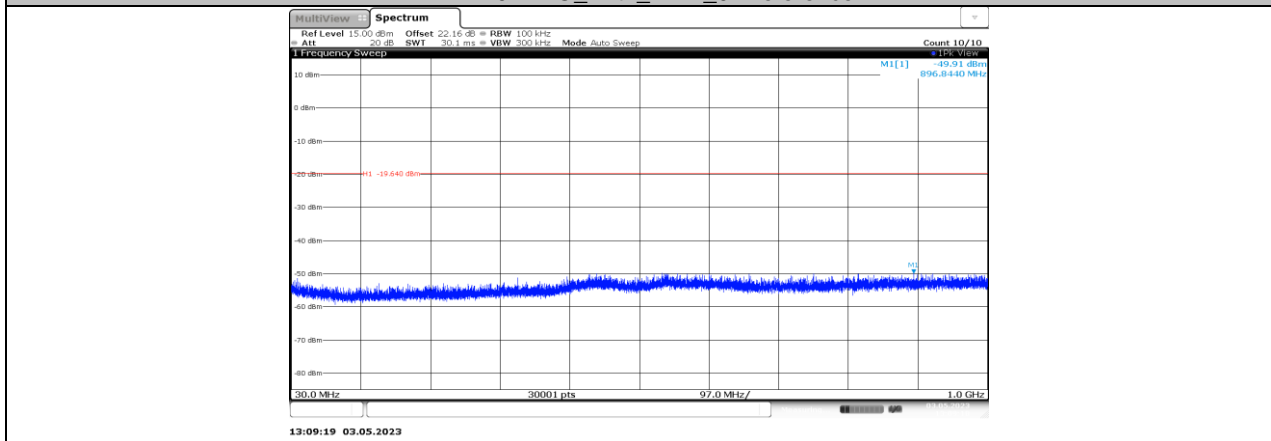
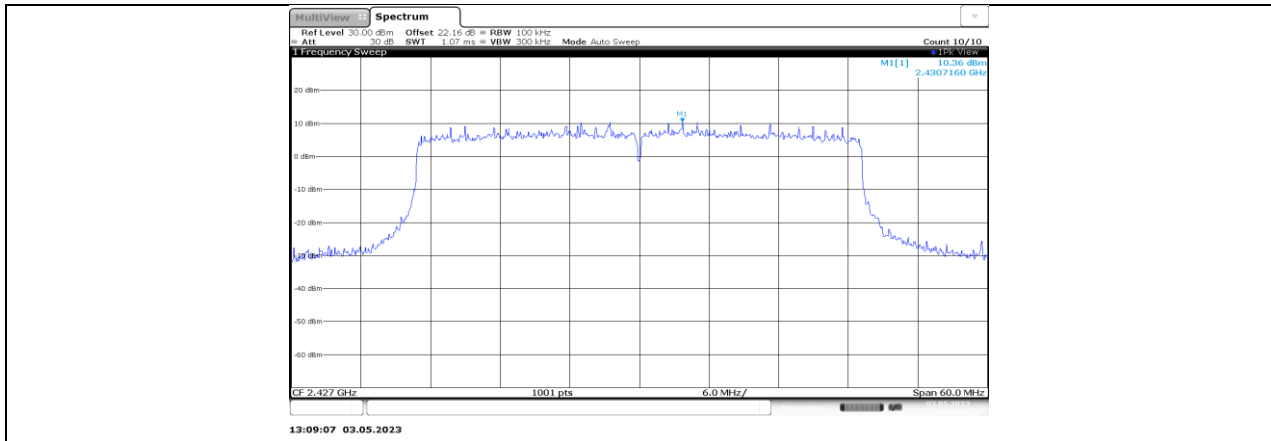


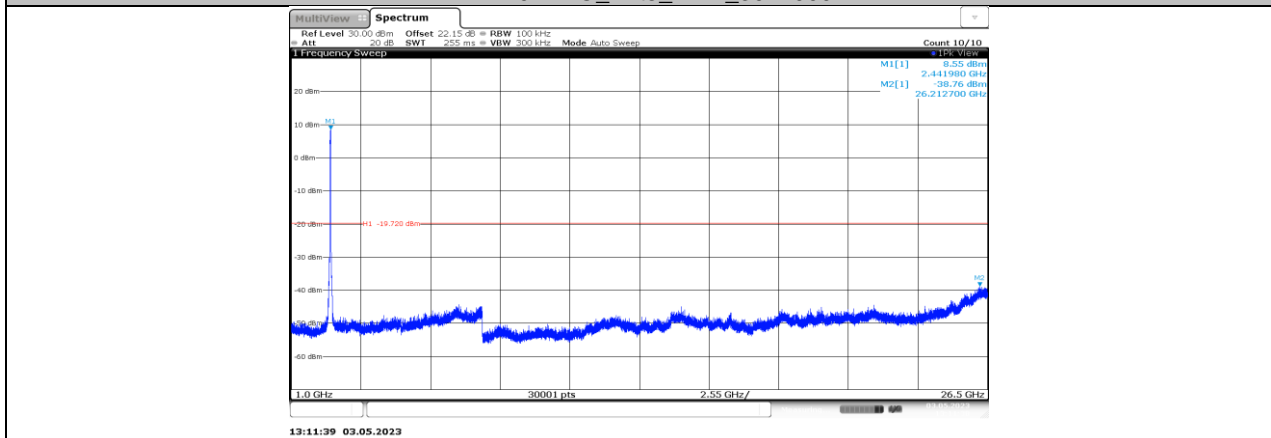
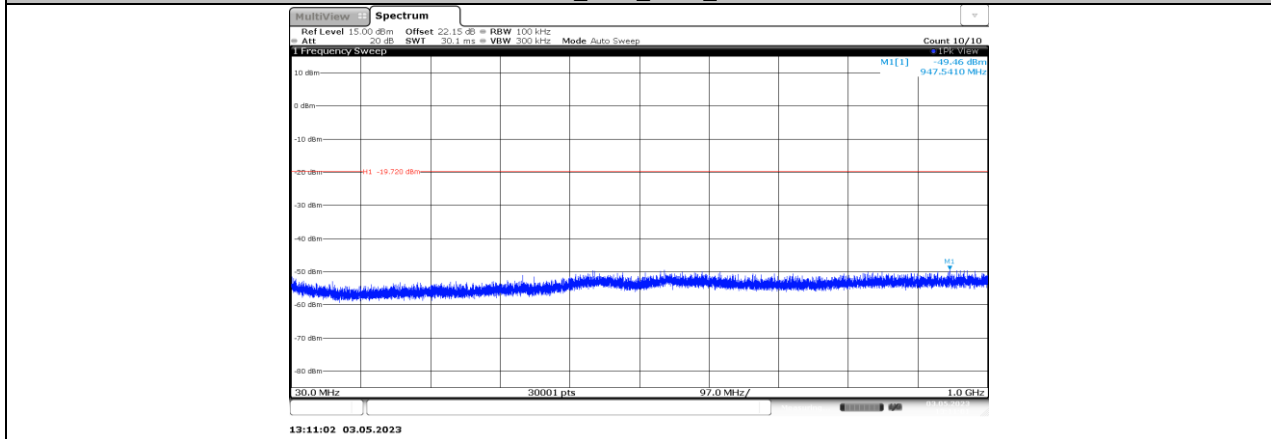
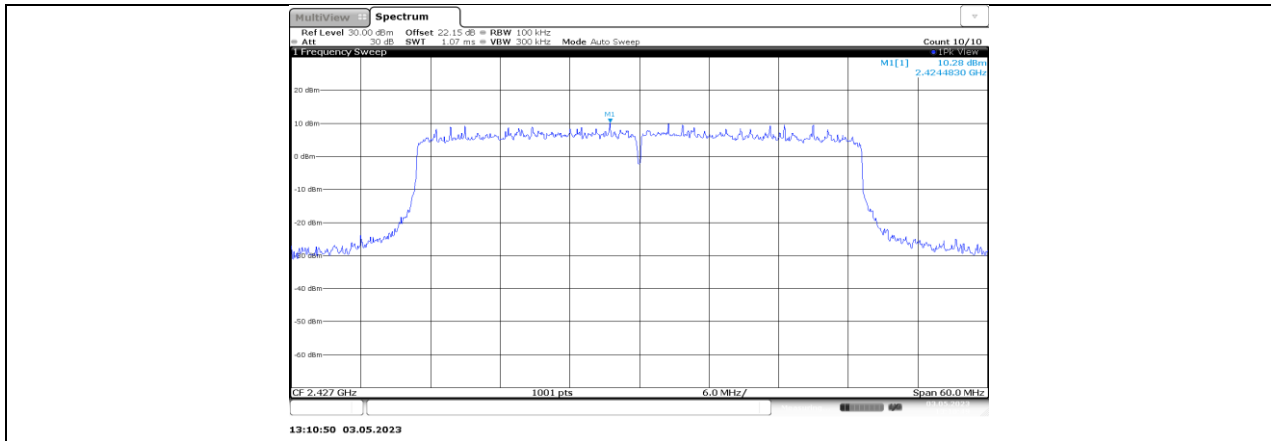
11AX40MIMO\_Ant2\_2422\_30~1000

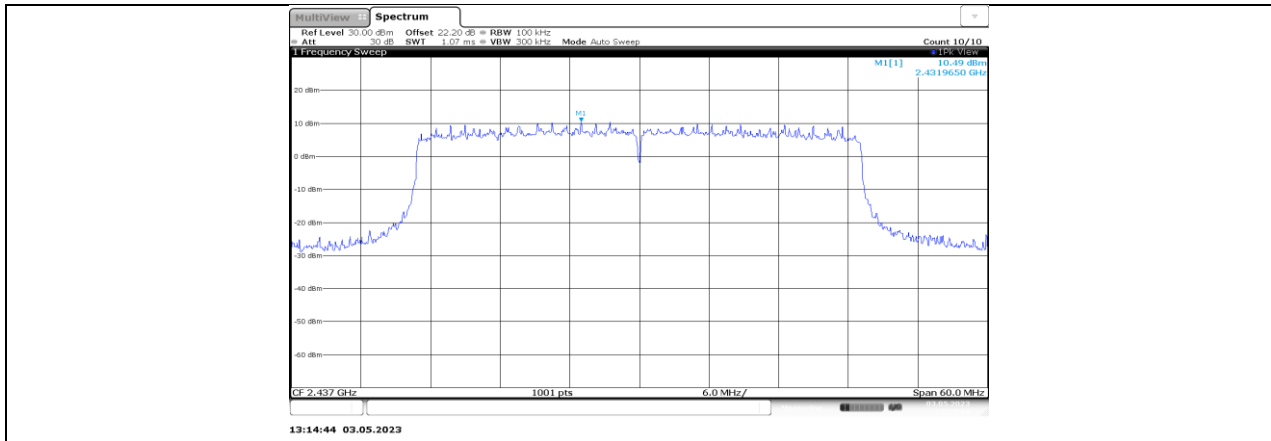


11AX40MIMO\_Ant2\_2422\_1000~26500

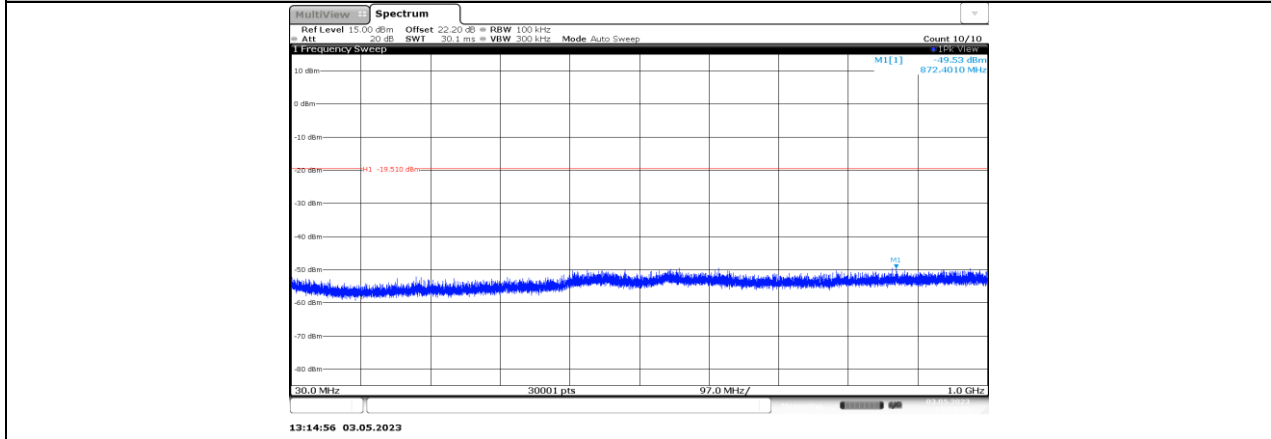




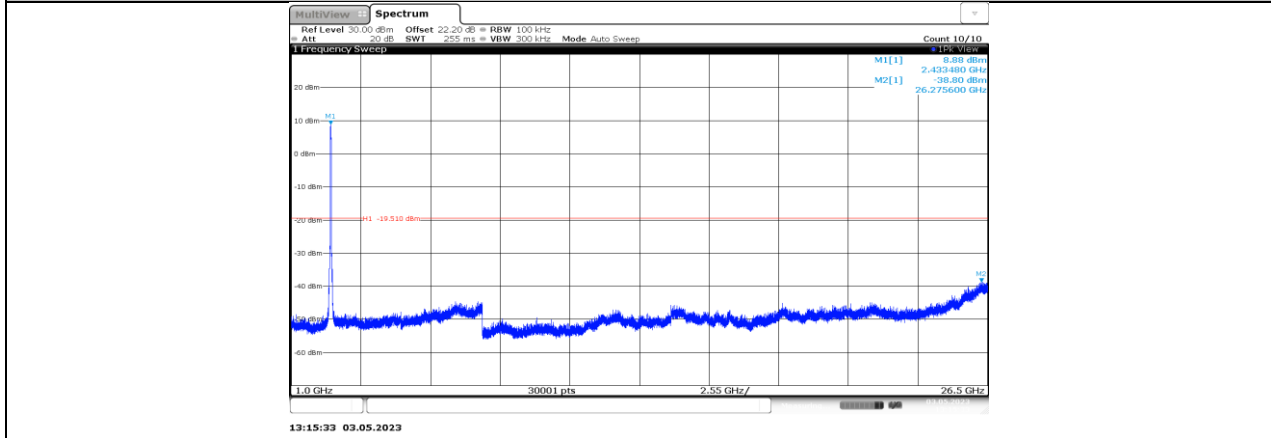




11AX40MIMO\_Ant2\_2437\_0-Reference

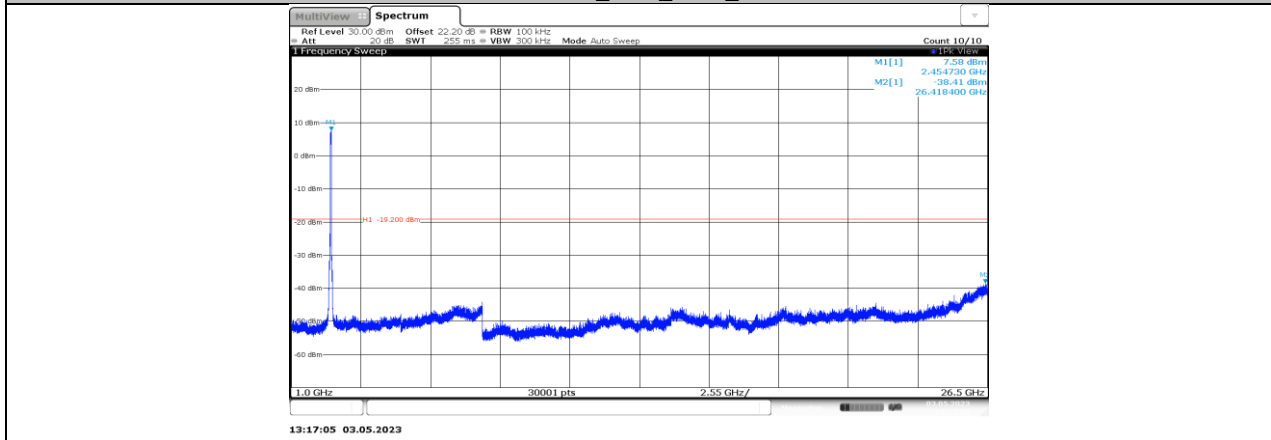
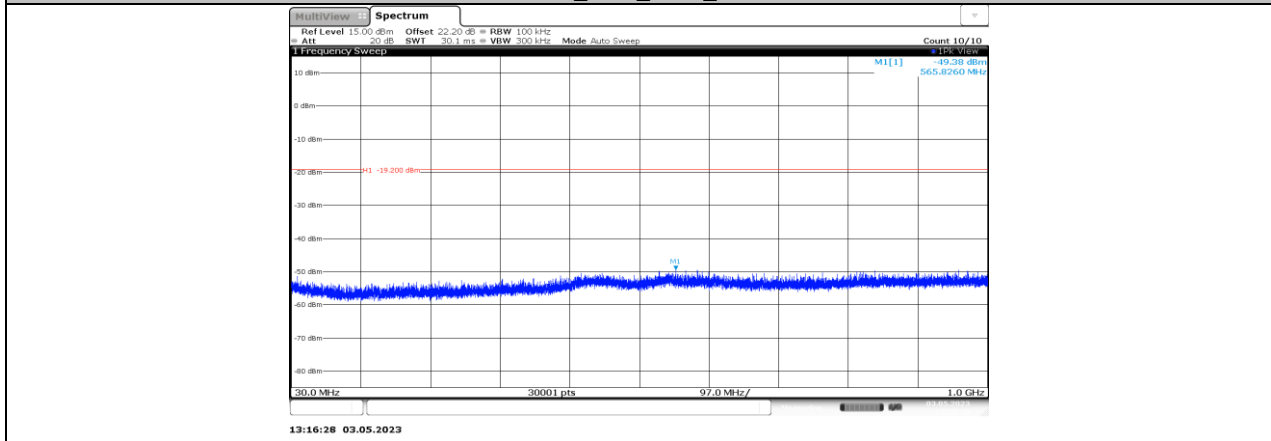
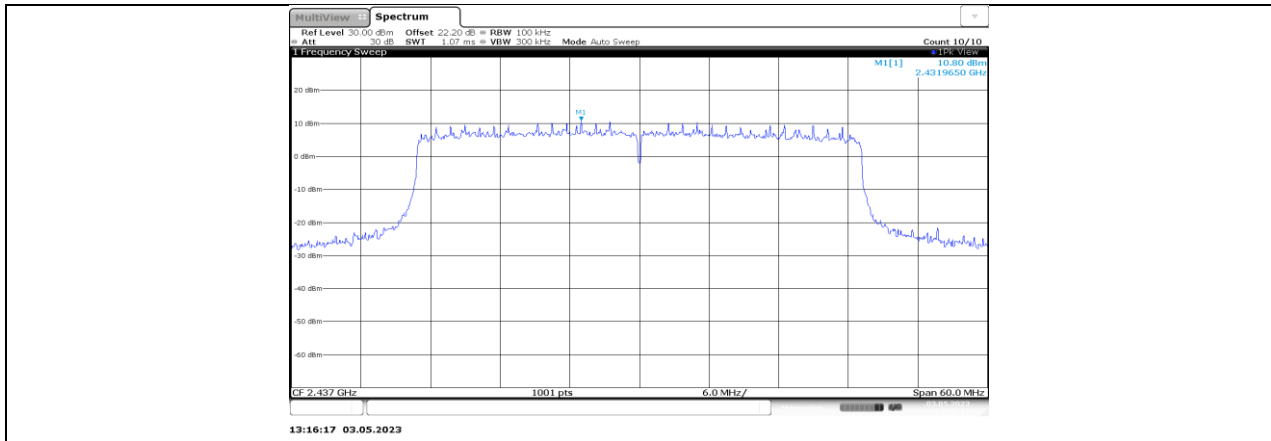


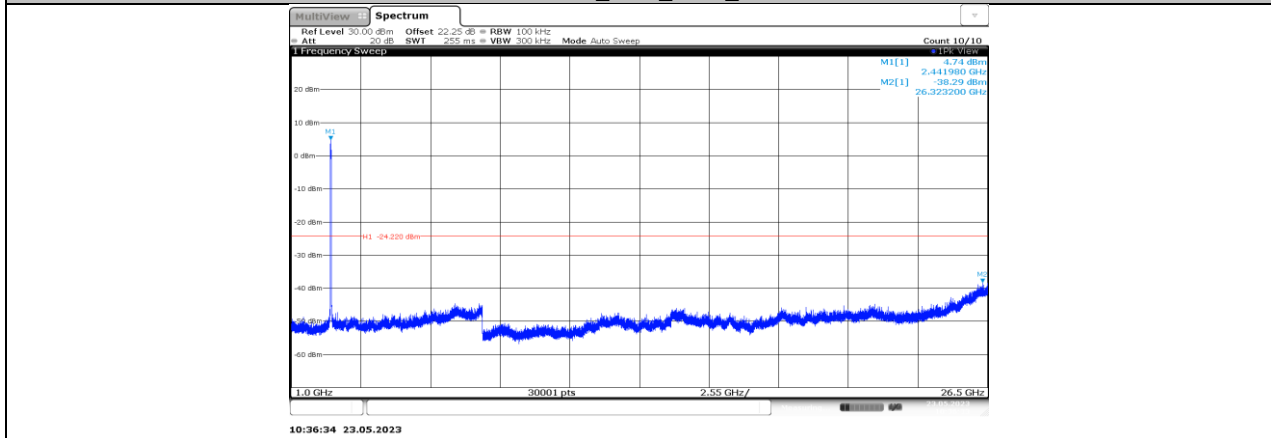
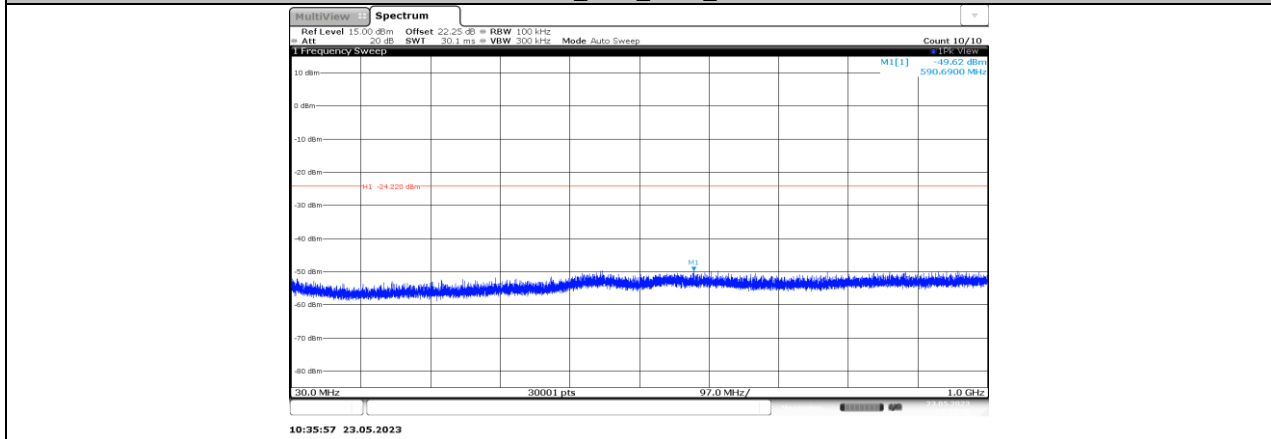
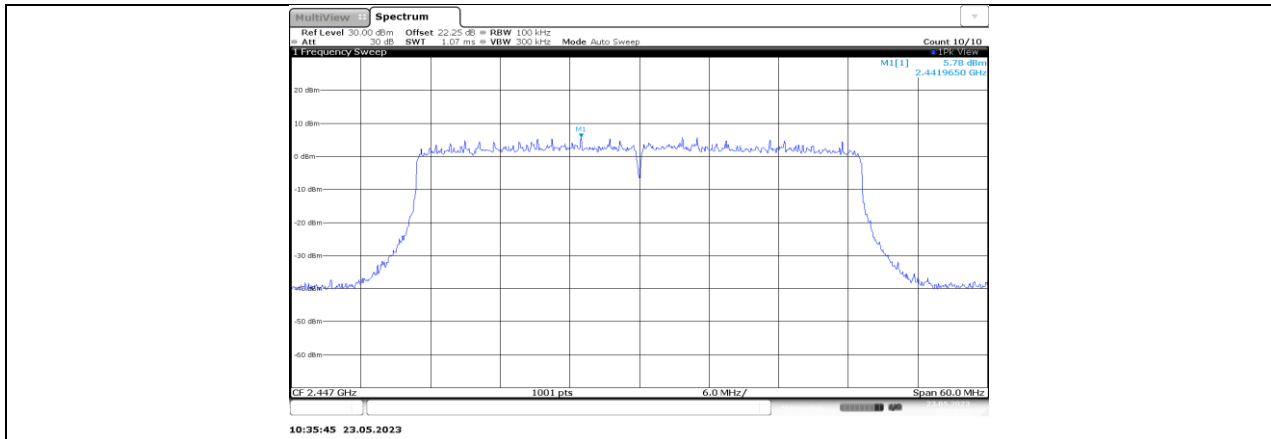
11AX40MIMO\_Ant2\_2437\_30~1000

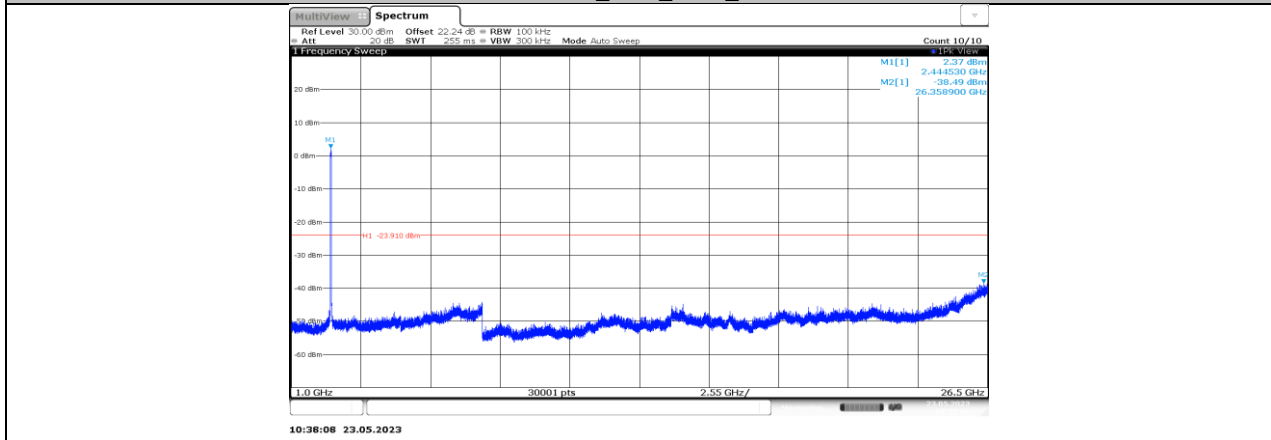
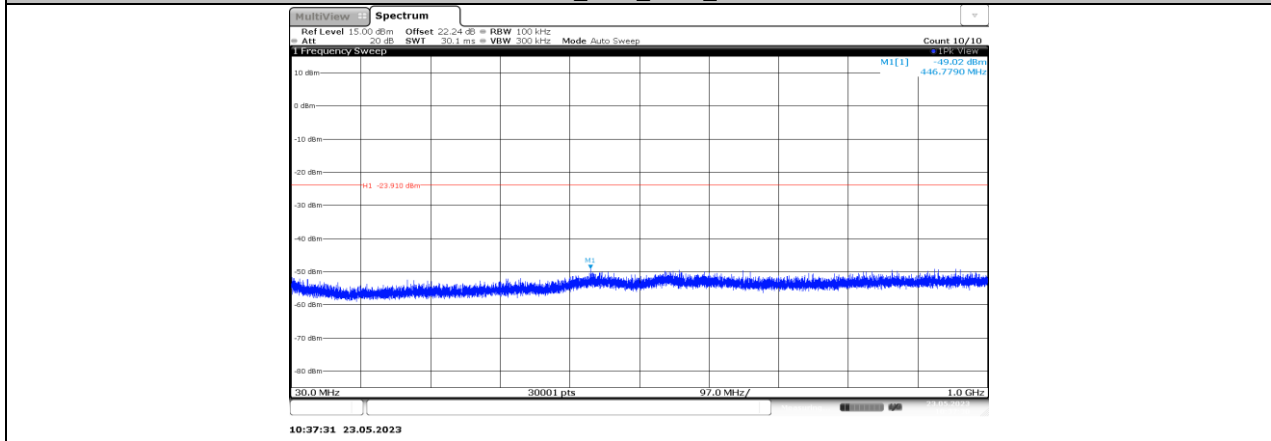
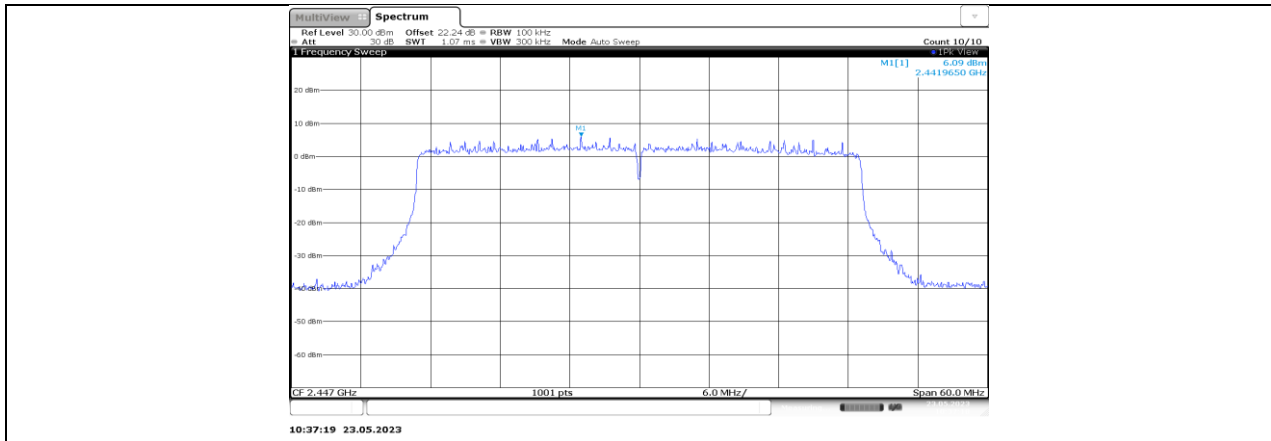


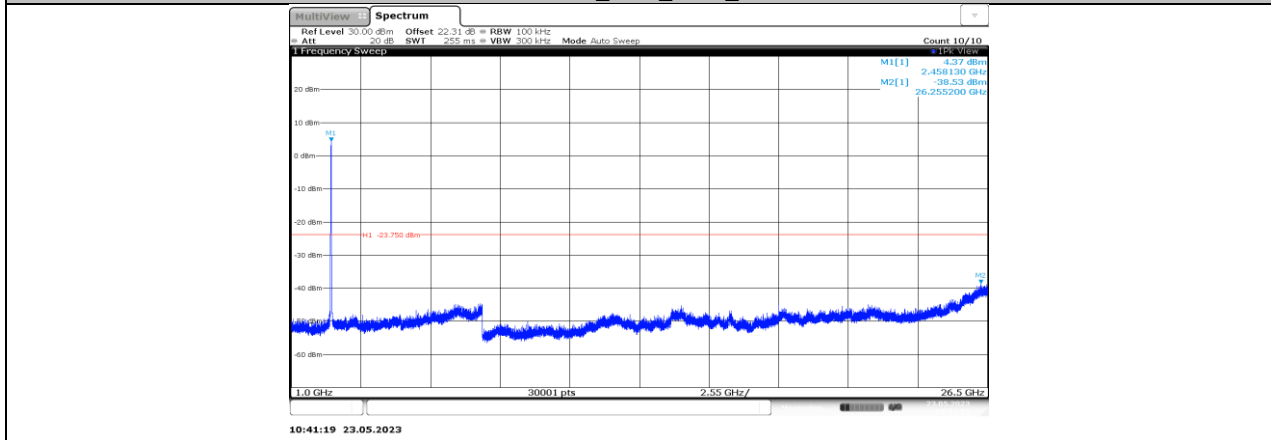
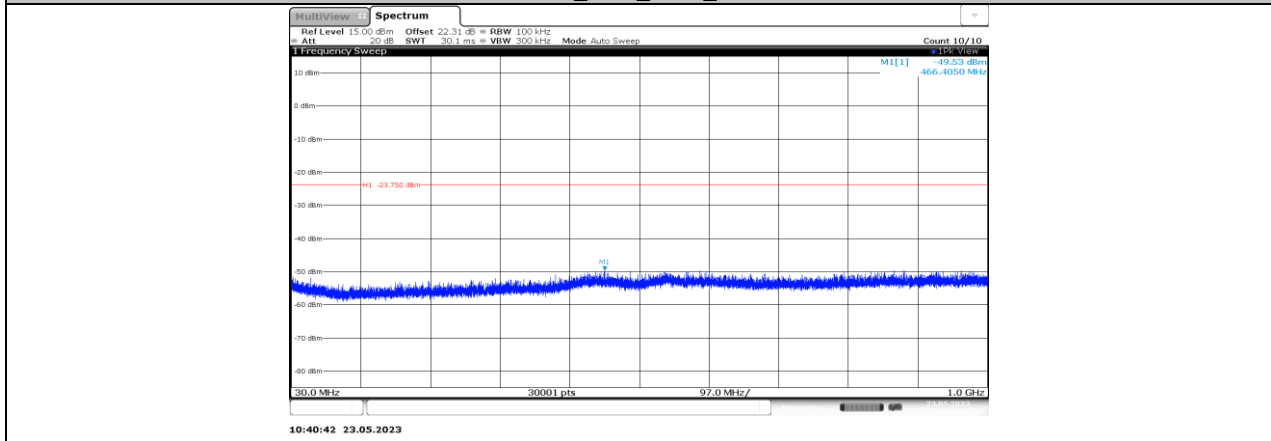
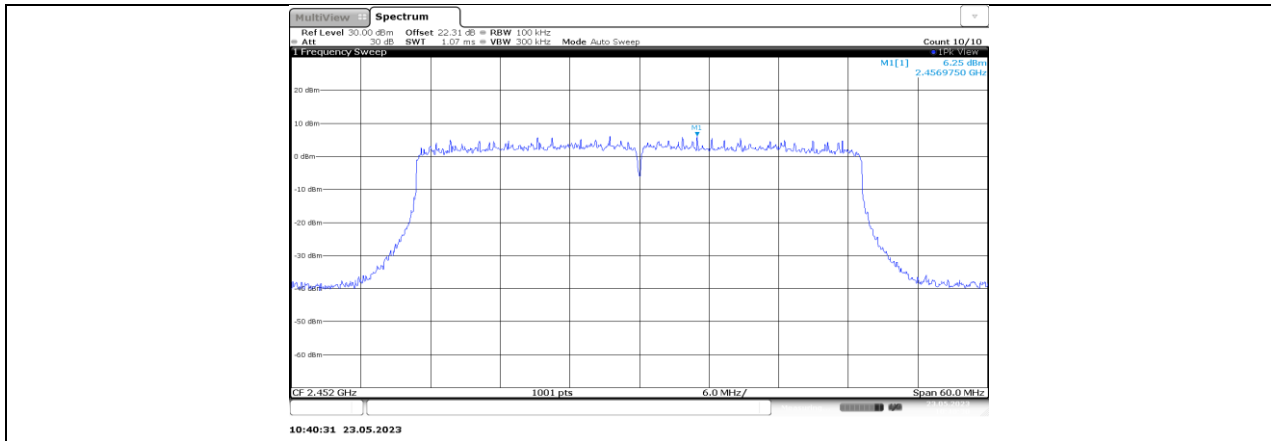
11AX40MIMO\_Ant2\_2437\_1000~26500

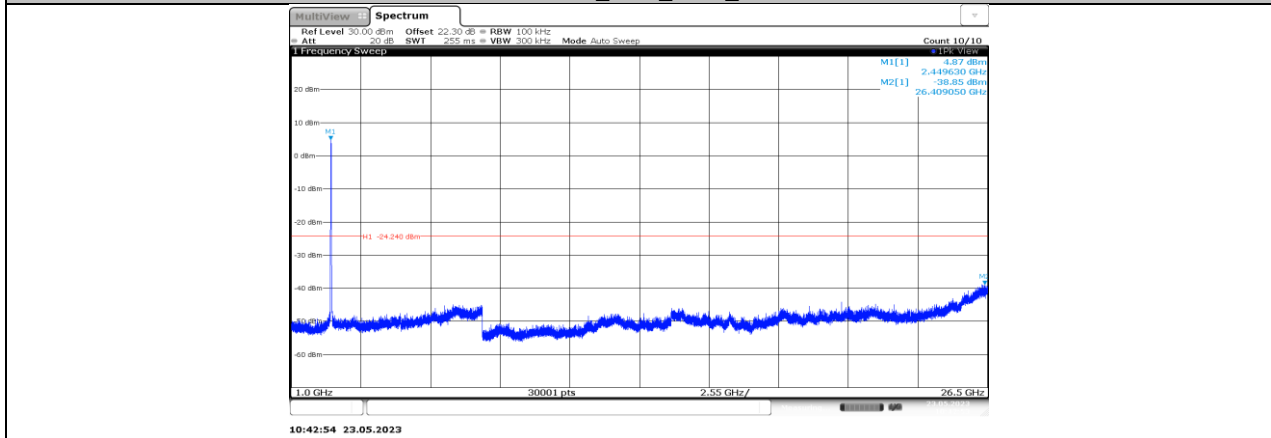
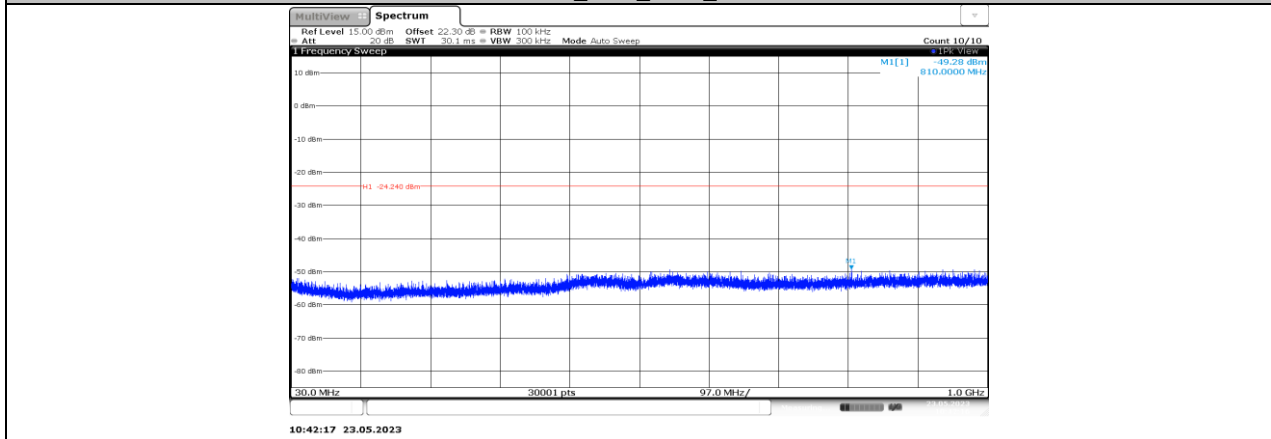
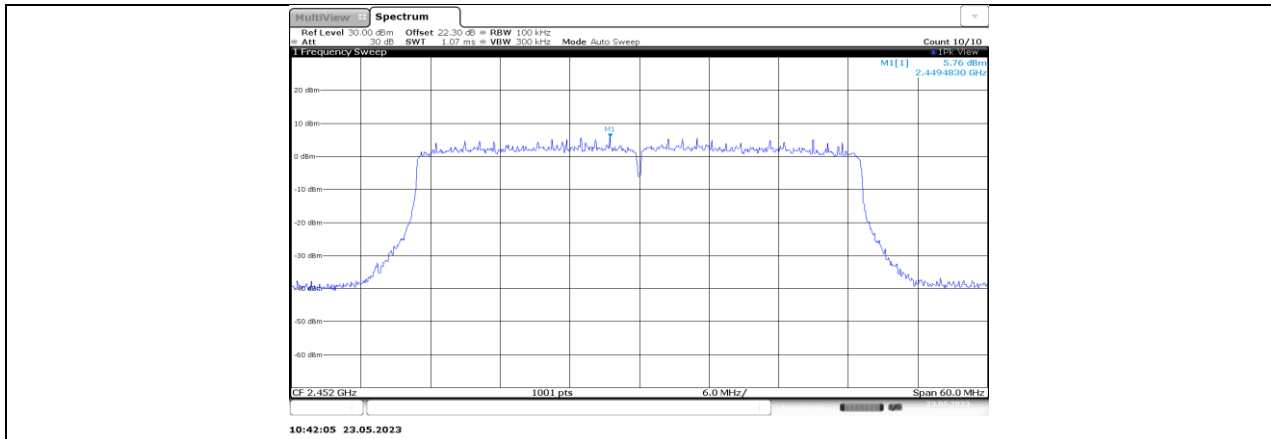












## 11.7. APPENDIX G: DUTY CYCLE

### 11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	1.97	2.09	0.9426	94.26	0.26	0.51	1
11G-CDD	1.97	2.11	0.9336	93.36	0.30	0.51	1
11AX20MIMO	5.43	5.56	0.9766	97.66	0.10	0.18	1
11AX40MIMO	3.93	4.04	0.9728	97.28	0.12	0.25	1

Note:

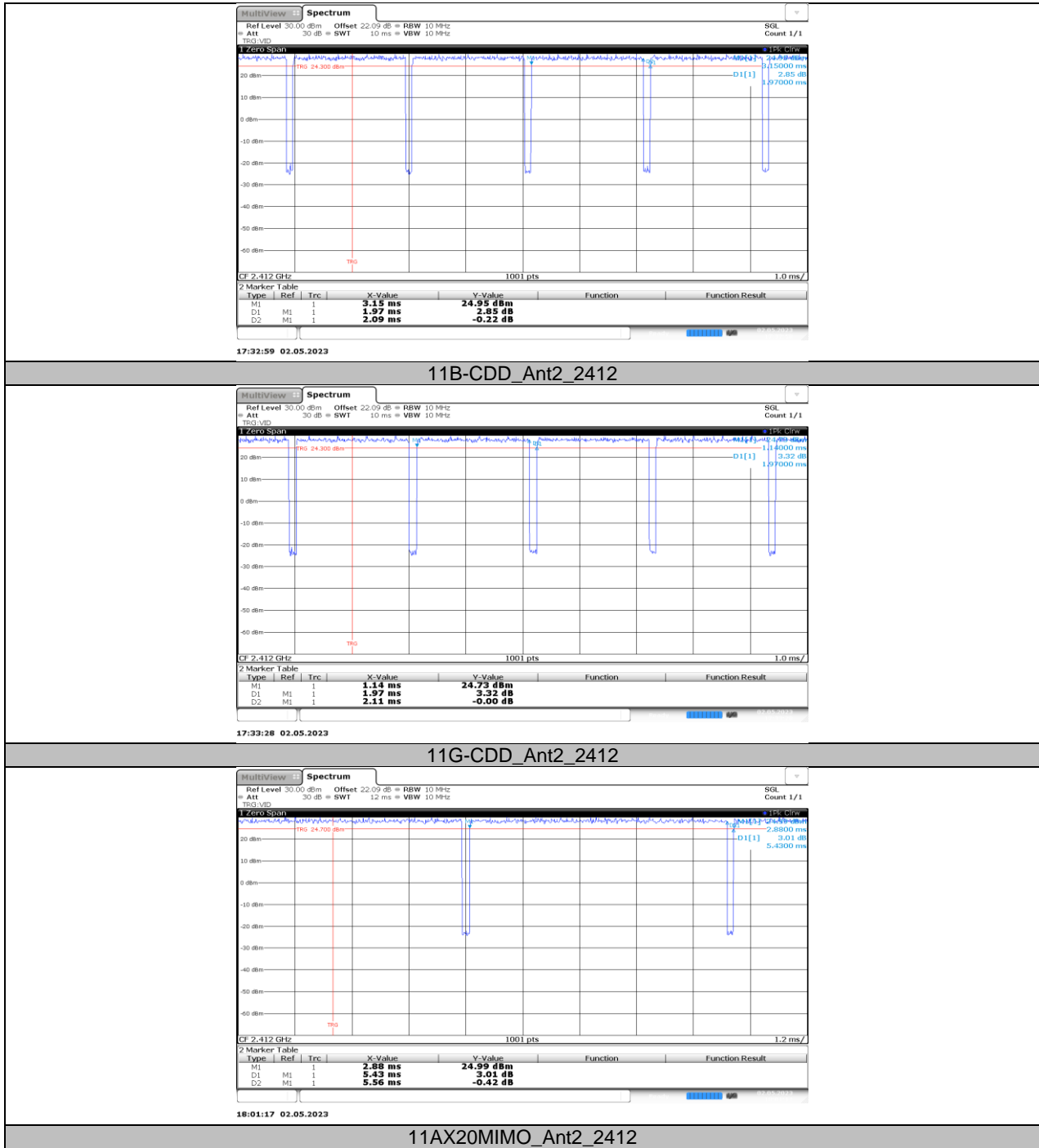
Duty Cycle Correction Factor= $10\log(1/x)$ .

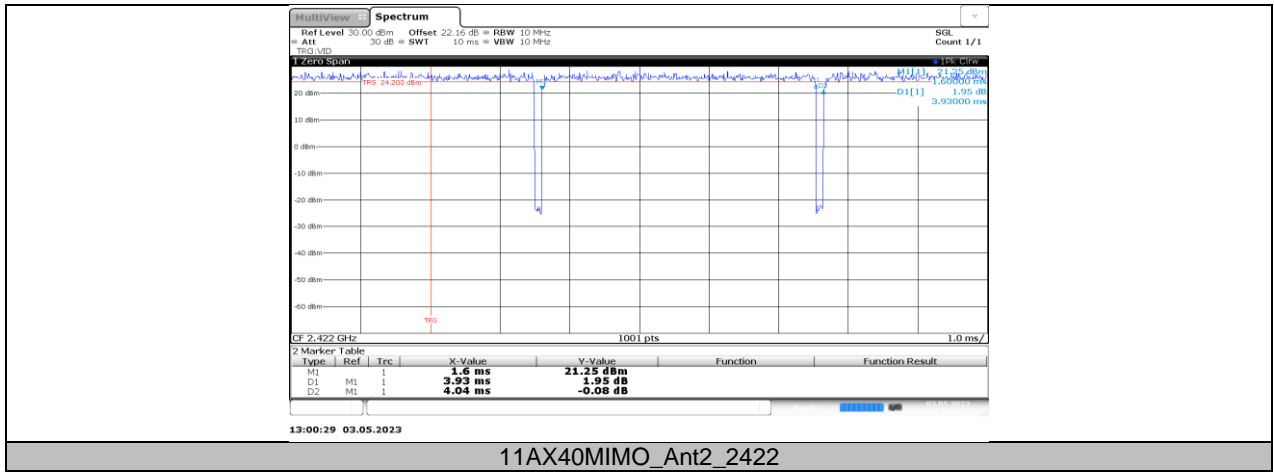
Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

### 11.7.2. Test Graphs





**END OF REPORT**