Puff Corporation

Declaration of Product Modifications

Date: 03/03/2023

This is to request a Class II permissive change for FCC ID: 2AWGI071018 originally granted on 08/04/2022.

The major change filed under this application is:

Change:

1.Bluetooth version upgrade

The following changes have been made to the-x1x revision of the ATMx2xx devices

- 1) Updated minor field in the CMSDK WRPR->CHIPREV register to 3. This indicates the new version of the device.
- 2) Connected the internal ground reference to the Application ADC such that ATM3221-x1x applications using the ADC now have no usage restrictions on the P8 GPIO.
- 3) Updated reset values for some registers to reduce ATMx2xx-x1x OTP content.
- 4) ATM22xx-x1x applications can operate without an external 32.768 kHz crystal. On V2.3, you may choose to use either the internal 32KHz clock or the external 32.768 KHz crystal for the sleep timer. The default is to use the external 32.768KHz crystal.

Do as followings for using the internal 32KHz clock.

- a. Program the OTP, the stepsare included in ATMx2xx Reference Manual
- b. The 32K_IN pin should be grounded, the 32K_OUTpin should be left floating. In our application, connected to ground through R50 resistor.
- 5) Enhancements to the ROM are covered in the release SDK 4_0_0 Release notes (ATMx2xx-RNSDK-0100)

2. Charging circuit optimization

lithium battery charging management IC model remains unchanged, wireless charging receiver chip model remains unchanged, R49 resistance is added, charging strategy is optimized, charging specifications remain unchanged, charging principle remains unchanged, and EMC performance of the whole machine is not affected.

3. Add debugging test points

Add six round test points with a diameter of 1.2mm on the bottom layer for debugging test without circuit principle changes. layout changes caused by these points do not affect the overall EMC.

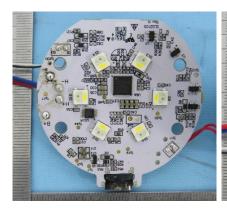
4. Change MCU.

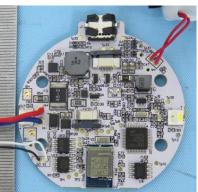
The main control MCU is changed from GD32F303CBT6 to GD32F303CGT6. The two MCU are of the same series. Only the difference between B and G represents the different size of flash memory.

5. Updated the antenna gain value.

Updated antenna gain from 0dBi to -1.95dBi.

Before change:





After the change:





These changes do not affect RF parameters.

Sincerely,

(Signature)

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