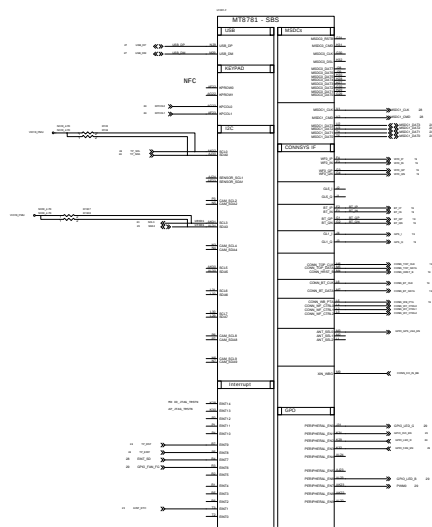


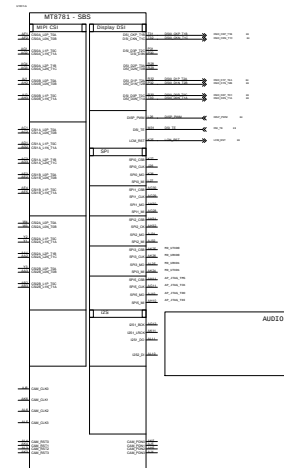
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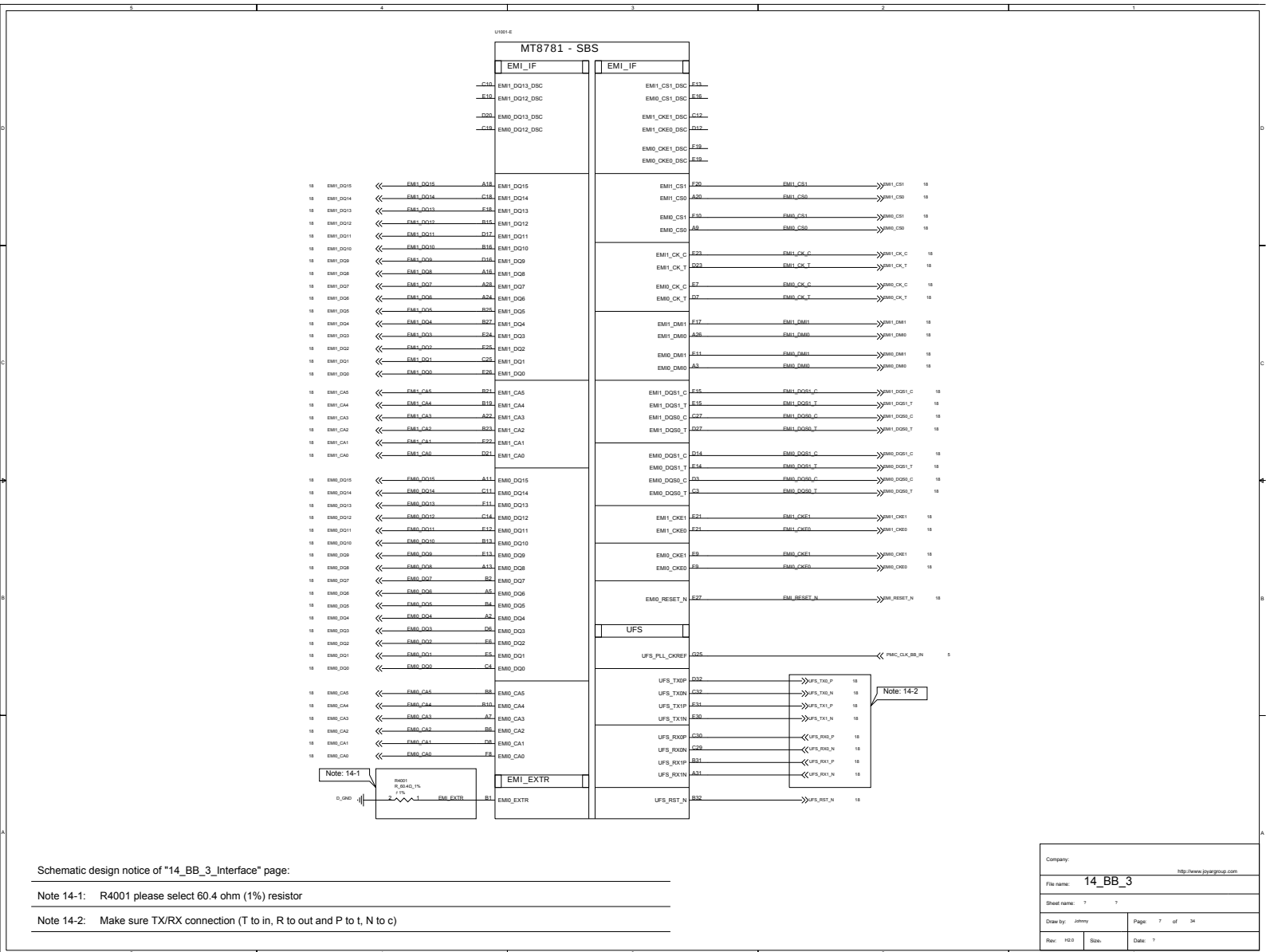
Date	Version	Page	Change notice
2021/12/xx	V0.1	10 20 44	Modify PDN_cap NC part
2022/01/xx	V0.2	20_POWER_MT6366-Buck	Remove un-used R2002, R2003 R / 0 / ohm / NC
2022/01/xx	V0.2	44.Memory_UFS_LPDDR4X	Remove R4401 & R4402 R / 0 / ohm test RES
2022/01/xx	V0.2	13_BB_2_CONN_CSI	Fixed GPIO100 (HW_ID0), GPIO101 (HW_ID1) txt
2022/01/xx	V0.2	20 21 22	Fixed Title name and Note:21-1 IC naming
2022/01/xx	V0.2	81_SYCLK_Clock-26MHz-TSX	Remove R8107 R / 0 / ohm test RES and Finetune Page81 Schematic design notice
2022/01/xx	V0.2	21_POWER_MT6366-LDO	Remove C2137 C / 4.7 / uF / 6.3V
2022/01/xx	V0.2	13_BB_2_CONN_CSI	Add finger print configure
2022/01/xx	V0.2	78_PERI_SENSORS_FingerPrint	Add finger print configure
2022/01/xx	V0.2	32_RF_MT6186M_RF_TX	Modify C3299 from C / 10 / uF / 6.3V to C / 4.7 / uF / 6.3V
2022/01/xx	V0.2	68_PERI_CTP	Modify VTP_PMU net to VI028_PMU
2022/01/xx	V0.2	77_PERI_SENSORS_MEMS_ALS/PS	Modify VLD028_EXT net to VI028_PMU
2022/01/xx	V0.2	27_POWER_MT6375-Charger + PP	Change R2705 to R / 0.005 / ohm, HFCL0603MW33R005F
2022/01/xx	V0.2	79_POWER_FLASH_DRIVER	Change U7901 to U / LM3643YFFR
2022/01/xx	V0.2	13_BB_2_CONN_CSI	Fixed typo GPIO111 (GPIO_DRDI)
2022/02/xx	V0.2	10_BB_PDN1	Change C1033 to C / 2.2 / uF / 6.3V / NC
2022/02/xx	V0.2	11_BB_IOVDD	Change C1131 to C / 1 / uF / 6.3V / NC
2022/02/xx	V0.2	82_SYCLK_RTC32K	Change R8203 to R / 0 / ohm / NC
2022/02/xx	V0.2	27_POWER_MT6375-Charger + PP	Modify MT6375 gauge notice
2022/02/xx	V0.2	21_POWER_MT6366-LDO	Modify Note 21-1 content
2022/02/xx	V0.2	23_POWER_MainPMIC-Charger	Change R2304 to R / 4.7 / K
2022/02/xx	V0.2	29_POWER_EXT	
			Modify AFVDD/AVDD note
2022/02/xx	V0.2	69_PERI_CAMERA_Rear	Remove R6900 to C6904 and modify CN6000 capacitor tolerance, it
2022/02/xx	V0.2	70_PERI_CAMERA_Front	Remove R7000 to C7012 and modify CN7004 capacitor tolerance, it
2022/02/xx	V0.2	77_PERI_SENSORS_MEMS_ALS/PS	Remove R7700 to C7704 and modify CN7704 capacitor tolerance, it
2022/02/xx	V0.2	79_POWER_FLASH_DRIVER	Modify Note 77 to interrupt bin of MEMS sensor must be assign to SCP_EINT
2022/02/xx	V0.2	21_POWER_MT6366-LDO	Remove R2100 and add Note, please refer to Camera Power
2022/02/xx	V0.2	10_BB_PDN1	Remove R1000 chapter in Baseband Design Notice
2022/02/xx	V0.2	20_POWER_MT6366-Buck	Change C1006, C1007 to NC
2022/02/xx	V0.2		Change C2033, C2042 to NC

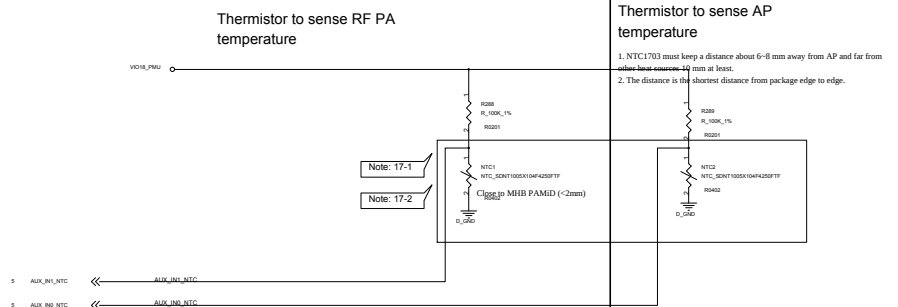
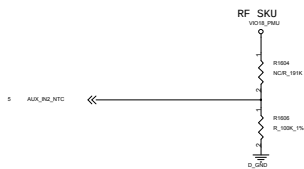
Company: http://www.psagroup.com		
File name: 04_Change_Notice		
Sheet name: 1 1		
Draw by: Jimmy	Page: 2 of 34	
Rev: 00.0	Size:	Date: 1



GPIO111	GPIO101	GPIO100	PCB ID
GPIO111	GPIO101	GPIO100	Internal phone (Note: DP DTB detected via GC ID)(MTKxxx) with MT8175T1 show SKU
Lo	Lo	Hi	
Lo	Hi	Lo	
Lo	Hi	Hi	
Hi	Lo	Lo	EV8 (MTKxxx) = MTK Internal DTBs
Hi	Lo	Hi	EV8 (MTKxxx) = Customer's DTBs







Schematic design notice:

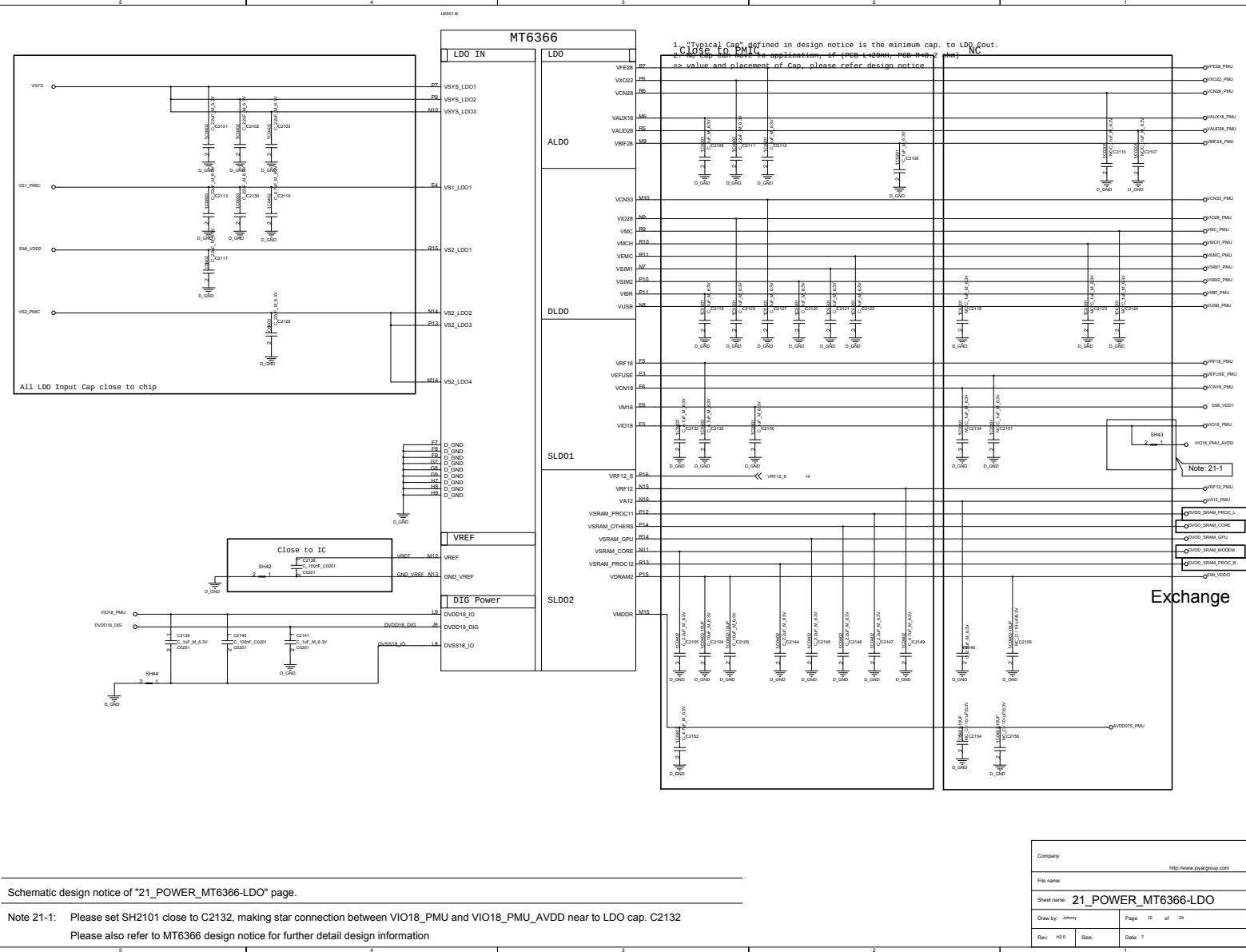
Note 17-1: SW Default Configuration is as followings
AUX_IN0_NTC is for AP Temp.
AUX_IN1_NTC is for MHB PAMiD Temp.

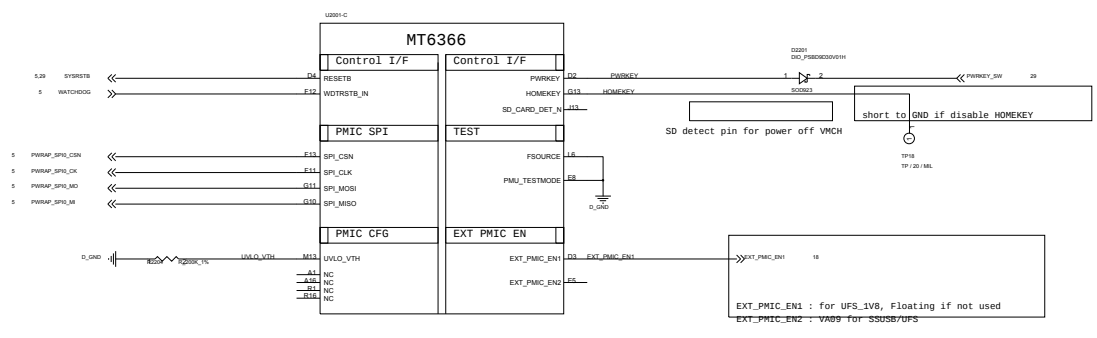
Note 17-2: If your design or placement is different with SW Default Config.
Please refer to "MTXXXX Thermal User Manuel.docx" and modify SW setting.

Schematic design notice of "16_BB_AUXADC" page.

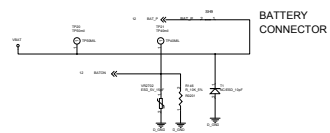
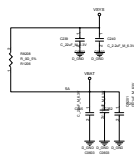
	AUX_IN2 (RF SKU/PCB ID)	
	(ADC range is 0.05~1.45V)	
SKU1 EU+CHN+JPN	0.05V	0V(R1604=NC, R1606=100K)
SKU2 NA	0.21V	0.21V(R1604=750K, R1606=100K)
Reserved	0.42V	0.41V(R1604=330K, R1606=100K)
Reserved	0.62V	0.62V(R1604=191K, R1606=100K)
Reserved	0.82V	0.82V(R1604=120K, R1606=100K)
Reserved	1.04V	0.99V(R1604=100K, R1606=137K)
Reserved	1.24V	1.24V(R1604=100K, R1606=220K)
Reserved	1.45V	1.45V(R1604=100K, R1606=422K)

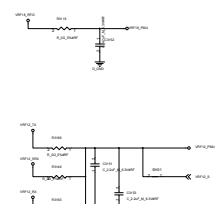
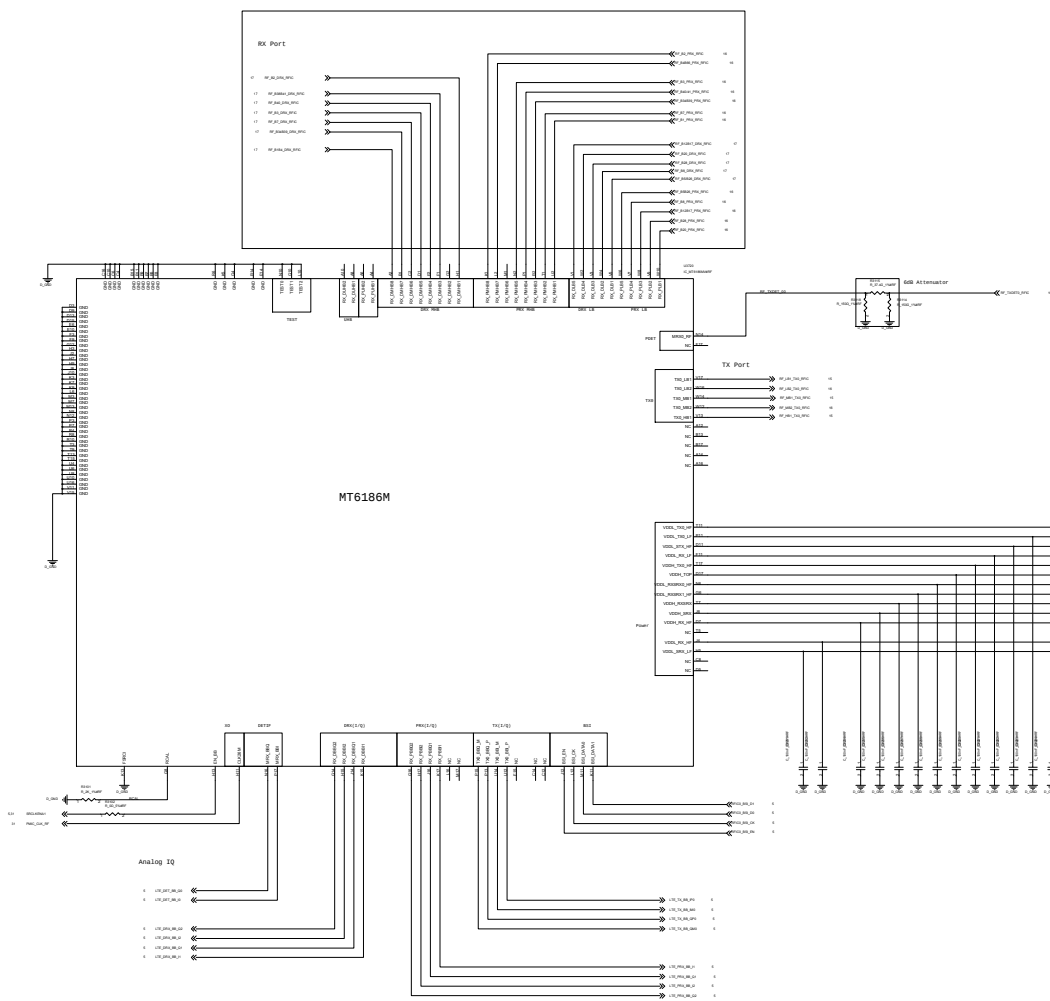
Company:		http://www.pcsengroup.com	
File name:			
Sheet name:		16_BB_AUXADC	
Draw by:	Jimmy	Page:	8 of 34
Rev:	10.0	Date:	7





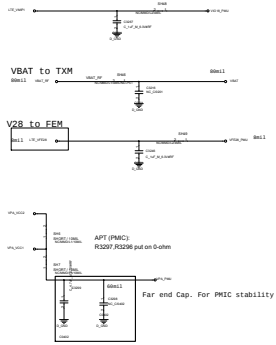
Switching Charger



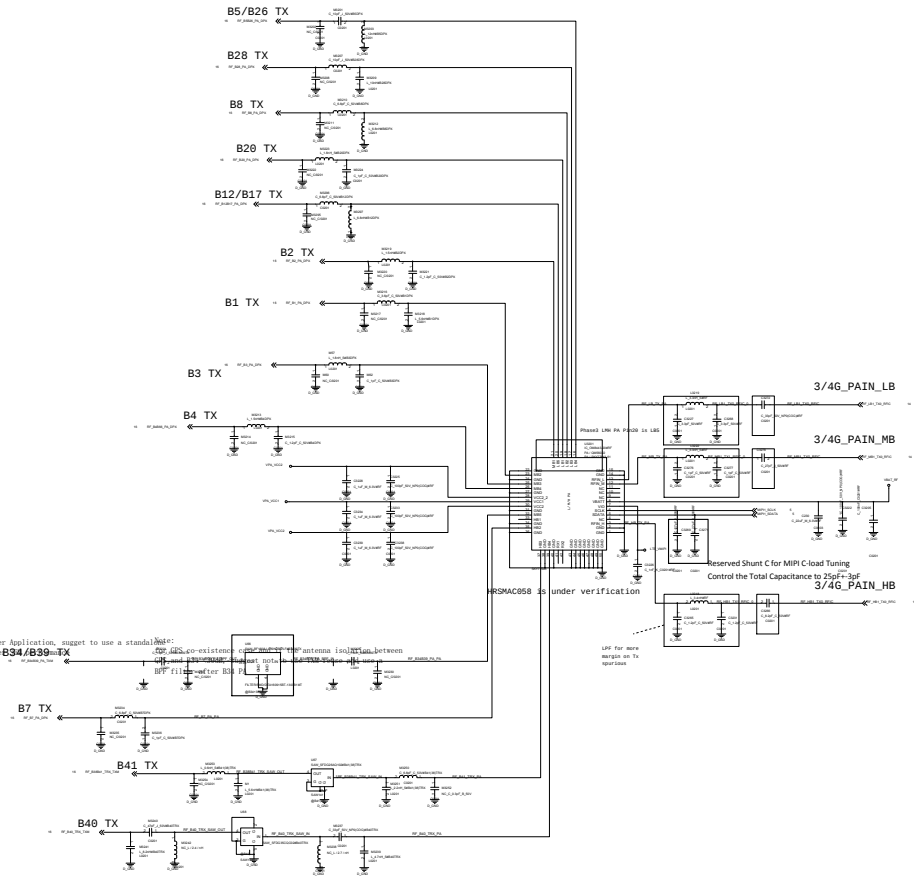


Title		Rev	
31_RF_MT6186M_PIN_OUT		1.0	
Author		Date	
Check		Date	

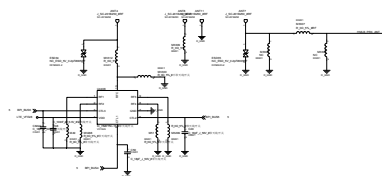
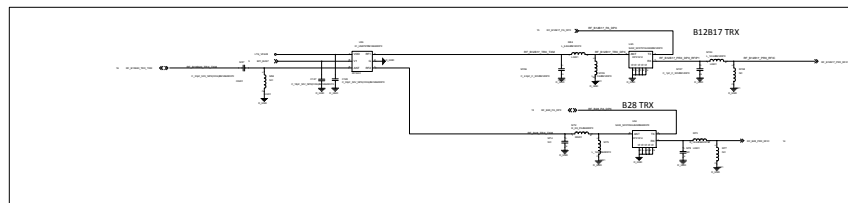
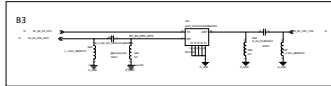
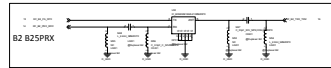
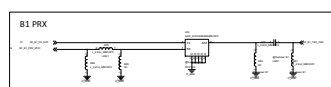
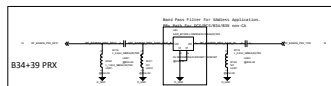
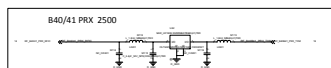
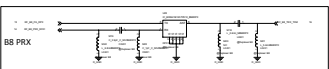
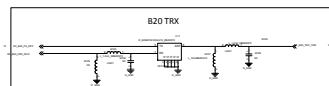
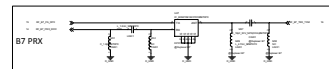
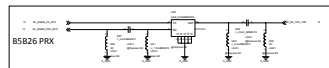
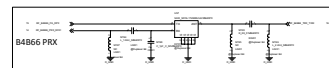
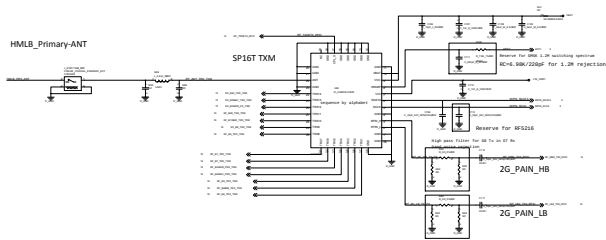
Power Net Connection



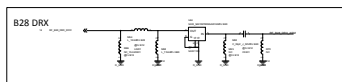
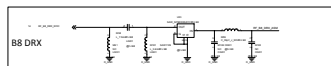
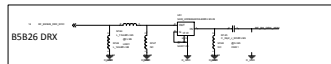
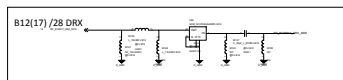
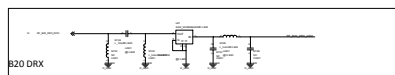
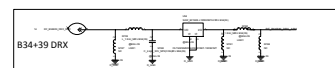
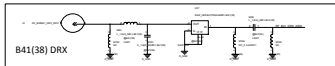
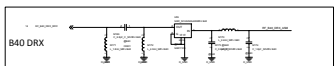
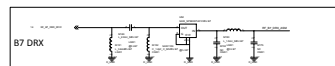
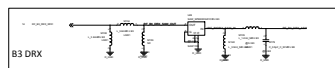
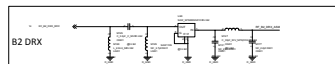
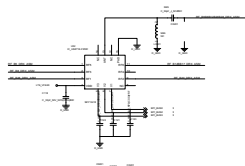
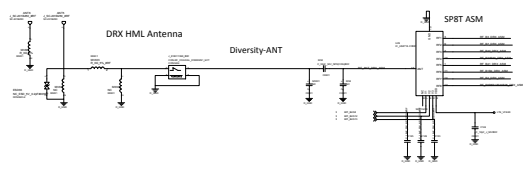
Note:
For B39 Multi-cluster Application, suggest to use a standard 100nF capacitor for B39 TX SW for better B39/B39-TX



Document	32_RF_MT6186M_RF_TX
Revision	1.0
Drawn by	John
Check by	John
Date	2018-08-15



Rev	1
Date	2010-01-01
By	RF
Check	RF
Appr	RF
Rev	1



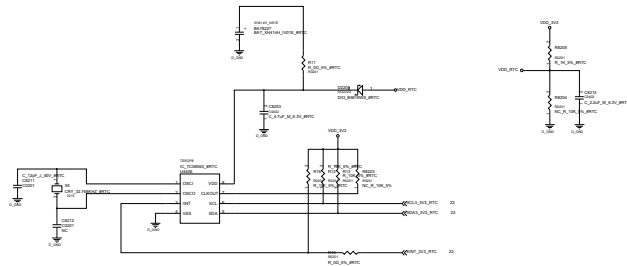
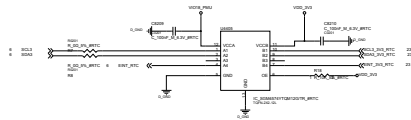
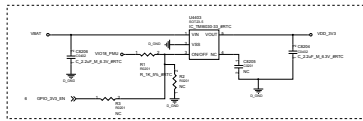


Company:		http://www.jysagroup.com	
File name:			
Sheet name:		61_PERI_AUDIO_PMIC-IF	
Draw by: Jimmy		Page: 20 of 34	
Rev:	HD-0	Draw:	Date: 7

Close to it Close to audio jack

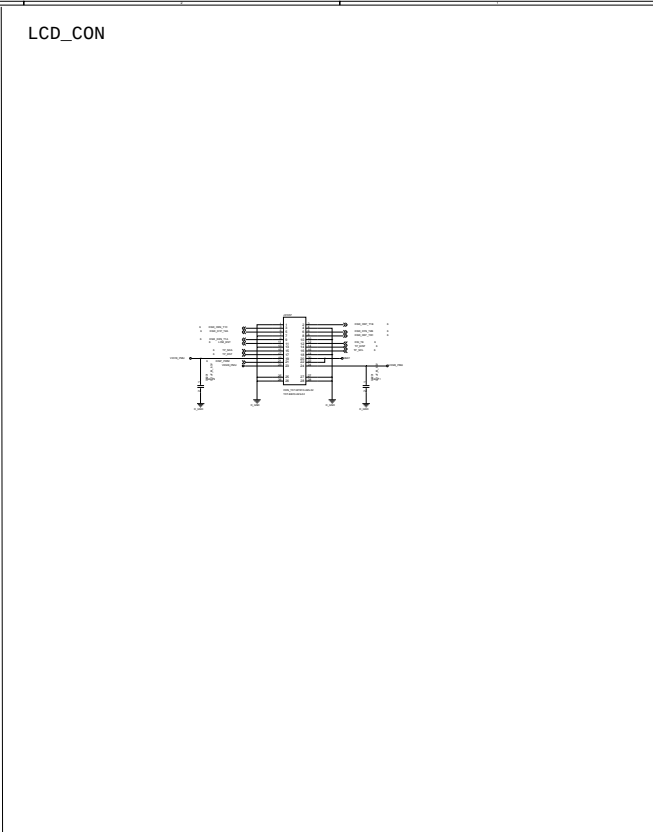


Platform	R73357	
Node	ACC mode	CC mode
8287	MP_83357	MP_83357
Key-Detection	ACCDET	RI_VDDA_P
CC2D9	3uP	0 GHz
CC2D9	3uP	0 GHz
RI2D9	3A	NC
RI2D8	1.5A	NC
RI2D9	NC	2.0GHz
CC2D1	6.7uP	0 GHz
RI2D9	0 GHz	NC
RI2D4	4.7A mode	4.0A mode



65_PERI_SPEAKER_AMP	
Rev	1
Rev	1
Rev	1

EMC



The diagram illustrates the LCM Backlight LED Driver circuit. It features a power supply section with **LCD_POWER** and **LCD_BIAS** inputs. A **100k** resistor and a **100nF** capacitor are connected to the power supply. The **LCM Backlight LED Driver** IC is shown with its input pins connected to the power supply and its output pins connected to the LED array. The LED array is represented by a series of diodes.

The diagram illustrates the LCM Backlight LED Driver circuit. It features a power supply section with **LCD_POWER** and **LCD_BIAS** inputs. A **100k** resistor and a **100nF** capacitor are connected to the power supply. The **LCM Backlight LED Driver** IC is shown with its input pins connected to the power supply and its output pins connected to the LED array. The LED array is represented by a series of diodes.

The diagram illustrates the LCM Backlight LED Driver circuit. It features a power supply section with **LCD_POWER** and **LCD_BIAS** inputs. A **100k** resistor and a **100nF** capacitor are connected to the power supply. The **LCM Backlight LED Driver** IC is shown with its input pins connected to the power supply and its output pins connected to the LED array. The LED array is represented by a series of diodes.

LCD_POWER

LCM Backlight LED Driver

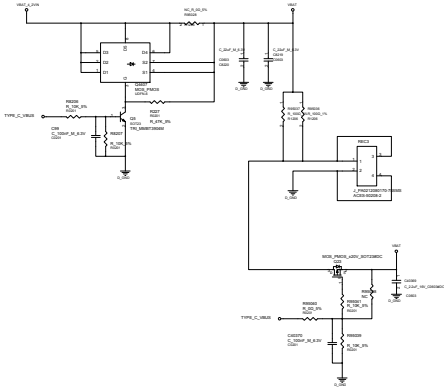
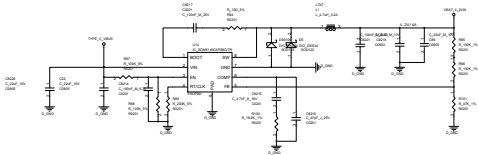
LCD_BIAS

Part Name		LCD_MODULE	
Part No.		LCD_MODULE	
Part Desc.		LCD_MODULE	
Part Qty.		1	
Part Unit		PCB	
Part Loc.		PCB	

Company:		http://www.joyangroup.com	
File name:		69_PERI_CAMERA	
Sheet name: ?		?	
Draw by: Jimmy		Page: 25 of 34	
Rev: H2.0	Size:	Date: ?	

Company:		http://www.joyargroup.com	
File name:			
Sheet name: 70_PERI_CAMERA_StereoCAM			
Draw by: Jimmy		Page: 26 of 34	
Rev: H2.0	Size:	Date: 7	

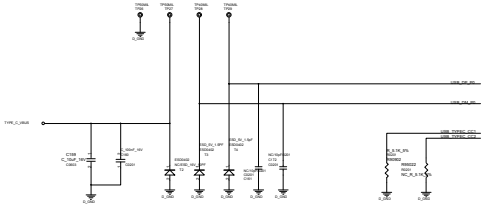
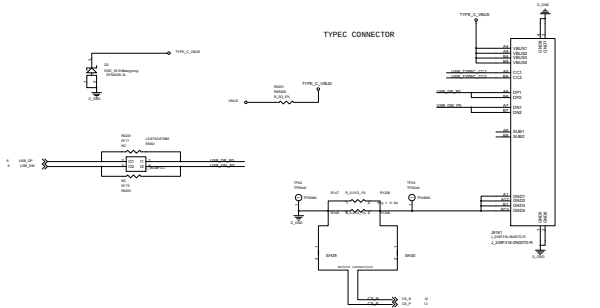
DCDC



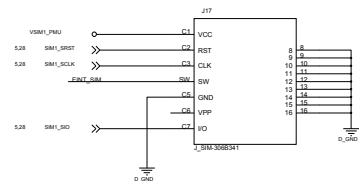
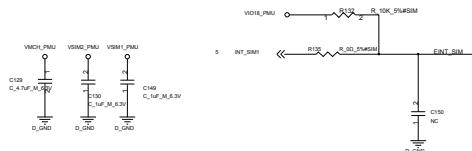
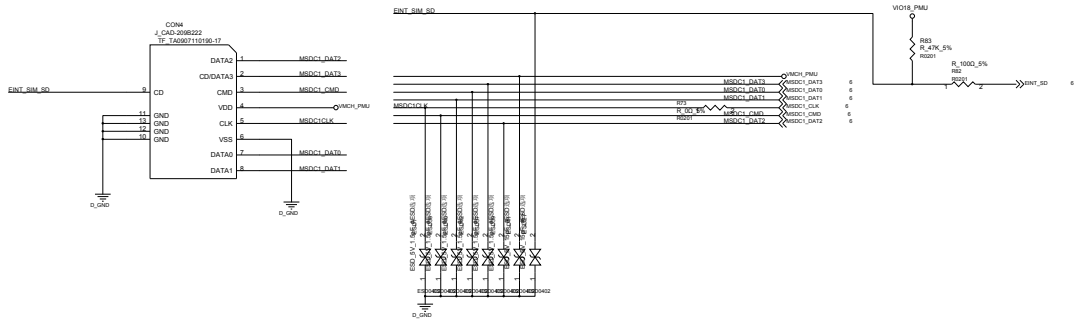
Schematic design notice of "71_PERI_USB_DTB" page.

Note 71-1:
For better ESD & surge performance we need choose suitable device for system protection.
Please refer to the latest version of (surge device selection guide) provided by MTK.

Type_c con



Company	http://www.mtksys.com		
File name	71_PERI_USB_DTB		
Drawn by	71_PERI_USB_DTB	Page	1 of 1
Checked by		Date	



Schematic design notice of "74_PERI_SIM" page.

Note 74-1: Let Vpp pin no connect when SIM function is not supported.

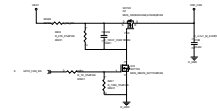
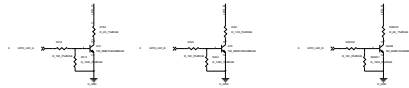
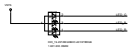
Note 74-2:

For better ESD & surge performance we need choose suitable device for system protection.
Please refer to the latest version of [Surge device selection guide] provided by MTK.

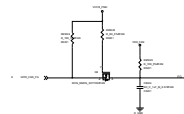
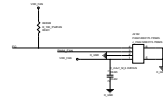
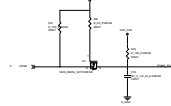
Company:		http://www.jyargroup.com	
File name:		74_PERI_SIM	
Sheet name:		7	
Draw by:		Jimmy	
Rev:		H20	
Size:		Date: 7	

USB_SWITCH

USB_HUB

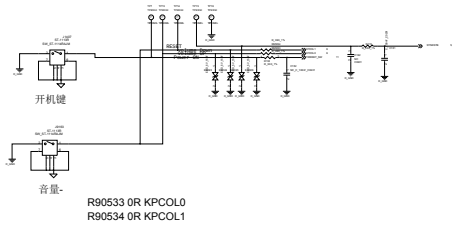


FAN



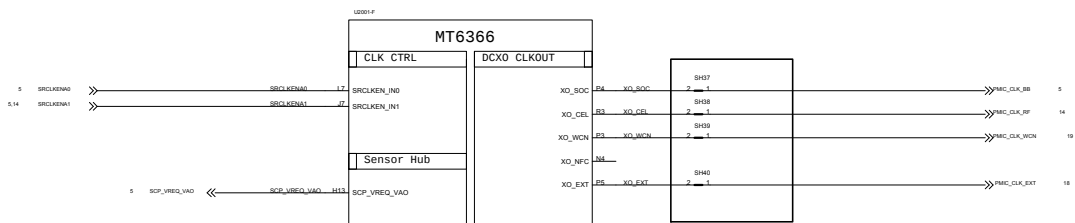
Power Key / Key Pad

DO NOT put pull-up resistor on PWRKEY



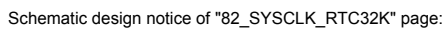
Doc No.	0000000000000000
Doc Name	75_PEN_KEYPAD
Doc Version	1.0
Doc Date	2023.12.14
Doc Author	0000000000000000
Doc Reviewer	0000000000000000

G-Sensor	HALL	VIBRATOR
Accerometer + Gyro Sensor	ALS & PS Sensor	FingerPrint
Schematic design notice of "77_PERI_SENSORS_MEMs_ALS/PS" page. Note 77-1: [M sensor] Keep a minimum distance of 15mm from power ICs / PCB traces of more than 100mA / magnet component. Check HW design notice for more detail. Note 77-2: [A+G] For optimized GPS performance, please check HW design notice for Sensor selection guide. Note 77-3: [A+G] MUST use SPI for optimized sensor hub performance DO NOT USE I2C Note 77-4: [A+G] Suggest choose sensor support FIFO watermark interrupt otherwise we cannot support H6-sensor, daydream VR. And Sensor-location accuracy will become worse. Note 77-5: [Baro] Reserve Baro sensor for LPFe feature (Must for North America Operator / NA SKU) Note 77-6: DO NOT share Sensor hub I2C to other non-SCP device. Note 77-7: Interrupt pin of MEMS sensor must be assign to SCP_EINT Company 77_PERI_SENSORS_MEMs_ALS/PS Sheet name 1 1 Sheet type none Page 1 of 16 Rev: 000 Rev: 000 Date: 1		

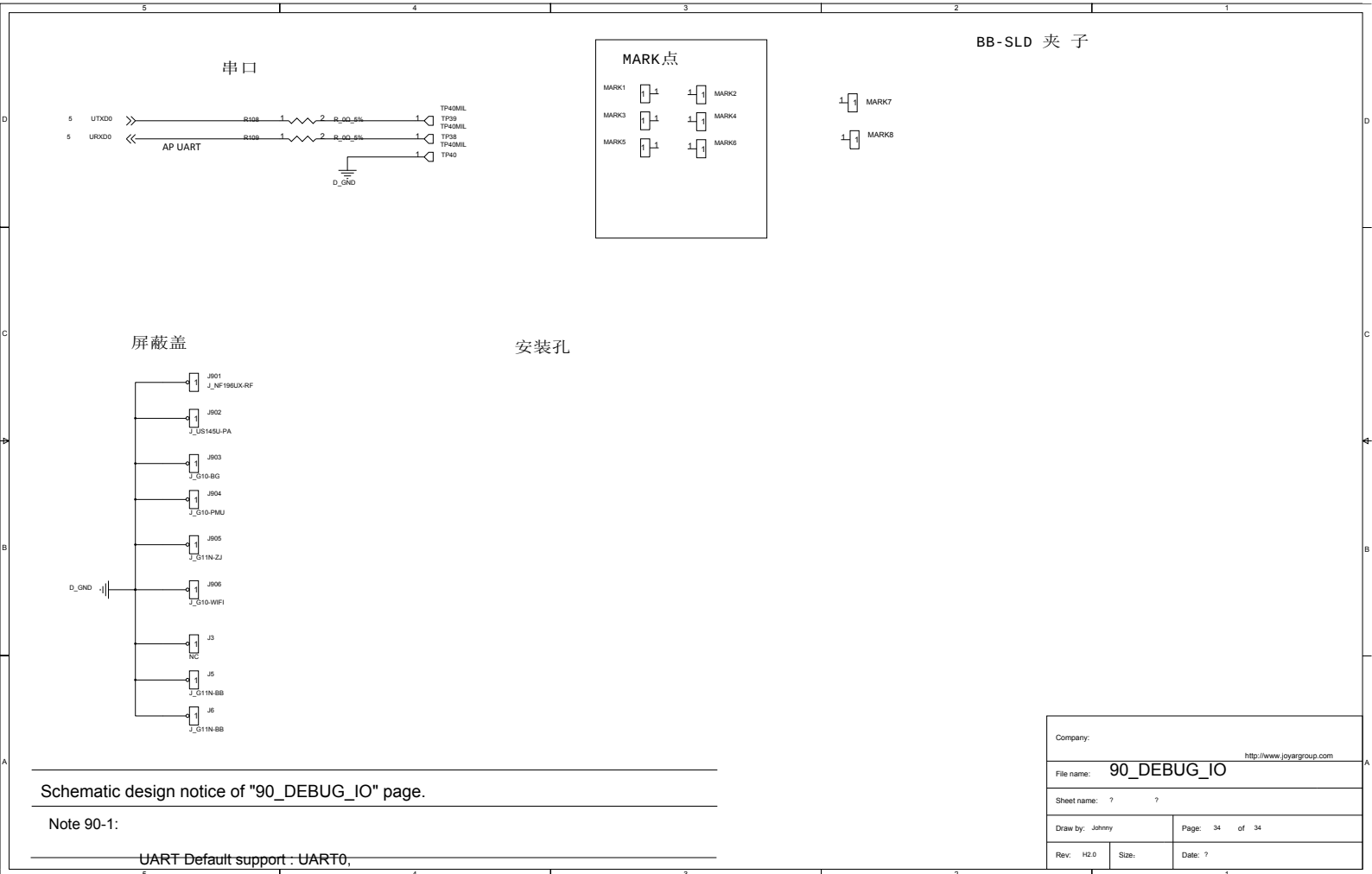


Schematic design notice of "80_SYSCLK_CoClock-26MHz" page:

Company: http://www.psagroup.com			
File name: 80_SYSCLK_CoClock-26MHz			
Sheet name: 1 1			
Draw by: Jimmy		Page: 31 of 34	
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Company:		http://www.jyargroup.com	
File name:		82_SYSCLK_RTC32K	
Sheet name:		7 7	
Draw by: Johnny		Page: 33 of 34	
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Schematic design notice of "90_DEBUG_IO" page.

Note 90-1:

UART Default support : UART0,
JTAG Default support : AP
JTAG/MD_IO_JTAG

Company: <http://www.joyargroup.com>

File name: 90_DEBUG_IO

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