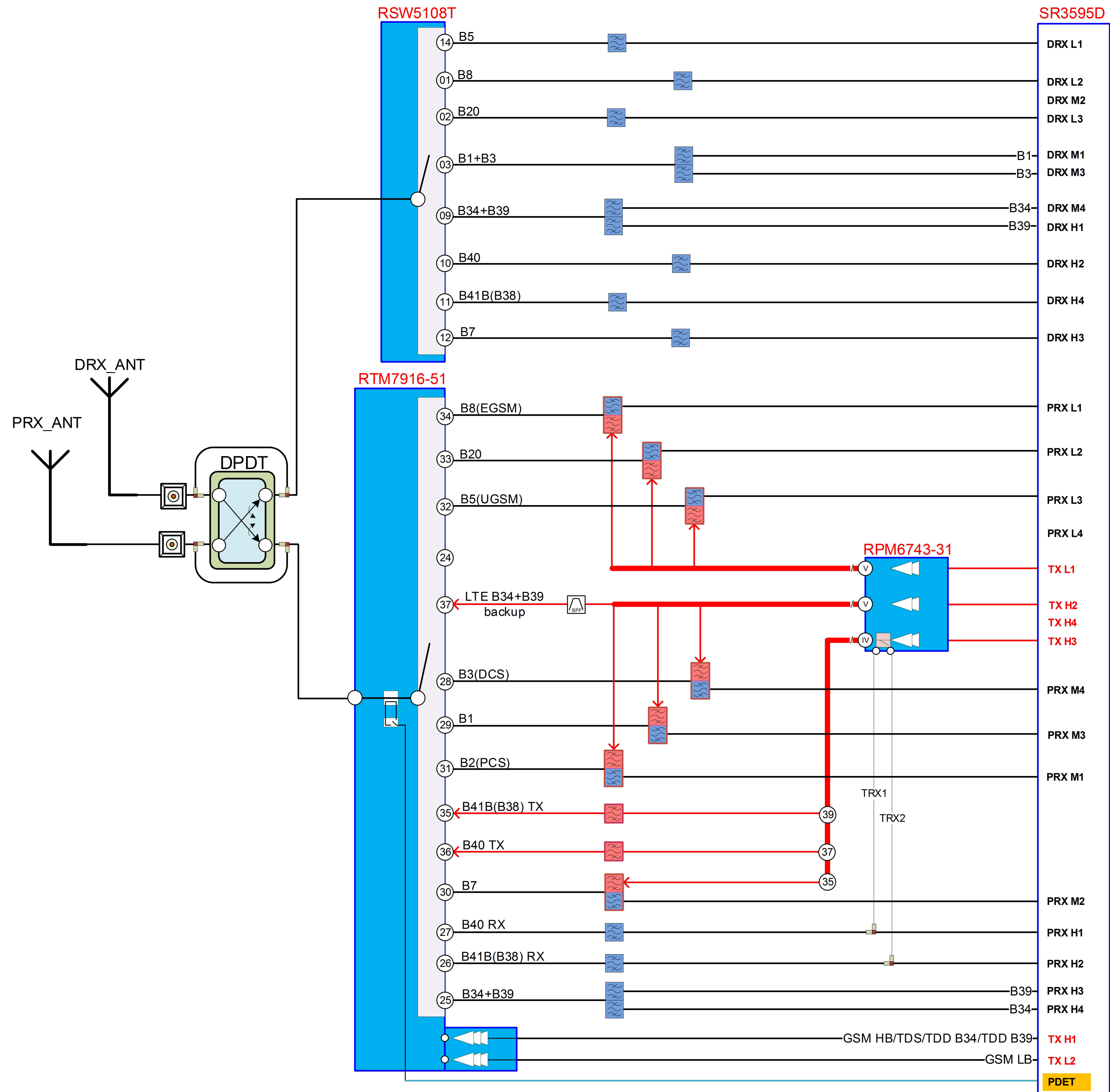


RF BLOCK DIAGRAM

REVISION RECORD			
REV	REV NO.	APPROVED	DATE



Platform:
UMS9230+SR3595D

Product support:
China+Europe+India

Band support:
 TDD LTE: B34/39/40/41B(38)
 FDD LTE: B1/3/5/7/8/20
 WCDMA: B1/2/5/8
 TDSCDMA: B34/39
 GSM: B2/3/5/8
 DL CA: 1C, 3C, 5B, 7B, 7C, 8B, 38C, 39C, 40C, 41C
 3A-3A, 5A-5A, 7A-7A, 40A-40A, 41A-41A
 UL CA: 1C, 3C, 7C, 8B, 38C, 39C, 40C, 41C

Key Part:
 RTM7916-51
 RPM6743-31
 RSW5108T

DESIGNER:		DATE:		DEPARTMENT:	
EQA:		DATE:		HWE	
APPROVED:		DATE:		COMPANY:	
				UNISOC	
CONFIDENTIAL AND PROPRIETARY					

V0.1.0

UMS9230_D_CS_A_SCH_V0.1.0

Change_Note

REVISION RECORD			
DATE	REV NO.	APPROVED	DATE

D

D

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DESIGNER:		DATED:		DEPARTMENT:	
EQA:		DATED:		HWE	
APPROVED:		DATED:		UNISOC	
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TITLE & REV:	
DOCUMENT NO.:	

Change_Note

REVISION RECORD			
REV	REV NO.	APPROVED	DATE

V0.1.0

UMS9230_D_CS_A_SCH_V0.1.0

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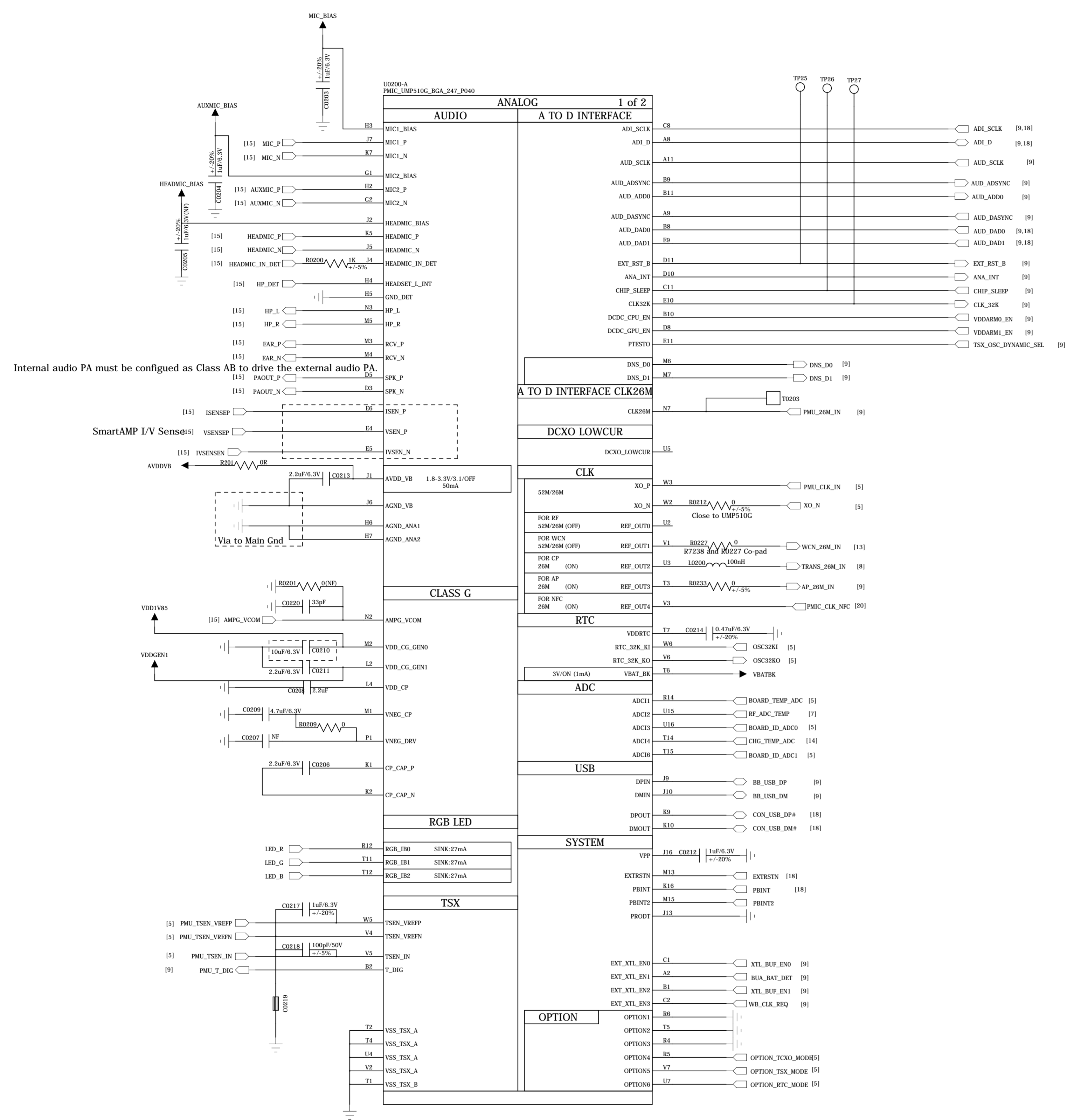
A

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DESIGNER:		DATE:	TITLE & REV:
EQA:		DATE:	DOCUMENT NO.:
APPROVED:		DATE:	DEPARTMENT: HWE
			COMPANY: UNISOC
			CONFIDENTIAL AND PROPRIETARY

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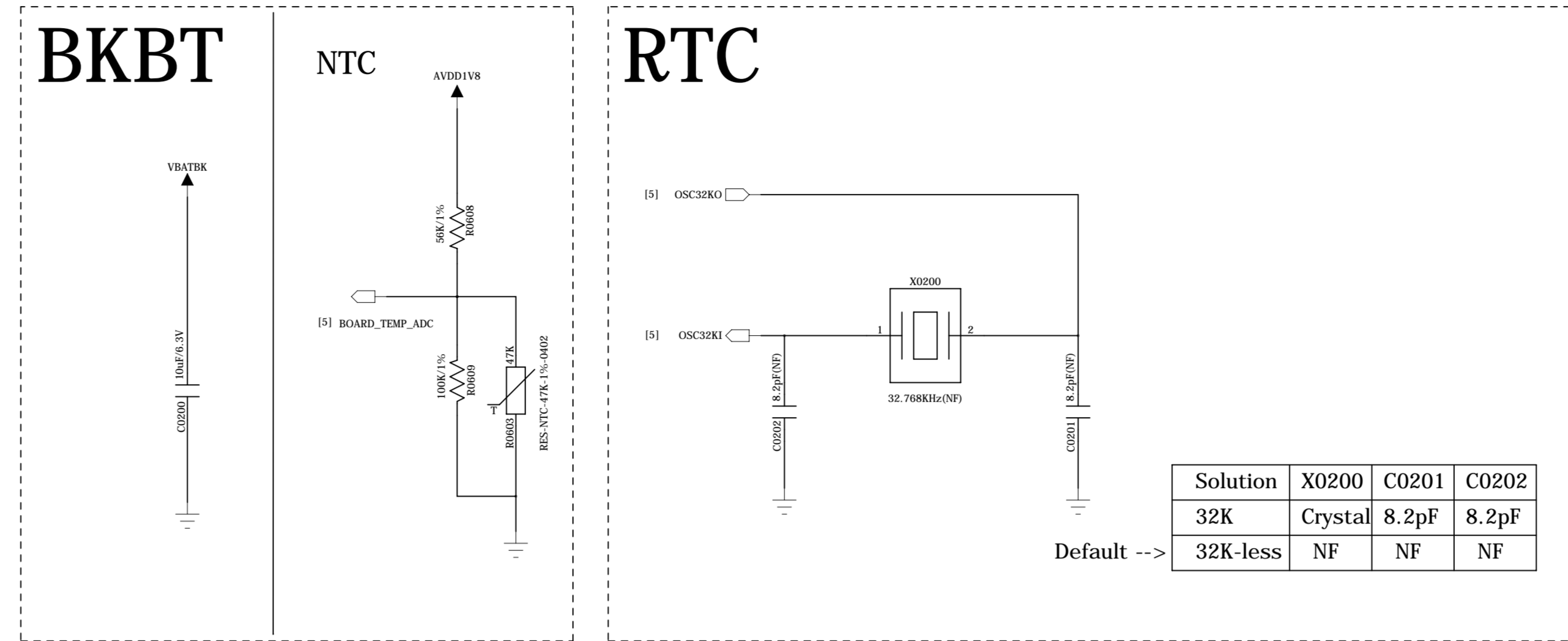
PMU_ANALOG



Internal audio PA must be configured as Class AB to drive the external audio PA

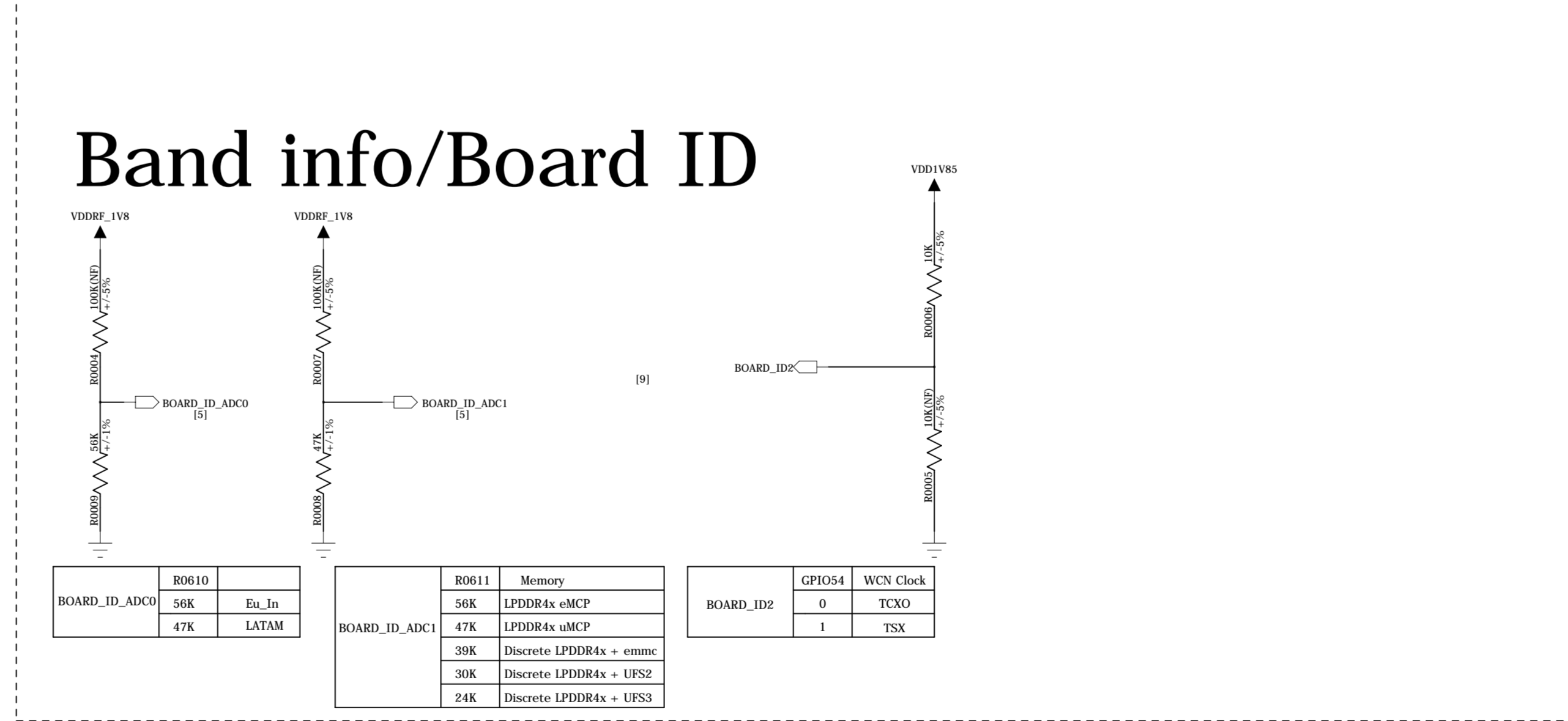
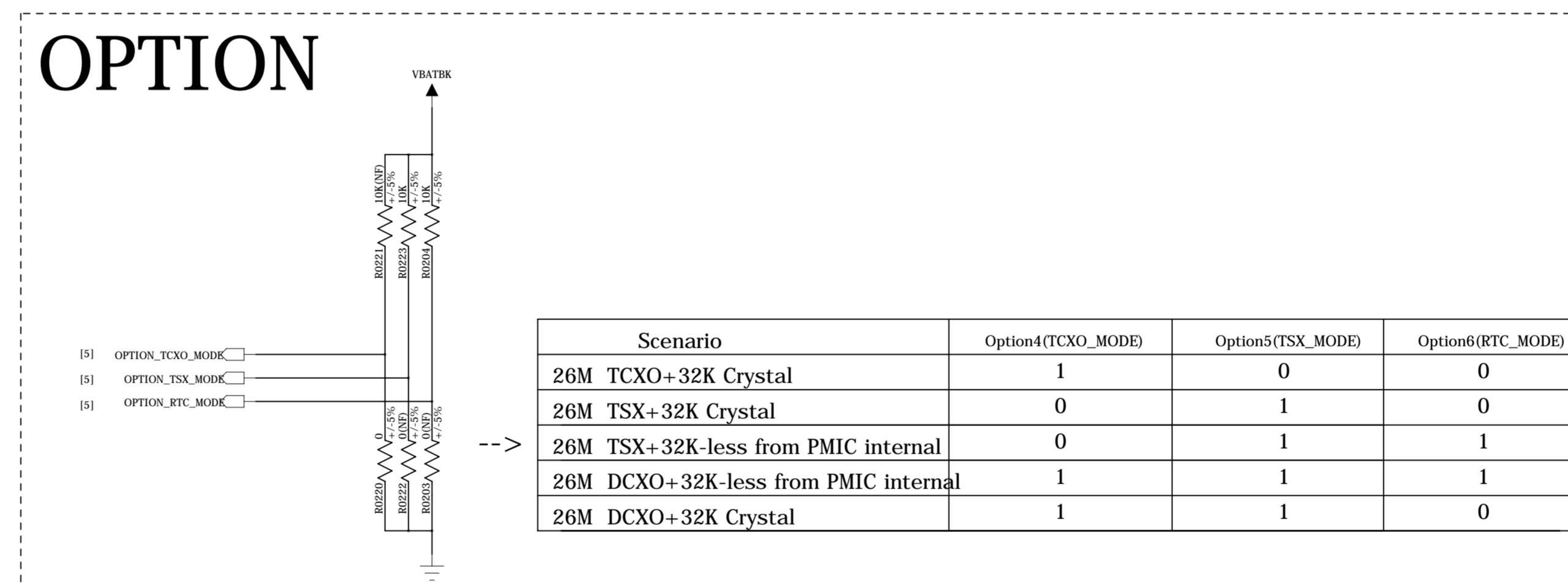
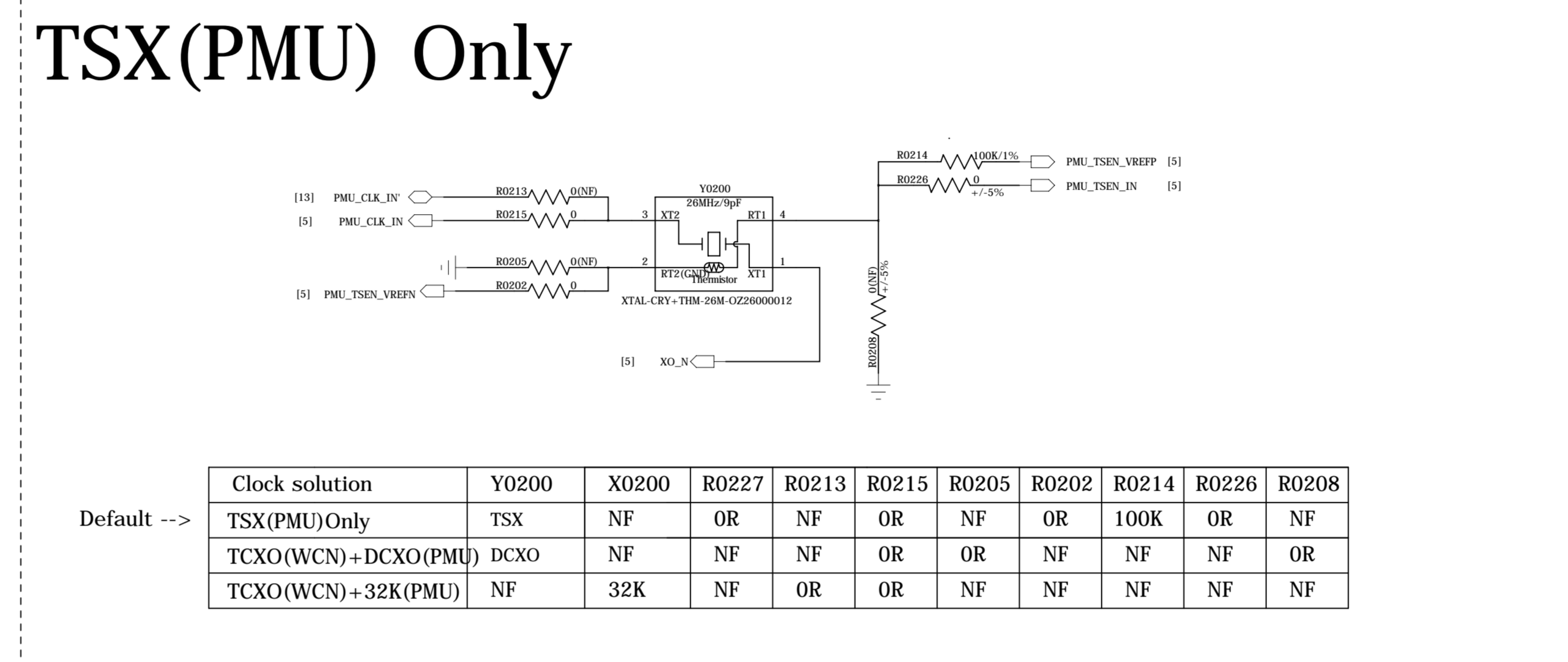
SmartAMP I/V Senses

VDD to Main Gnd



Solution	X0200	C0201	C0202
32K	Crystal	8.2pF	8.2pF
32K-less	NF	NF	NF

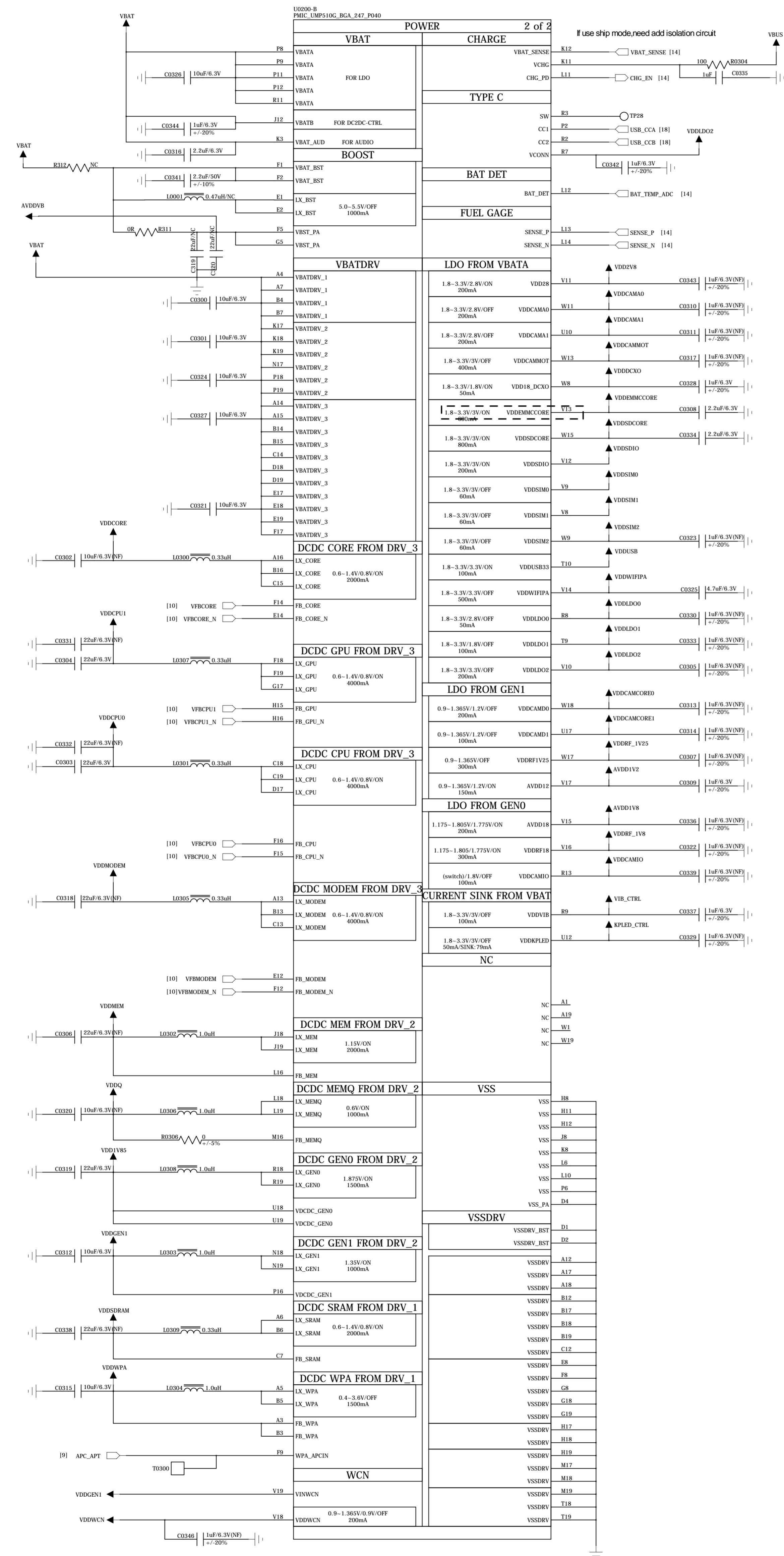
Default -->



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PMU_POWER



PMIC PN	PMIC VDDEMCCORE
UMP510G	3.0V/600mA

Memory	UPS VCC	UPS VCCQ	UPS VCCQ2

UPS2.X Device: PMIC VDDEMCCORE 3.0V(Back up external LDO or DCDC) Not Used
PMIC VDD1V85

Notes: If UPS ICC is greater than 600mA, please use external LDO or DCDC.

UMP510G DCDC output

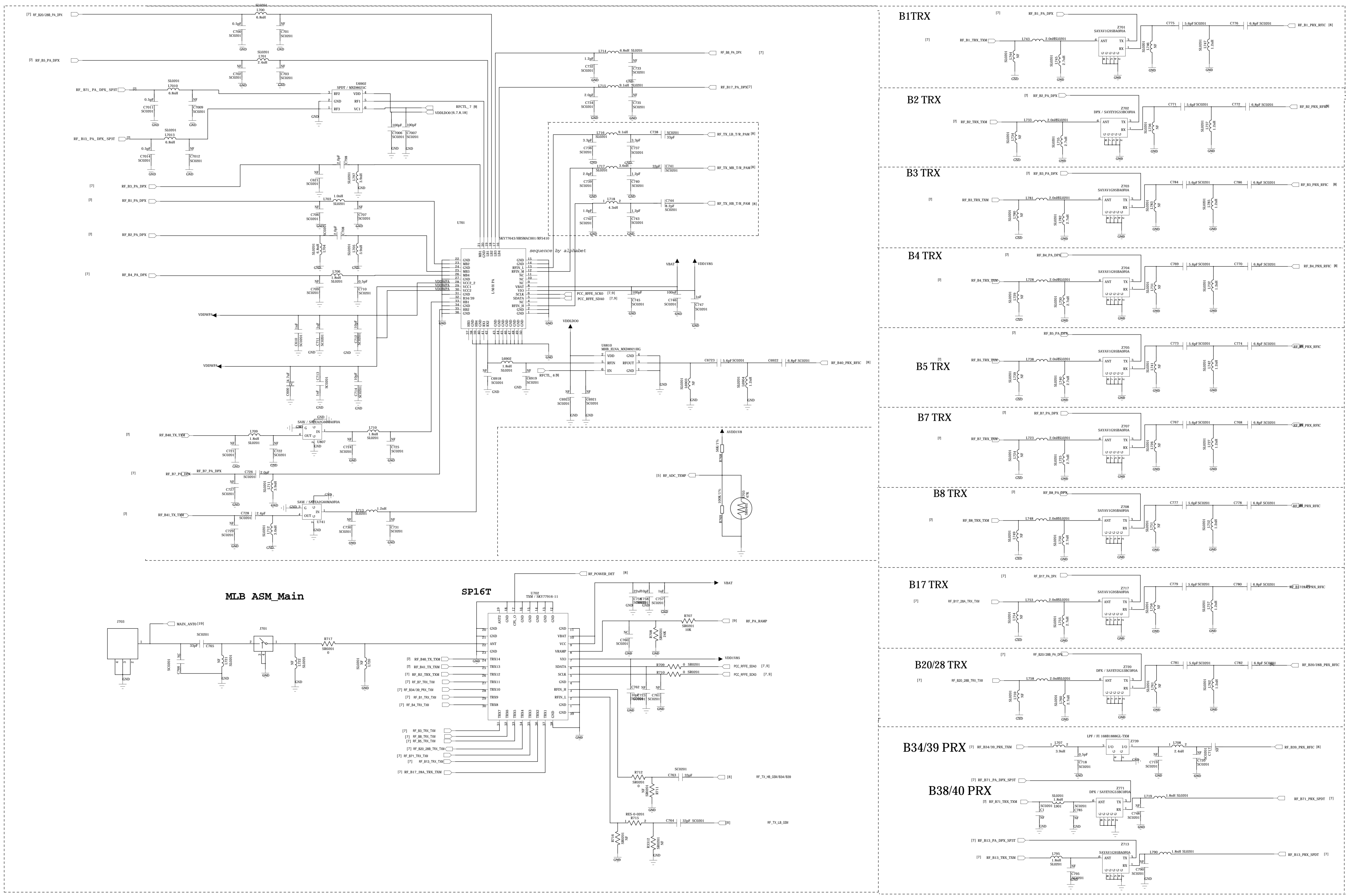
Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default
VBAT	DCDC CORE	0.3-1.3	2000	0.8	ON
	DCDC GPU	0.3-1.3	4000	0.8	ON
	DCDC CPU	0.3-1.3	4000	0.8	ON
	DCDC MODEM	0.3-1.3	4000	0.8	ON
	DCDC MEM	0.25-1.25	2000	1.15	ON
	DCDC MEMQ	0.25-1.25	1000	0.6	ON
	DCDC GEN0	1.8-2.5	1500	1.85	ON
	DCDC GEN1	0.6-1.5	1000	1.3	ON
	DCDC SRAM	0.3-1.3	2000	0.8	ON
	DCDC WPA	0.6-3.3	1500	/	OFF

UMP510G LDO output

Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default	
VBAT	VDD28	1.8-3.3	200	2.8	ON	
	VDDCAMA0	1.8-3.3	200	2.8	OFF	
	VDDCAMA1	1.8-3.3	200	2.8	OFF	
	VDDCAMPOT	1.8-3.3	400	3	OFF	
	VDD18_DCXO	1.8-3.3	50	1.8	ON	
	VDDMMCCORE	1.8-3.3	600	3	ON	
	VDDSDCORE	1.8-3.3	800	3	ON	
	VDDSDIO	1.8-3.3	200	3	ON	
	VDDSIM0	1.8-3.3	60	3	OFF	
	VDDSIM1	1.8-3.3	60	3	OFF	
	VDDSIM2	1.8-3.3	60	3	OFF	
	VDDUSB33	1.8-3.3	100	3.3	ON	
	VDDWIFIPA	1.8-3.3	500	3.3	OFF	
	VDDLDO0	1.8-3.3	50	2.8	OFF	
	VDDLDO1	1.8-3.3	100	3.3	OFF	
	VDDLDO2	1.8-3.3	200	3.3	OFF	
	GEN1	VDDCAMD0	1.0-1.25	400	1.1	OFF
		VDDCAMD1	1.0-1.25	200	1.1	OFF
		VDDRF1V25	1.0-1.25	300	1.25	OFF
	GEN0	AVDD12	1.0-1.25	150	1.2	ON
AVDD18		1.2-1.8	200	1.8	ON	
GEN1	VDDRF18	1.2-1.8	300	1.8	ON	
	VDDCAMIO	1.2-1.8	100	1.8	OFF	
GEN1	WCN	1.0-1.6	200	1	OFF	

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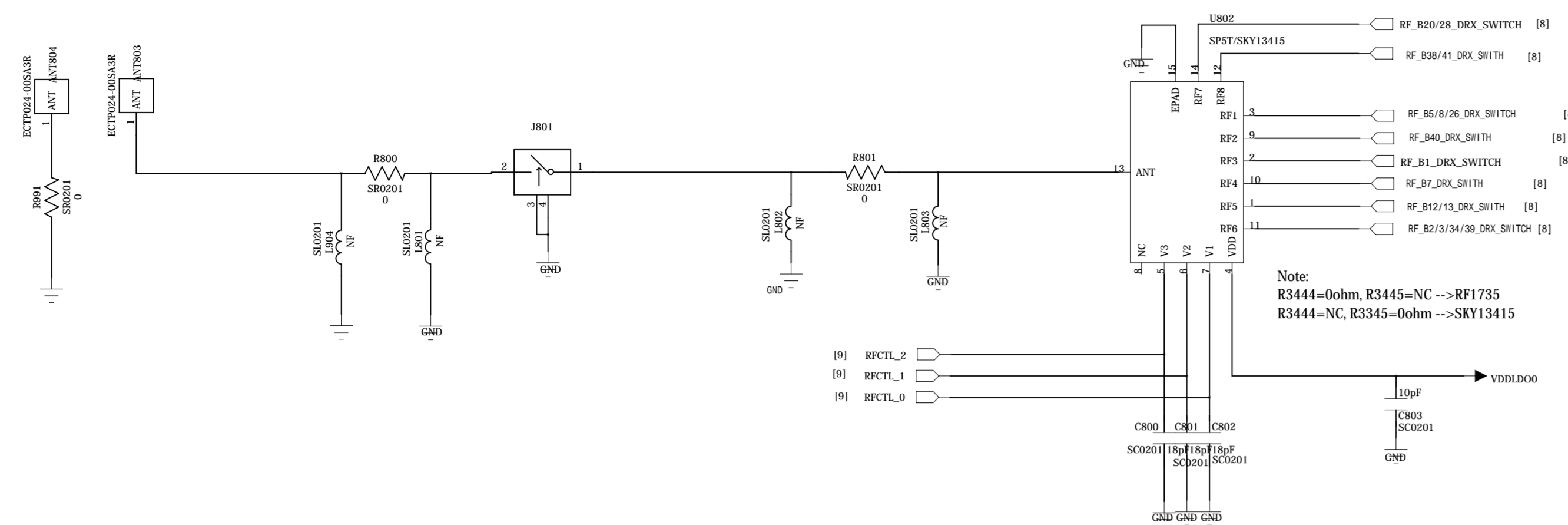
B

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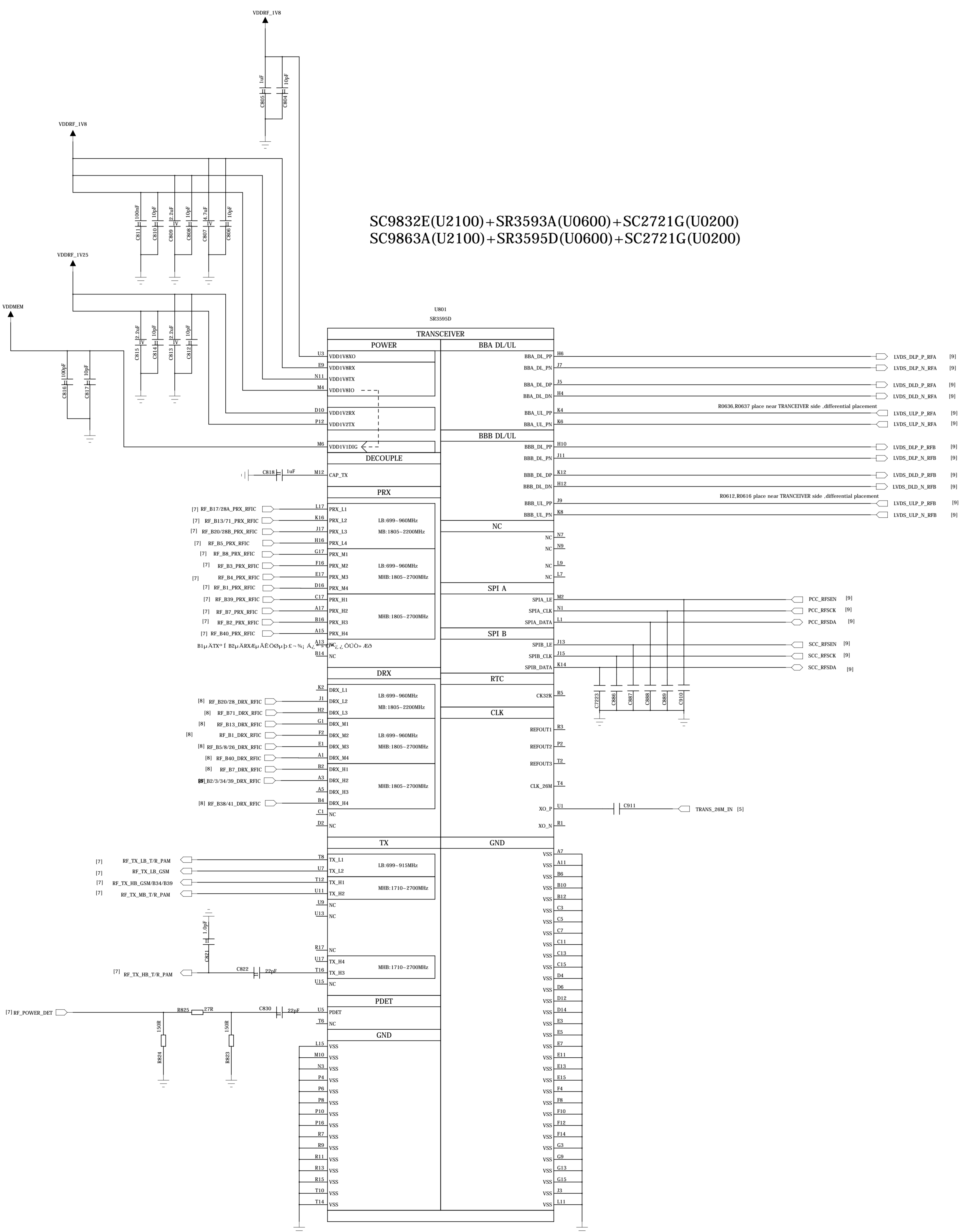
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MLB SP5T

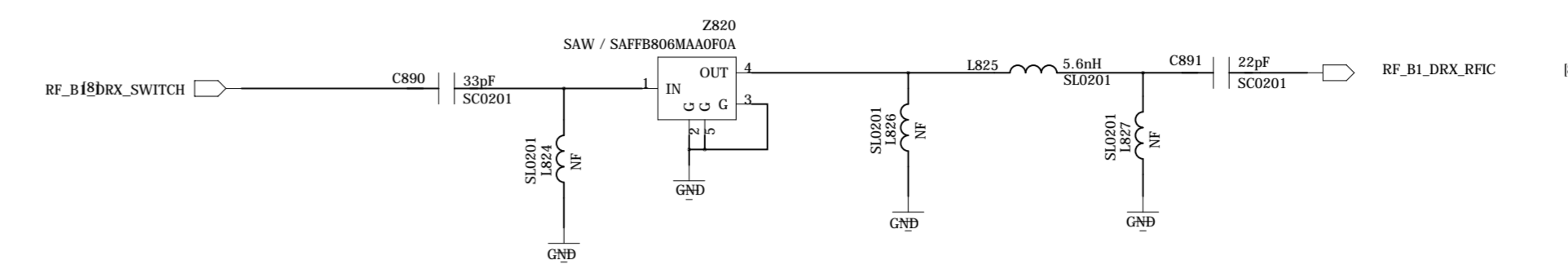


Note:
R3444-0ohm, R3445-NC --> RF1735
R3444-NC, R3445-0ohm --> SBY13415

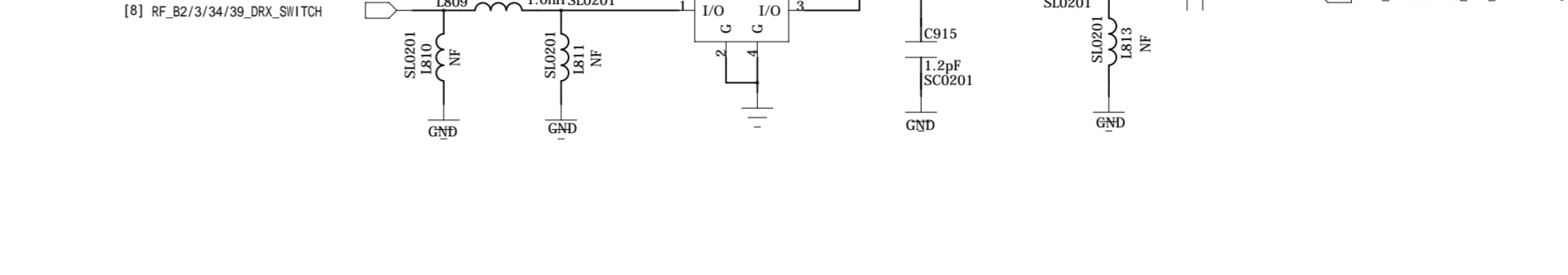
**SC9832E(U2100)+SR3593A(U0600)+SC2721G(U0200)
SC9863A(U2100)+SR3595D(U0600)+SC2721G(U0200)**



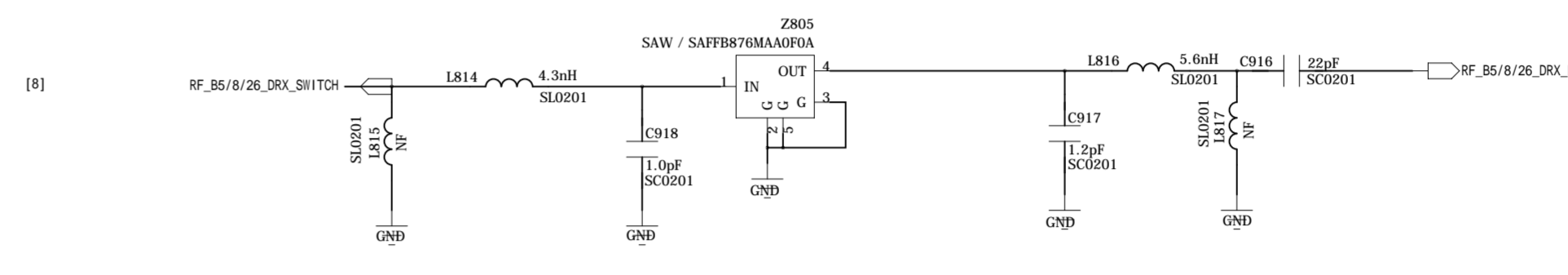
B1 DRX



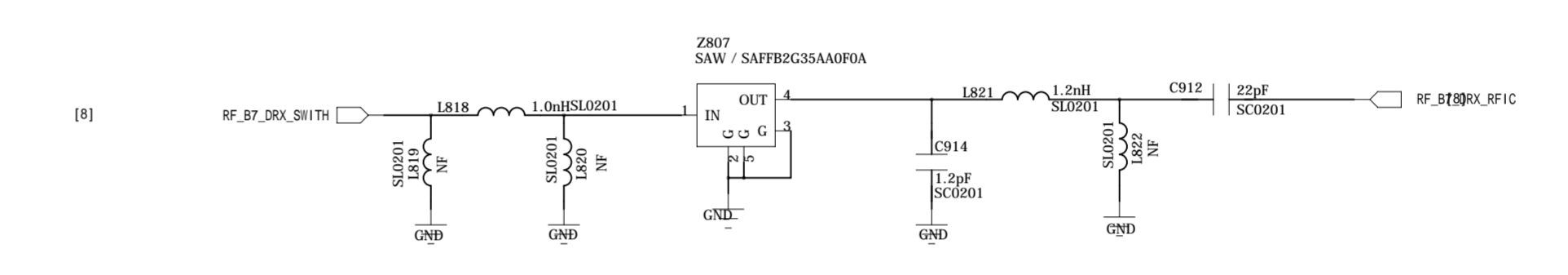
B2 DRX



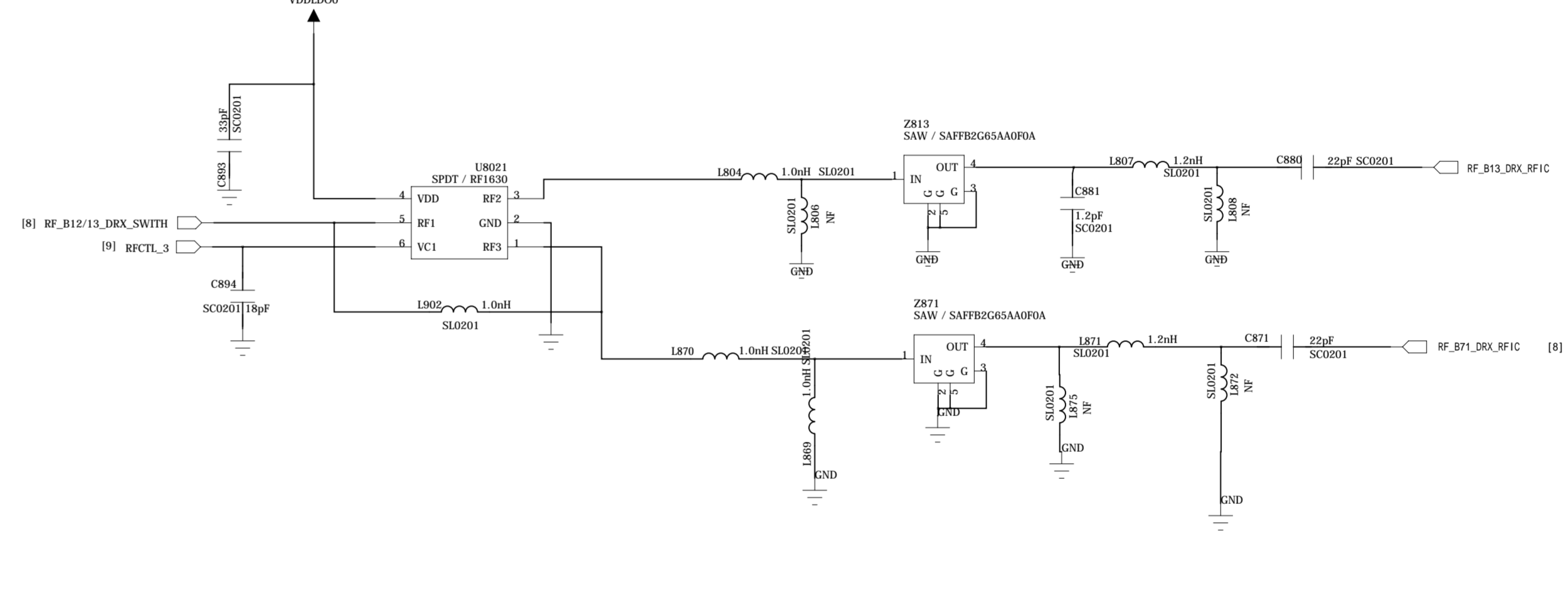
B26(B5) DRX



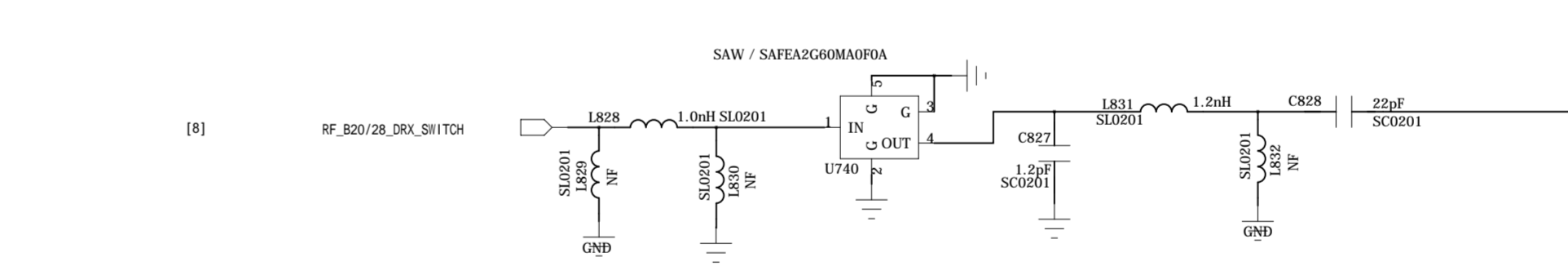
B7 DRX



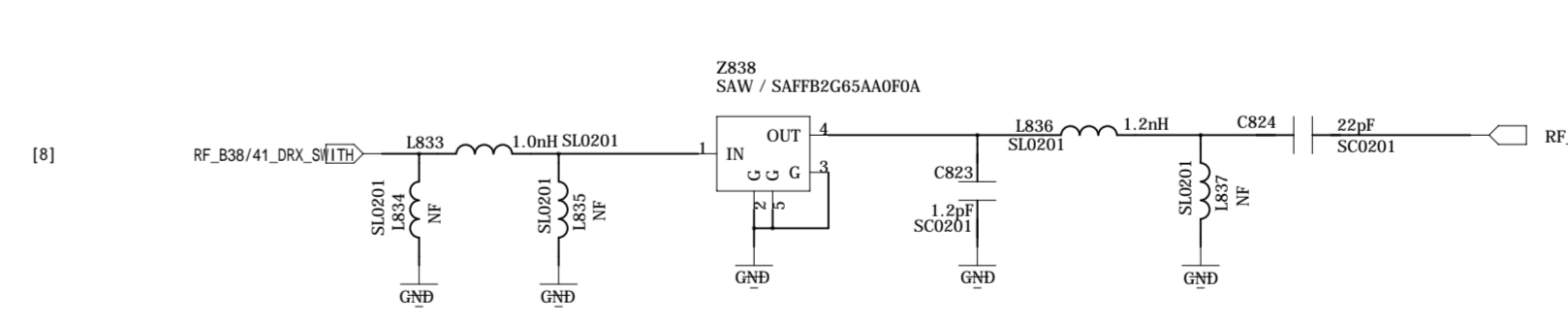
B12/13DRX



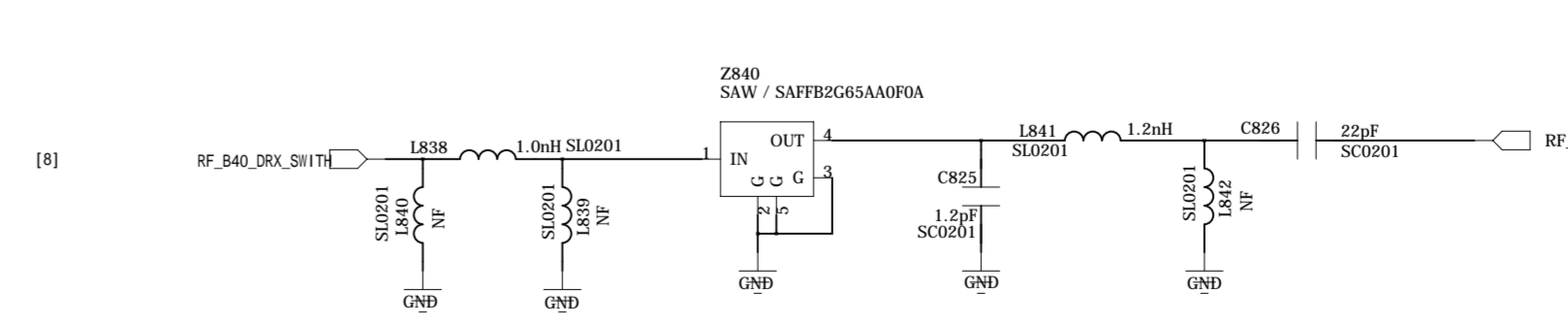
B20 DRX



B38 DRX



B40 DRX



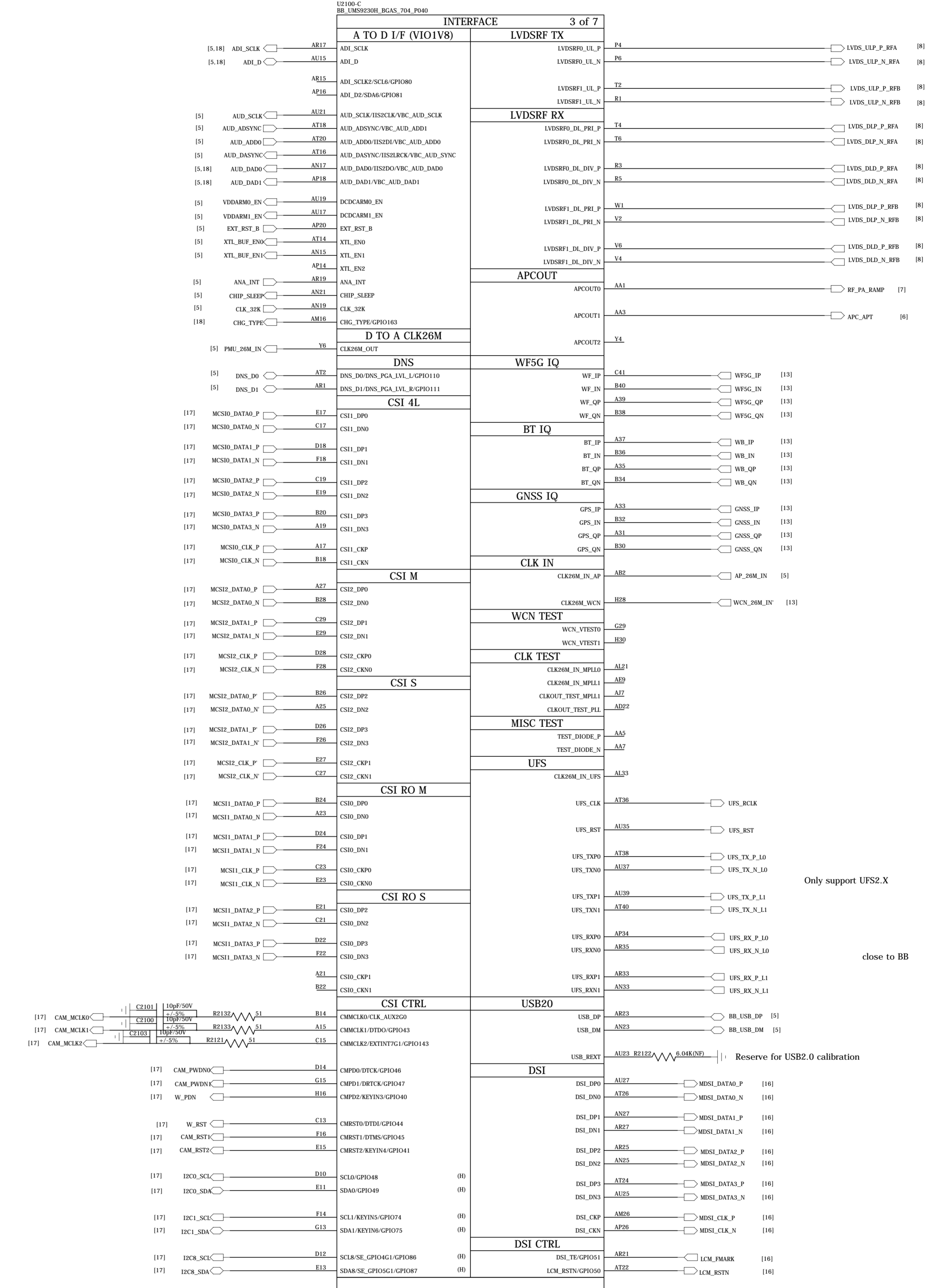
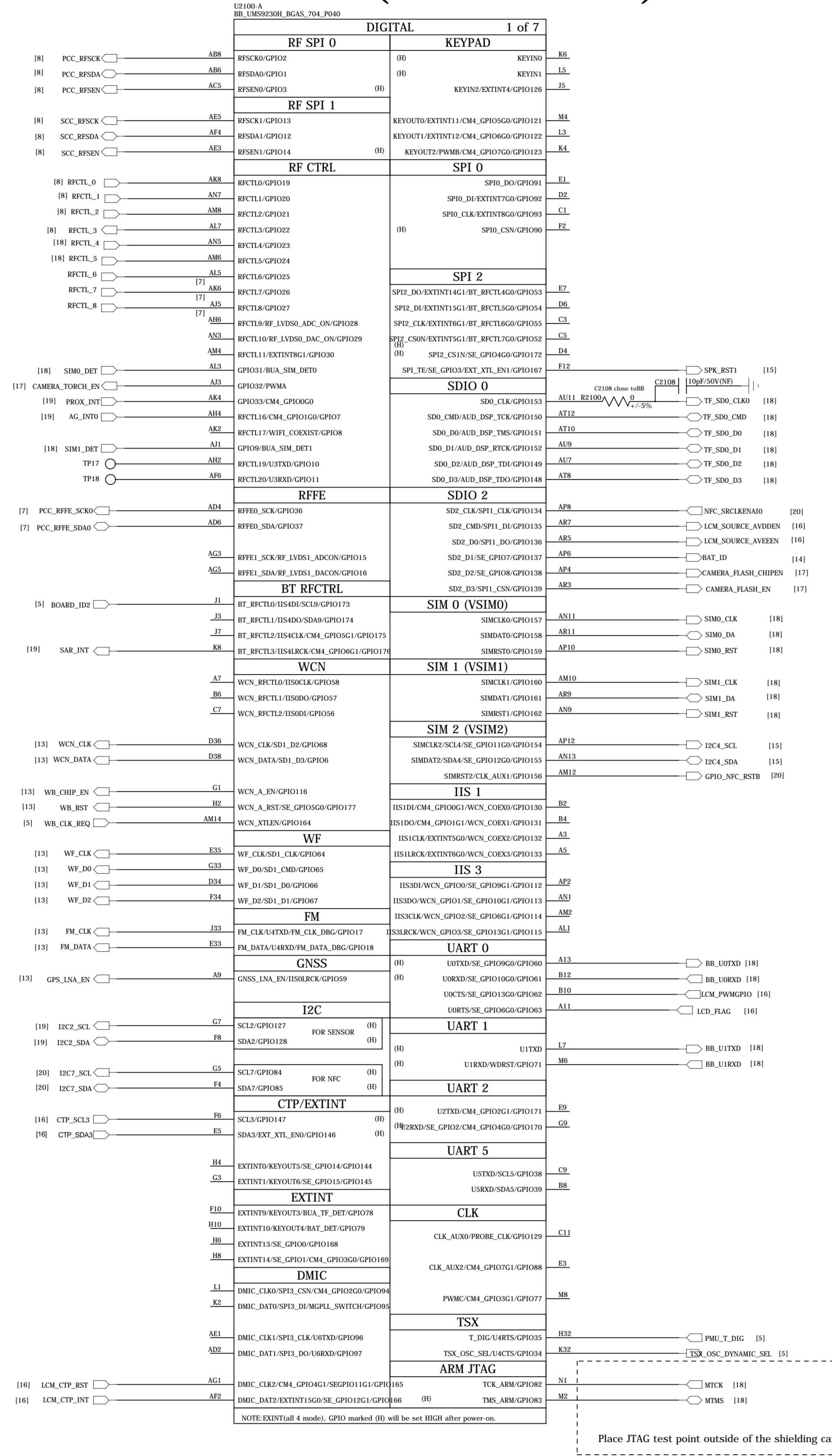
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DOCUMENT NO.:			
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DIGITAL

Design notes: In this reference design, T612/T606 can't change to T616(UMS9230T)

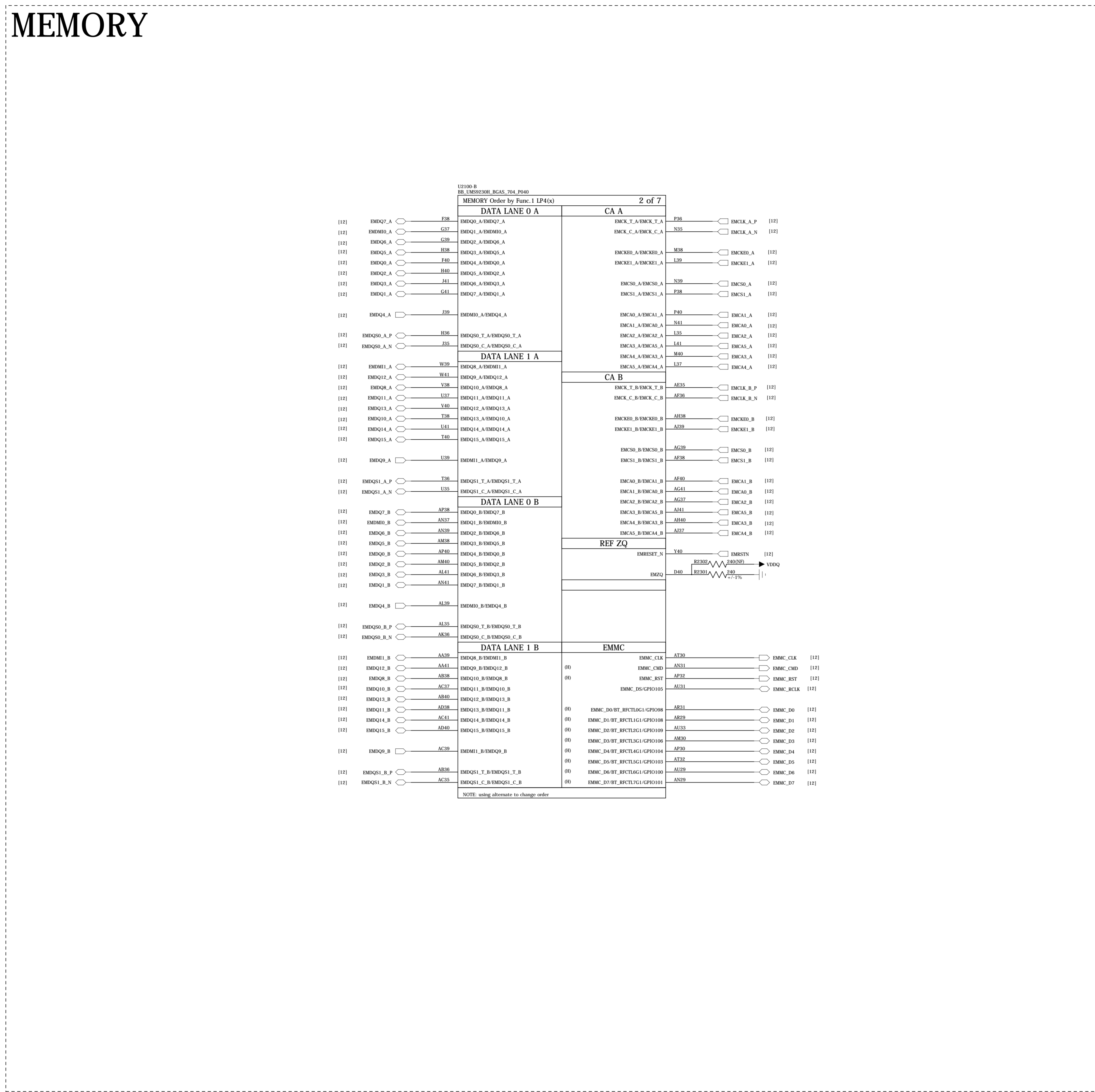
T606/T612(UMS9230H)



Only support UFS2.X
close to BB

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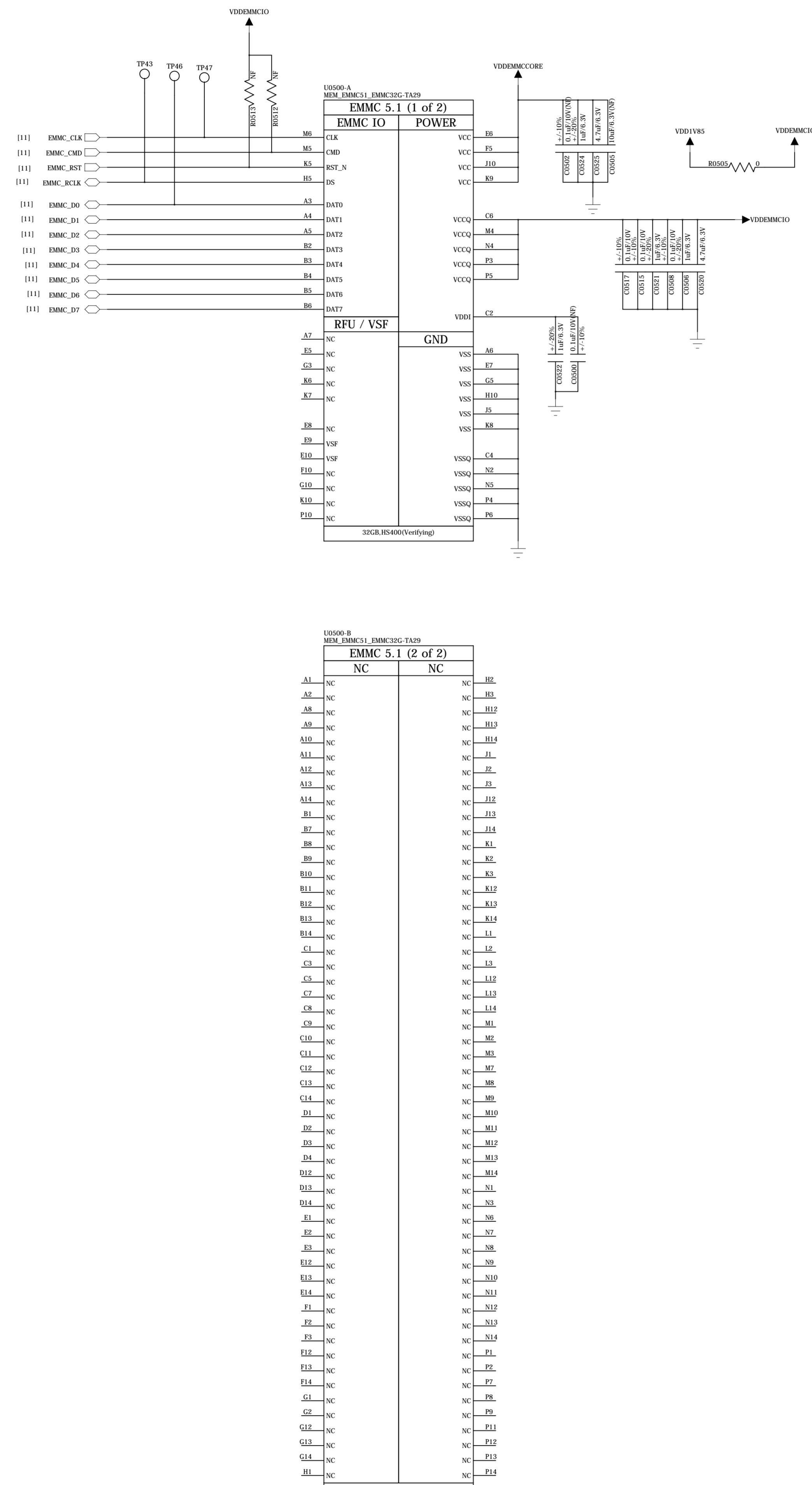
REVISION RECORD			
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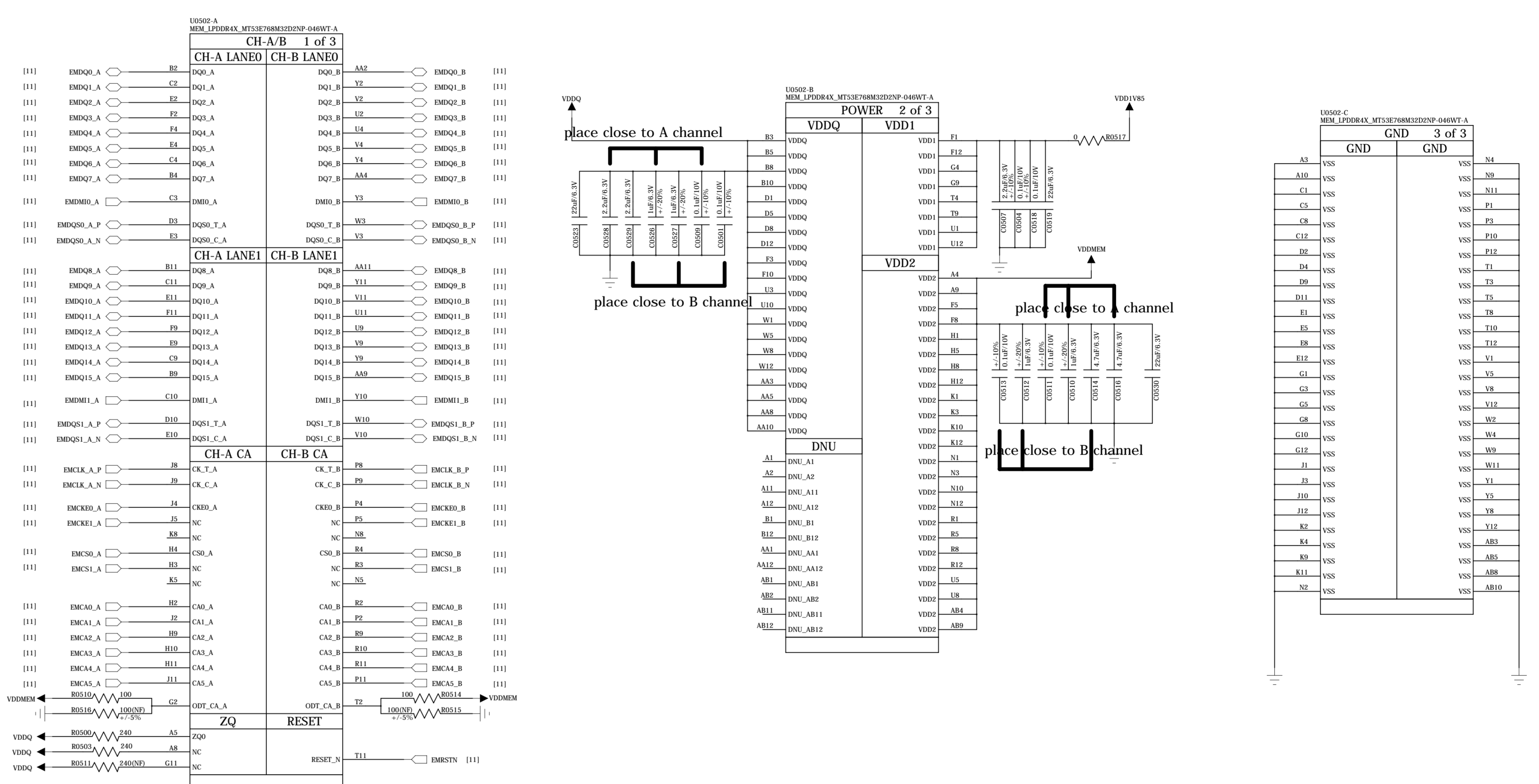
TITLE & REV:			
DOCUMENT NO.:			
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eMMC



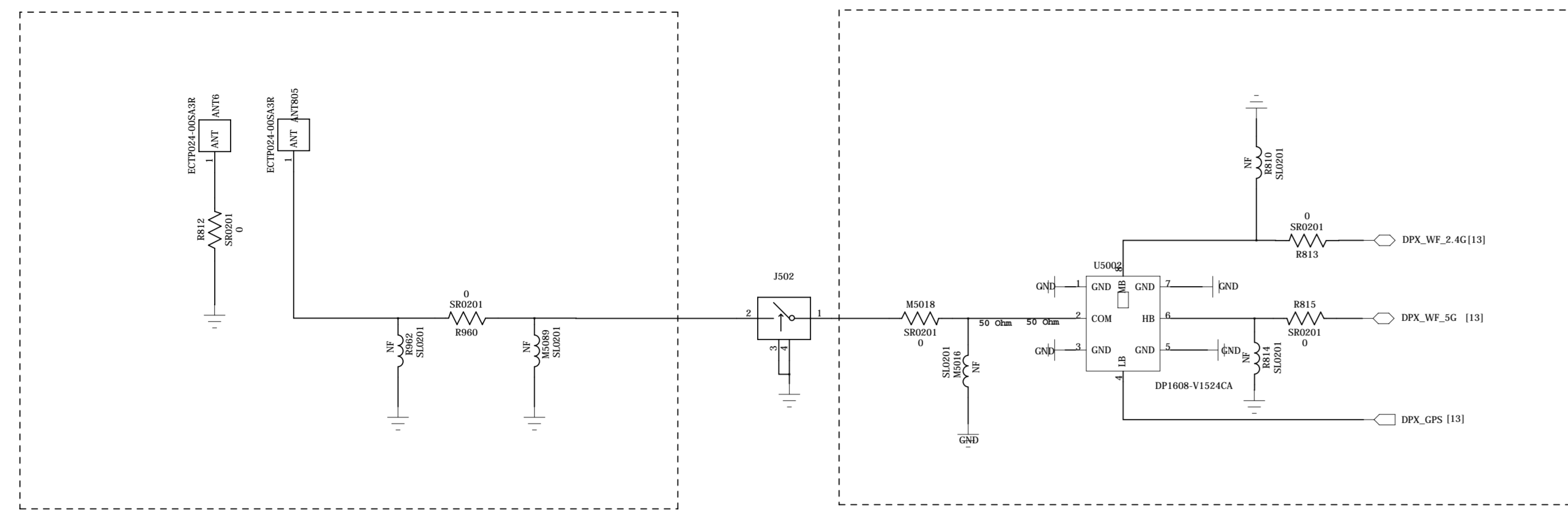
LPDDR4x



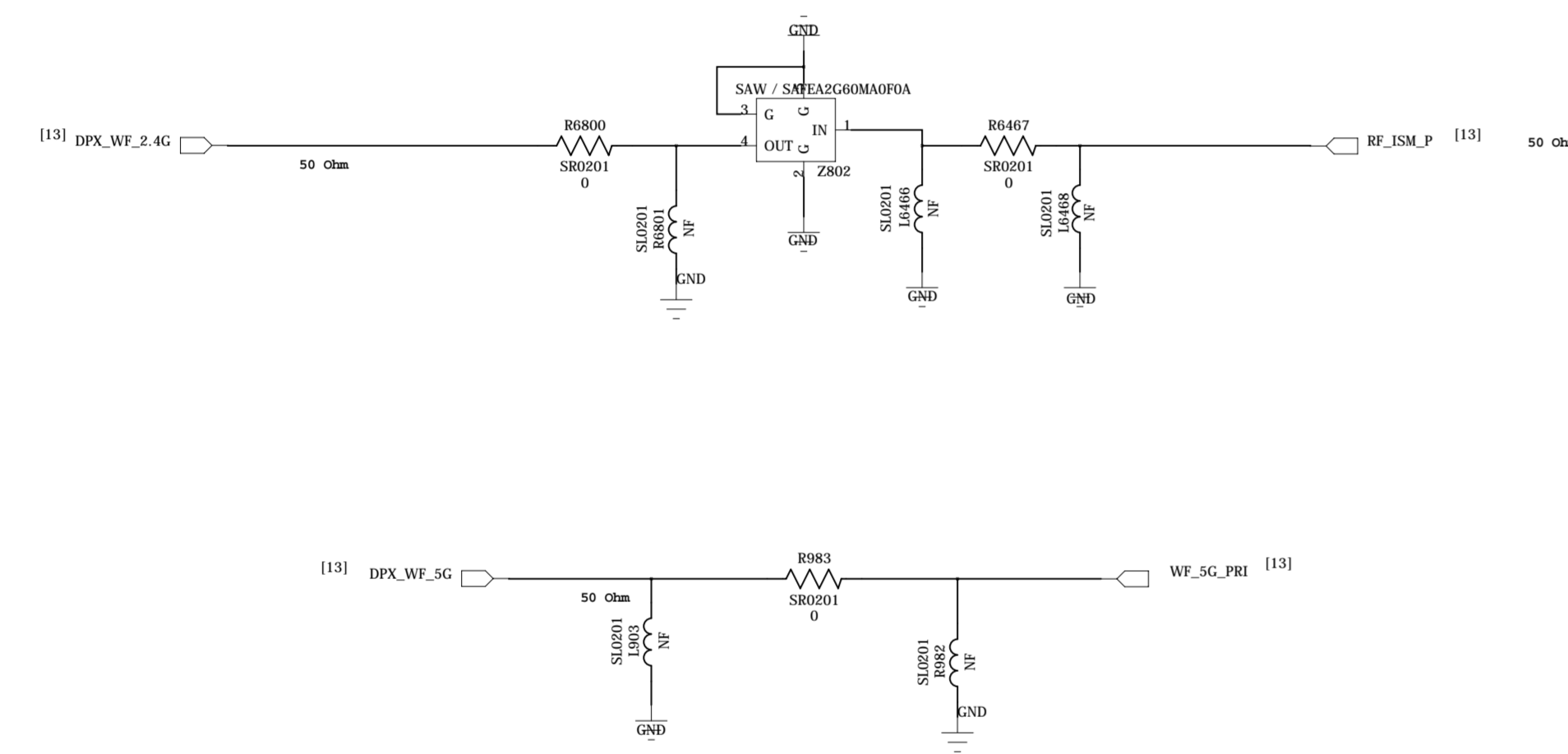
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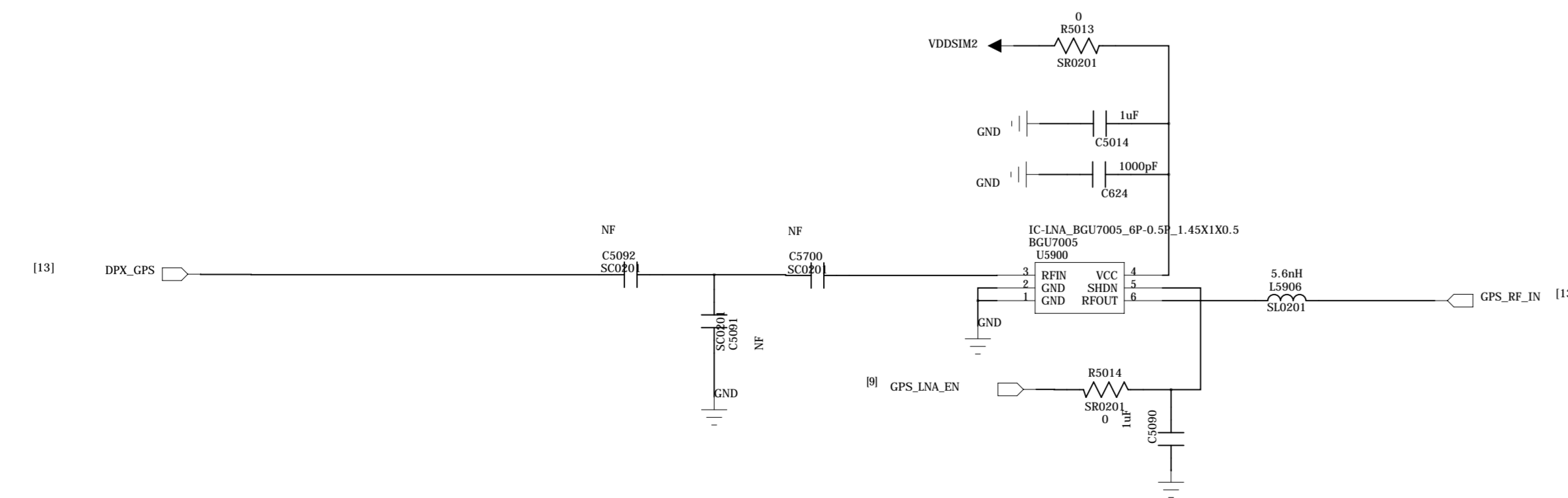
WIFI&BT ANT



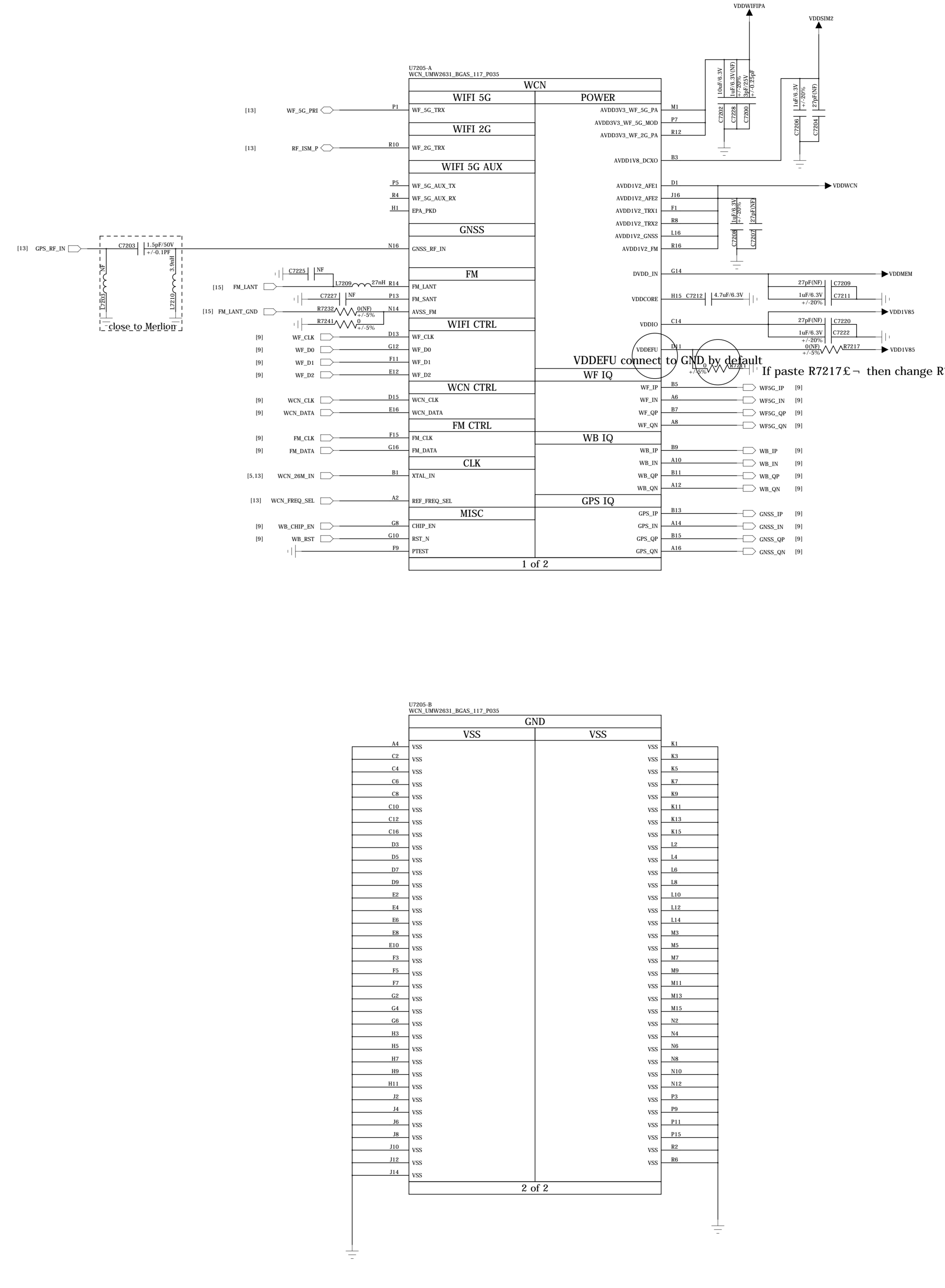
WIFI/BT SAW



GNSS SAW LNA

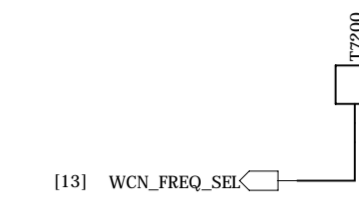


Merlion



Bonding Option

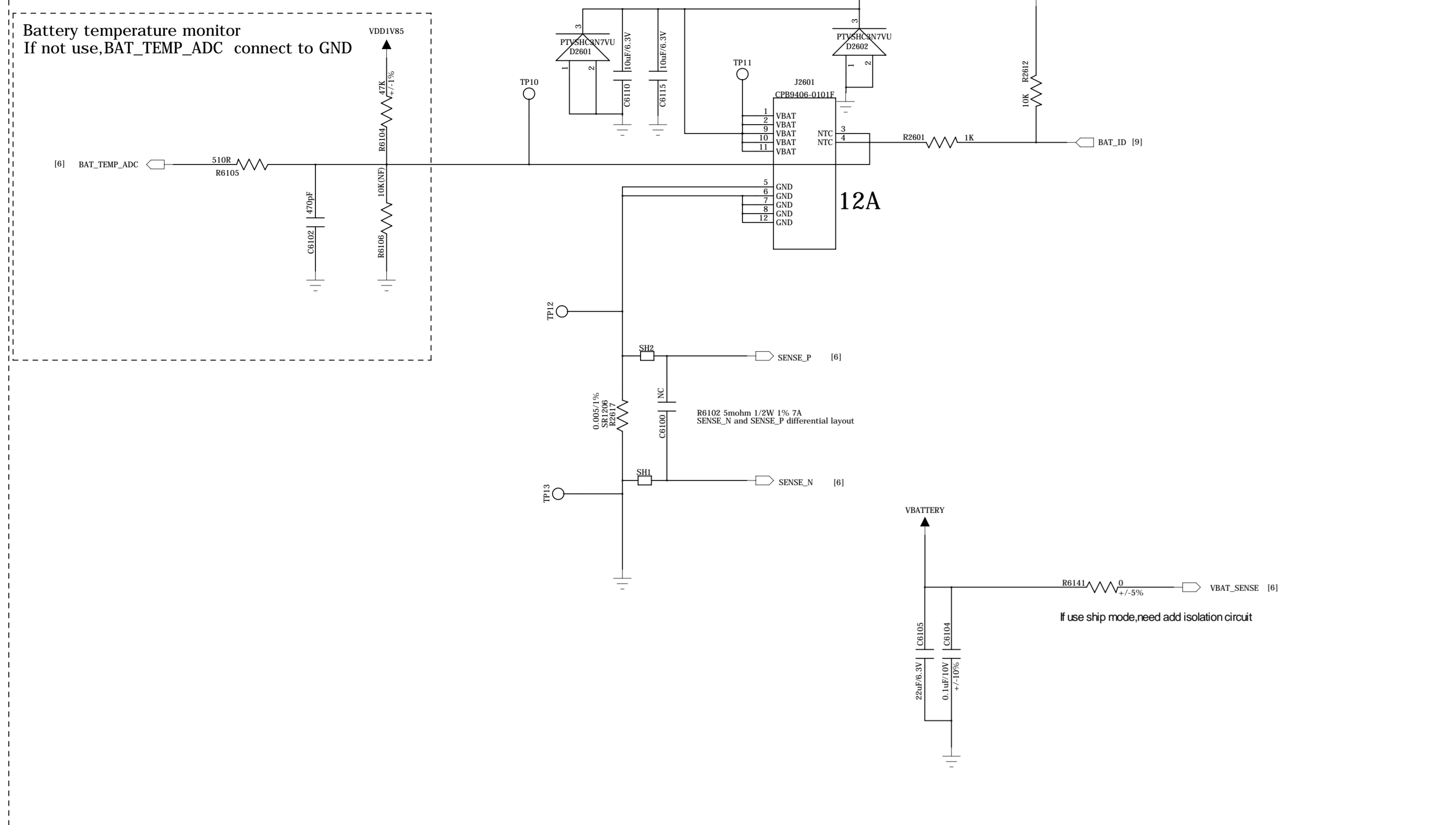
FREQ_SEL---Internal Pull-up	
0	52MHz
1	26MHz



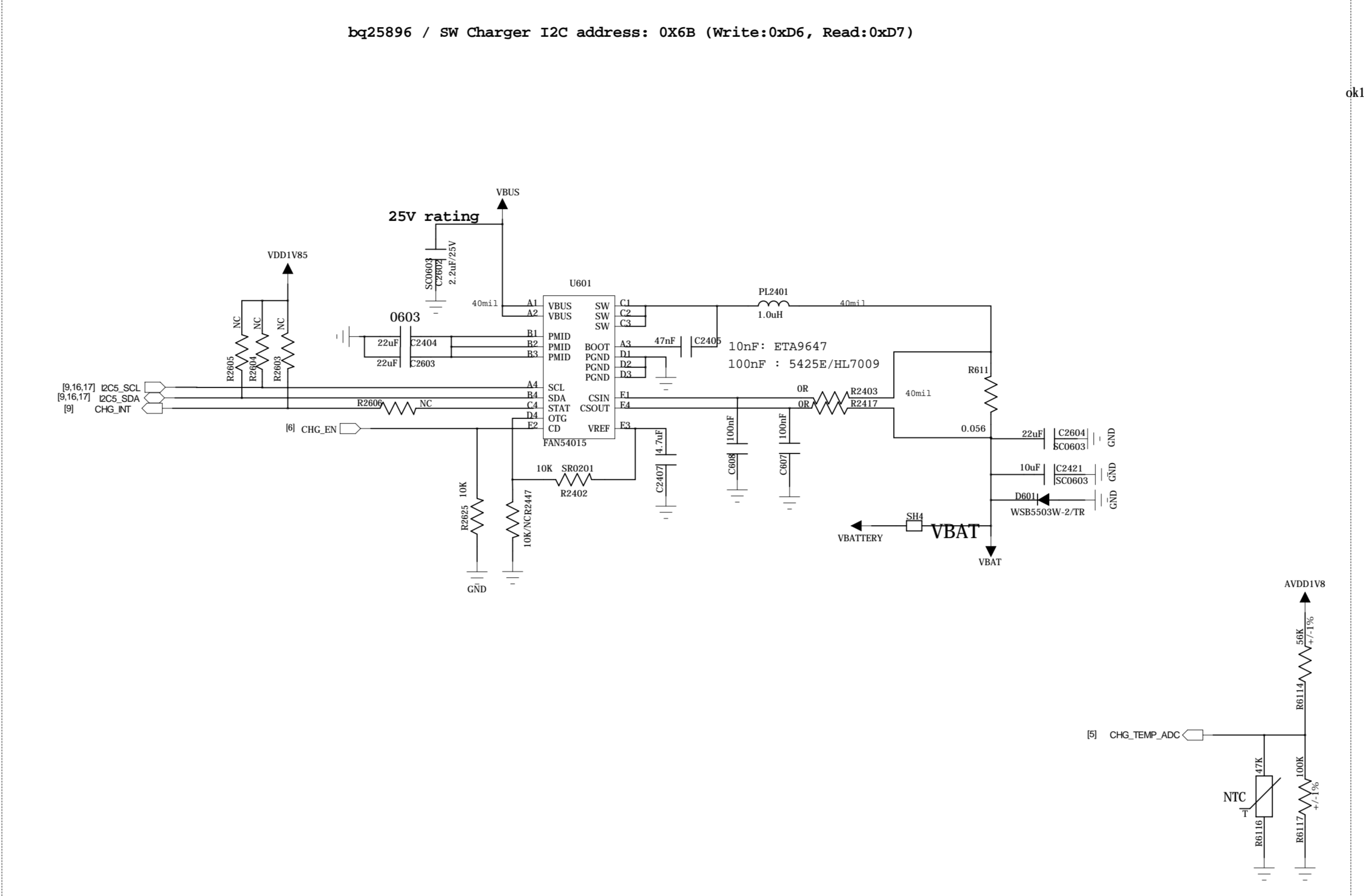
GND		VSS	
A4	VSS	B1	VSS
A5	VSS	B2	VSS
A6	VSS	B3	VSS
A7	VSS	B4	VSS
A8	VSS	B5	VSS
A9	VSS	B6	VSS
A10	VSS	B7	VSS
A11	VSS	B8	VSS
A12	VSS	B9	VSS
A13	VSS	B10	VSS
A14	VSS	B11	VSS
A15	VSS	B12	VSS
A16	VSS	B13	VSS
A17	VSS	B14	VSS
A18	VSS	B15	VSS
A19	VSS	B16	VSS
A20	VSS	B17	VSS
A21	VSS	B18	VSS
A22	VSS	B19	VSS
A23	VSS	B20	VSS
A24	VSS	B21	VSS
A25	VSS	B22	VSS
A26	VSS	B23	VSS
A27	VSS	B24	VSS
A28	VSS	B25	VSS
A29	VSS	B26	VSS
A30	VSS	B27	VSS
A31	VSS	B28	VSS
A32	VSS	B29	VSS
A33	VSS	B30	VSS
A34	VSS	B31	VSS
A35	VSS	B32	VSS
A36	VSS	B33	VSS
A37	VSS	B34	VSS
A38	VSS	B35	VSS
A39	VSS	B36	VSS
A40	VSS	B37	VSS
A41	VSS	B38	VSS
A42	VSS	B39	VSS
A43	VSS	B40	VSS
A44	VSS	B41	VSS
A45	VSS	B42	VSS
A46	VSS	B43	VSS
A47	VSS	B44	VSS
A48	VSS	B45	VSS
A49	VSS	B46	VSS
A50	VSS	B47	VSS
A51	VSS	B48	VSS
A52	VSS	B49	VSS
A53	VSS	B50	VSS
A54	VSS	B51	VSS
A55	VSS	B52	VSS
A56	VSS	B53	VSS
A57	VSS	B54	VSS
A58	VSS	B55	VSS
A59	VSS	B56	VSS
A60	VSS	B57	VSS
A61	VSS	B58	VSS
A62	VSS	B59	VSS
A63	VSS	B60	VSS
A64	VSS	B61	VSS
A65	VSS	B62	VSS
A66	VSS	B63	VSS
A67	VSS	B64	VSS
A68	VSS	B65	VSS
A69	VSS	B66	VSS
A70	VSS	B67	VSS
A71	VSS	B68	VSS
A72	VSS	B69	VSS
A73	VSS	B70	VSS
A74	VSS	B71	VSS
A75	VSS	B72	VSS
A76	VSS	B73	VSS
A77	VSS	B74	VSS
A78	VSS	B75	VSS
A79	VSS	B76	VSS
A80	VSS	B77	VSS
A81	VSS	B78	VSS
A82	VSS	B79	VSS
A83	VSS	B80	VSS
A84	VSS	B81	VSS
A85	VSS	B82	VSS
A86	VSS	B83	VSS
A87	VSS	B84	VSS
A88	VSS	B85	VSS
A89	VSS	B86	VSS
A90	VSS	B87	VSS
A91	VSS	B88	VSS
A92	VSS	B89	VSS
A93	VSS	B90	VSS
A94	VSS	B91	VSS
A95	VSS	B92	VSS
A96	VSS	B93	VSS
A97	VSS	B94	VSS
A98	VSS	B95	VSS
A99	VSS	B96	VSS
A100	VSS	B97	VSS
A101	VSS	B98	VSS
A102	VSS	B99	VSS
A103	VSS	B100	VSS
A104	VSS	B101	VSS
A105	VSS	B102	VSS
A106	VSS	B103	VSS
A107	VSS	B104	VSS
A108	VSS	B105	VSS
A109	VSS	B106	VSS
A110	VSS	B107	VSS
A111	VSS	B108	VSS
A112	VSS	B109	VSS
A113	VSS	B110	VSS
A114	VSS	B111	VSS
A115	VSS	B112	VSS
A116	VSS	B113	VSS
A117	VSS	B114	VSS
A118	VSS	B115	VSS
A119	VSS	B116	VSS
A120	VSS	B117	VSS
A121	VSS	B118	VSS
A122	VSS	B119	VSS
A123	VSS	B120	VSS
A124	VSS	B121	VSS
A125	VSS	B122	VSS
A126	VSS	B123	VSS
A127	VSS	B124	VSS
A128	VSS	B125	VSS
A129	VSS	B126	VSS
A130	VSS	B127	VSS
A131	VSS	B128	VSS
A132	VSS	B129	VSS
A133	VSS	B130	VSS
A134	VSS	B131	VSS
A135	VSS	B132	VSS
A136	VSS	B133	VSS
A137	VSS	B134	VSS
A138	VSS	B135	VSS
A139	VSS	B136	VSS
A140	VSS	B137	VSS
A141	VSS	B138	VSS
A142	VSS	B139	VSS
A143	VSS	B140	VSS
A144	VSS	B141	VSS
A145	VSS	B142	VSS
A146	VSS	B143	VSS
A147	VSS	B144	VSS
A148	VSS	B145	VSS
A149	VSS	B146	VSS
A150	VSS	B147	VSS
A151	VSS	B148	VSS
A152	VSS	B149	VSS
A153	VSS	B150	VSS
A154	VSS	B151	VSS
A155	VSS	B152	VSS
A156	VSS	B153	VSS
A157	VSS	B154	VSS
A158	VSS	B155	VSS
A159	VSS	B156	VSS
A160	VSS	B157	VSS
A161	VSS	B158	VSS
A162	VSS	B159	VSS
A163	VSS	B160	VSS
A164	VSS	B161	VSS
A165	VSS	B162	VSS
A166	VSS	B163	VSS
A167	VSS	B164	VSS
A168	VSS	B165	VSS
A169	VSS	B166	VSS
A170	VSS	B167	VSS
A171	VSS	B168	VSS
A172	VSS	B169	VSS
A173	VSS	B170	VSS
A174	VSS	B171	VSS
A175	VSS	B172	VSS
A176	VSS	B173	VSS
A177	VSS	B174	VSS
A178	VSS	B175	VSS
A179	VSS	B176	VSS
A180	VSS	B177	VSS
A181	VSS	B178	VSS
A182	VSS	B179	VSS
A183	VSS	B180	VSS
A184	VSS	B181	VSS
A185	VSS	B182	VSS
A186	VSS	B183	VSS
A187	VSS	B184	VSS
A188	VSS	B185	VSS
A189	VSS	B186	VSS
A190	VSS	B187	VSS
A191	VSS	B188	VSS
A192	VSS	B189	VSS
A193	VSS	B190	VSS
A194	VSS	B191	VSS
A195	VSS	B192	VSS
A196	VSS	B193	VSS
A197	VSS	B194	VSS
A198	VSS	B195	VSS
A199	VSS	B196	VSS
A200	VSS	B197	VSS
A201	VSS	B198	VSS
A202	VSS	B199	VSS
A203	VSS	B200	VSS
A204	VSS	B201	VSS
A205	VSS	B202	VSS
A206	VSS	B203	VSS
A207	VSS	B204	VSS
A208	VSS	B205	VSS
A209	VSS	B206	VSS
A210	VSS	B207	VSS
A211	VSS	B208	VSS
A212	VSS	B209	VSS
A213	VSS	B210	VSS
A214	VSS	B211	VSS
A215	VSS	B212	VSS
A216	VSS	B213	VSS
A217	VSS	B214	VSS
A218	VSS	B215	VSS
A219	VSS	B216	VSS
A220	VSS	B217	VSS
A221	VSS	B218	VSS
A222	VSS	B219	VSS
A223	VSS	B220	VSS
A224	VSS	B221	VSS
A225	VSS	B222	VSS
A226	VSS	B223	VSS
A227	VSS	B224	VSS
A228	VSS	B225	VSS
A229	VSS	B226	VSS
A230	VSS	B227	VSS
A231	VSS	B228	VSS
A232	VSS	B229	VSS
A233	VSS	B230	VSS
A234	VSS	B231	VSS
A235	VSS	B232	VSS
A236	VSS	B233	VSS
A237	VSS	B234	VSS
A238	VSS	B235	VSS
A239	VSS	B236	VSS
A240	VSS	B237	VSS
A241	VSS	B238	VSS
A242	VSS	B239	VSS
A243	VSS	B240	VSS
A244	VSS	B241	VSS
A245	VSS	B242	VSS
A246	VSS	B243	VSS
A247	VSS	B244	VSS
A248	VSS	B245	VSS
A249	VSS	B246	VSS
A250	VSS	B247	VSS
A251	VSS	B248	VSS
A252	VSS	B249	VSS
A253	VSS	B250	VSS
A254	VSS	B251	VSS
A255	VSS	B252	VSS
A256	VSS	B253	VSS
A257	VSS	B254	VSS
A258	VSS	B255	VSS
A259	VSS	B256	VSS
A260	VSS	B257	VSS
A261	VSS	B258	VSS
A262	VSS	B259	VSS
A263	VSS	B260	VSS
A264	VSS	B261	VSS
A265	VSS	B262	VSS
A266	VSS	B263	VSS
A267	VSS	B264	VSS
A268	VSS	B265	VSS
A269	VSS	B266	VSS
A270	VSS	B267	VSS
A271	VSS	B268	VSS
A272	VSS	B269	VSS
A273	VSS	B270	VSS
A274	VSS	B271	VSS
A275	VSS	B272	VSS
A276	VSS	B273	VSS
A277	VSS	B274	VSS
A278	VSS	B275	VSS
A279	VSS	B276	VSS
A280	VSS	B277	VSS
A281	VSS	B278	VSS
A282	VSS	B279	VSS
A283	VSS	B280	VSS
A284	VSS	B281	VSS
A285	VSS	B282	VSS
A286	VSS	B283	VSS
A287	VSS	B284	VSS
A288	VSS	B285	VSS
A289	VSS	B286	VSS
A290	VSS	B287	VSS
A291	VSS	B288	VSS
A292	VSS	B289	VSS
A293	VSS	B290	VSS
A294	VSS	B291	VSS
A295	VSS	B292	VSS
A296	VSS	B293	VSS
A297	VSS	B294	VSS
A298	VSS	B295	VSS
A299	VSS	B296	VSS
A300	VSS	B297	VSS
A301	VSS	B298	VSS
A302	VSS	B299	VSS
A303	VSS	B300	VSS
A304	VSS	B301	VSS
A305	VSS	B302	VSS
A306	VSS	B303	VSS
A307	VSS	B304	VSS
A308	VSS	B305	VSS
A309	VSS	B306	VSS
A310	VSS	B307	VSS
A311	VSS	B308	VSS
A312	VSS	B309	VSS
A313	VSS	B310	VSS
A314	VSS	B311	VSS
A315	VSS	B312	VSS
A316	VSS	B313	VSS
A317	VSS	B314	VSS
A318	VSS	B315	VSS
A319	VSS	B316	VSS
A320	VSS	B317	VSS
A321	VSS	B318	VSS
A322	VSS	B319	VSS
A323	VSS	B320	VSS
A324	VSS	B321	VSS
A325	VSS	B322	VSS
A326	VSS	B323	VSS
A327	VSS	B324	VSS
A328	VSS	B325	VSS
A329	VSS	B326	VSS
A330	VSS	B327	VSS
A331	VSS	B328	VSS
A332	VSS	B329	VSS
A333	VSS	B330	VSS
A334	VSS	B331	VSS
A335	VSS	B332	VSS
A336	VSS	B333	VSS
A337	VSS	B334	VSS
A338	VSS	B335	VSS
A339	VSS	B336	VSS
A340	VSS	B337	VSS
A341	VSS	B338	VSS
A342	VSS	B339	VSS
A343	VSS	B340	VSS
A344	VSS	B341	VSS
A345	VSS	B342	VSS
A346	VSS	B343	VSS
A347	VSS	B344	VSS
A348	VSS	B345	VSS
A349	VSS	B346	VSS
A350	VSS	B347	VSS
A351	VSS	B348	VSS
A352	VSS	B349	VSS
A353	VSS	B350	VSS
A354	VSS	B351	VSS
A355	VSS	B352	VSS
A356	VSS	B353	VSS
A357	VSS	B3	

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BATTERY



Main Charger



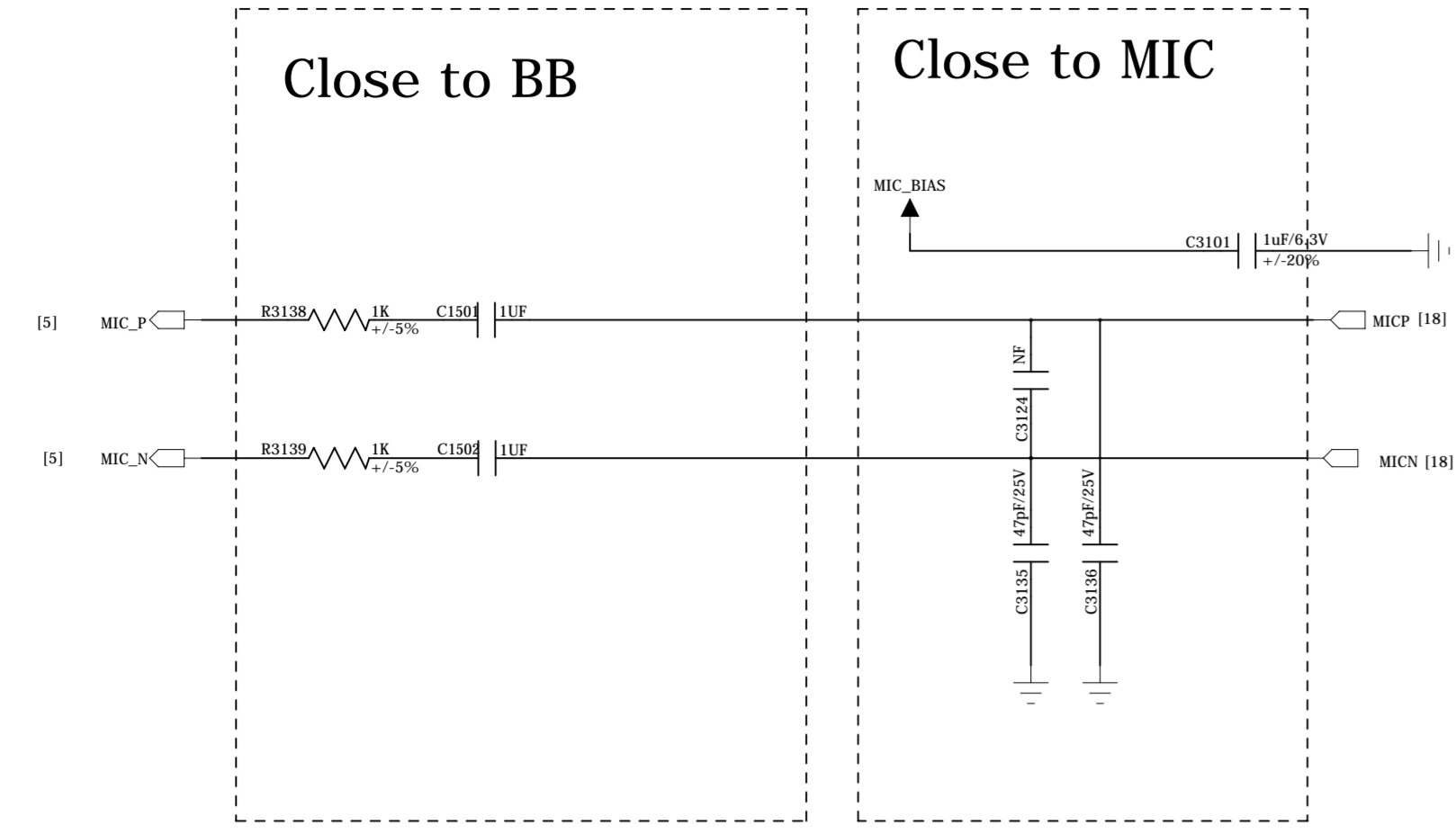
DESIGNER:	DATE:	DEPARTMENT:
		Hardware DEPT.
EQ:	DATE:	COMPANY:
		Spreadtrum communications, Inc.
APPROVED:	DATE:	

TITLE & REV:
DOCUMENT NO.:

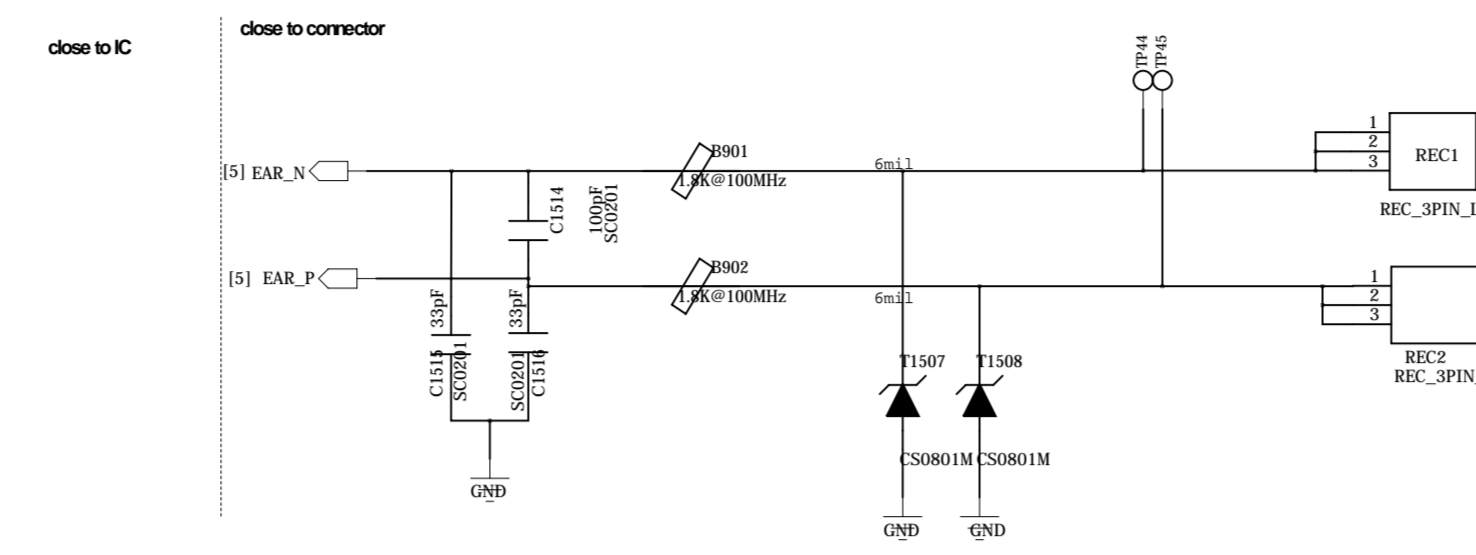
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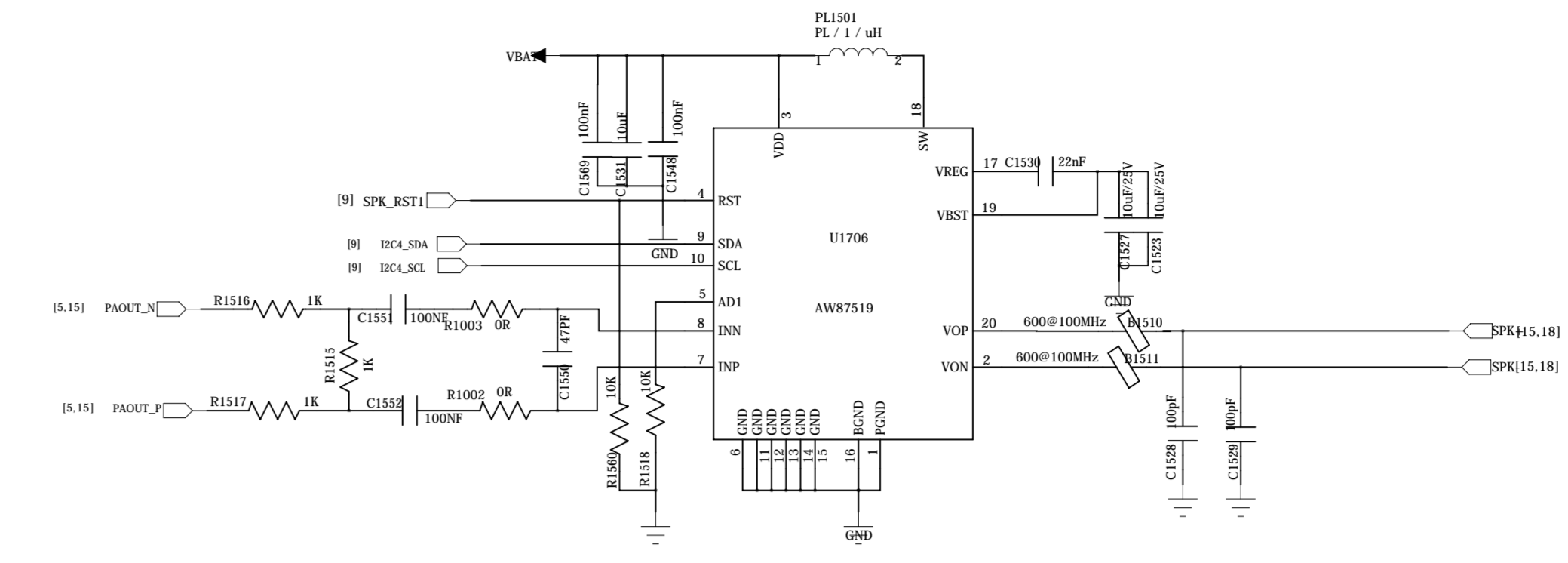
MAIN MIC



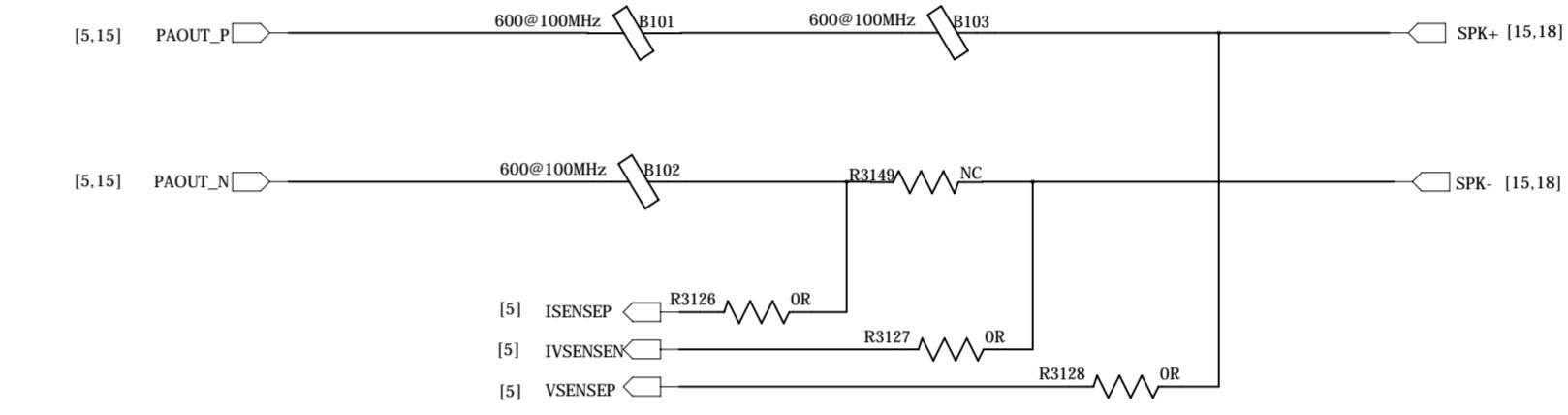
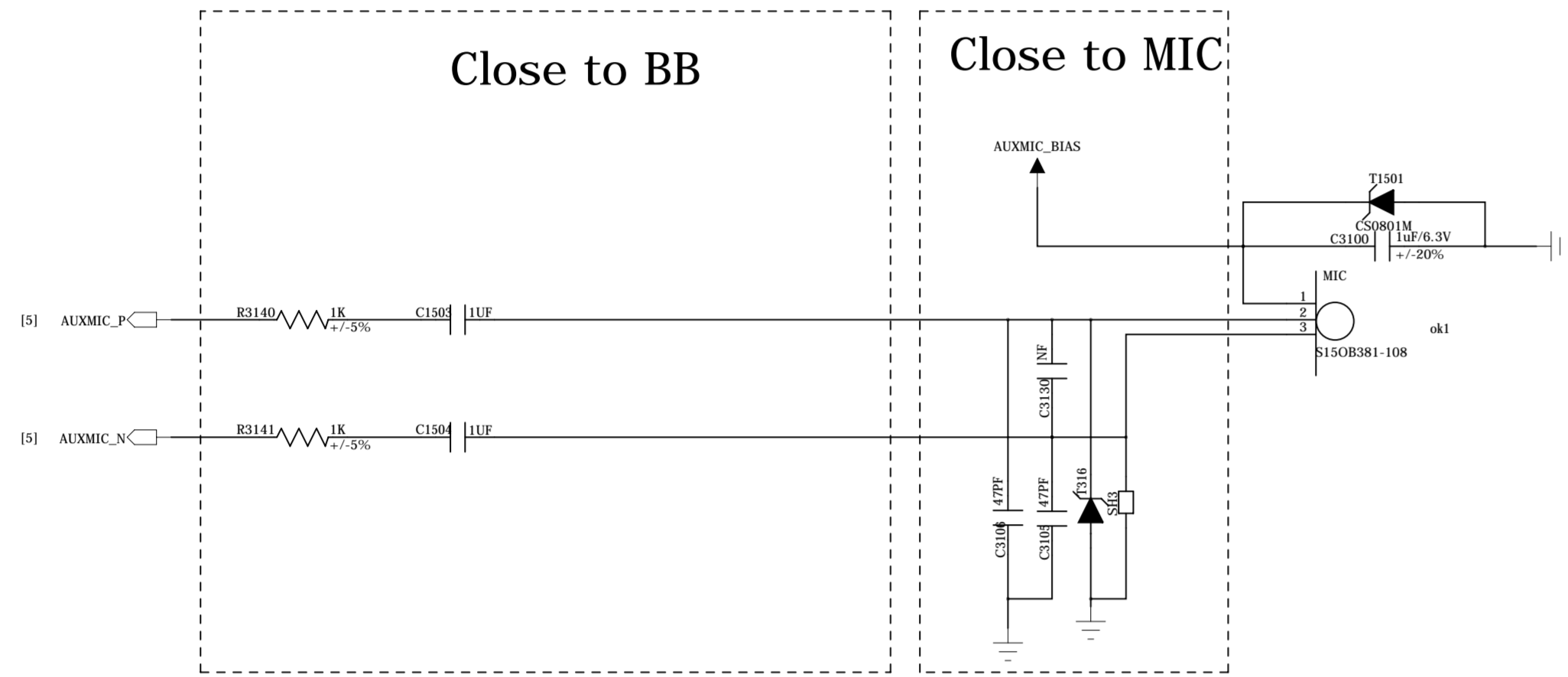
RECEIVER



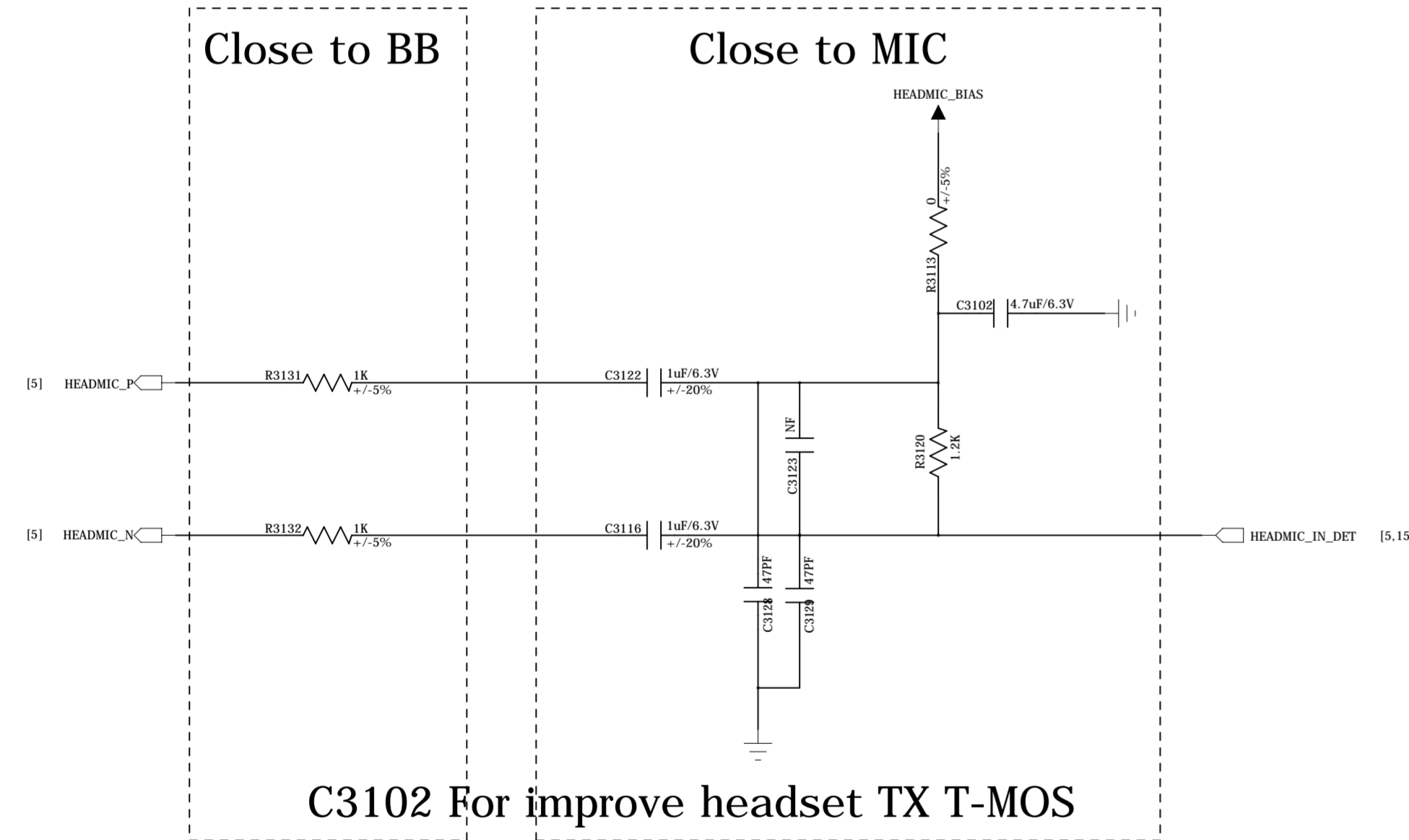
Audio PA



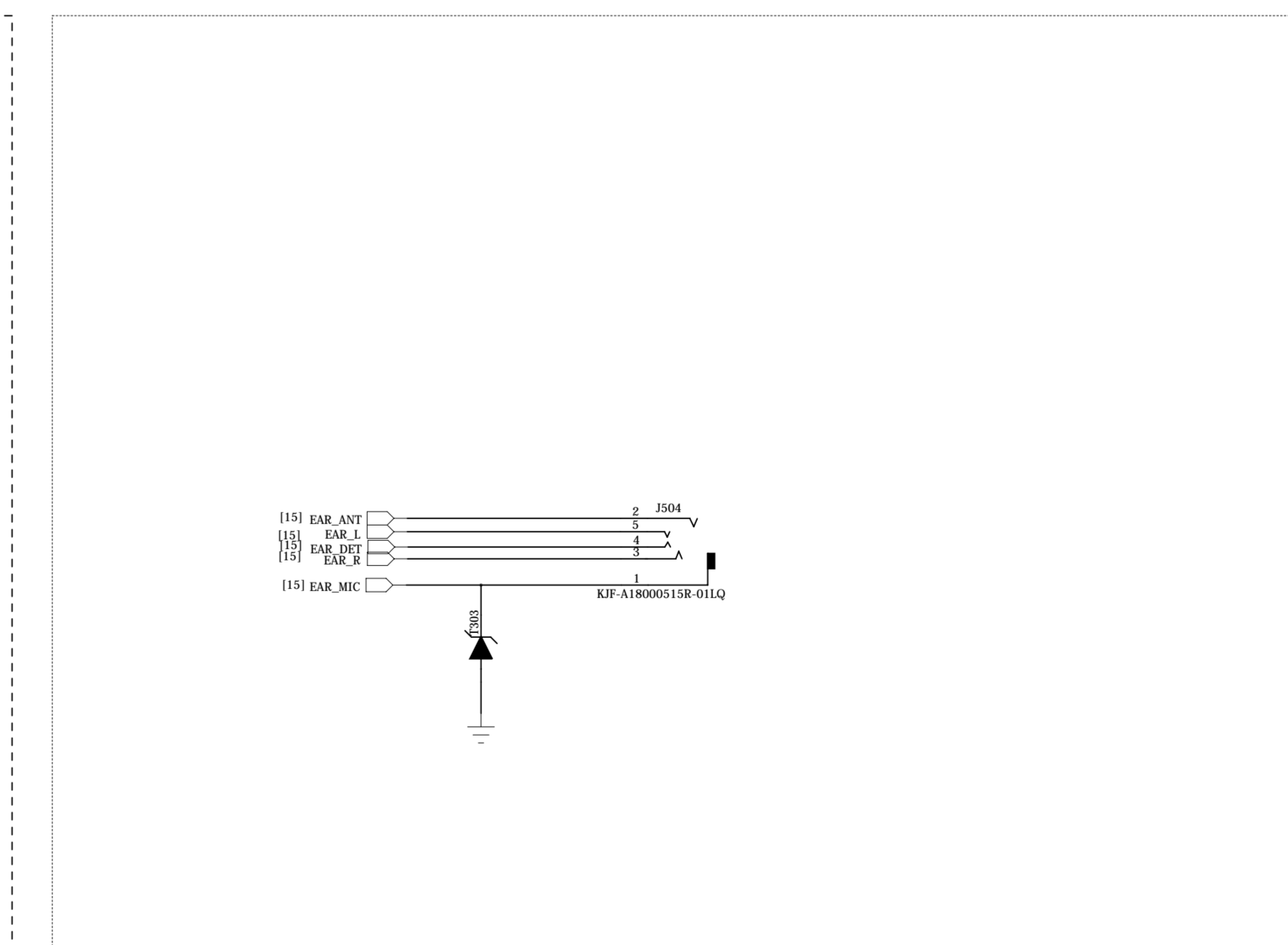
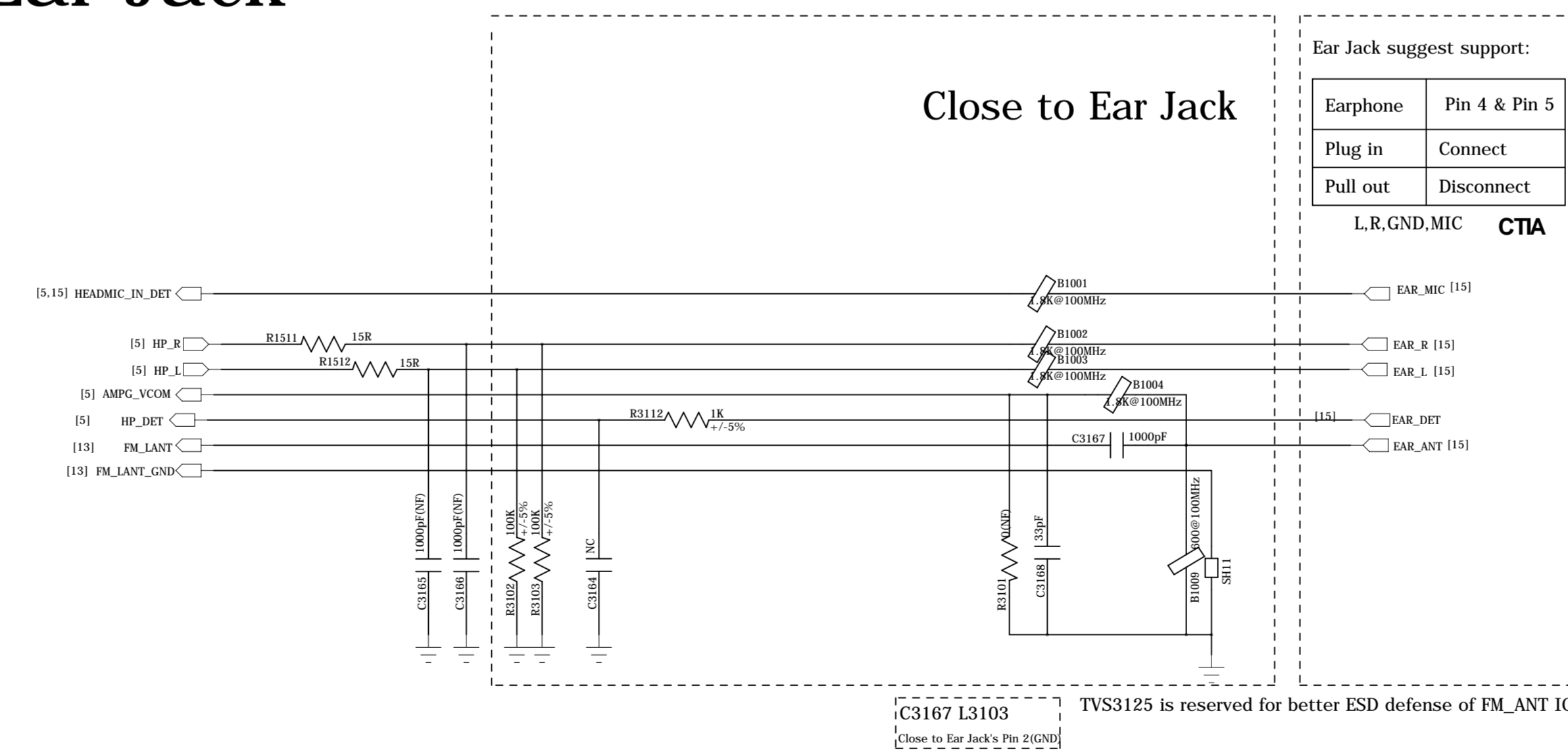
AUX MIC



HEAD MIC



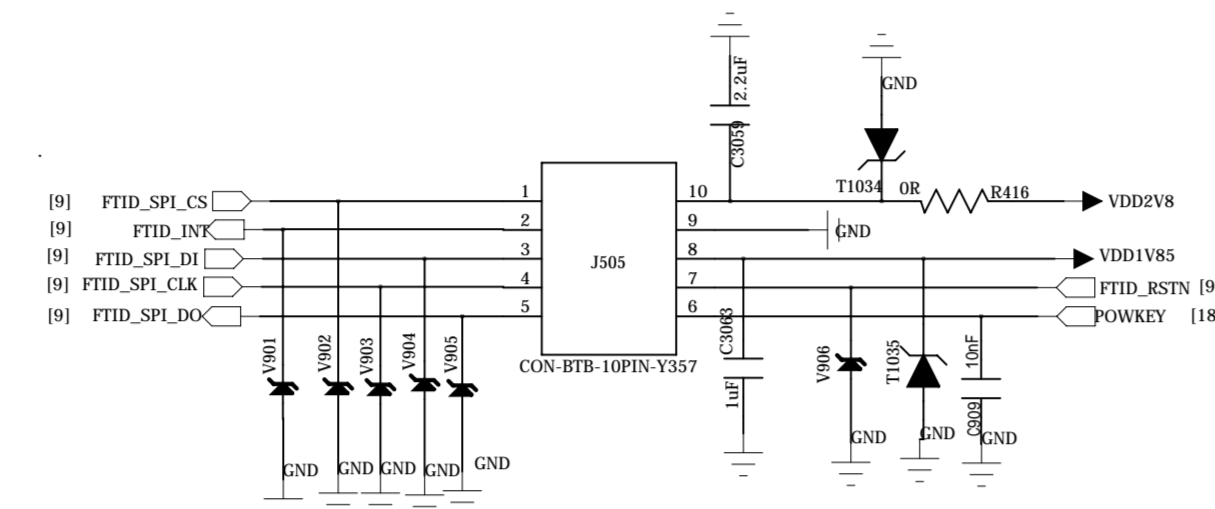
3.5mm Ear Jack



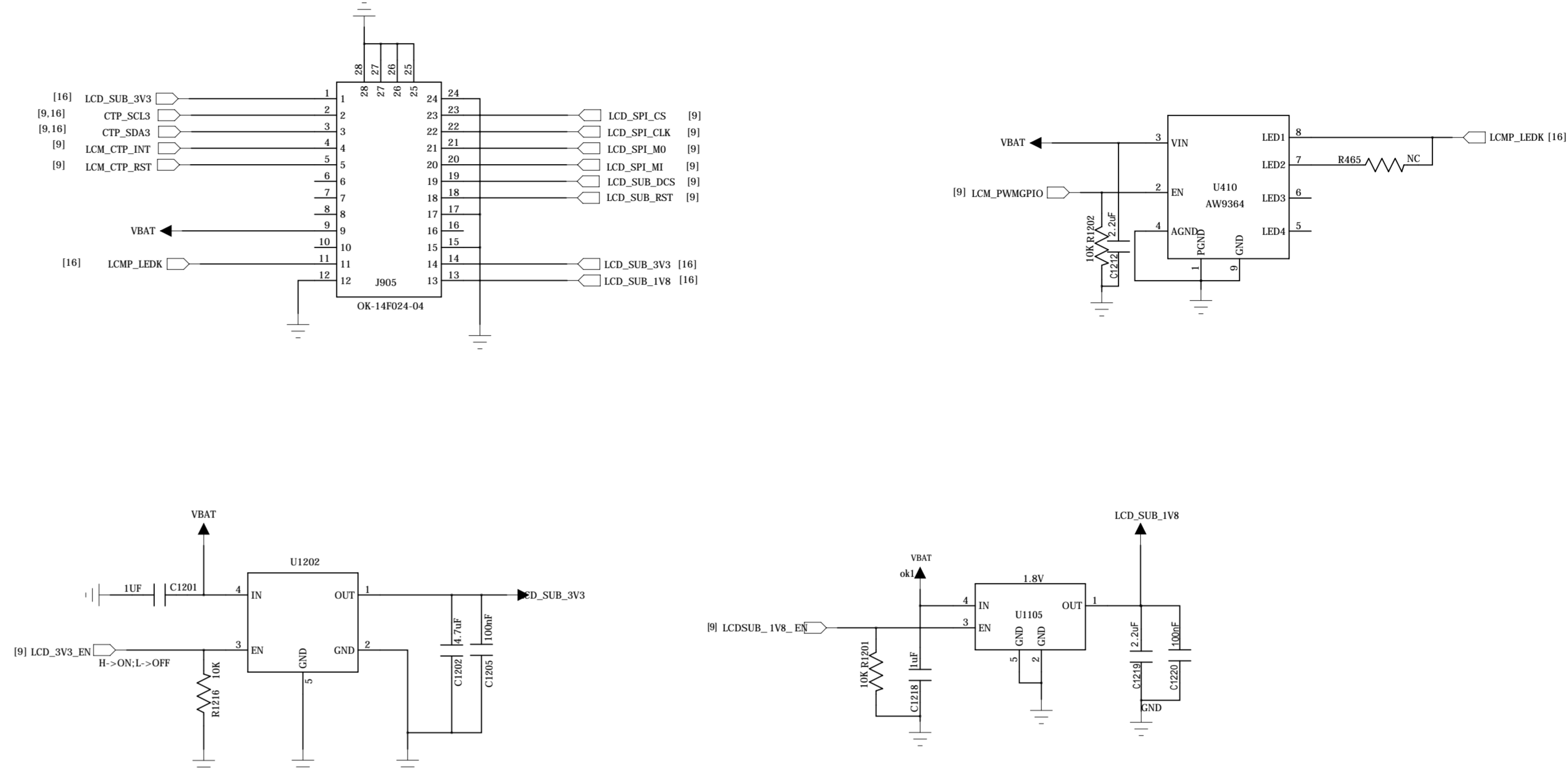
TITLE & REV:			
DOCUMENT NO.:			
DESIGNER:	DATE:	DEPARTMENT:	Hardware DEPT.
EQ:	DATE:	COMPANY:	Spreadtrum communications, Inc.
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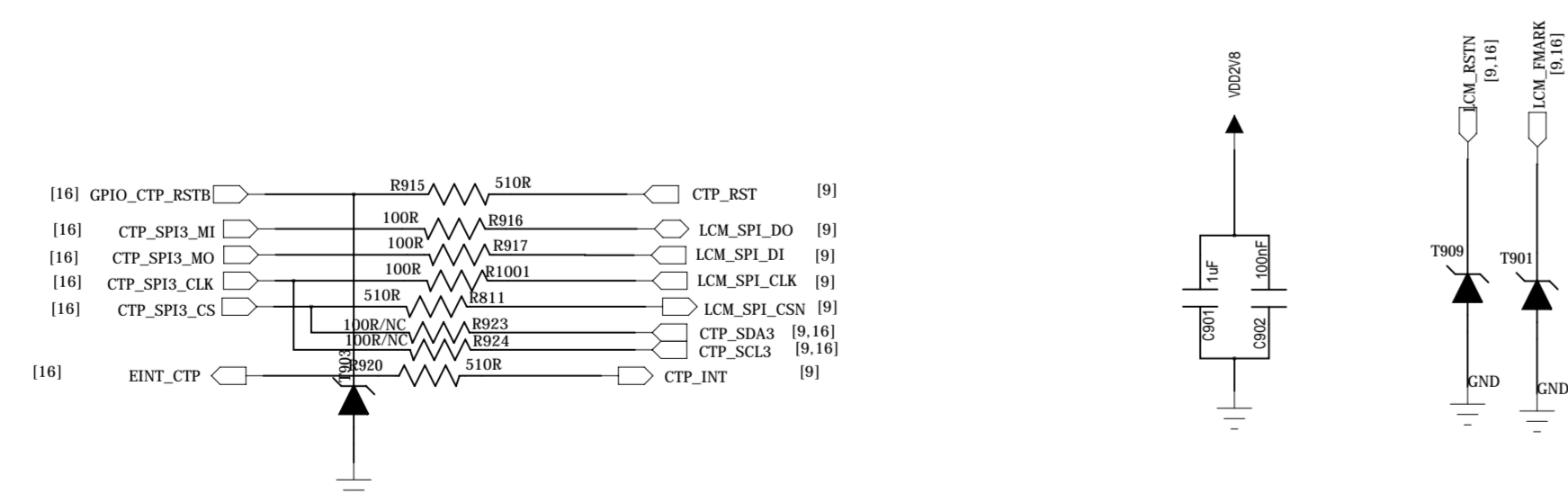
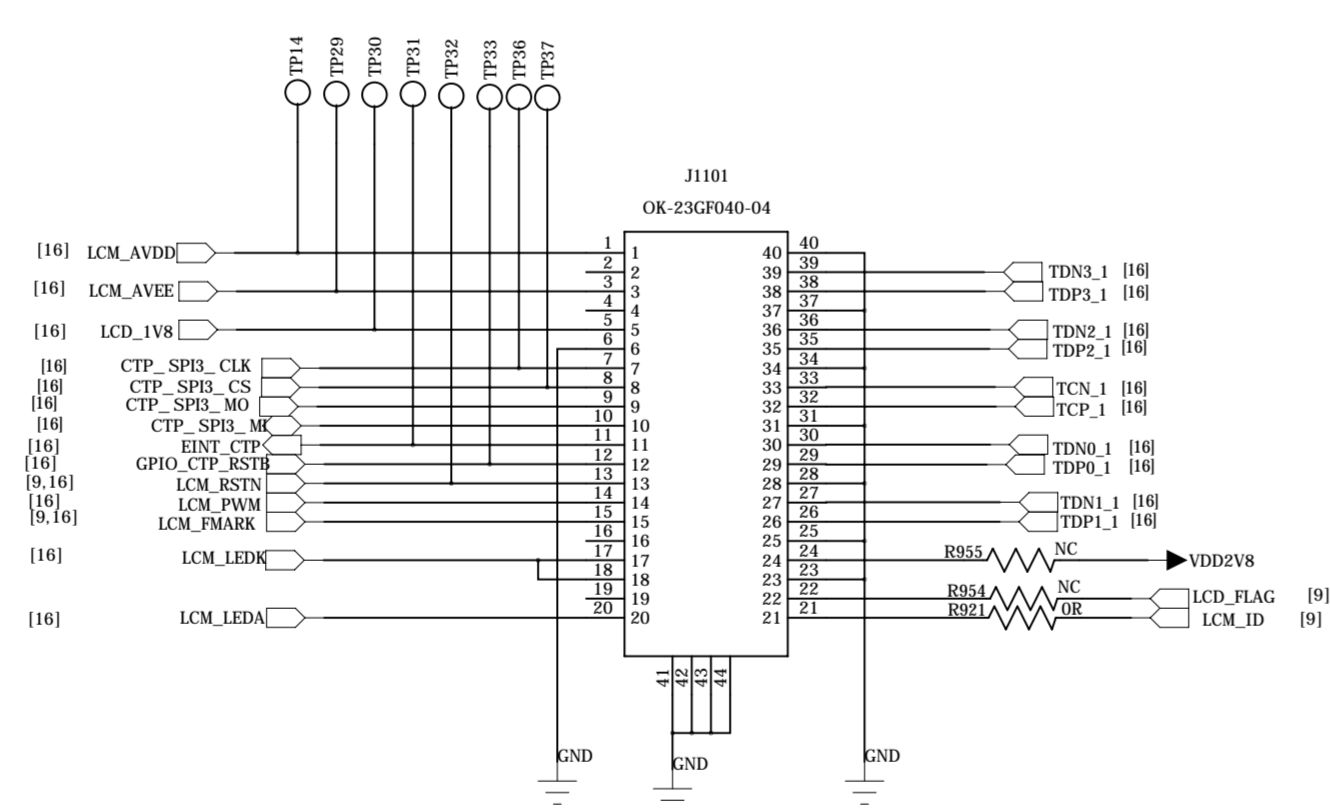
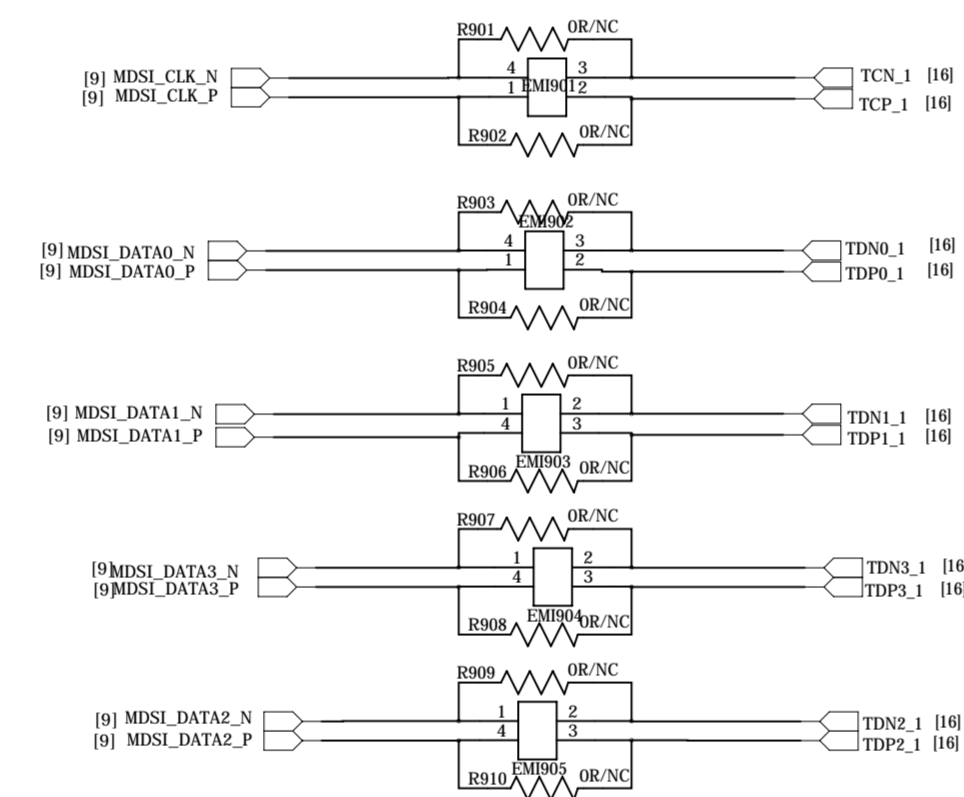
FRINGER



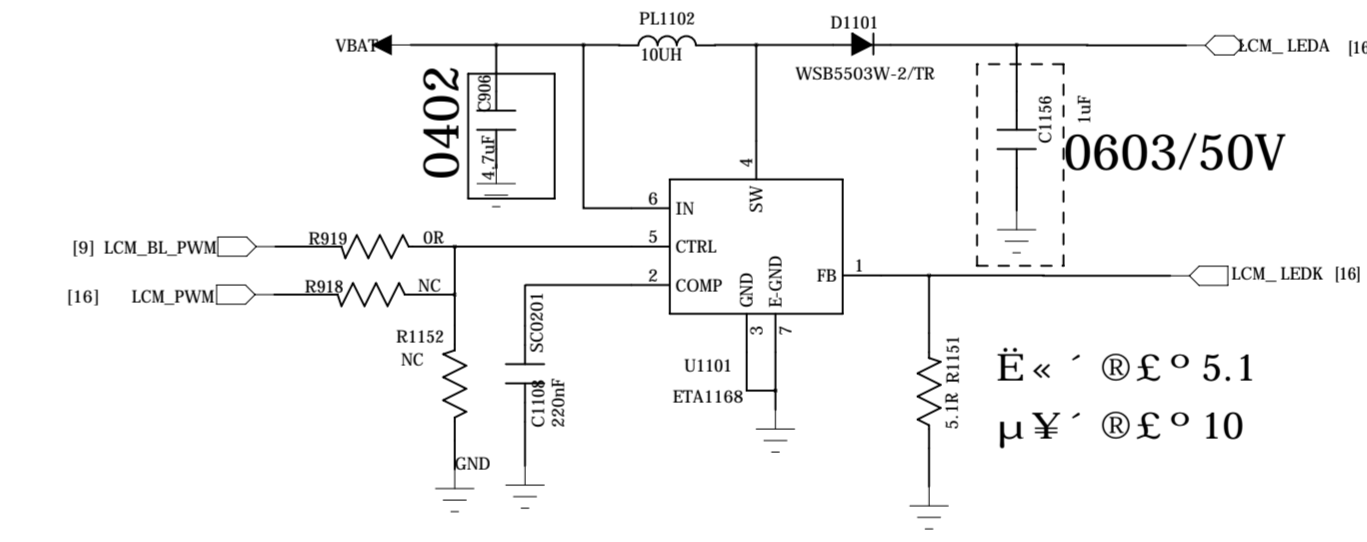
SUB_LCM



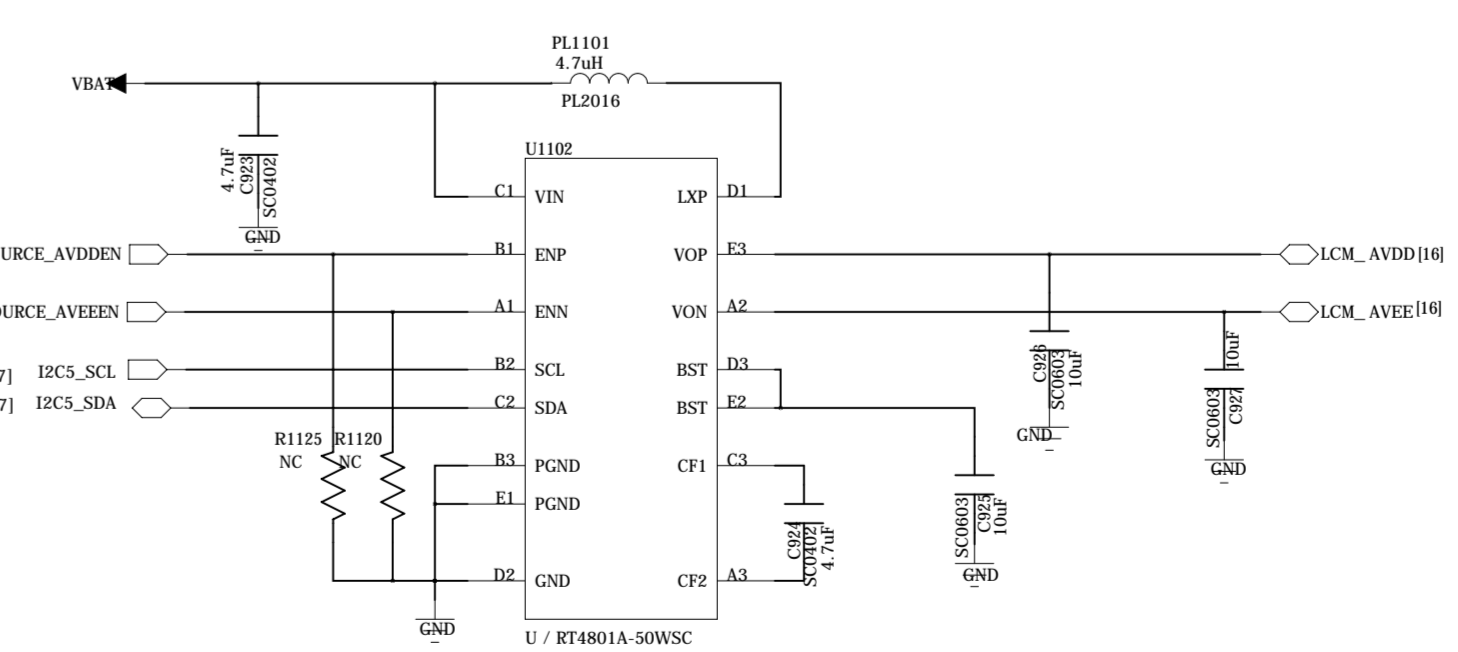
LCM(MIPI)



Back Light Driver



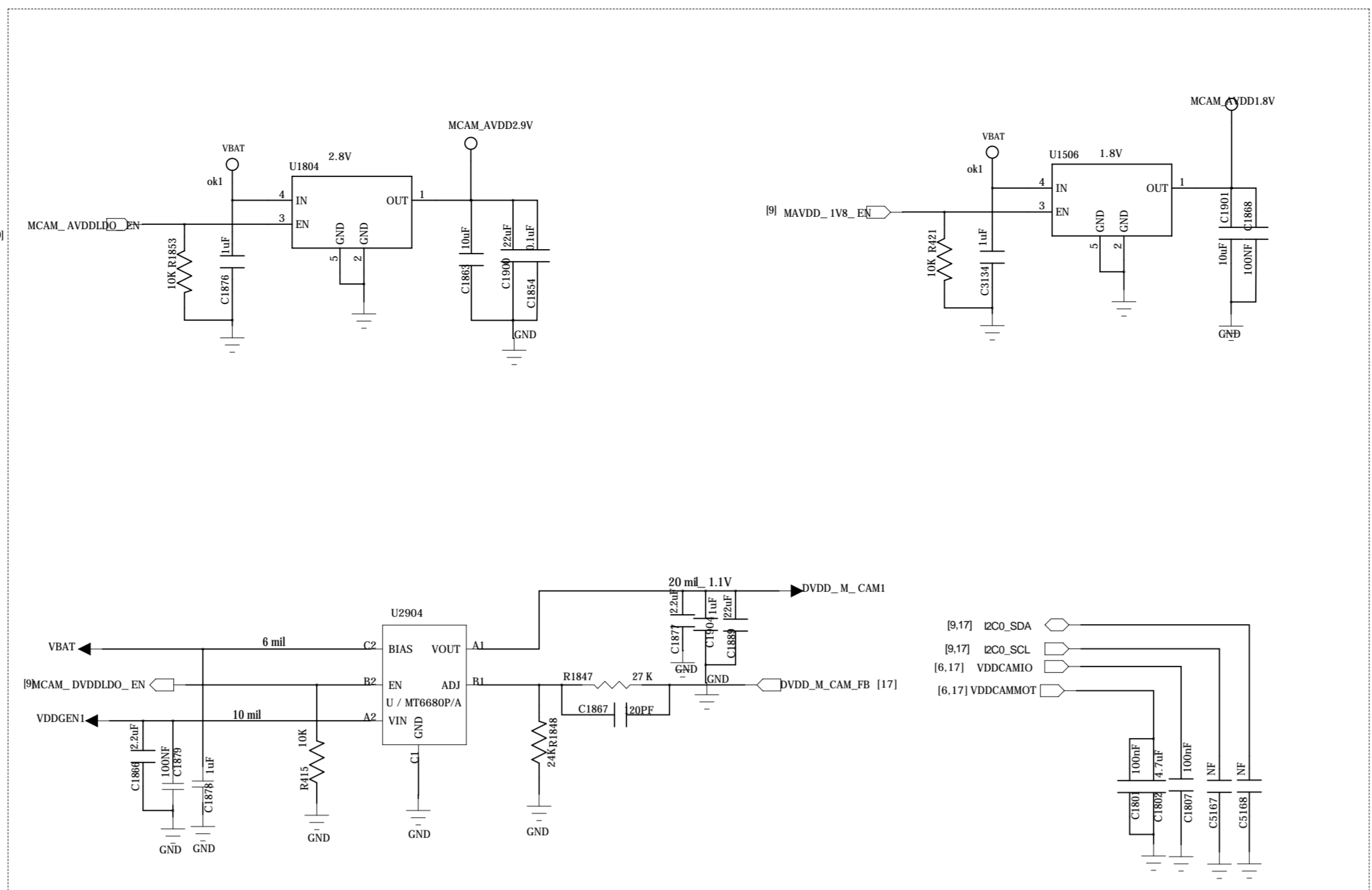
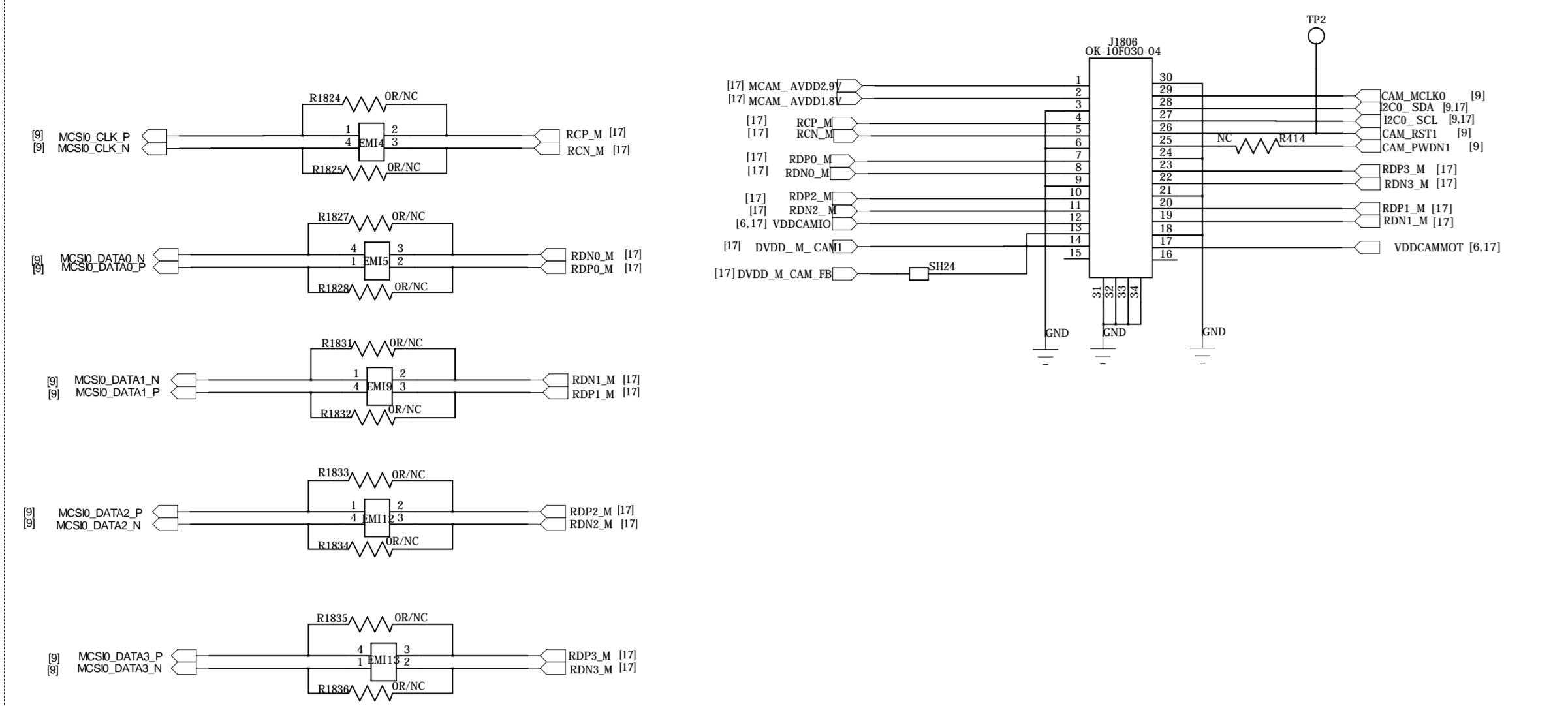
Source Driver



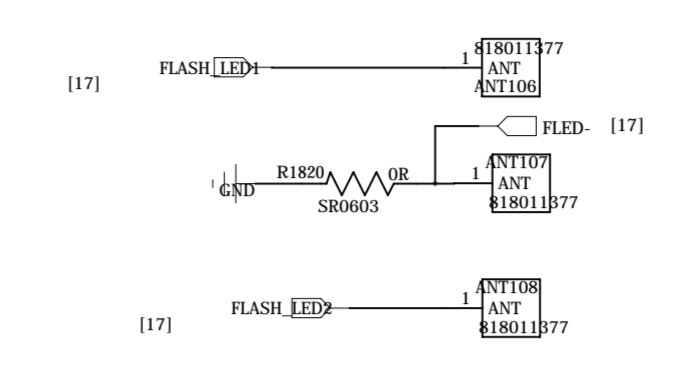
TITLE & REV.		
DOCUMENT NO.:		
DESIGNER:	DATE:	DEPARTMENT:
EDA:	DATE:	COMPANY:
APPROVED:	DATE:	Spreadtrum communications, Inc.
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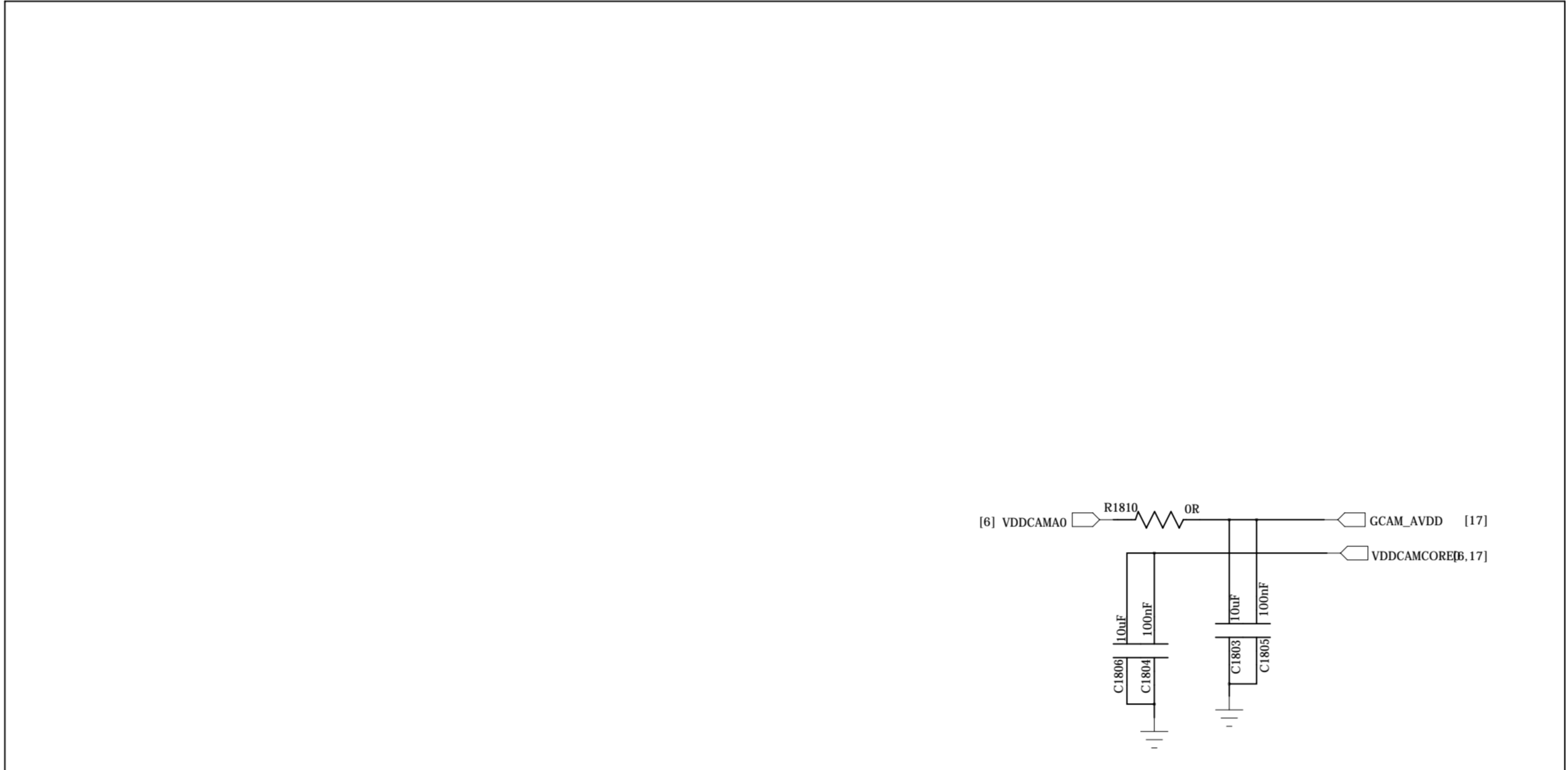
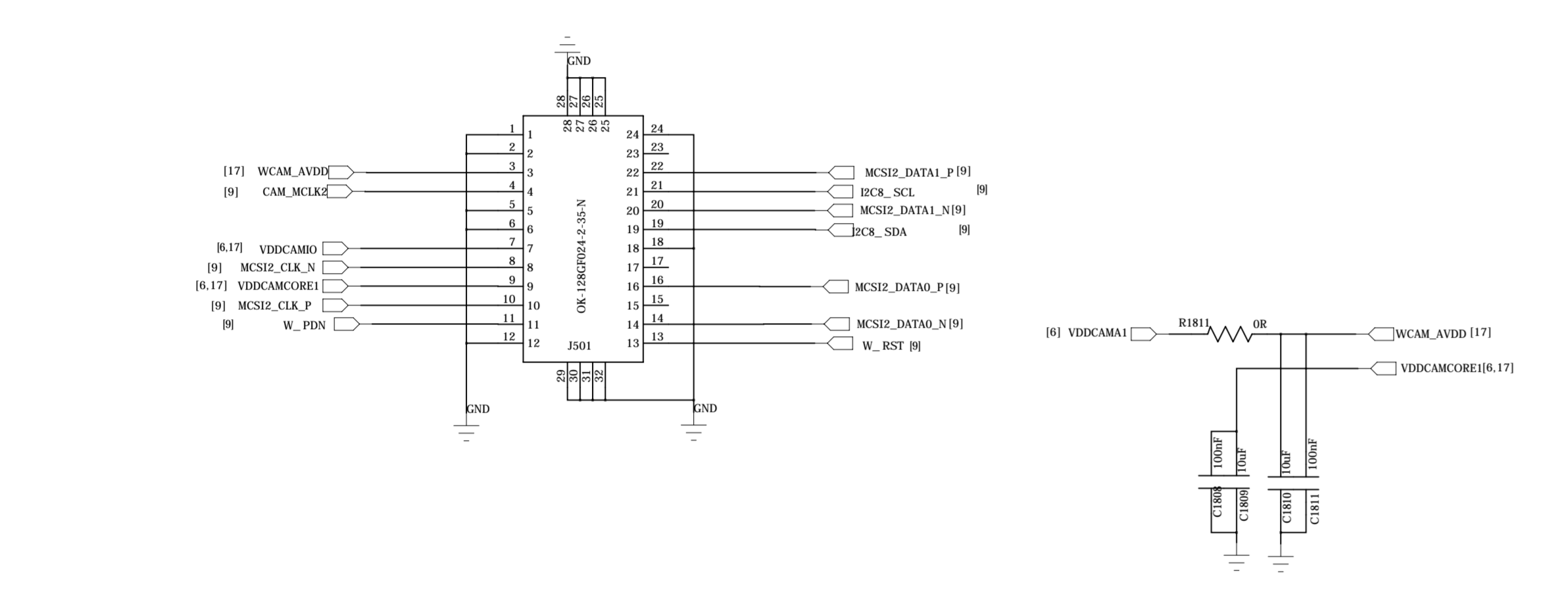
Rear main camera



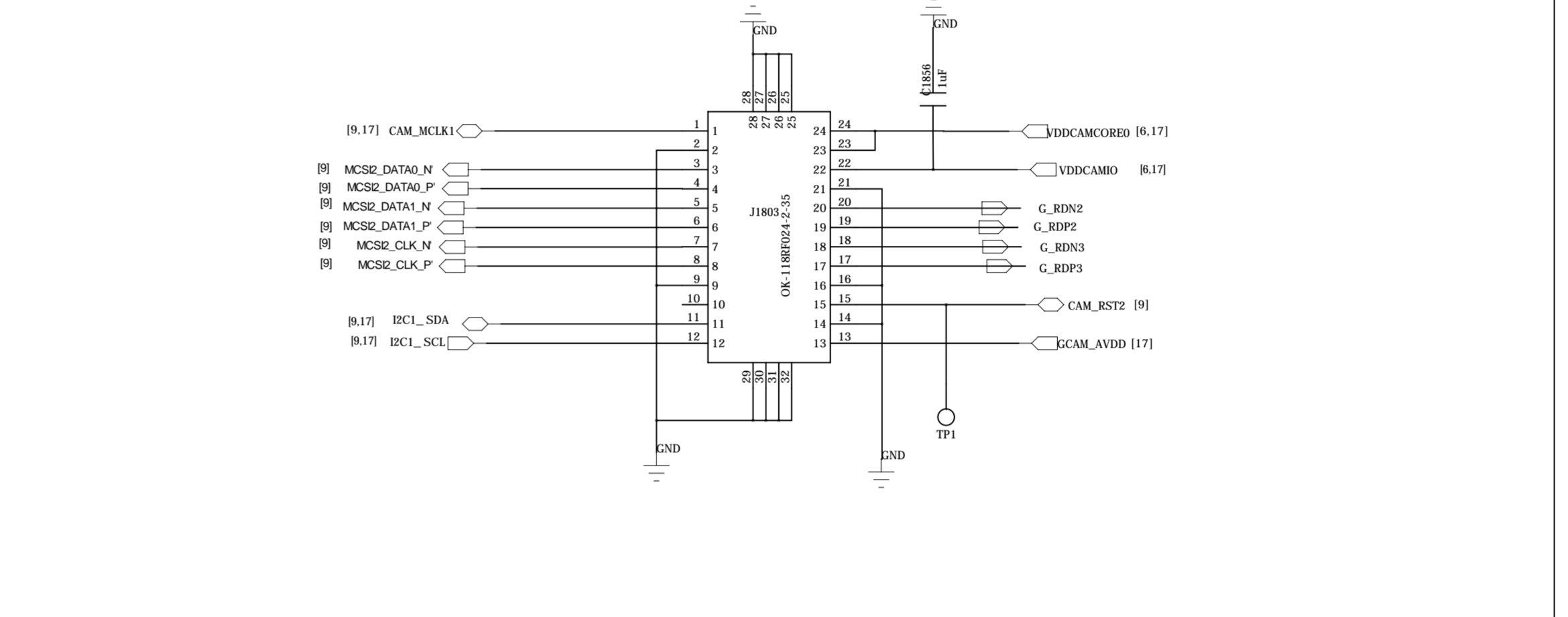
Reserve for RF desense issue debug



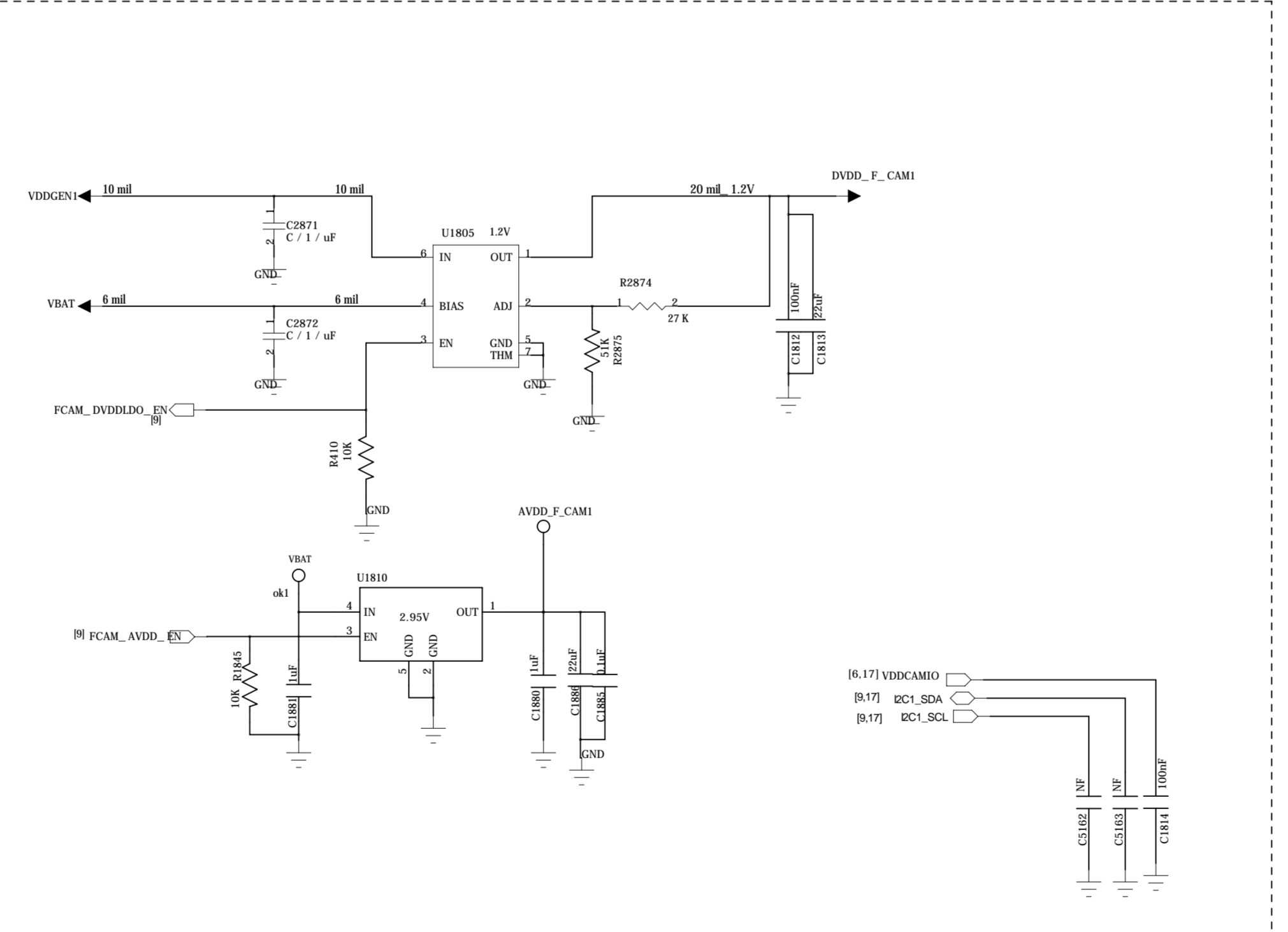
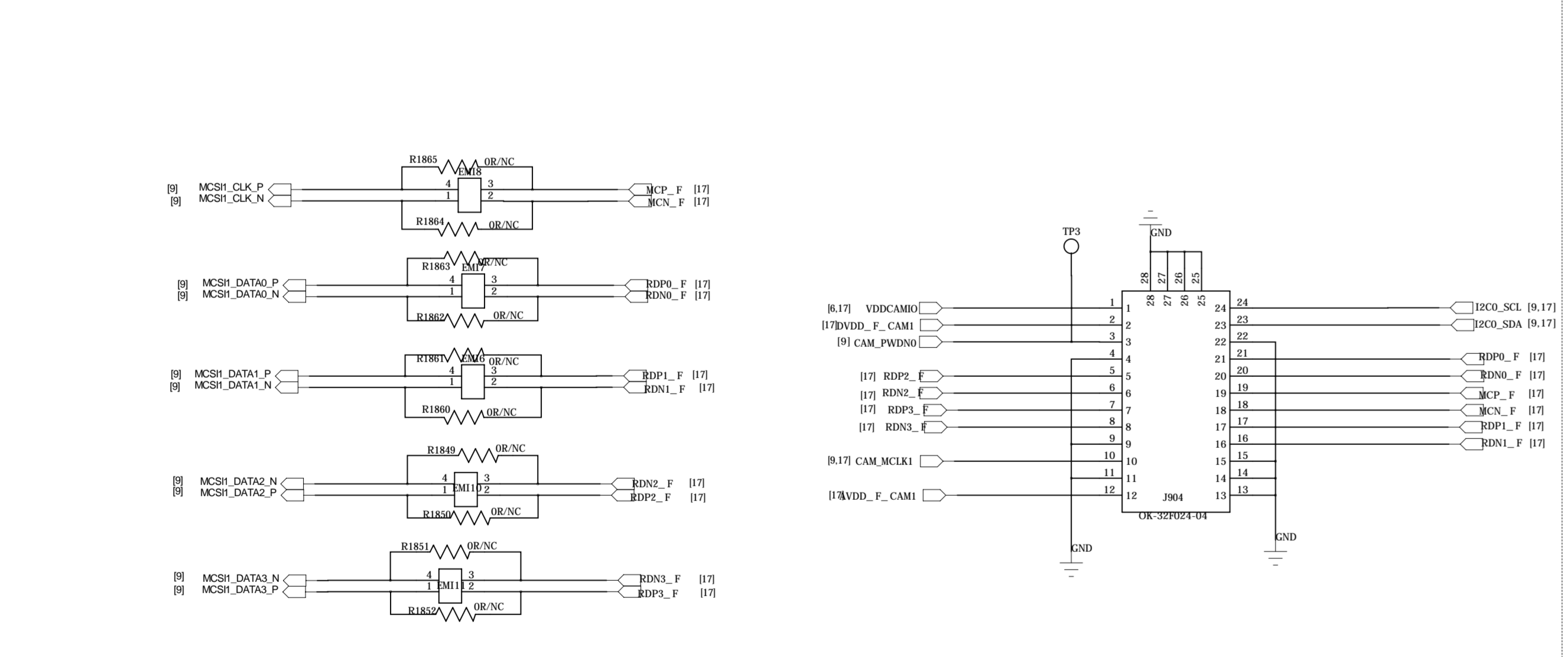
W_CAM



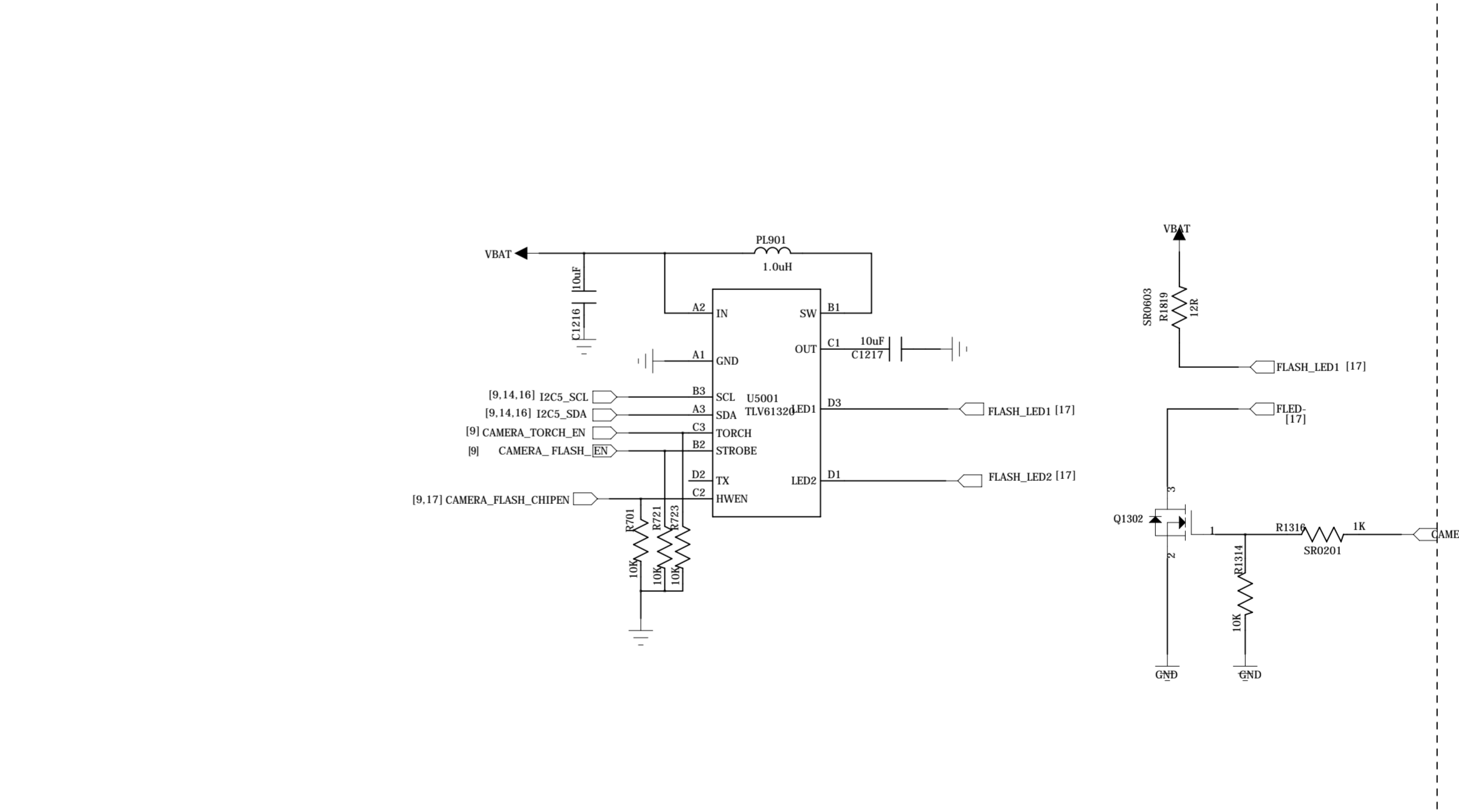
Ultra wide camera



Front camera



Flash LED



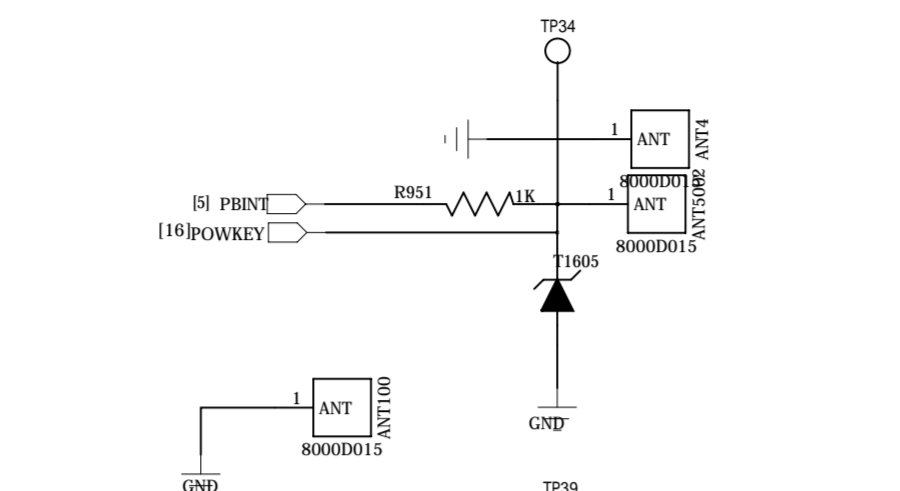
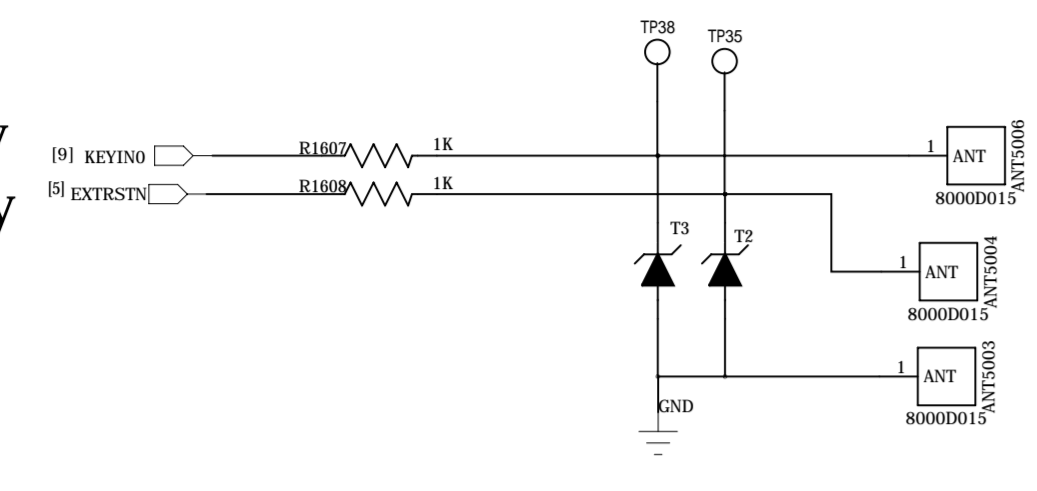
TITLE & REV:			
DOCUMENT NO.:			
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Volume Key

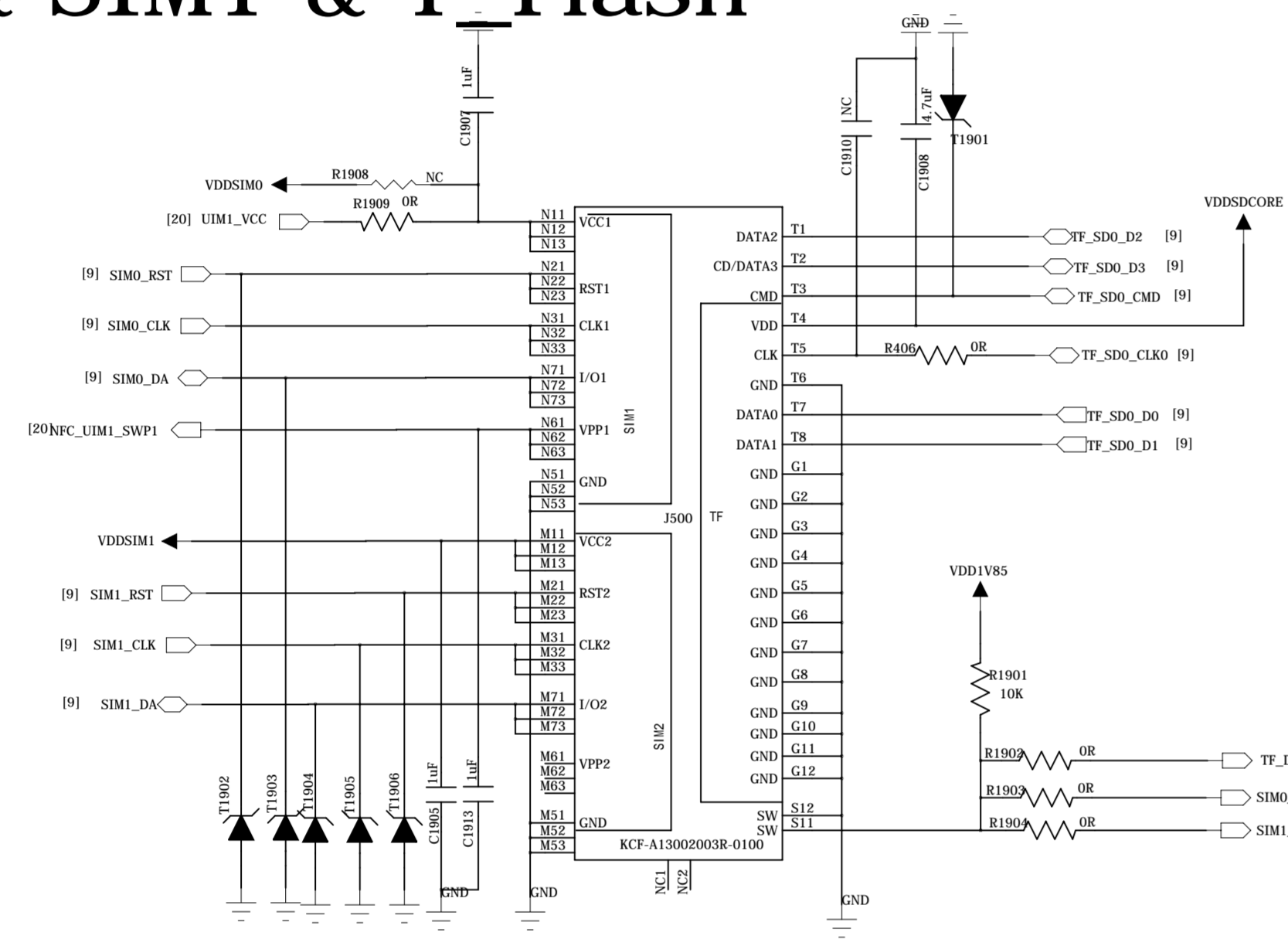
POWER KEY

Volume- Key
Volume+ Key



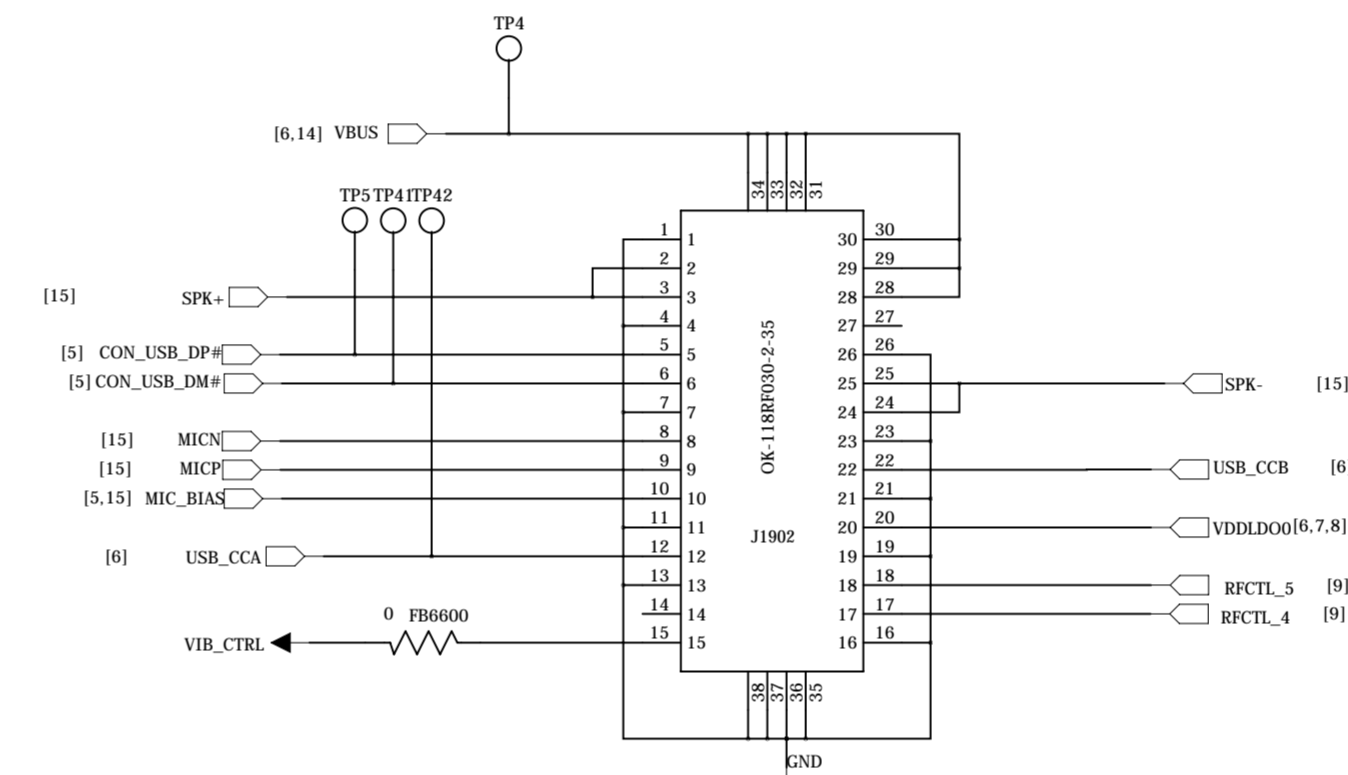
Two keys :EXTRSTN+PBINT
support two keys 7S reset ,also support single key 7S reset with UMP510G

SIM0 & SIM1 & T Flash

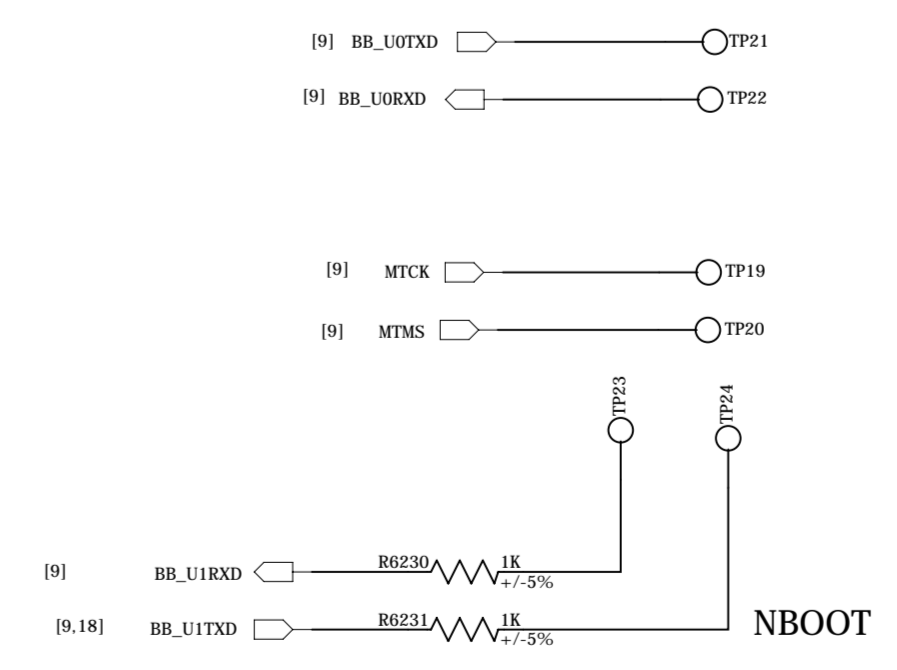


Tray	Tray DET Value
Without Tray	H
Tray Inserted	L

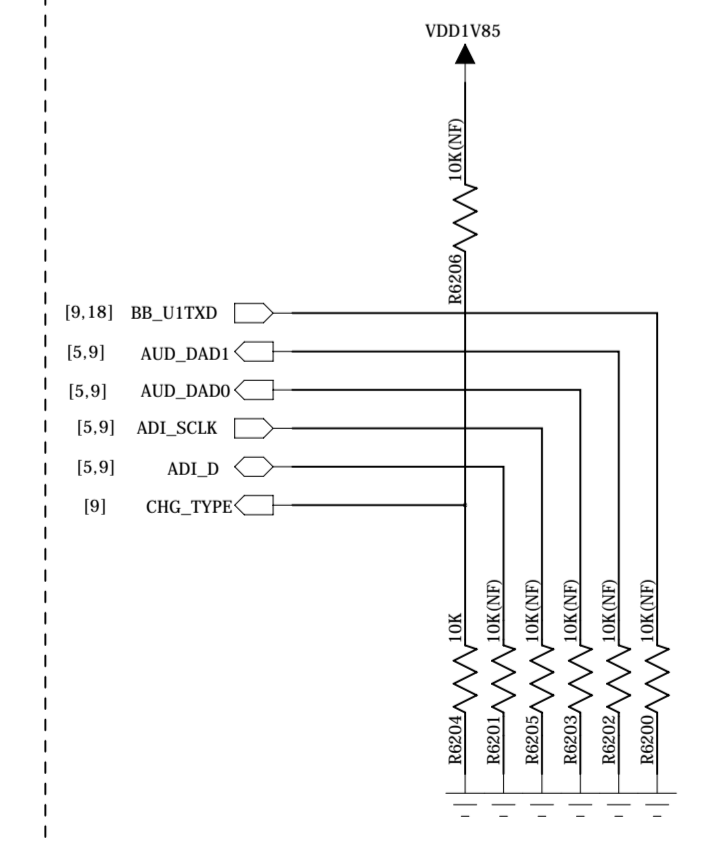
SUB



Test Points



Strapping pin



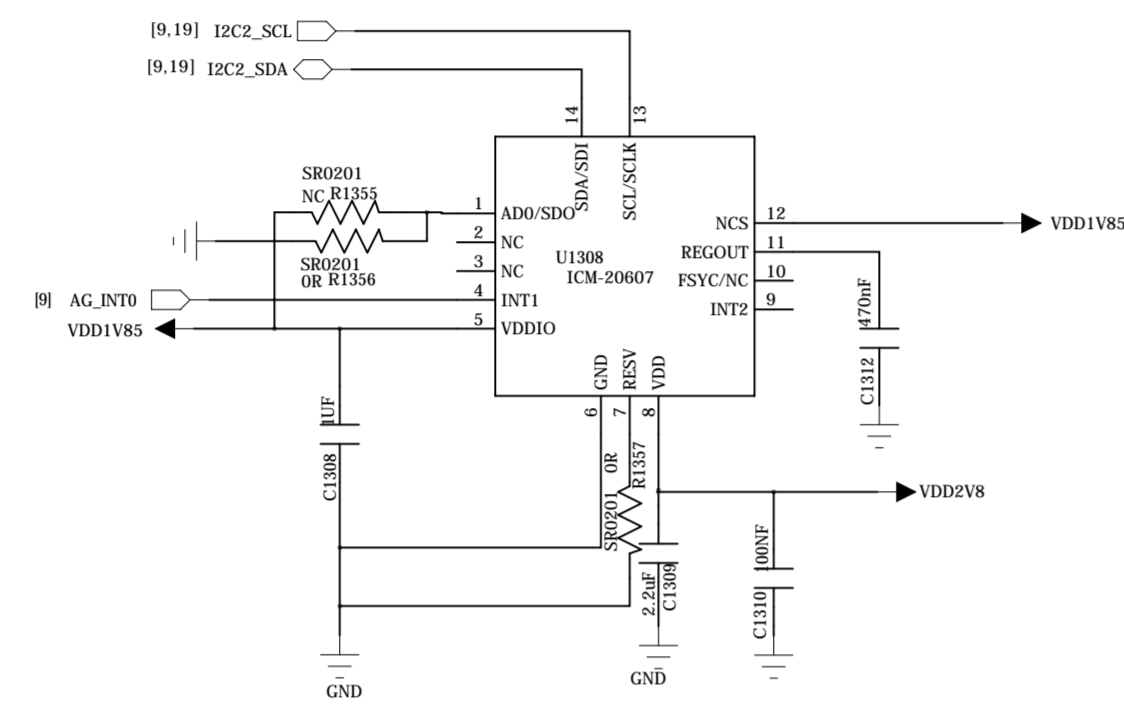
Ball name	Signal name in boot code	Value	Descriptions	Default vale
UITXD	USB_DLOAD_EN	0	USB download(no time out)	1 (ie= 1, wpu)
		1	UART download	
AUD_DAD1	ARM_BOOT_MD0	00	Reserved	01 (ie= 1, wpu) (MSB=MD1, LSB=MD0)
		01	UFS boot	
		10	SD boot	
AUD_DAD0	ARM_BOOT_MD1	0	eMMC 6.5Mhz	1 (ie= 1, wpu)
		1	Reserved	
ADI_SCLK	ARM_BOOT_MD2	0	Reserved	1 (ie= 1, wpu)
		1	Reserved	
ADI_D	ARM_BOOT_MD3	0	NON-KEY Download(With timeout) mode	1 (ie= 1, wpu)
		1	Normal mode	
CHG_TYPE	ARM_BOOT_MD4	0	Other(DCP/other)	0 (ie= 1, wpd)
		1	USB insert (SDP/CDP)	

FIDUCIALS

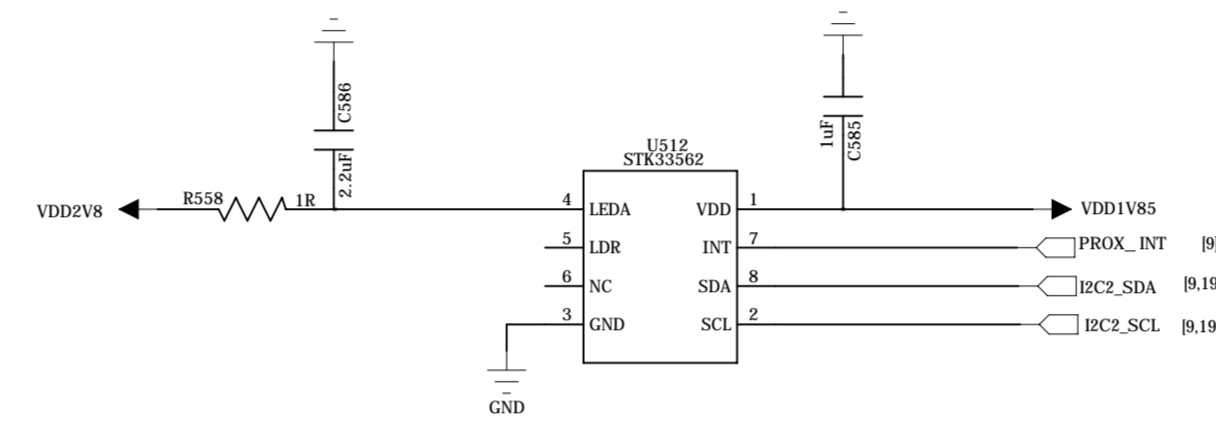
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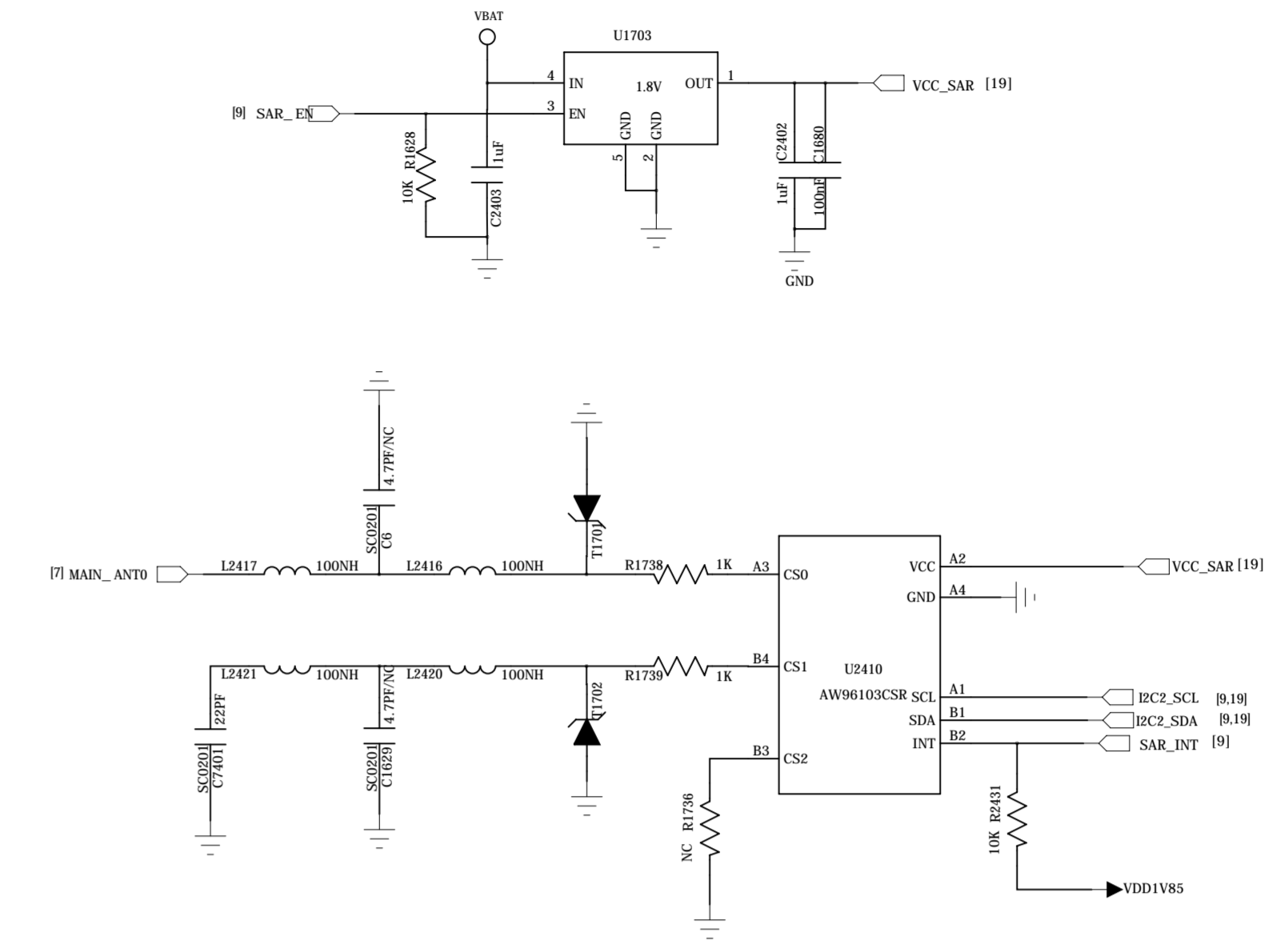
Accelerometer I3C



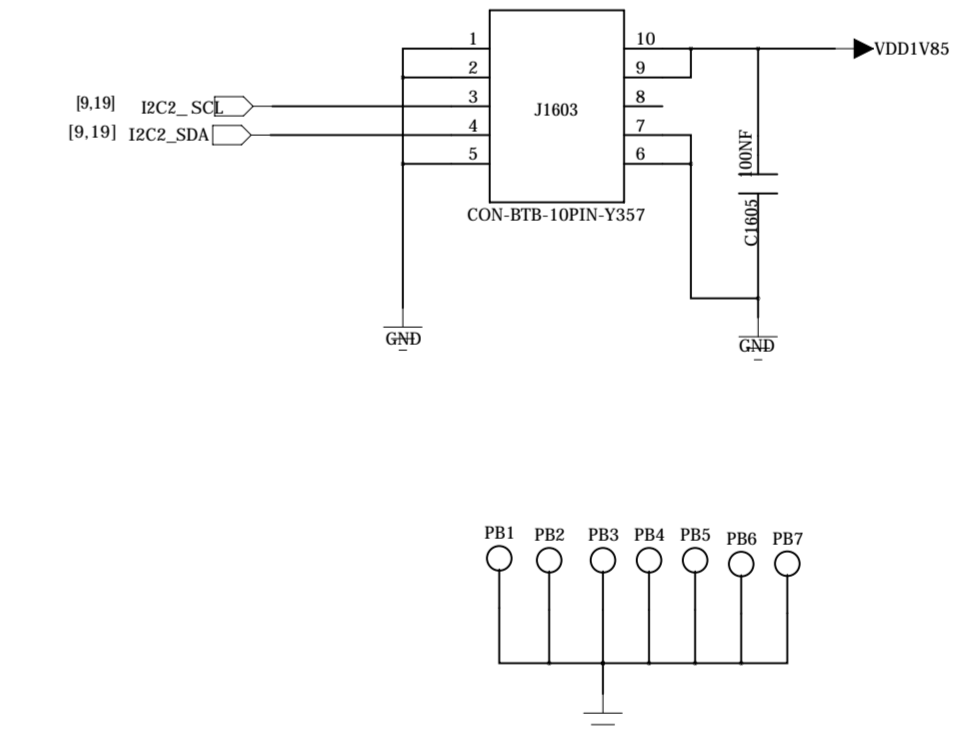
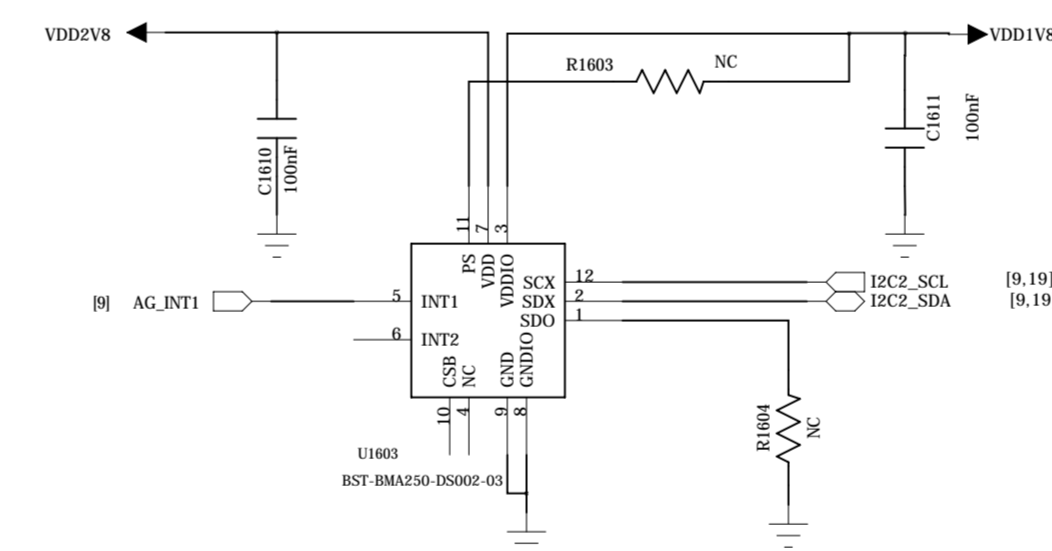
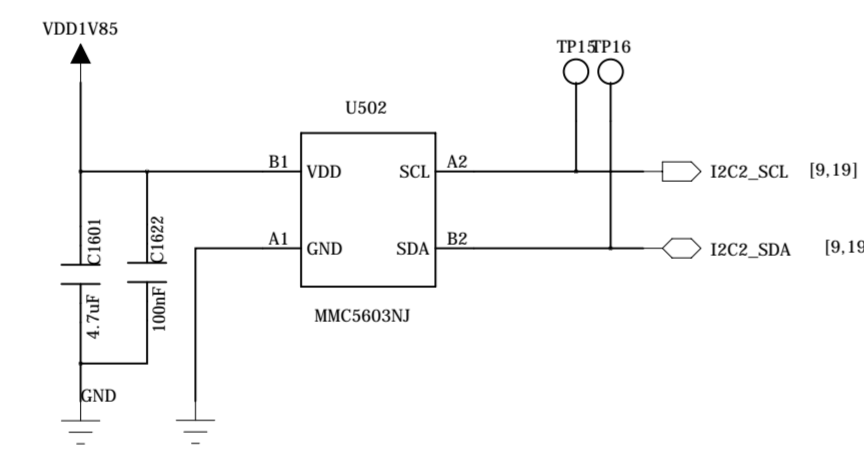
ALS & PROX



SAR Sensor



Magnetic Sensor



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