

2021/05/24

Initial draft.

2021/08/24

- 1.Remove ED3、ED4、L2、C6、C7 for optim ize eye diagrams (HS).
- 2.Reserved LDO circuit for VBUS drop voltage issue.
- 3.Add R19、R20 100R for I2C damping.
- 4.Remove test point I2C1_SCL、I2C1_SDA.
- 5.Add test point D+/D- for factory demand.
- 6.RF tuning value.
R16 change 0R to 2.2nH.

2021/11/18

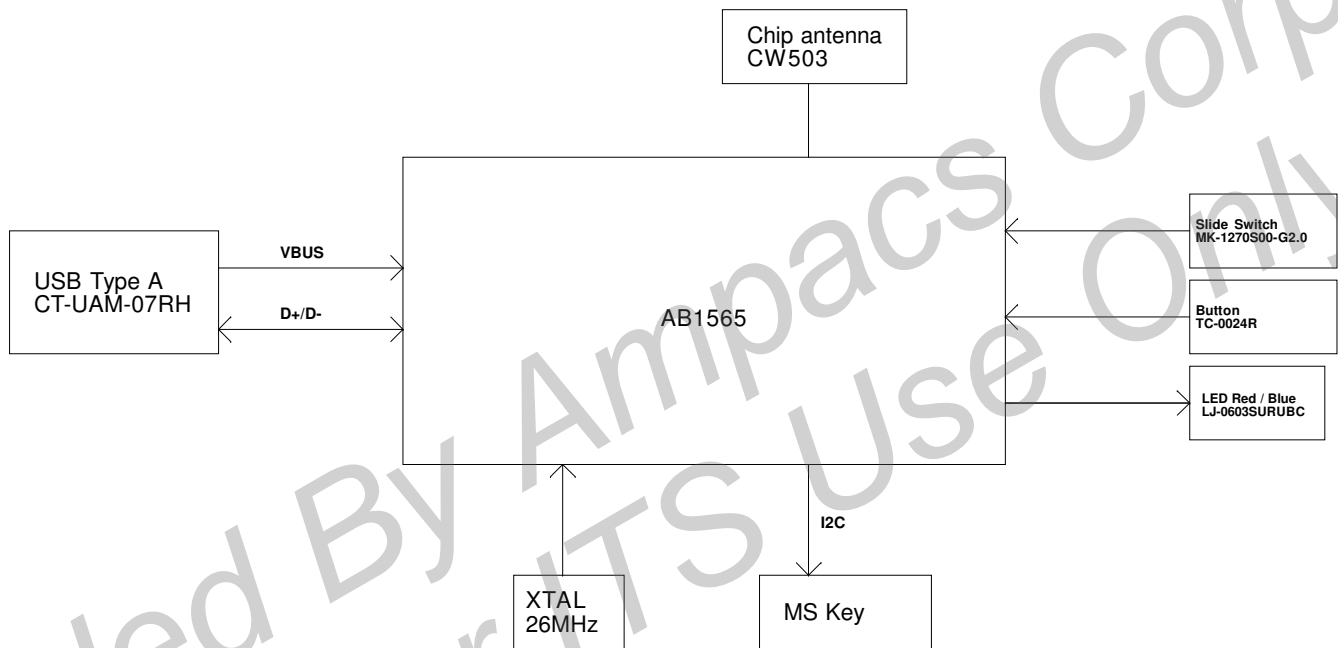
- 1.R17、R18 change 2.2K to 2.7K for XBOX.
- 2.Add ED3、ED4 for customer demand.
- 3.EMI test pass.
L1 change Bead to 0R.

2021/12/17


- 1.R19、R20 from NC to 100R for XBOX.

Provided By Ampacs Corporation
For ITS Use Only

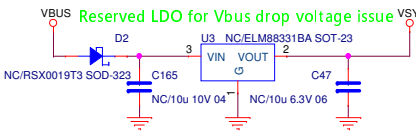
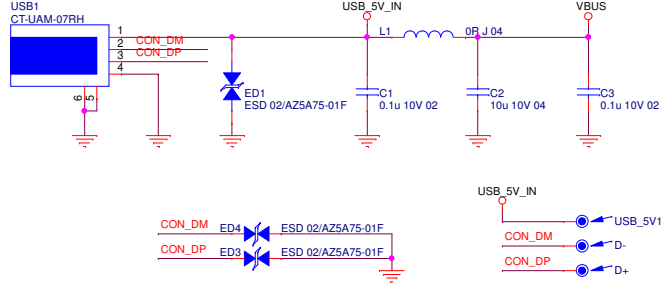
Approved by	Checked by	Designer	AMPACS CORPORATION	
Eddie		Elaine	Project Number	
			RIG700AB_HX_TX	
			Size Custom	Title History
Date: Friday, December 17, 2021		Sheet 1 of 4	Rev 2C	



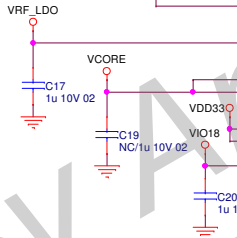
Provided By Ampacs Corporation
 For ITS Use Only

Approved by	Checked by	Designer	AMPACS CORPORATION	 AMPACS CORPORATION
Eddie		Elaine	Project Number RIG700AB_HX_TX	
			Size Custom Title Block Diagram	Rev 2C
			Date: Friday, December 17, 2021	Sheet 2 of 4

USB TYPE-A



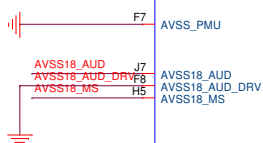
C11 C15 close to IC.



C17-C22 close to IC.

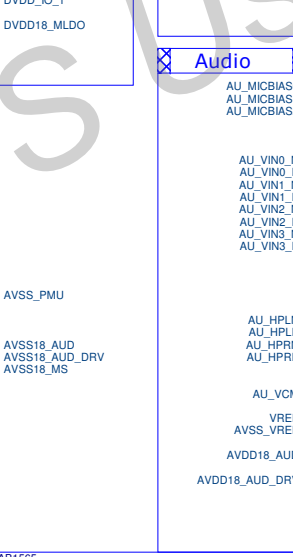
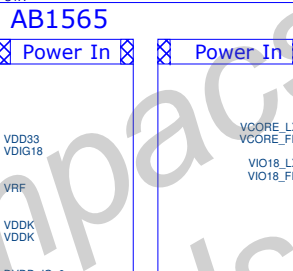
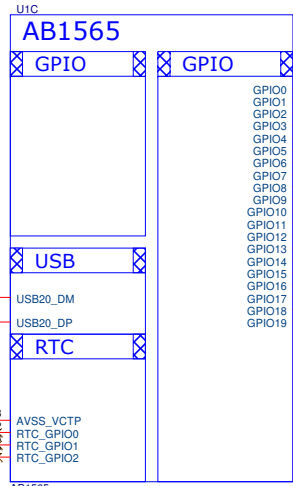
VBAT cap value should be smaller than 10uF

PAIRING LED

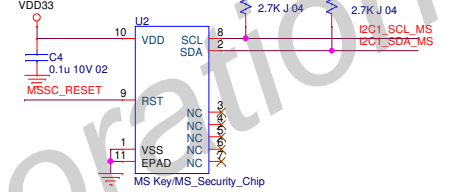


C25 Connect to AVSS18_AUD First

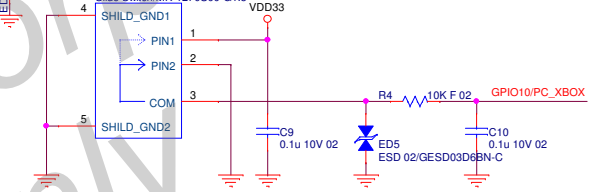
C25 close to IC.



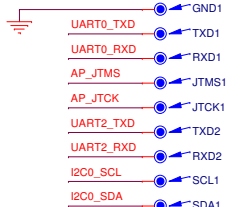
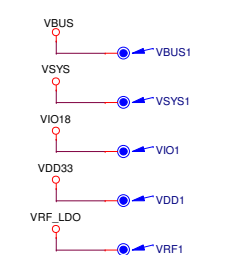
MS Security Chip



Slide Switch



C16 Connect to AVSS18_MS First

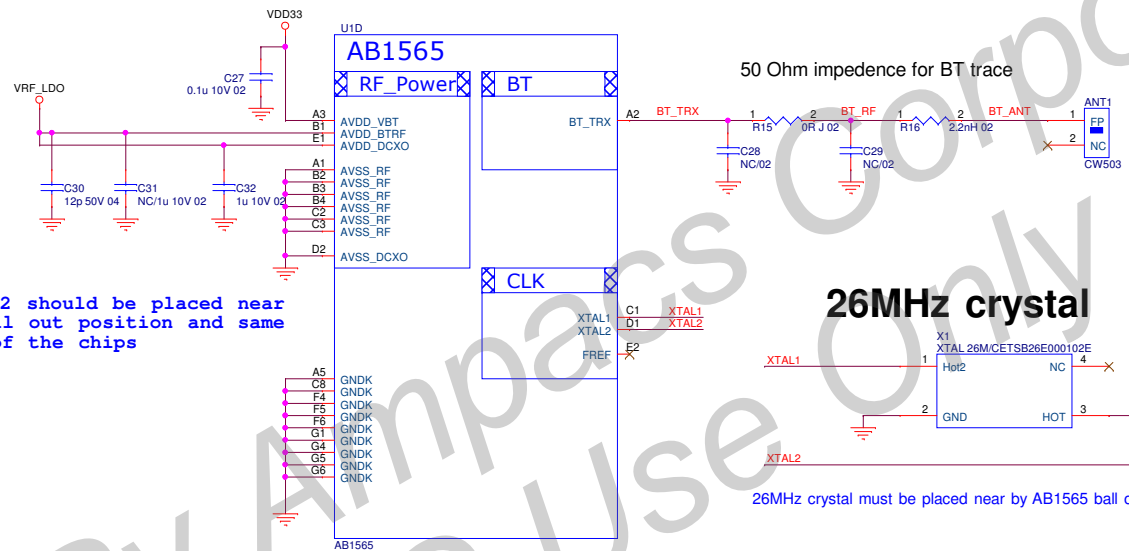


SLT Mode
 {GPIO13} SLT mode
 0: normal
 1: SLT
 The SLT mode is only used for debug in Airaha side .Please keep GPIOM3 be floating.

DL Mode
 {GPIO2} DL mode
 0: no USB download
 1: USB download
 Please reserve the test point if needs DL mode. Please keep GPIOM2 be floating if not use DL mode.

GPIO13_STL_MODE → STL1
 GPIO2_DL_MODE → DL1

Approved by	Checked by	Designer	AMPACS CORPORATION	
Eddie		Elaine	Project Number RIG700AB_HX_TX	AMPACS CORPORATION
			Size Custom	Rev 2C
			Title AB1565_Main	Date: Friday, December 17, 2021 Sheet 3 of 4



C30-C32 should be placed near by ball out position and same side of the chips

26MHz crystal

26MHz crystal must be placed near by AB1565 ball out position

Provided By Ampacs Corporation
For ITS Use

Approved by	Checked by	Designer	AMPACS CORPORATION	
Eddie		Elaine	Project Number RIG700AB_HX_TX	AMPACS CORPORATION
			Size Custom Title AB1565_RF	Rev 2C
			Date: Friday, December 17, 2021	Sheet 4 of 4