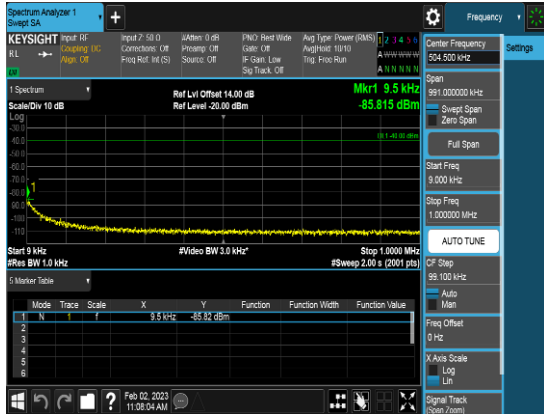


SCC_10M_3574.4MHz_QPSK_FULLLRB_CSE0a



SCC_10M_3574.4MHz_QPSK_FULLLRB_CSE0b



SCC_10M_3574.4MHz_QPSK_FULLLRB_CSE1



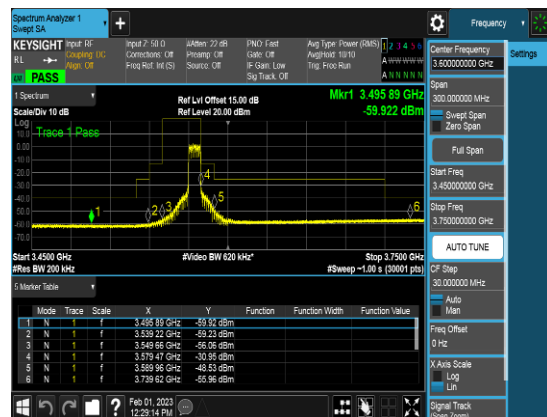
SCC_10M_3574.4MHz_QPSK_FULLLRB_CSE2



SCC_10M_3574.4MHz_QPSK_FULLLRB_CSE3



SCC_10M_3574.4MHz_QPSK_FULLLRB_CSEpar t96

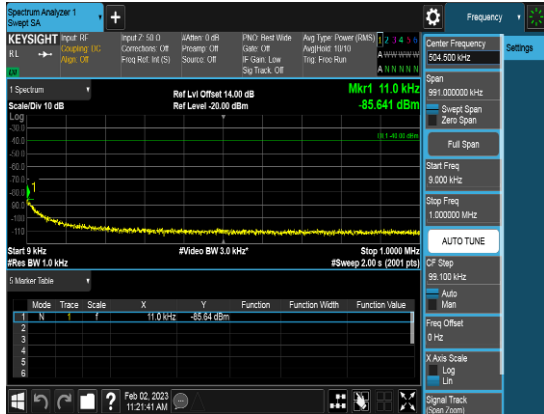


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SCC_10M_3634.5MHz_Q16_1RB_CSE0a



SCC_10M_3634.5MHz_Q16_1RB_CSE0b



SCC_10M_3634.5MHz_Q16_1RB_CSE1



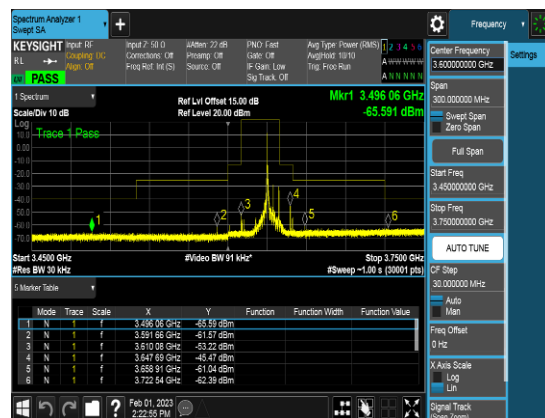
SCC_10M_3634.5MHz_Q16_1RB_CSE2



SCC_10M_3634.5MHz_Q16_1RB_CSE3



SCC_10M_3634.5MHz_Q16_1RB_CSEpart96

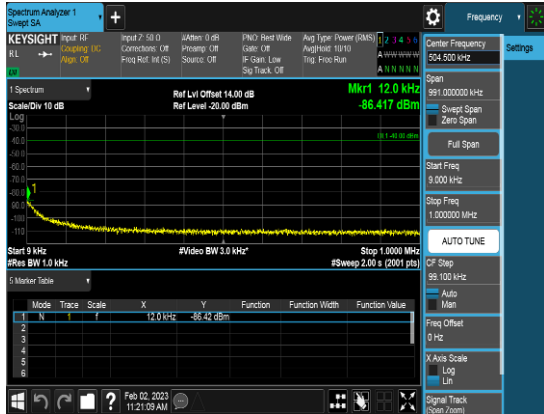


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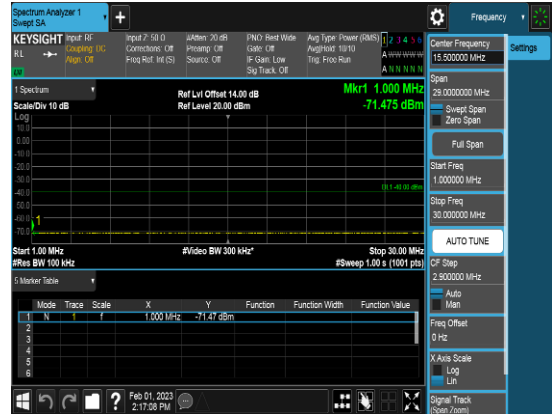
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SCC_10M_3634.5MHz_Q16_FULLLRB_CSE0a



SCC_10M_3634.5MHz_Q16_FULLLRB_CSE0b



SCC_10M_3634.5MHz_Q16_FULLLRB_CSE1



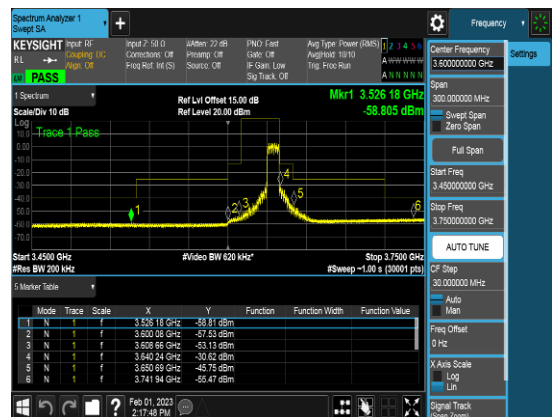
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SCC_10M_3634.5MHz_Q16_FULLLRB_CSE3



SCC_10M_3634.5MHz_Q16_FULLLRB_CSEpart 6

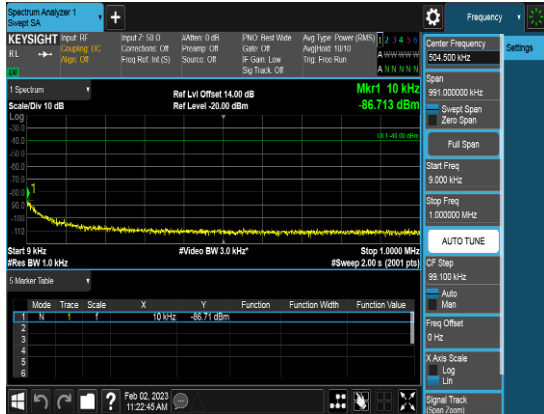


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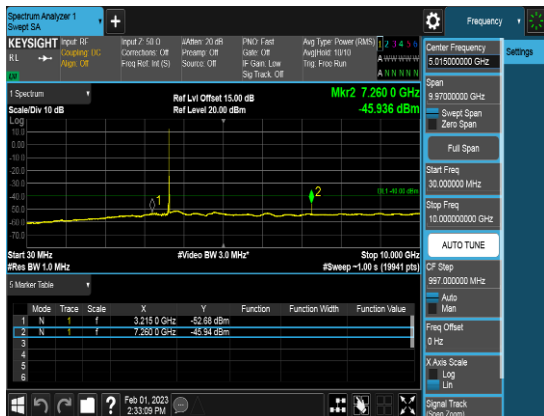
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SCC_10M_3634.5MHz_Q64_1RB_CSE0b



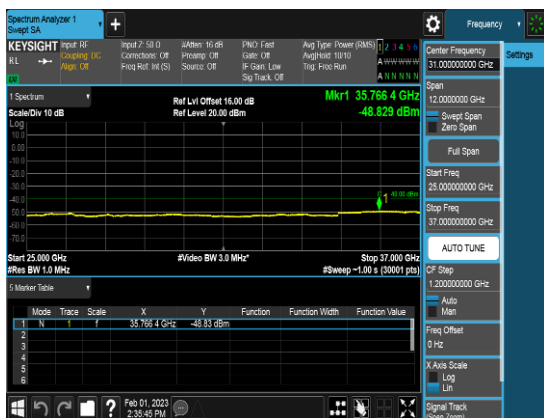
SCC_10M_3634.5MHz_Q64_1RB_CSE1



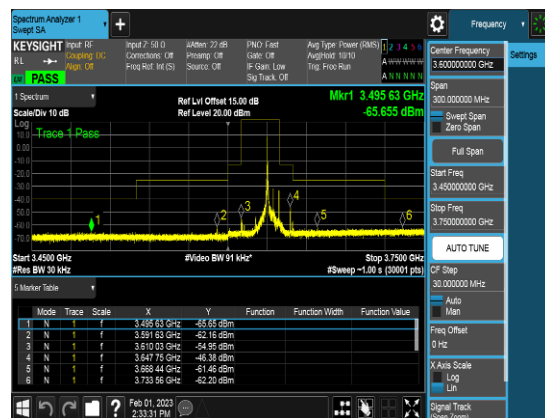
SCC_10M_3634.5MHz_Q64_1RB_CSE2



SCC_10M_3634.5MHz_Q64_1RB_CSE3



SCC_10M_3634.5MHz_Q64_1RB_CSEpart96

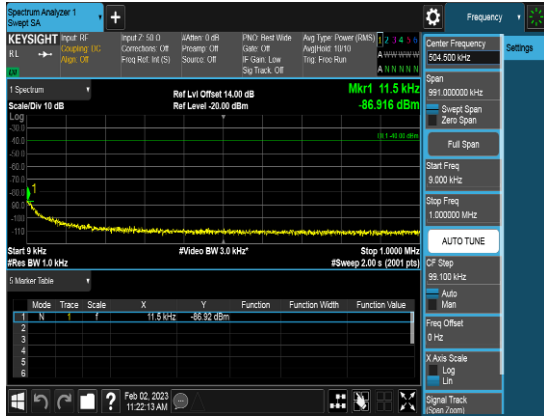


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SCC_10M_3634.5MHz_Q64_FULLLRB_CSE0a



SCC_10M_3634.5MHz_Q64_FULLLRB_CSE0b



SCC_10M_3634.5MHz_Q64_FULLLRB_CSE1



SCC_10M_3634.5MHz_Q64_FULLLRB_CSE2



SCC_10M_3634.5MHz_Q64_FULLLRB_CSE3



SCC_10M_3634.5MHz_Q64_FULLLRB_CSEpart 6

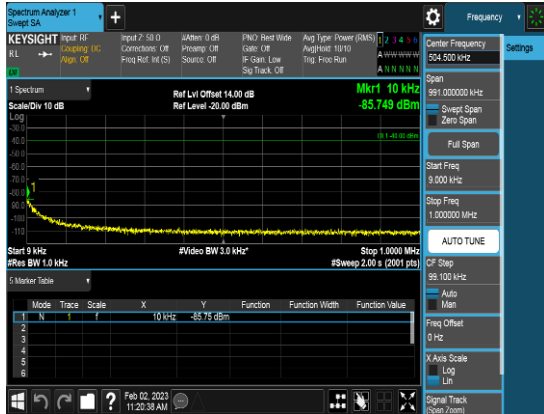


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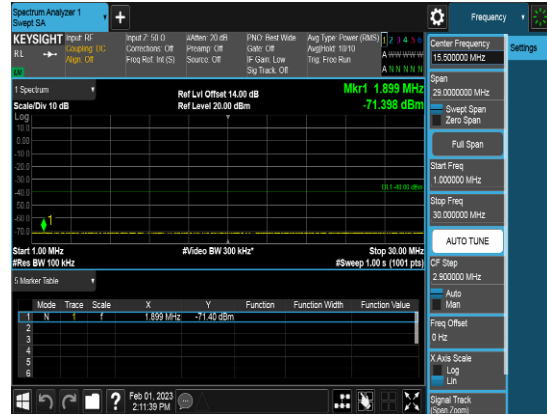
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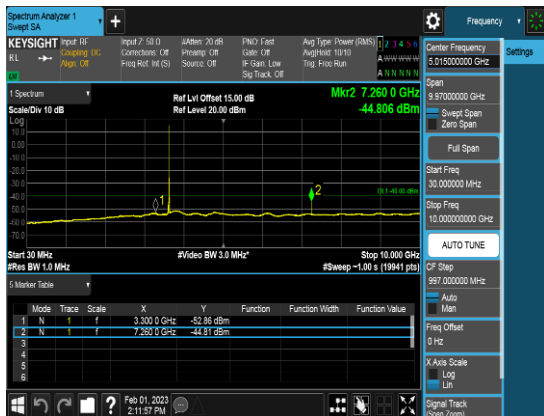
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SCC_10M_3634.5MHz_QPSK_1RB_CSE0b



SCC_10M_3634.5MHz_QPSK_1RB_CSE1



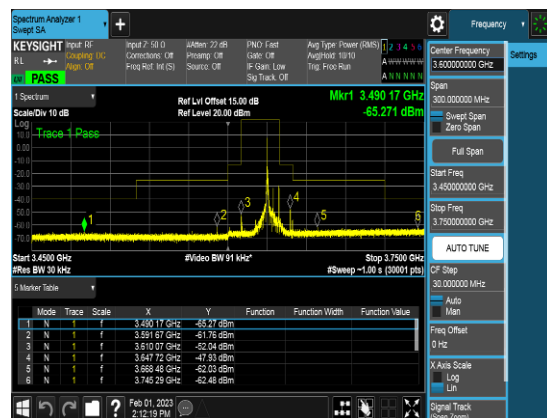
SCC_10M_3634.5MHz_QPSK_1RB_CSE2



SCC_10M_3634.5MHz_QPSK_1RB_CSE3



SCC_10M_3634.5MHz_QPSK_1RB_CSEpart96



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SCC_10M_3634.5MHz_QPSK_FULLLRB_CSE0a



SCC_10M_3634.5MHz_QPSK_FULLLRB_CSE0b



SCC_10M_3634.5MHz_QPSK_FULLLRB_CSE1



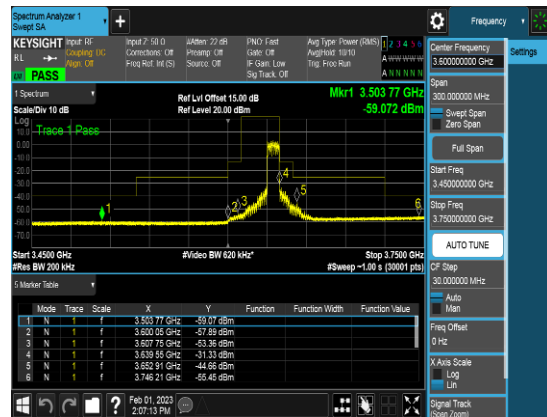
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SCC_10M_3634.5MHz_QPSK_FULLLRB_CSE3



SCC_10M_3634.5MHz_QPSK_FULLLRB_CSEpar t96

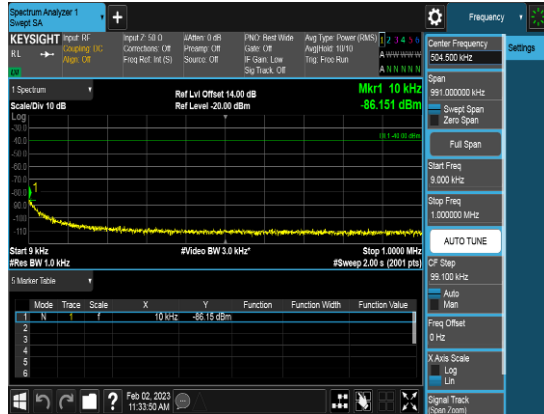


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SCC_10M_3694.5MHz_Q16_1RB_CSE0a



SCC_10M_3694.5MHz_Q16_1RB_CSE0b



SCC_10M_3694.5MHz_Q16_1RB_CSE1



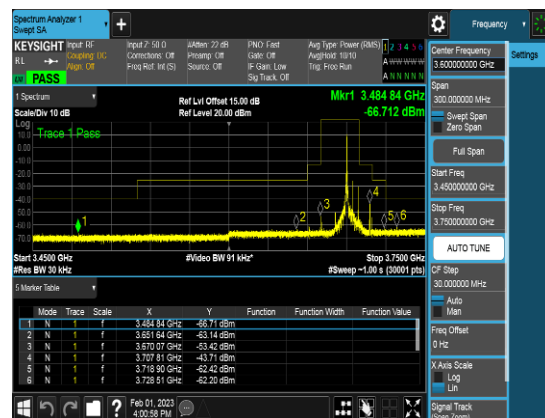
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SCC_10M_3694.5MHz_Q16_1RB_CSE3



SCC_10M_3694.5MHz_Q16_1RB_CSEpart96



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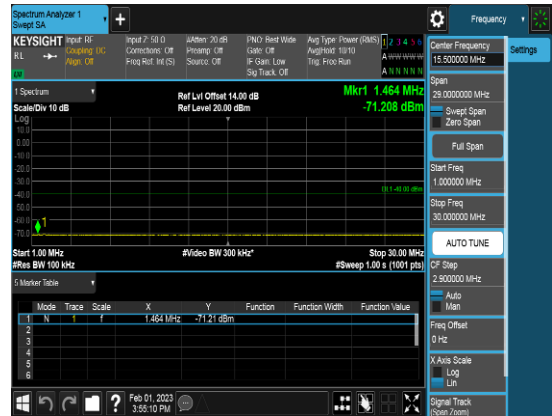
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SCC_10M_3694.5MHz_Q16_FULLLRB_CSE0a



SCC_10M_3694.5MHz_Q16_FULLLRB_CSE0b



SCC_10M_3694.5MHz_Q16_FULLLRB_CSE1



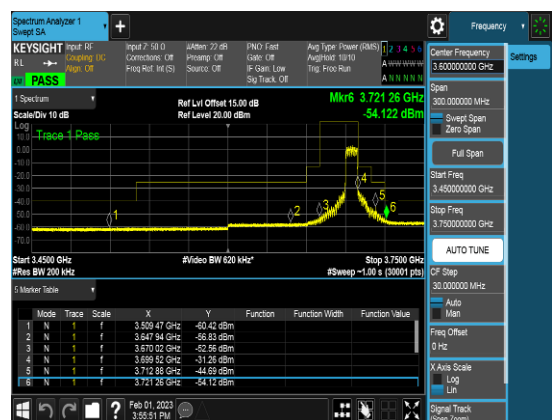
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SCC_10M_3694.5MHz_Q16_FULLLRB_CSE3



SCC_10M_3694.5MHz_Q16_FULLLRB_CSEpart 6

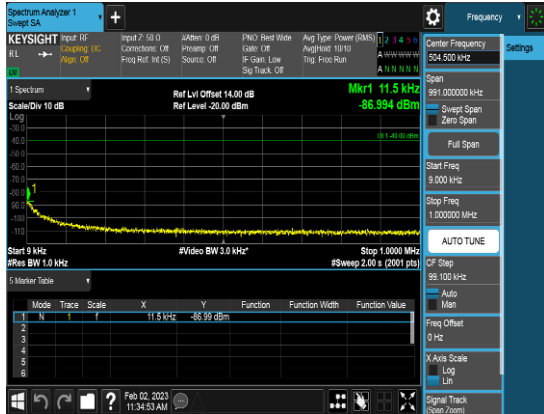


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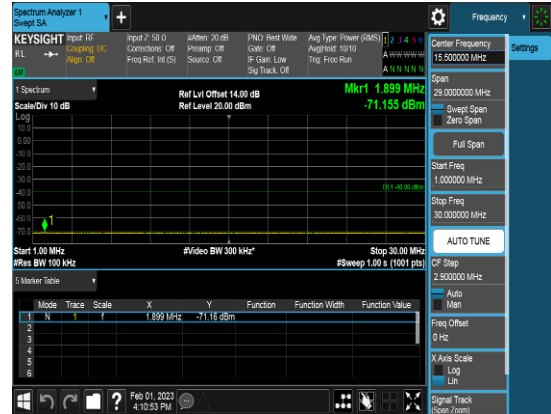
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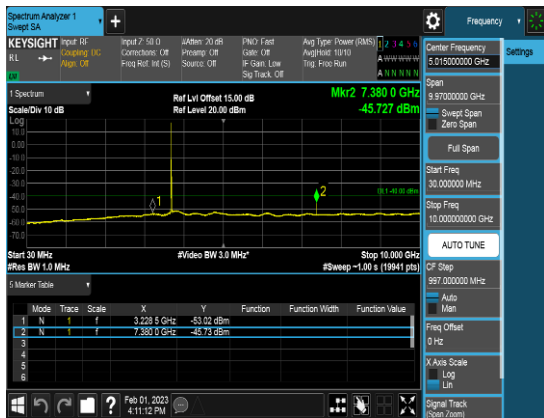
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SCC_10M_3694.5MHz_Q64_1RB_CSE0b



SCC_10M_3694.5MHz_Q64_1RB_CSE1



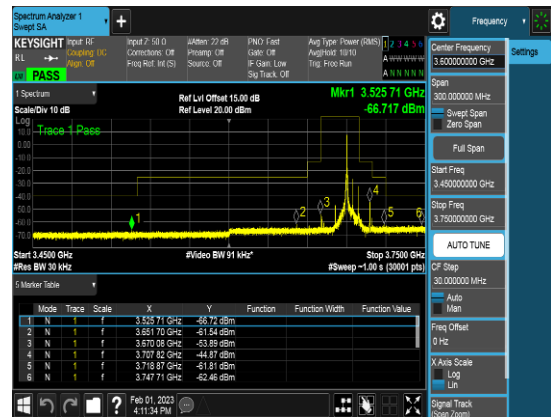
SCC_10M_3694.5MHz_Q64_1RB_CSE2



SCC_10M_3694.5MHz_Q64_1RB_CSE3



SCC_10M_3694.5MHz_Q64_1RB_CSEpart96



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