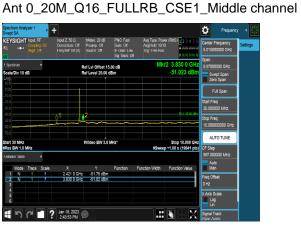


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#### Ant 0\_20M\_Q16\_FULLRB\_CSE1\_High channel



ISpan Zoon



Ant 0\_20M\_Q16\_FULLRB\_CSE2\_Low channel



Ant 0\_20M\_Q16\_FULLRB\_CSE1\_Low channel



Ant 0\_20M\_Q16\_FULLRB\_CSE2\_High channel



Ant 0\_20M\_Q16\_FULLRB\_CSE2\_Middle channel



Ant 0\_20M\_Q16\_FULLRB\_CSE3\_High channel

Ant 0\_20M\_Q16\_FULLRB\_CSE3\_Low channel



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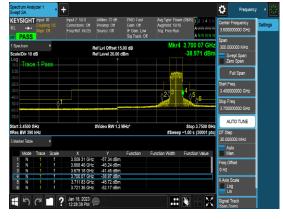
Ant 0\_20M\_Q16\_FULLRB\_CSE3\_Middle channel



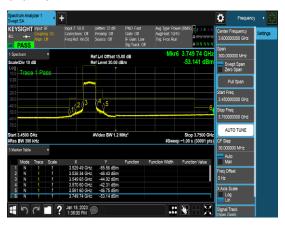
Ant 0\_20M\_Q16\_FULLRB\_CSEpart96\_High channel

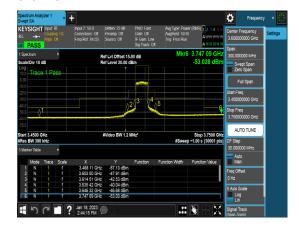


Ant 0\_20M\_Q16\_FULLRB\_CSEpart96\_Low channel



Ant 0\_20M\_Q16\_FULLRB\_CSEpart96\_Middle channel







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Ant 0\_20M\_Q64\_1RB0\_CSE0a\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSE0a\_Middle channel



Ant 0\_20M\_Q64\_1RB0\_CSE0b\_Low channel



Ant 0\_20M\_Q64\_1RB0\_CSE0a\_Low channel



Ant 0\_20M\_Q64\_1RB0\_CSE0b\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSE0b\_Middle channel





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### Ant 0\_20M\_Q64\_1RB0\_CSE1\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSE1\_Middle channel



Ant 0\_20M\_Q64\_1RB0\_CSE2\_Low channel



Ant 0\_20M\_Q64\_1RB0\_CSE1\_Low channel



Ant 0\_20M\_Q64\_1RB0\_CSE2\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSE2\_Middle channel





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Ant 0\_20M\_Q64\_1RB0\_CSE3\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSE3\_Middle channel



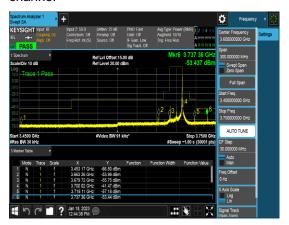
Ant 0\_20M\_Q64\_1RB0\_CSEpart96\_Low channel



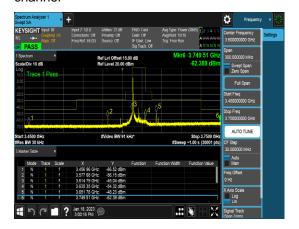
Ant 0\_20M\_Q64\_1RB0\_CSE3\_Low channel



Ant 0\_20M\_Q64\_1RB0\_CSEpart96\_High channel



Ant 0\_20M\_Q64\_1RB0\_CSEpart96\_Middle channel





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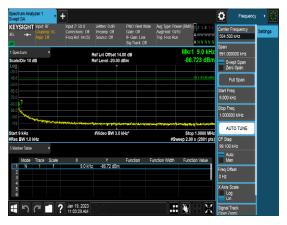


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Ant 0\_20M\_Q64\_FULLRB\_CSE0a\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSE0a\_Middle channel



Ant 0\_20M\_Q64\_FULLRB\_CSE0b\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSE0a\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSE0b\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSE0b\_Middle channel





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Ant 0\_20M\_Q64\_FULLRB\_CSE1\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSE1\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSE1\_Middle channel



Ant 0\_20M\_Q64\_FULLRB\_CSE2\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSE2\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSE2\_Middle channel





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Ant 0\_20M\_Q64\_FULLRB\_CSE3\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSE3\_Middle channel



Ant 0\_20M\_Q64\_FULLRB\_CSEpart96\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSE3\_Low channel



Ant 0\_20M\_Q64\_FULLRB\_CSEpart96\_High channel



Ant 0\_20M\_Q64\_FULLRB\_CSEpart96\_Middle channel





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### Ant 0\_20M\_QPSK\_1RB0\_CSE0a\_High channel



Ant 0\_20M\_QPSK\_1RB0\_CSE0a\_Middle channel



Ant 0\_20M\_QPSK\_1RB0\_CSE1\_High channel



Ant 0\_20M\_QPSK\_1RB0\_CSE0a\_Low channel



Ant 0\_20M\_QPSK\_1RB0\_CSE0b\_Middle channel



Ant 0\_20M\_QPSK\_1RB0\_CSE1\_Low channel





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#### Ant 0\_20M\_QPSK\_1RB0\_CSE1\_Middle channel



Ant 0\_20M\_QPSK\_1RB0\_CSE2\_Low channel



Ant 0\_20M\_QPSK\_1RB0\_CSE3\_High channel



Ant 0\_20M\_QPSK\_1RB0\_CSE2\_High channel



Ant 0\_20M\_QPSK\_1RB0\_CSE2\_Middle channel



Ant 0\_20M\_QPSK\_1RB0\_CSE3\_Low channel





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Ant 0\_20M\_QPSK\_1RB0\_CSE3\_Middle channel



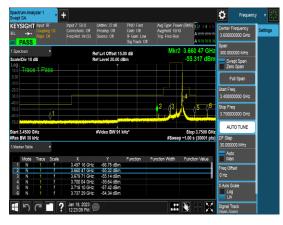
Ant 0\_20M\_QPSK\_1RB0\_CSEpart96\_Low channel



Ant 0\_20M\_QPSK\_1RB\_CSE0b\_Low channel



Ant 0\_20M\_QPSK\_1RB0\_CSEpart96\_High channel



Ant 0\_20M\_QPSK\_1RB0\_CSEpart96\_Middle channel



Ant 0\_20M\_QPSK\_1RRB0\_CSE0b\_High channel





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