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### PCC\_20M\_3680.1MHz\_Q16\_1RB\_PAPR

# | Prepared | Property | Prepared | Prepared

PCC\_20M\_3680.1MHz\_Q64\_1RB\_PAPR



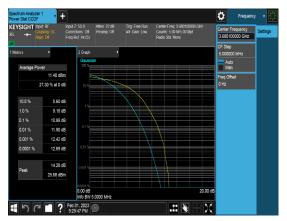
PCC\_20M\_3680.1MHz\_QPSK\_1RB\_PAPR



PCC\_20M\_3680.1MHz\_Q16\_FULLRB\_PAPR



PCC\_20M\_3680.1MHz\_Q64\_FULLRB\_PAPR



PCC\_20M\_3680.1MHz\_QPSK\_FULLRB\_PAPR





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### SCC\_10M\_3574.4MHz\_Q16\_1RB\_PAPR

# | President | Property | President | Property | President | Presid

SCC\_10M\_3574.4MHz\_Q64\_1RB\_PAPR



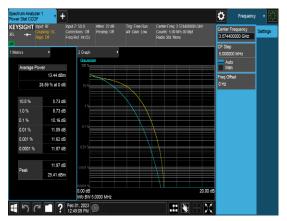
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SCC\_10M\_3574.4MHz\_Q16\_FULLRB\_PAPR



SCC\_10M\_3574.4MHz\_Q64\_FULLRB\_PAPR



SCC\_10M\_3574.4MHz\_QPSK\_FULLRB\_PAPR





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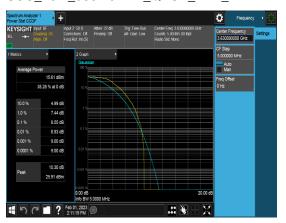
### SCC\_10M\_3634.5MHz\_Q16\_1RB\_PAPR

# ### Care Company Compa

SCC\_10M\_3634.5MHz\_Q64\_1RB\_PAPR



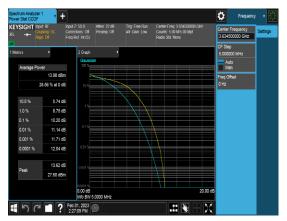
SCC\_10M\_3634.5MHz\_QPSK\_1RB\_PAPR



SCC\_10M\_3634.5MHz\_Q16\_FULLRB\_PAPR



SCC\_10M\_3634.5MHz\_Q64\_FULLRB\_PAPR



SCC\_10M\_3634.5MHz\_QPSK\_FULLRB\_PAPR





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### SCC\_10M\_3694.5MHz\_Q16\_1RB\_PAPR

# | Process State Control | Process |

SCC\_10M\_3694.5MHz\_Q64\_1RB\_PAPR



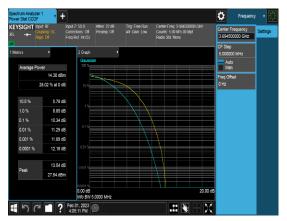
SCC\_10M\_3694.5MHz\_QPSK\_1RB\_PAPR



SCC\_10M\_3694.5MHz\_Q16\_FULLRB\_PAPR



SCC\_10M\_3694.5MHz\_Q64\_FULLRB\_PAPR



SCC\_10M\_3694.5MHz\_QPSK\_FULLRB\_PAPR





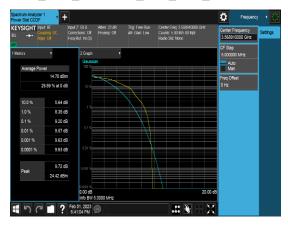
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PCC 20MHz + SCC 20MHz

PCC\_20M\_3560MHz\_Q16\_1RB\_PAPR



PCC\_20M\_3560MHz\_Q64\_1RB\_PAPR



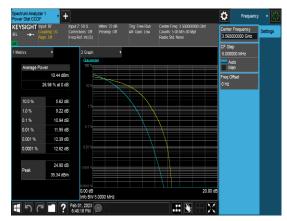
PCC\_20M\_3560MHz\_QPSK\_1RB\_PAPR



PCC\_20M\_3560MHz\_Q16\_FULLRB\_PAPR



PCC\_20M\_3560MHz\_Q64\_FULLRB\_PAPR



PCC\_20M\_3560MHz\_QPSK\_FULLRB\_PAPR





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### PCC\_20M\_3620.1MHz\_Q16\_1RB\_PAPR

# | Control | Cont

PCC\_20M\_3620.1MHz\_Q64\_1RB\_PAPR



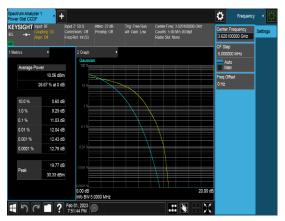
PCC\_20M\_3620.1MHz\_QPSK\_1RB\_PAPR



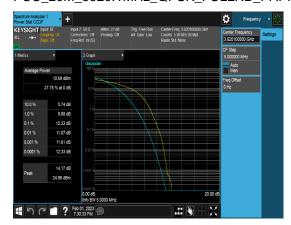
PCC\_20M\_3620.1MHz\_Q16\_FULLRB\_PAPR



PCC\_20M\_3620.1MHz\_Q64\_FULLRB\_PAPR



PCC\_20M\_3620.1MHz\_QPSK\_FULLRB\_PAPR





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### PCC\_20M\_3670.2MHz\_Q16\_1RB\_PAPR

# ## PROJECT OF STATE O

PCC\_20M\_3670.2MHz\_Q64\_1RB\_PAPR



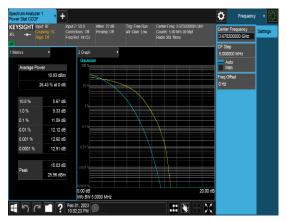
PCC\_20M\_3670.2MHz\_QPSK\_1RB\_PAPR



PCC\_20M\_3670.2MHz\_Q16\_FULLRB\_PAPR



PCC\_20M\_3670.2MHz\_Q64\_FULLRB\_PAPR



PCC\_20M\_3670.2MHz\_QPSK\_FULLRB\_PAPR





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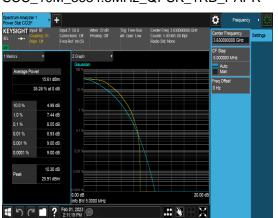
### SCC\_10M\_3634.5MHz\_Q16\_1RB\_PAPR



SCC\_10M\_3634.5MHz\_Q64\_1RB\_PAPR



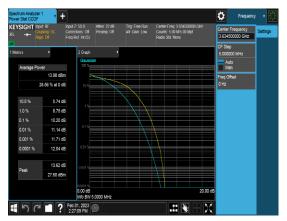
SCC\_10M\_3634.5MHz\_QPSK\_1RB\_PAPR



SCC\_10M\_3634.5MHz\_Q16\_FULLRB\_PAPR



SCC\_10M\_3634.5MHz\_Q64\_FULLRB\_PAPR



SCC\_10M\_3634.5MHz\_QPSK\_FULLRB\_PAPR





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### SCC\_20M\_3579.8MHz\_Q16\_1RB\_PAPR

# ## PROJECT OF THE PRO

SCC\_20M\_3579.8MHz\_Q64\_1RB\_PAPR



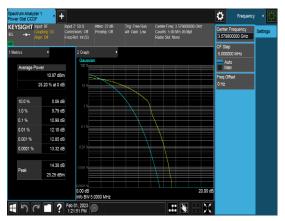
SCC\_20M\_3579.8MHz\_QPSK\_1RB\_PAPR



SCC\_20M\_3579.8MHz\_Q16\_FULLRB\_PAPR



SCC\_20M\_3579.8MHz\_Q64\_FULLRB\_PAPR



SCC\_20M\_3579.8MHz\_QPSK\_FULLRB\_PAPR





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### SCC\_20M\_3690MHz\_Q16\_1RB\_PAPR

# | Continue Analyses | Property | Property (in the Continue Analyses | Property (in th

SCC\_20M\_3690MHz\_Q64\_1RB\_PAPR



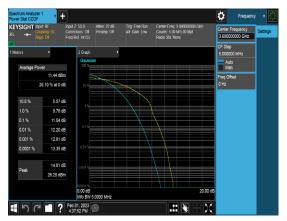
SCC\_20M\_3690MHz\_QPSK\_1RB\_PAPR



SCC\_20M\_3690MHz\_Q16\_FULLRB\_PAPR



SCC\_20M\_3690MHz\_Q64\_FULLRB\_PAPR



SCC\_20M\_3690MHz\_QPSK\_FULLRB\_PAPR



- End of the Appendix -



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