

Page: 47 of 108

### Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSE0a



Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSE1



Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSE3



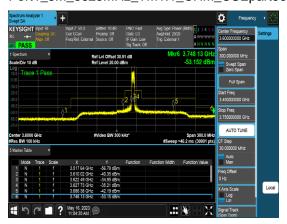
Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSE0b



Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSE2



Port1\_5M\_3625MHz\_TM1.1\_CHM\_CSEpart96





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Page: 48 of 108

### Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSE0a



Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSE1



Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSE3



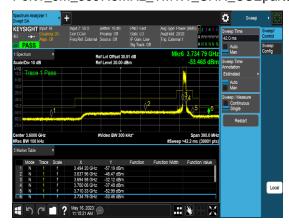
Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSE0b



Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSE2



Port1\_5M\_3697.5MHz\_TM1.1\_CHH\_CSEpart96





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Page: 49 of 108

### Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSE0a



Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSE1



Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSE3



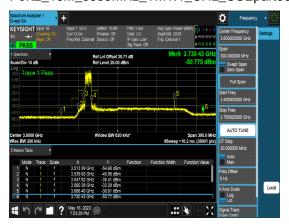
Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSE0b



Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSE2



Port2\_10M\_3555MHz\_TM1.1\_CHL\_CSEpart96





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Page: 50 of 108

### Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSE0a



Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSE1



Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSE3



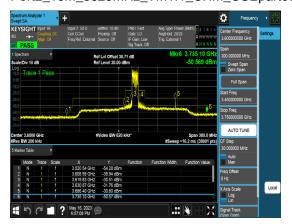
Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSE0b



Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSE2



Port2\_10M\_3625MHz\_TM1.1\_CHM\_CSEpart96





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Page: 51 of 108

### Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSE0a



Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSE1



Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSE3



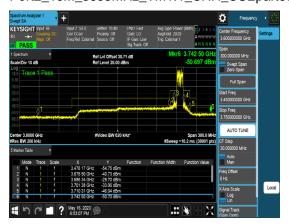
Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSE0b



Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSE2



Port2\_10M\_3695MHz\_TM1.1\_CHH\_CSEpart96





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Page: 52 of 108

### Port2\_15M\_3557.5MHz\_TM1.1\_CHL\_CSE0a



Port2\_15M\_3557.5MHz\_TM1.1\_CHL\_CSE1



Port2\_15M\_3557.5MHz\_TM1.1\_CHL\_CSE3



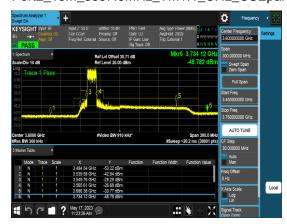
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Port2\_15M\_3557.5MHz\_TM1.1\_CHL\_CSE2



Port2\_15M\_3557.5MHz\_TM1.1\_CHL\_CSEpart96





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Page: 53 of 108

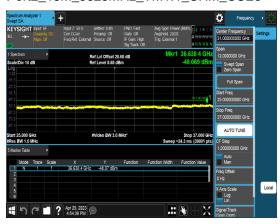
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Port2\_15M\_3625MHz\_TM1.1\_CHM\_CSE1



Port2\_15M\_3625MHz\_TM1.1\_CHM\_CSE3



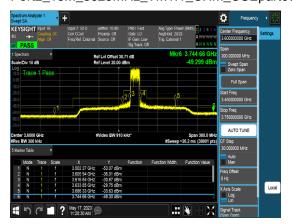
Port2\_15M\_3625MHz\_TM1.1\_CHM\_CSE0b



Port2\_15M\_3625MHz\_TM1.1\_CHM\_CSE2



Port2\_15M\_3625MHz\_TM1.1\_CHM\_CSEpart96





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Page: 54 of 108

### Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSE0a



Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSE1



Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSE3



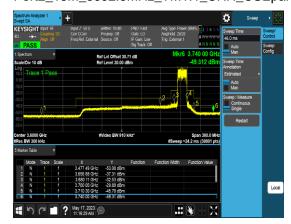
Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSE0b



Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSE2



Port2\_15M\_3692.5MHz\_TM1.1\_CHH\_CSEpart96





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Page: 55 of 108

### Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSE0a



#### Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSE1



Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSE3



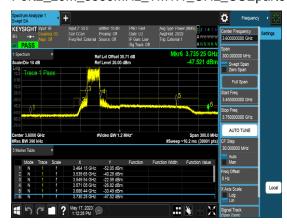
Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSE0b



Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSE2



Port2\_20M\_3560MHz\_TM1.1\_CHL\_CSEpart96





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Page: 56 of 108

### Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSE0a



Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSE1



Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSE3



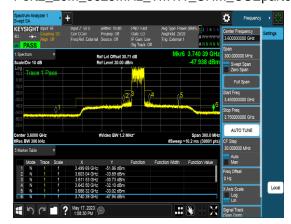
Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSE0b



Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSE2



Port2\_20M\_3625MHz\_TM1.1\_CHM\_CSEpart96





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Page: 57 of 108

### Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSE0a



Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSE1



Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSE3



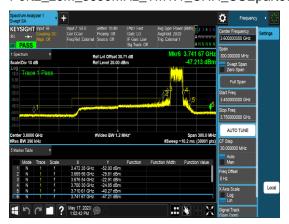
Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSE0b



Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSE2



Port2\_20M\_3690MHz\_TM1.1\_CHH\_CSEpart96





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