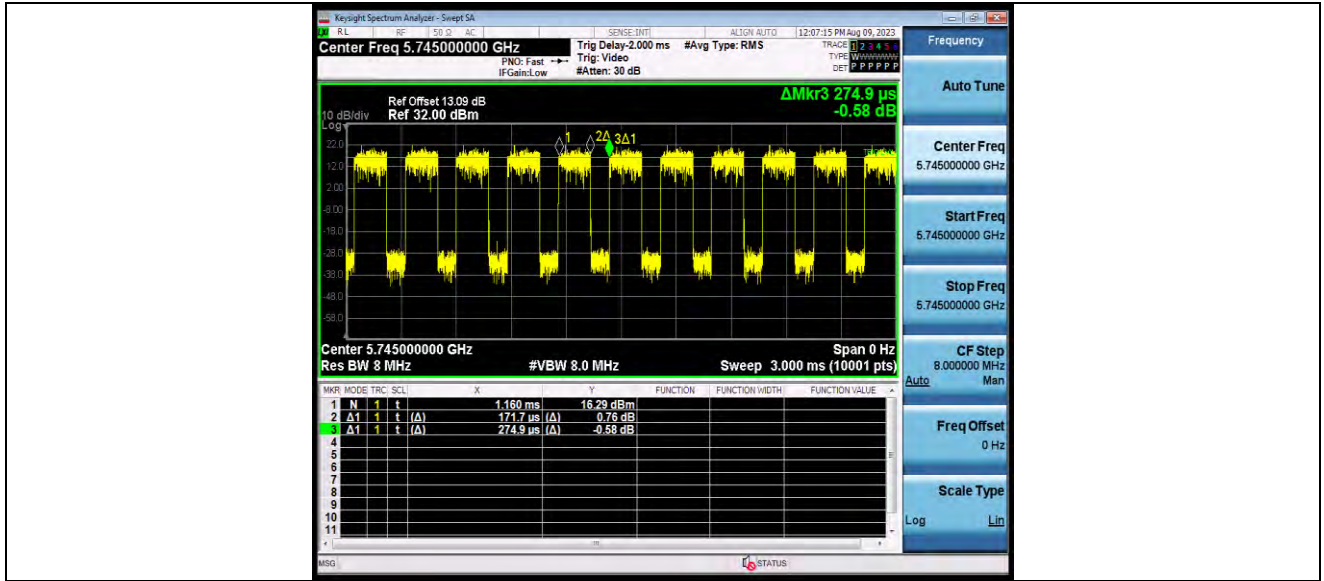


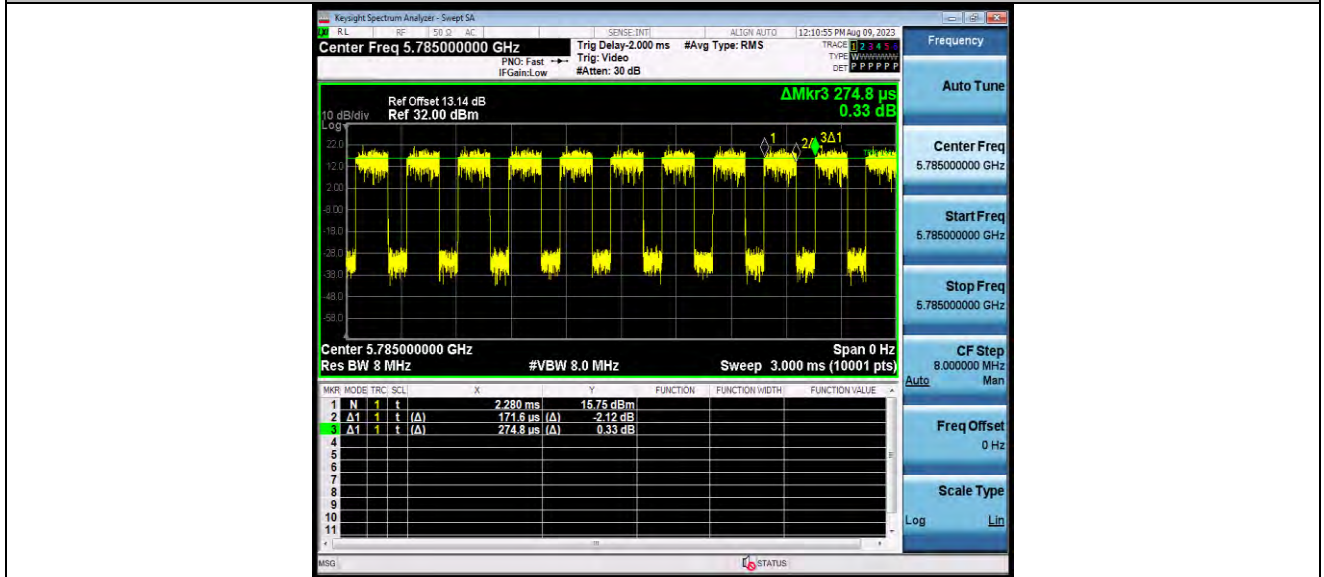


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11A_Ant1_5785

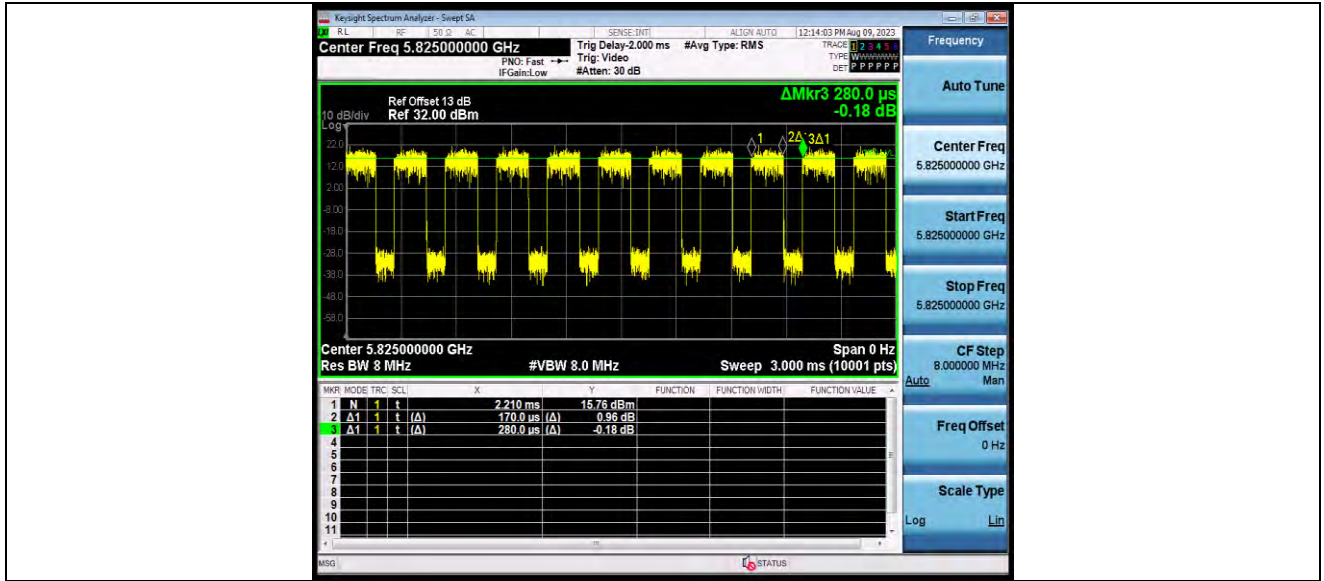


11A_Ant1_5825

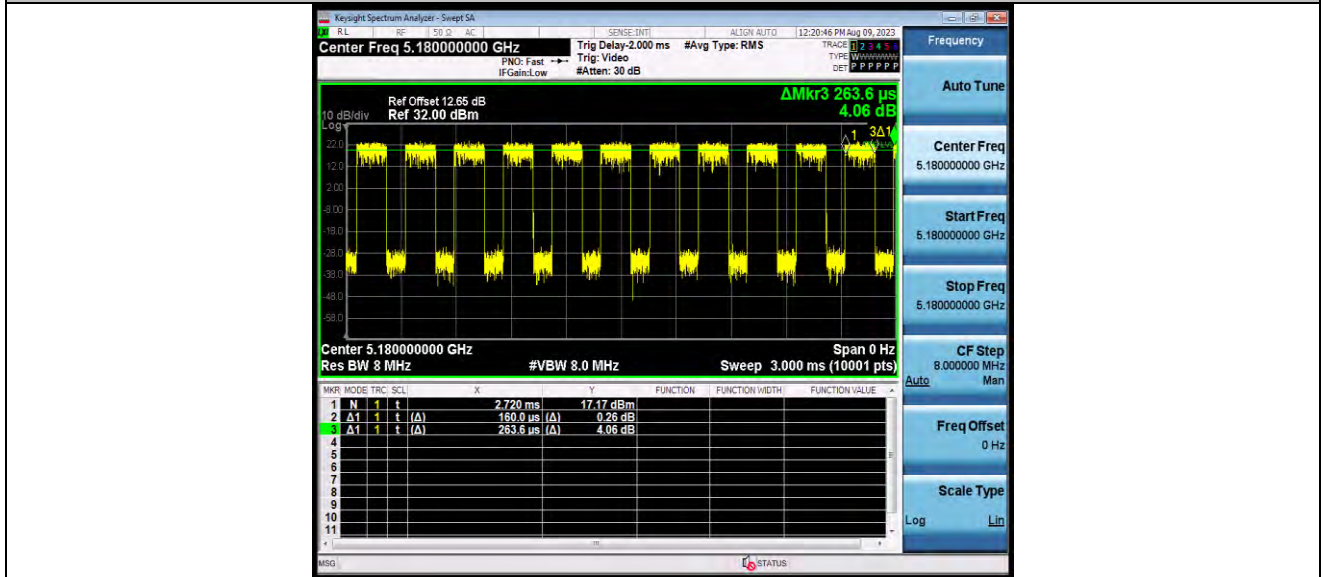


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5180

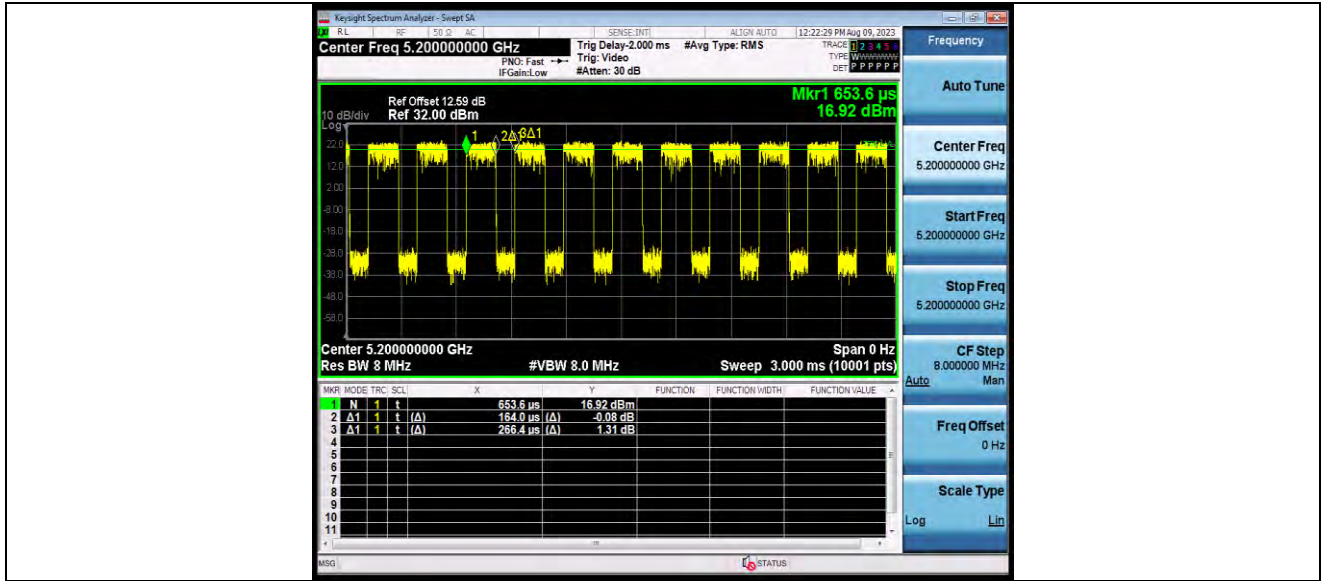


11N20SISO_Ant1_5200

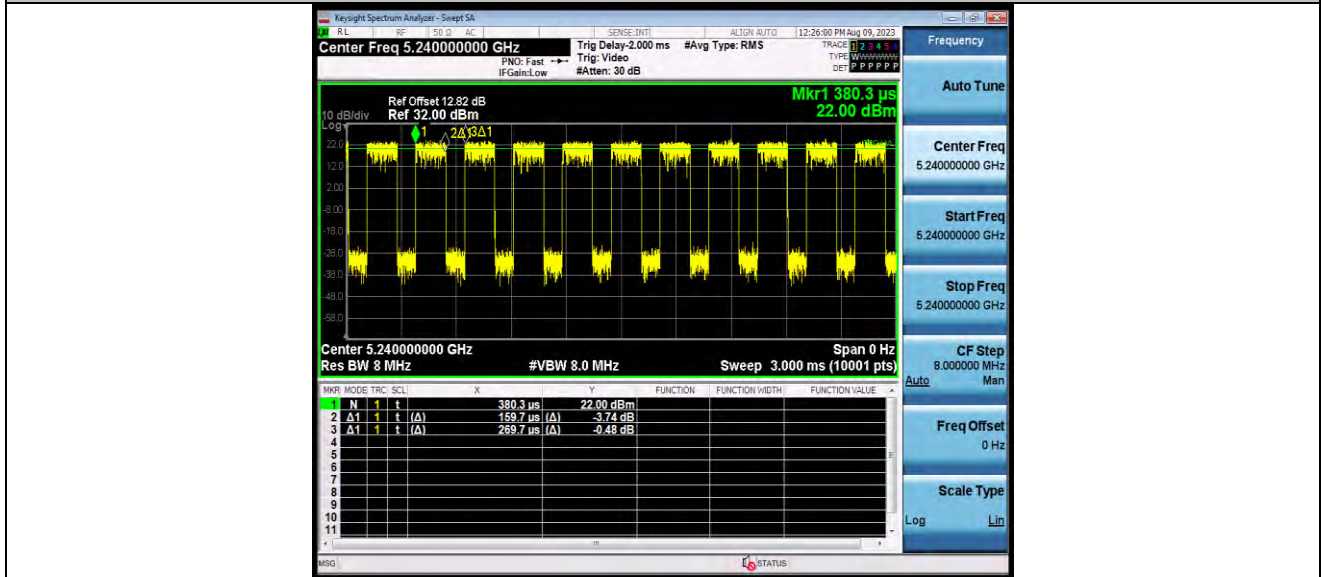


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5240

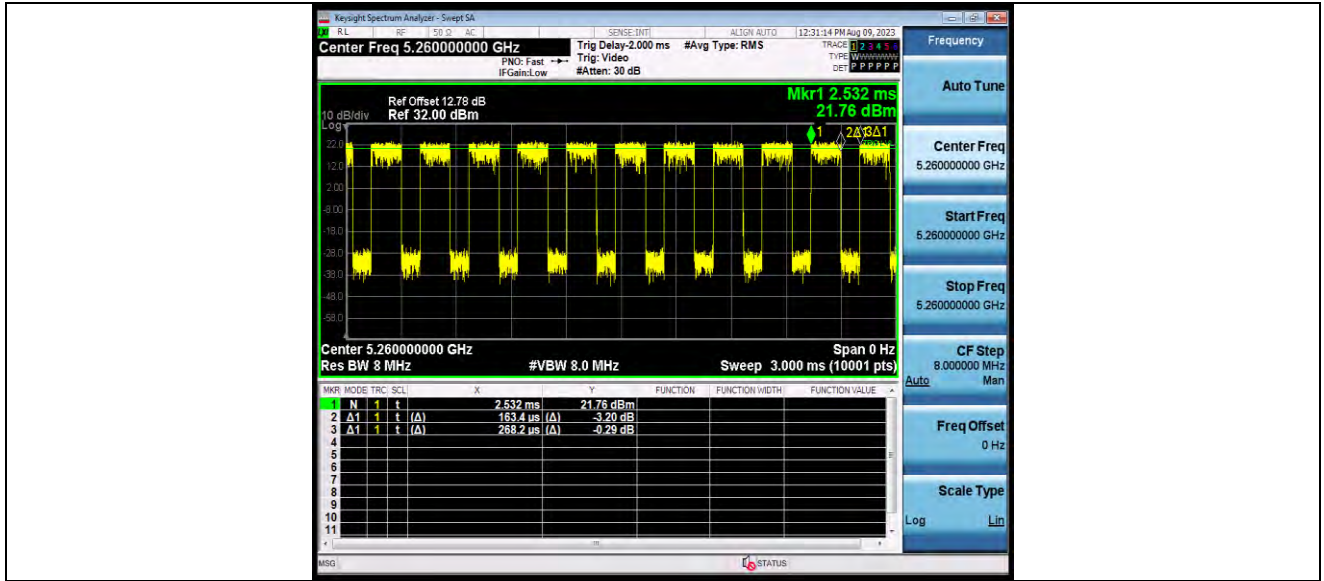


11N20SISO_Ant1_5260

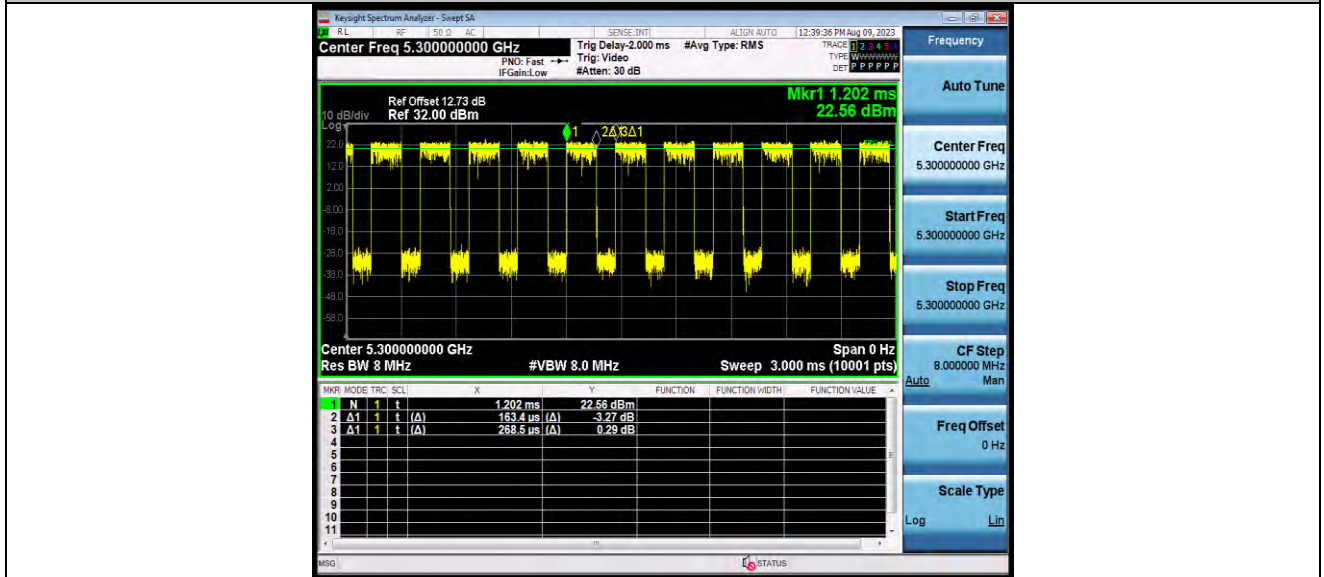


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5300

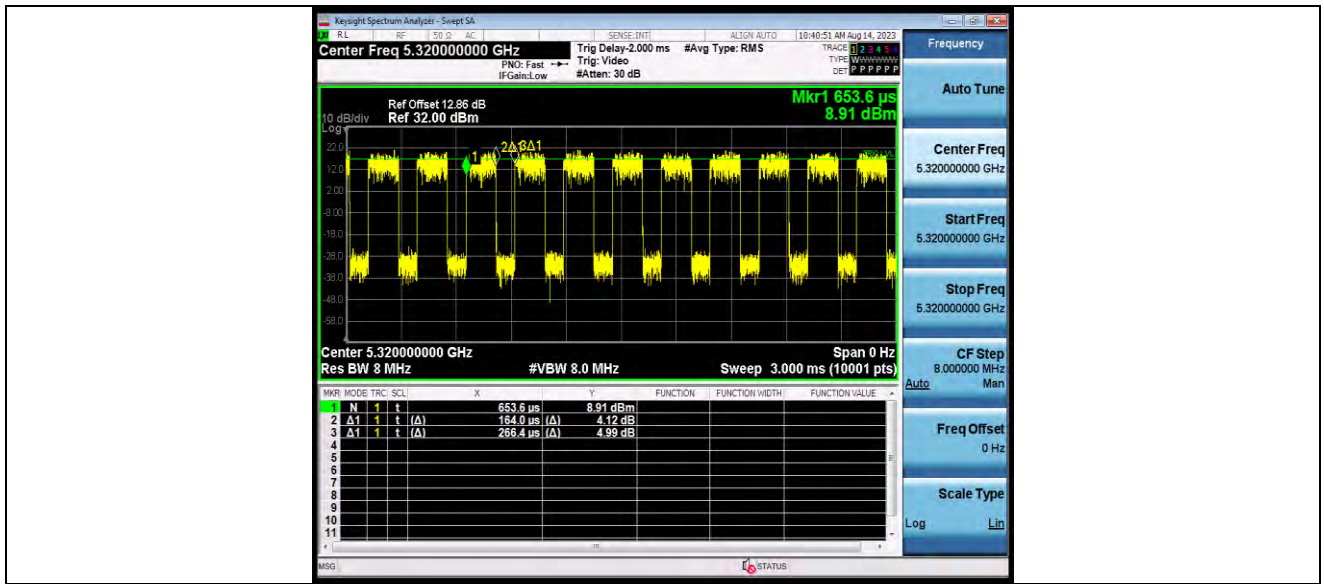


11N20SISO_Ant1_5320

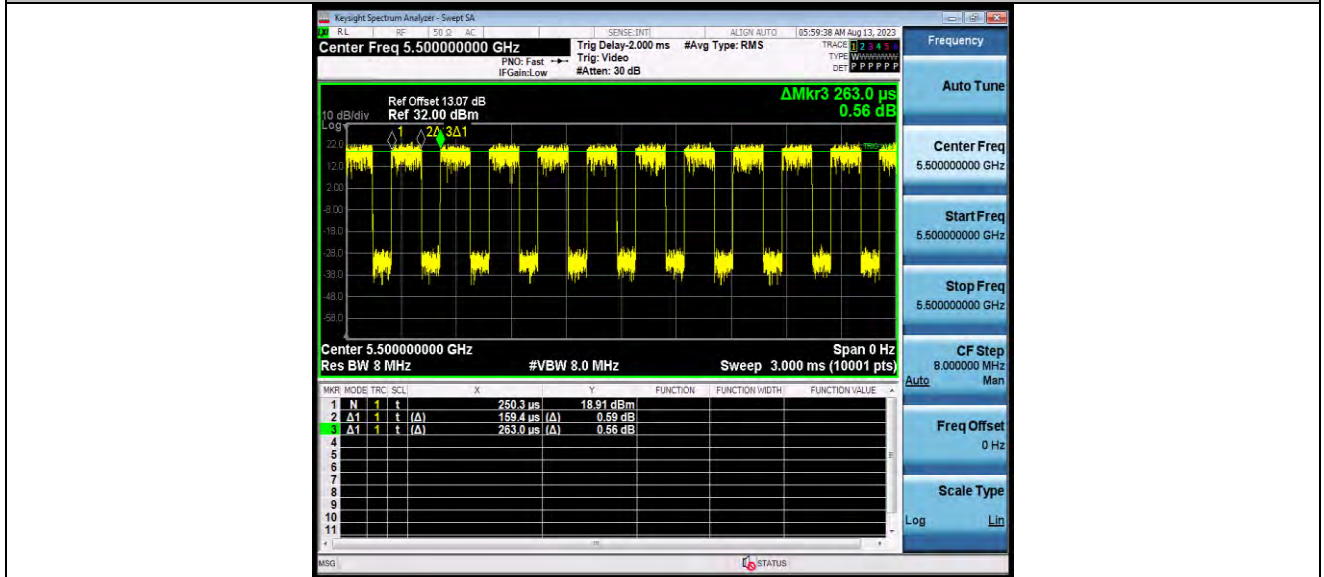


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5500

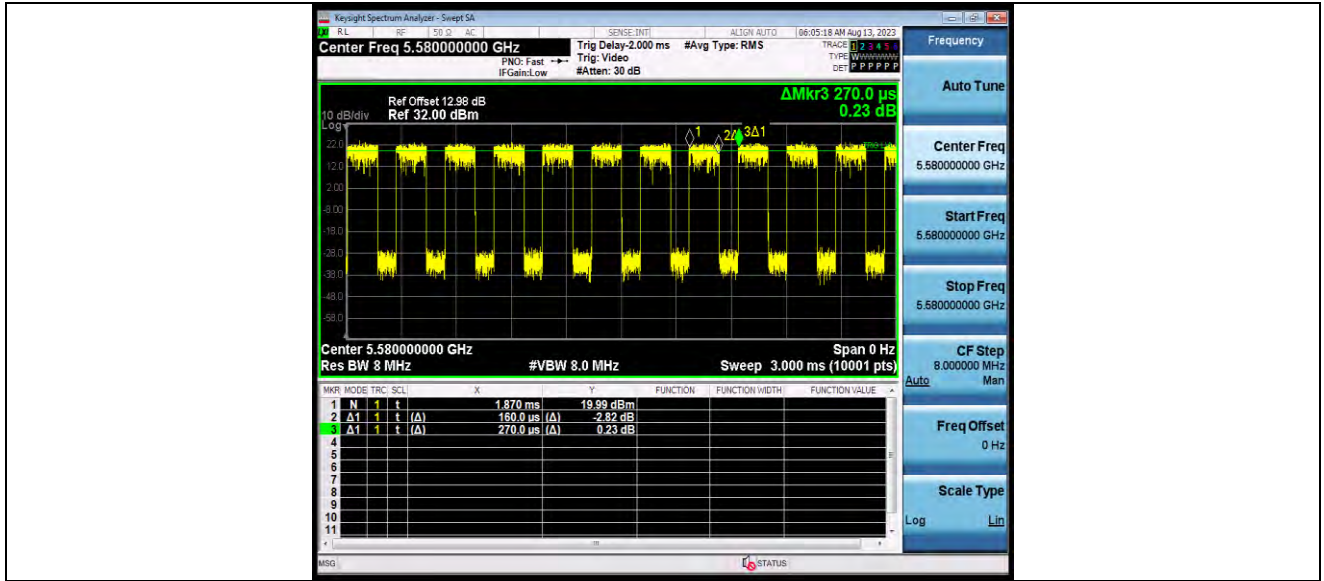


11N20SISO_Ant1_5580

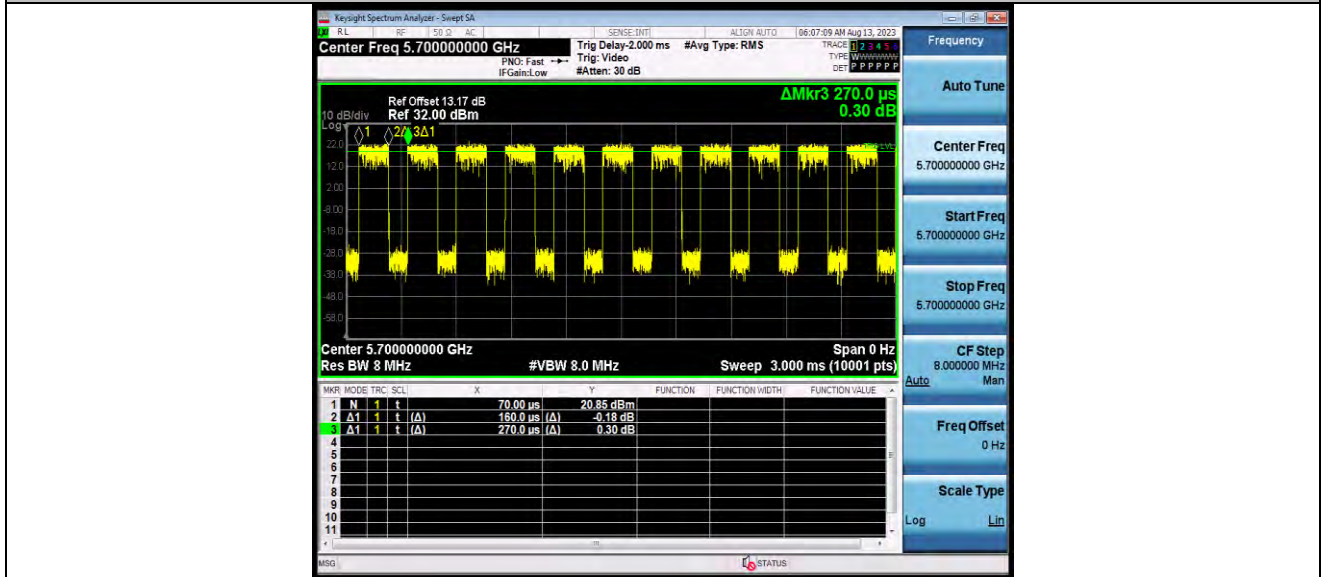


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5700

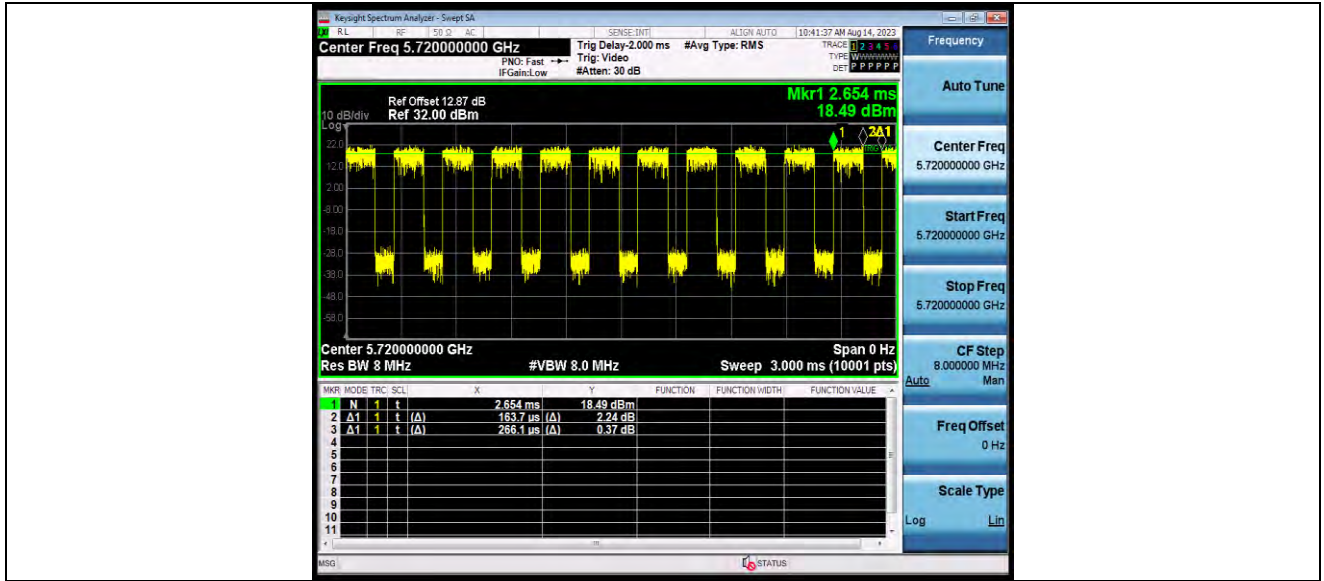


11N20SISO_Ant1_5720

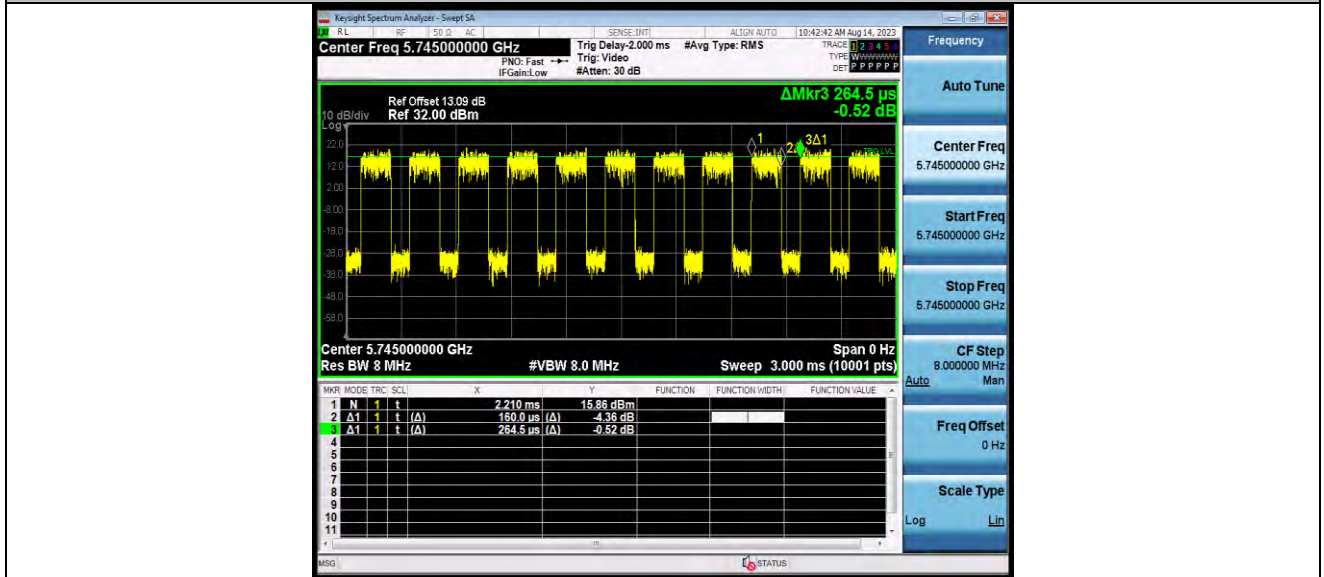


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5745

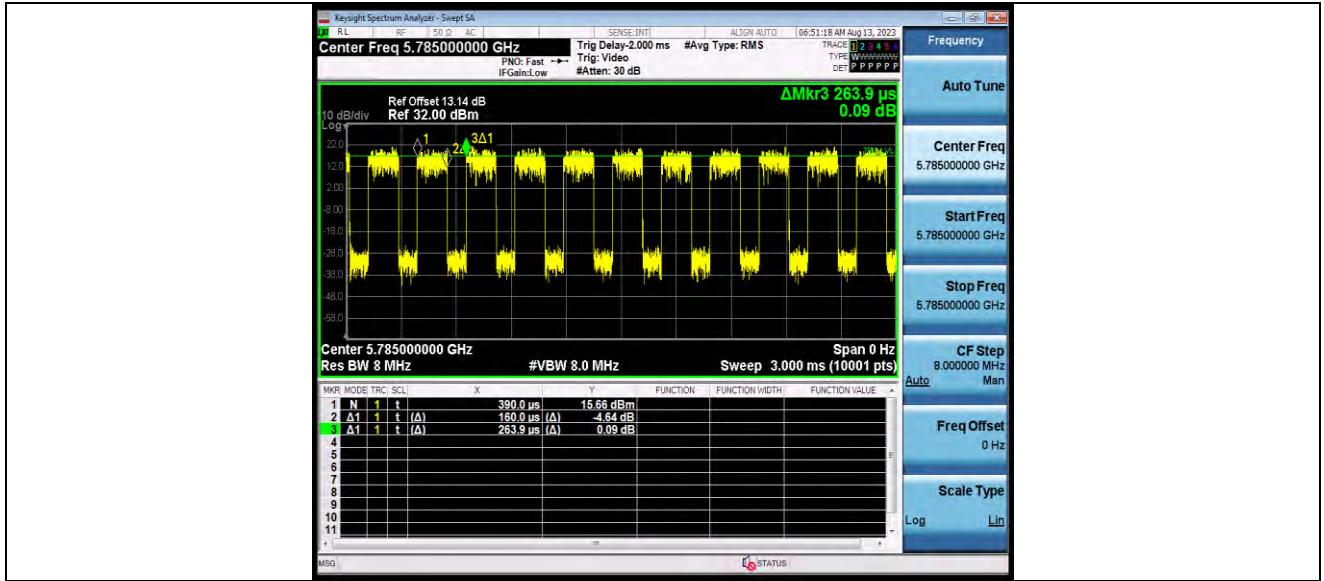


11N20SISO_Ant1_5785

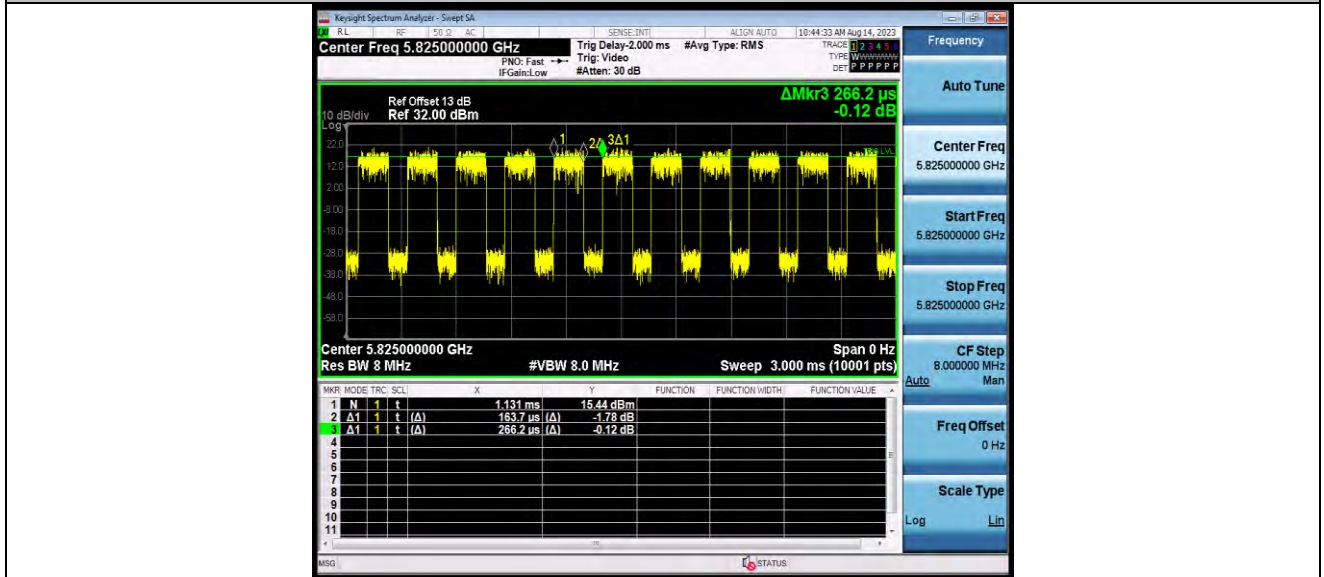


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5825

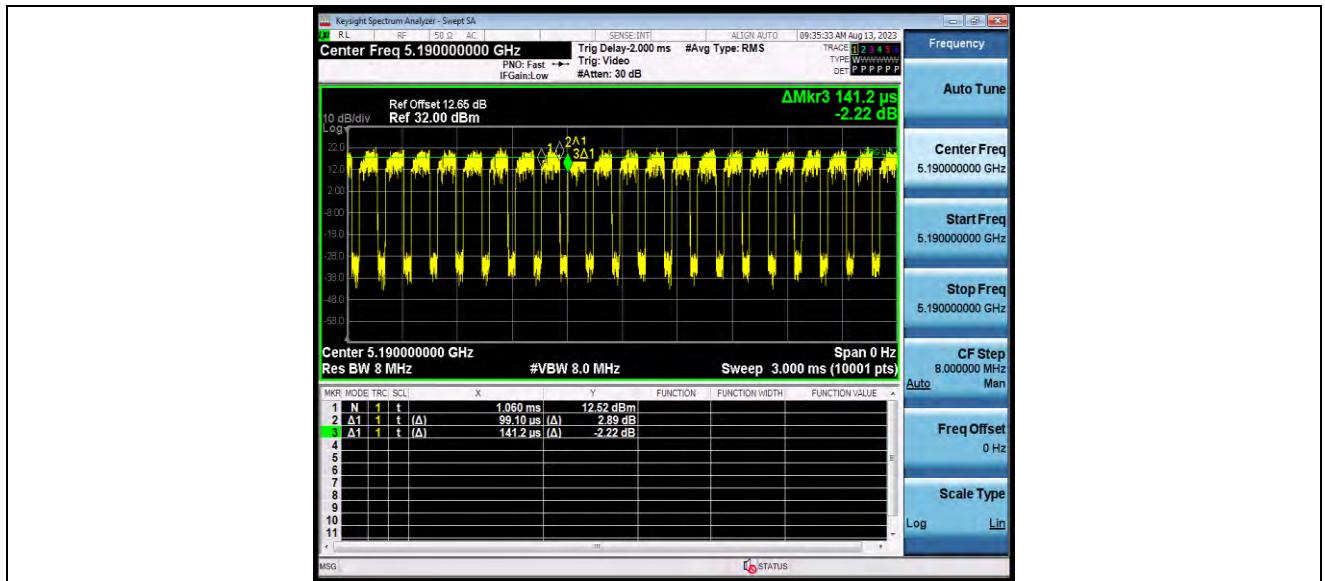


11N40SISO_Ant1_5190

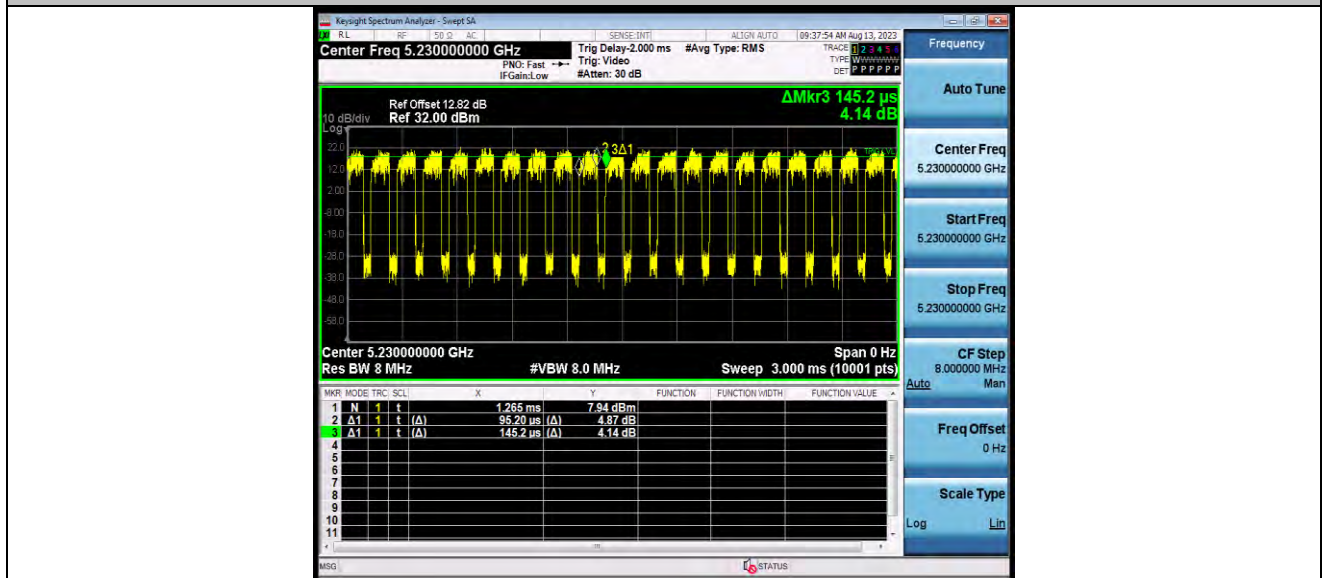


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5230

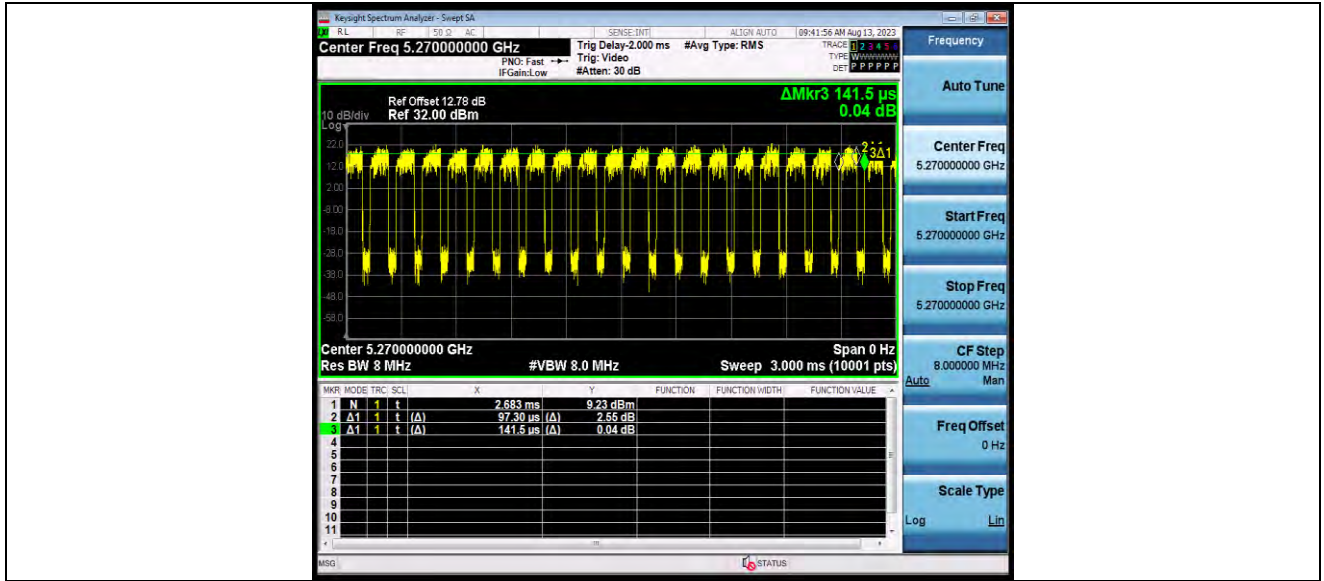


11N40SISO_Ant1_5270

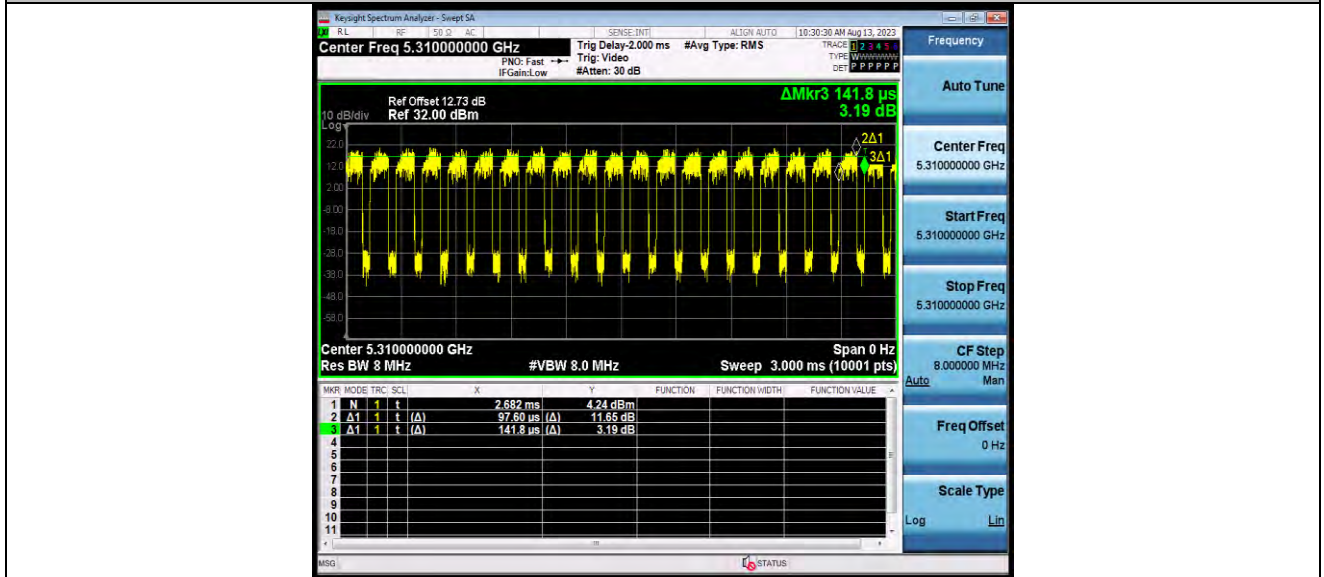


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5310

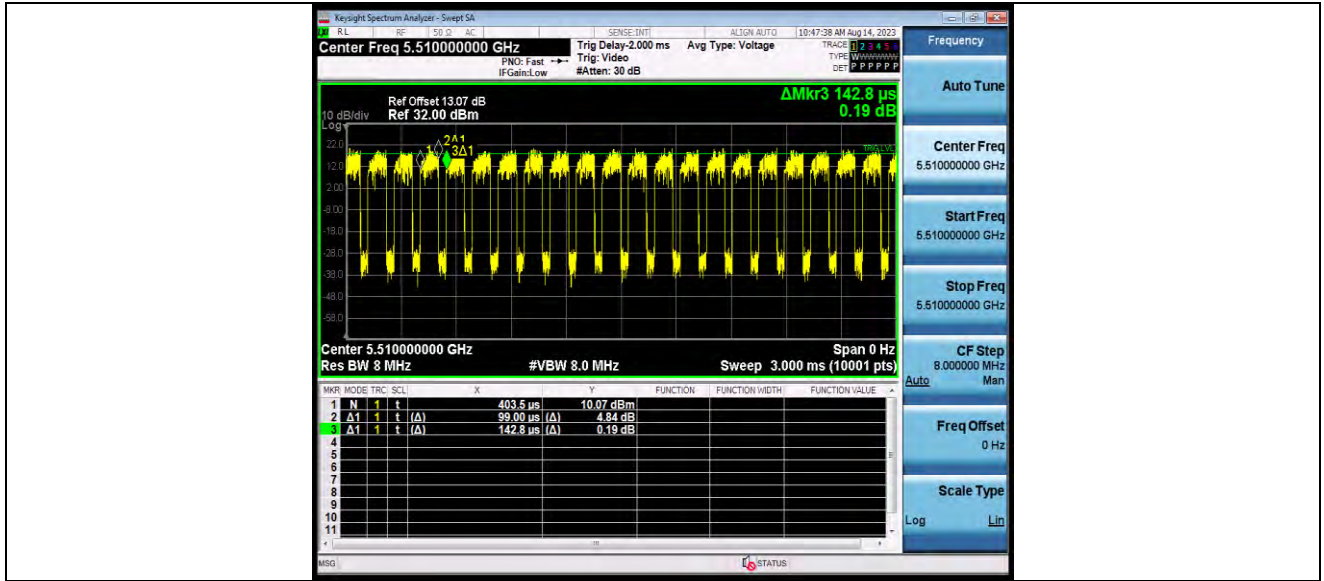


11N40SISO_Ant1_5510

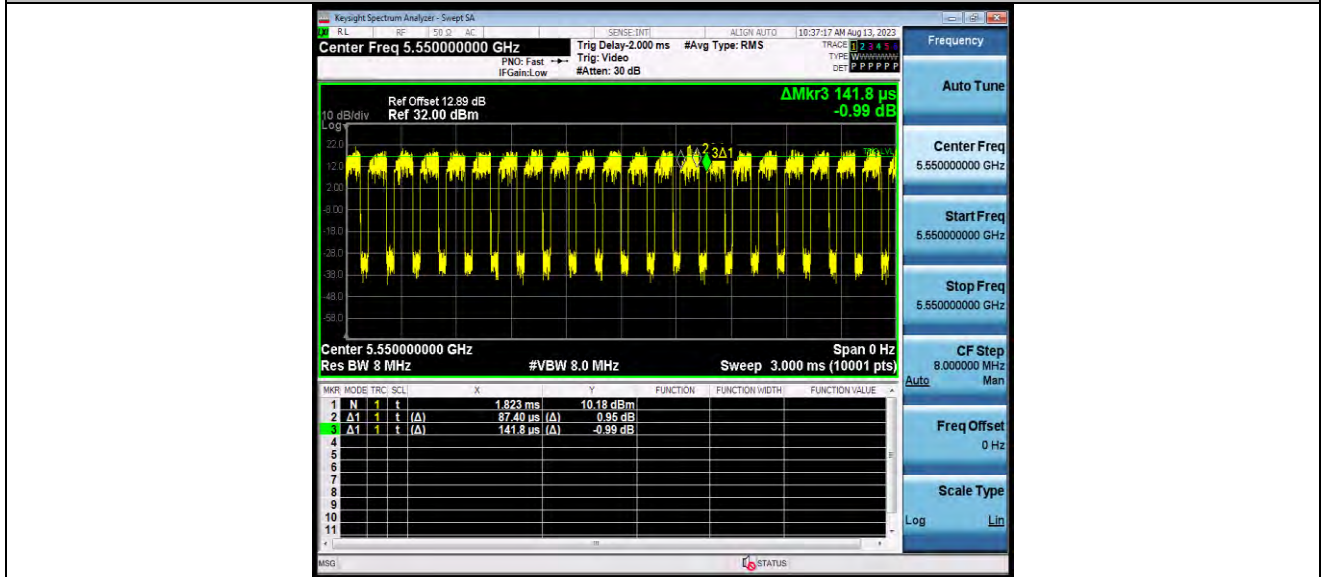


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5550

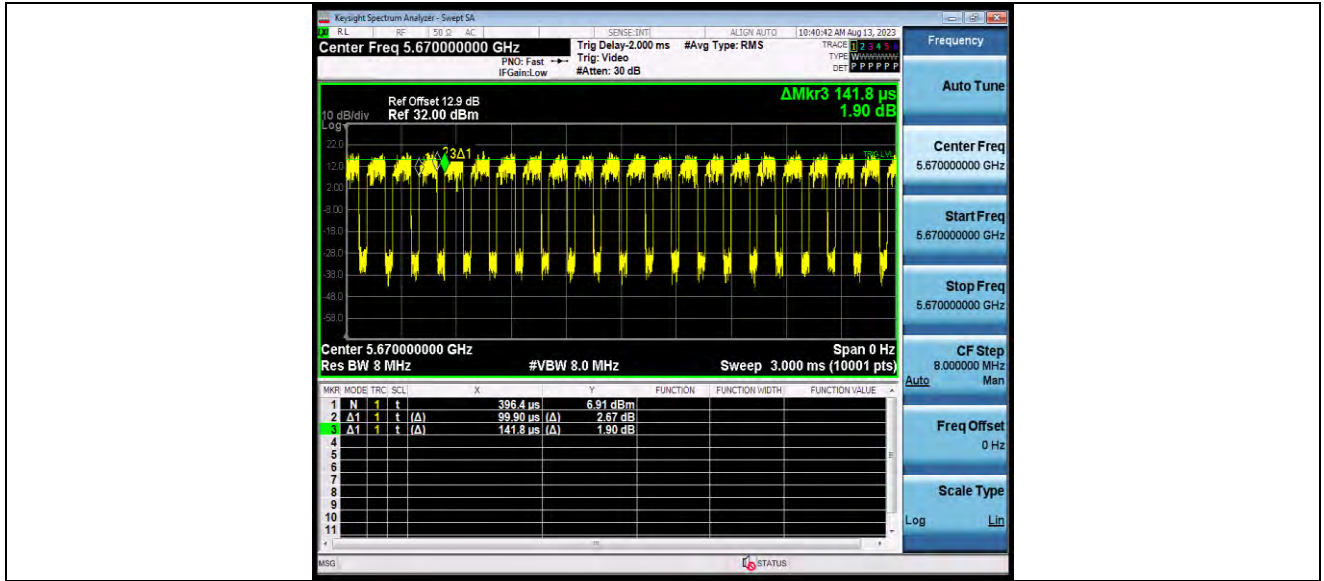


11N40SISO_Ant1_5670

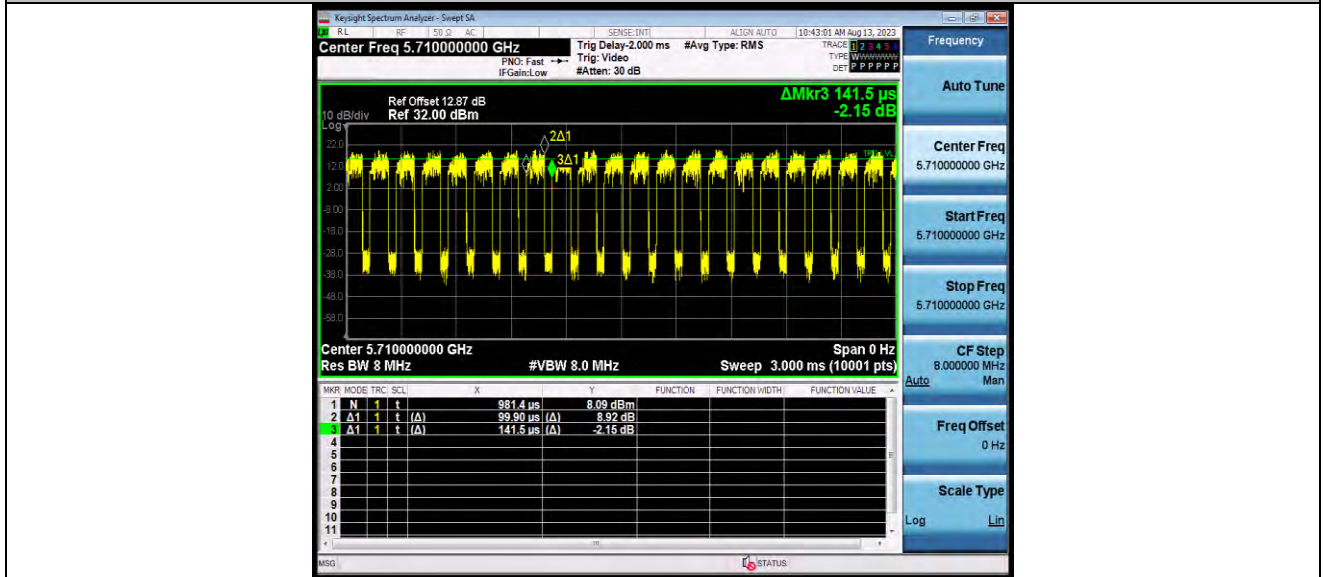


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5710

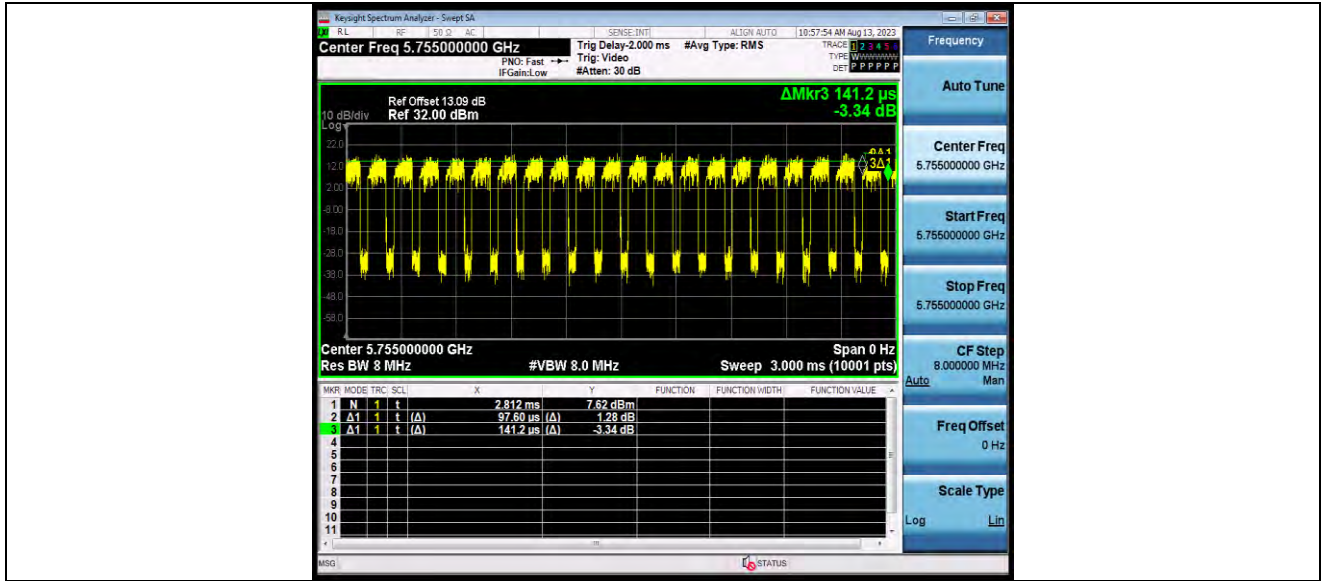


11N40SISO_Ant1_5755

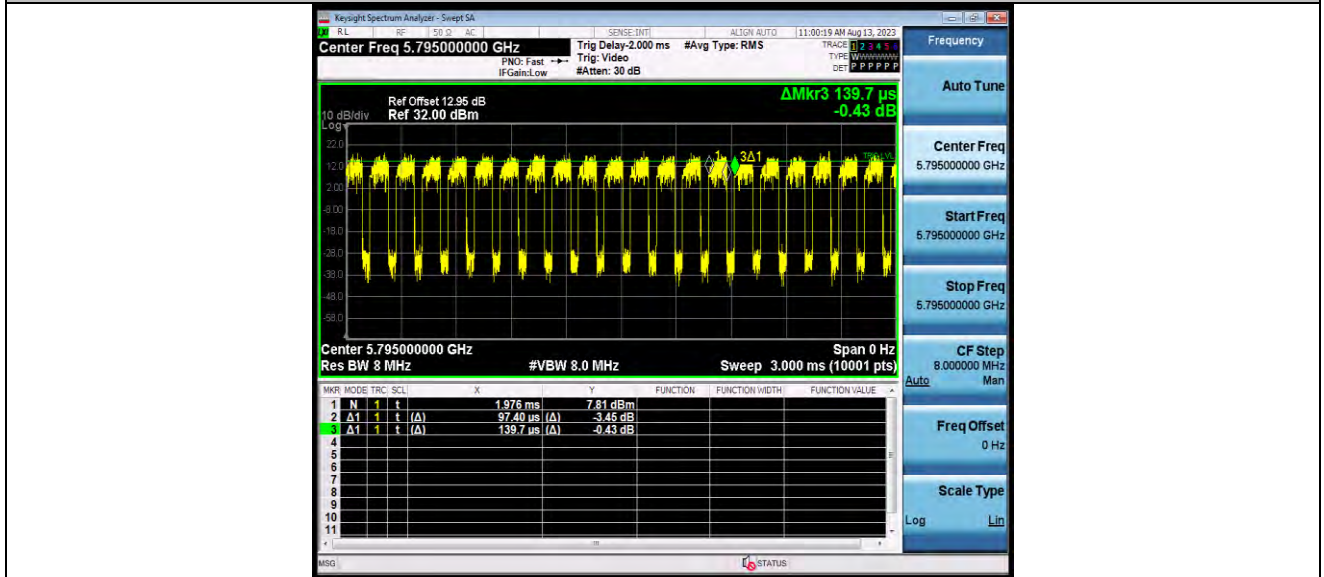


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5795

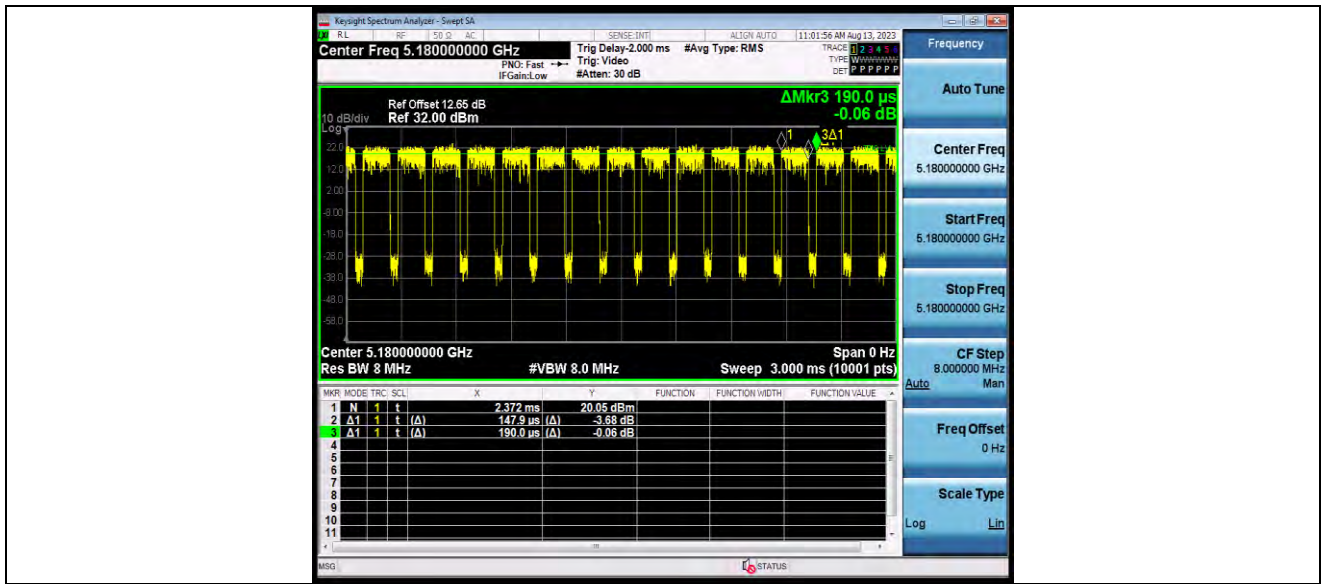


11AC20SISO_Ant1_5180

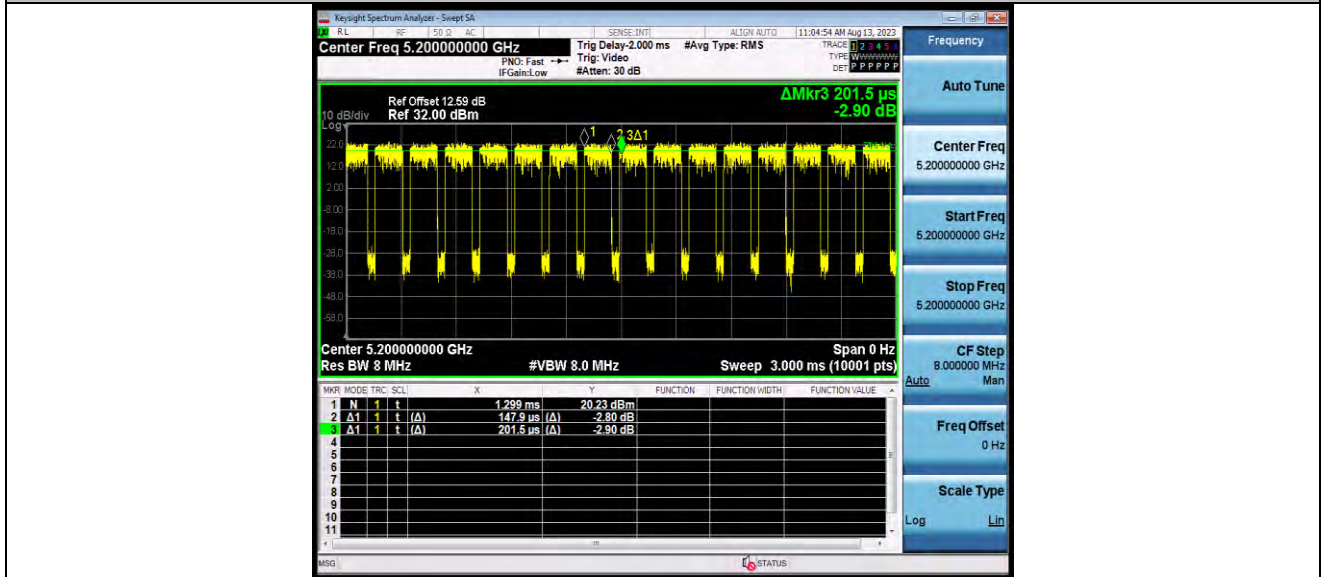


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5200

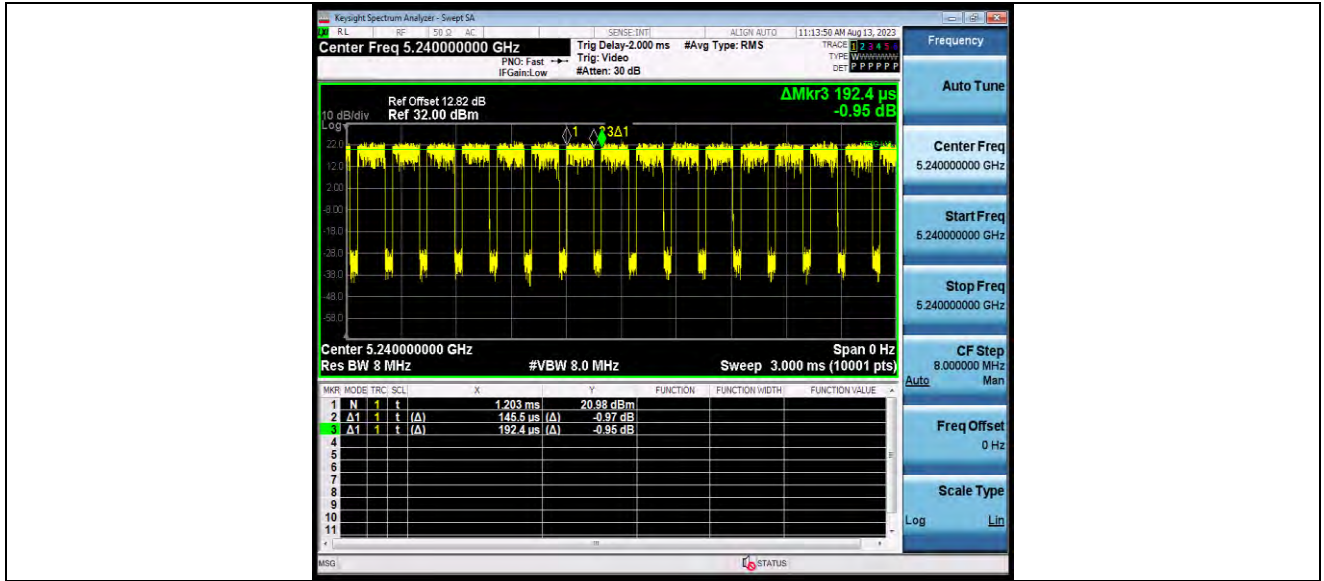


11AC20SISO_Ant1_5240

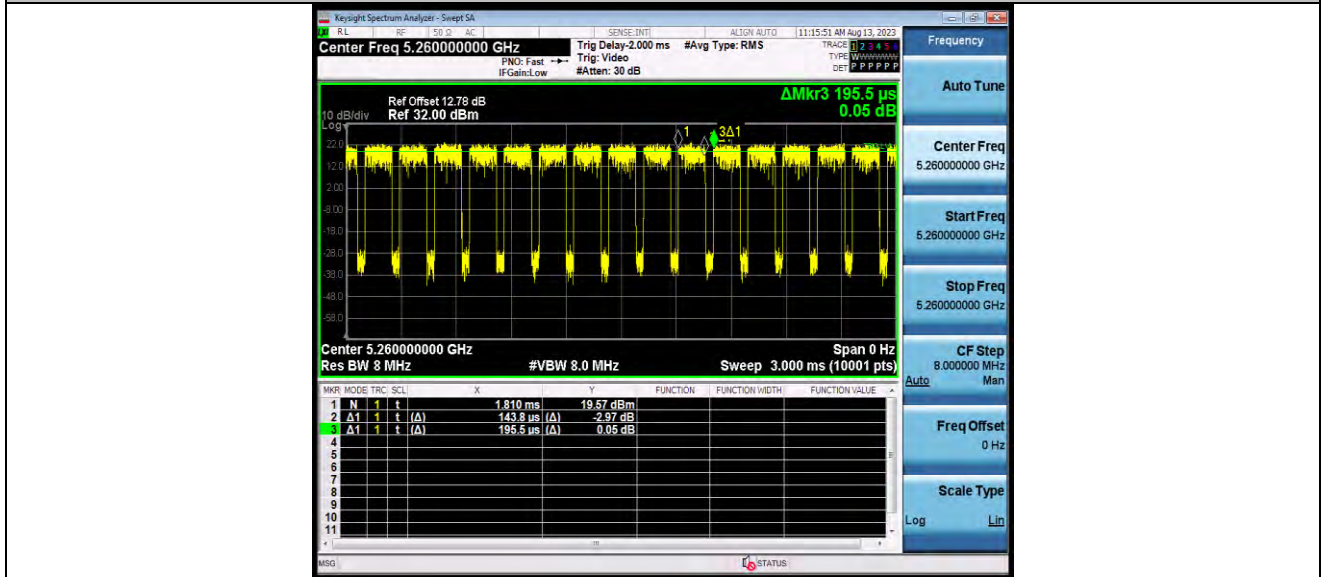


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5260

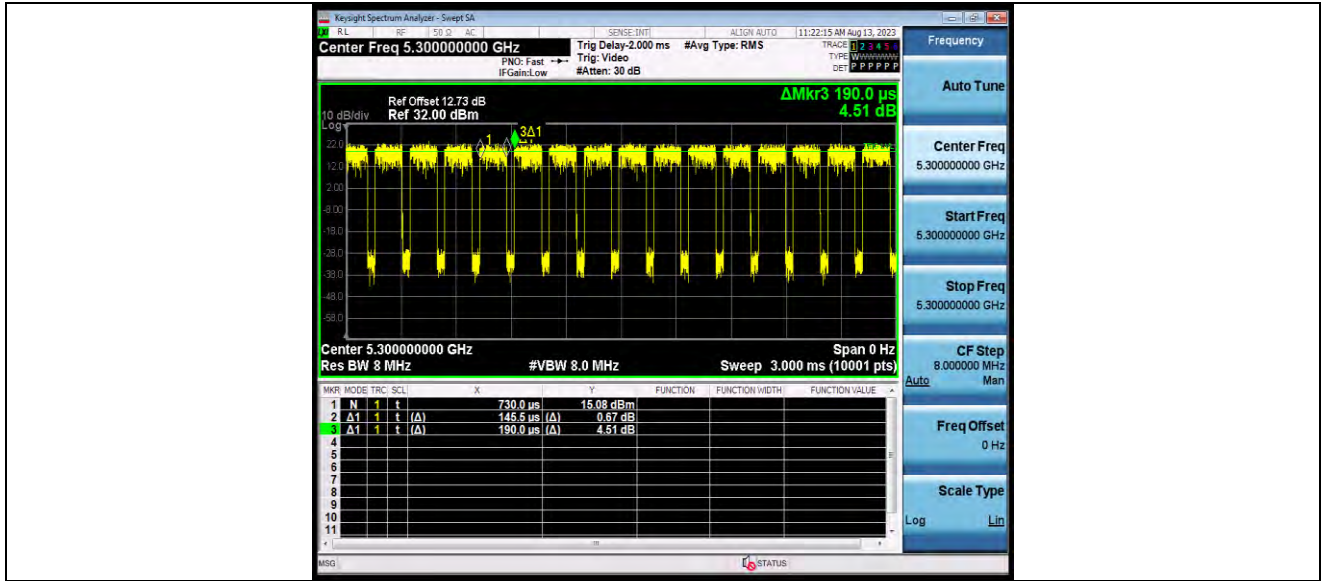


11AC20SISO_Ant1_5300

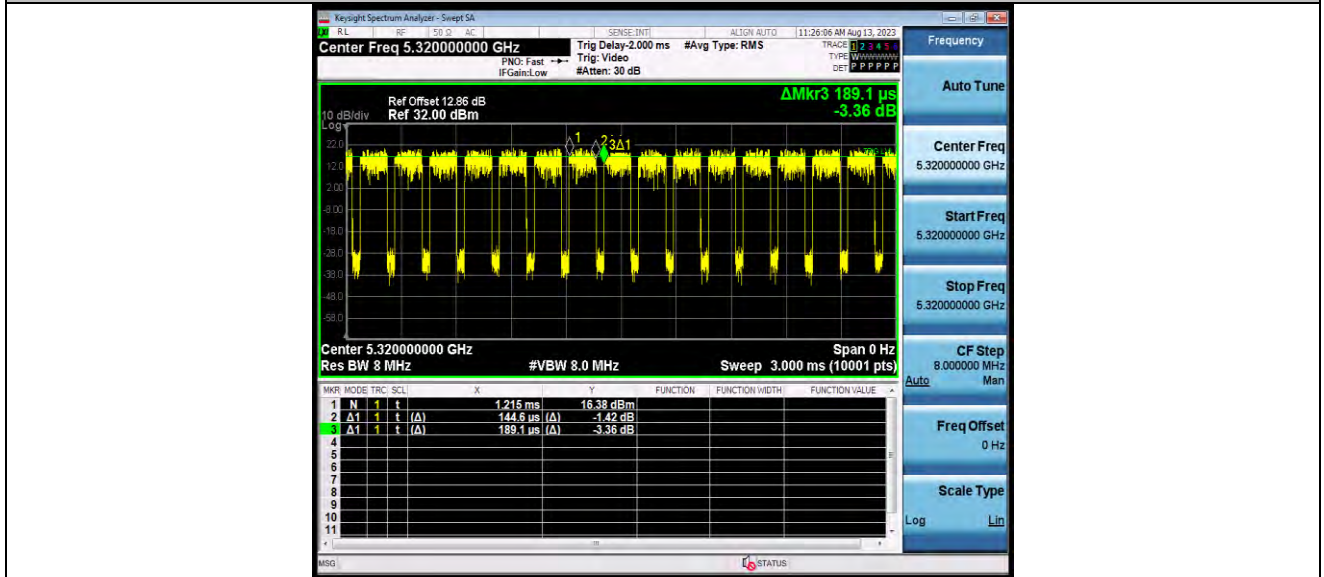


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5320

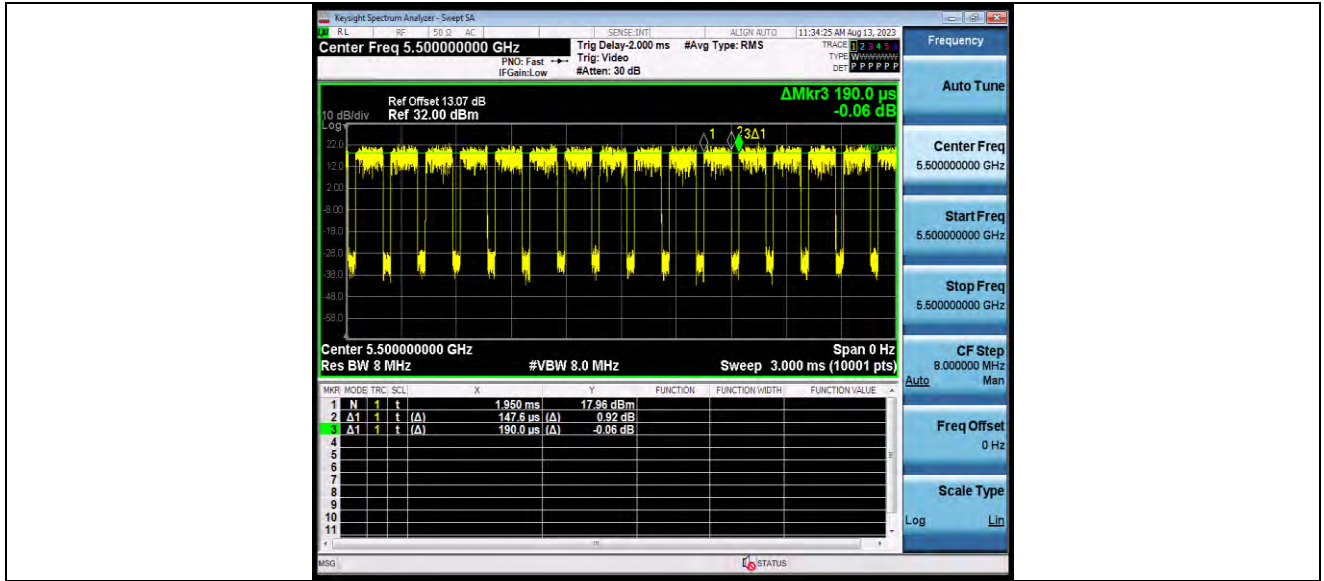


11AC20SISO_Ant1_5500

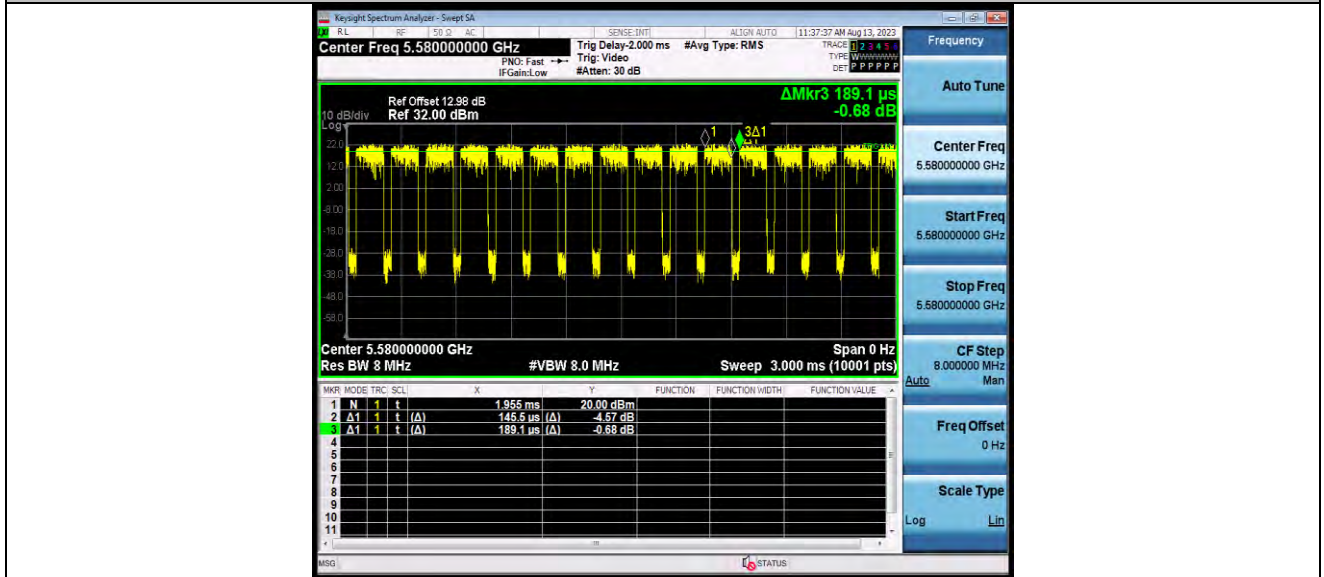


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5580

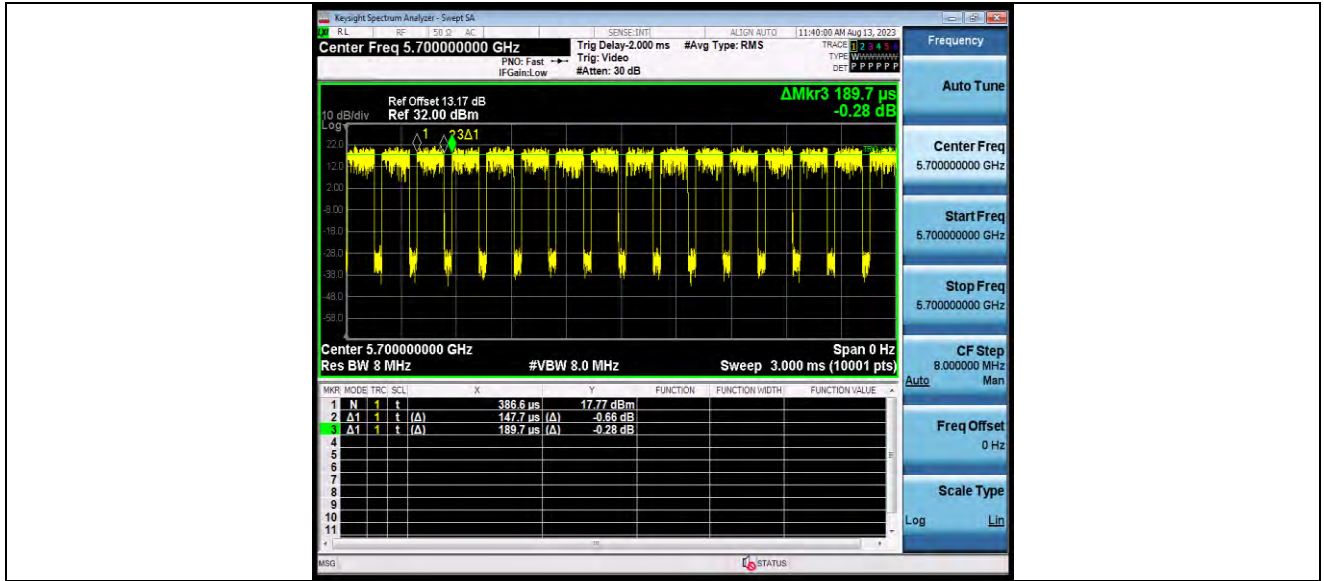


11AC20SISO_Ant1_5700

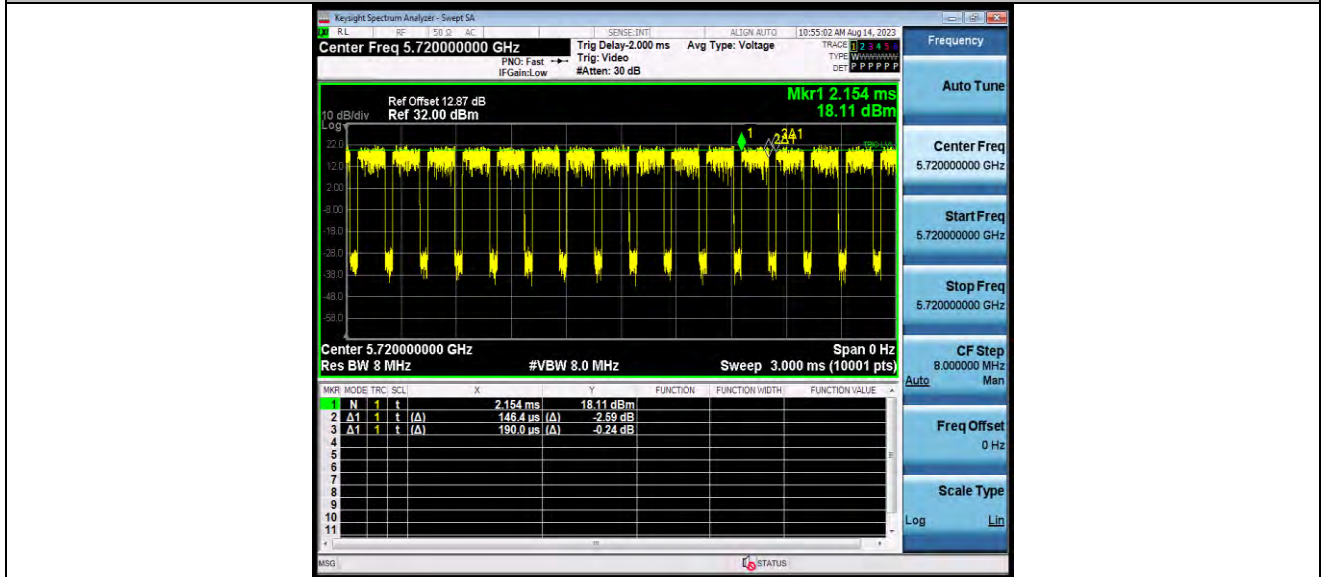


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5720

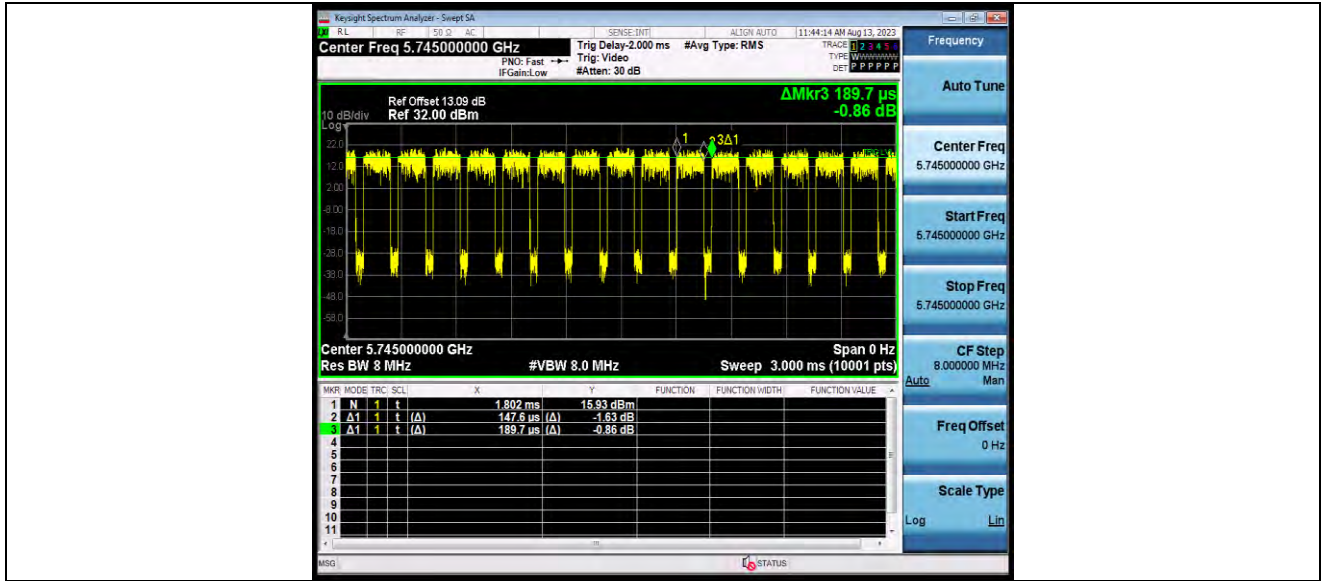


11AC20SISO_Ant1_5745

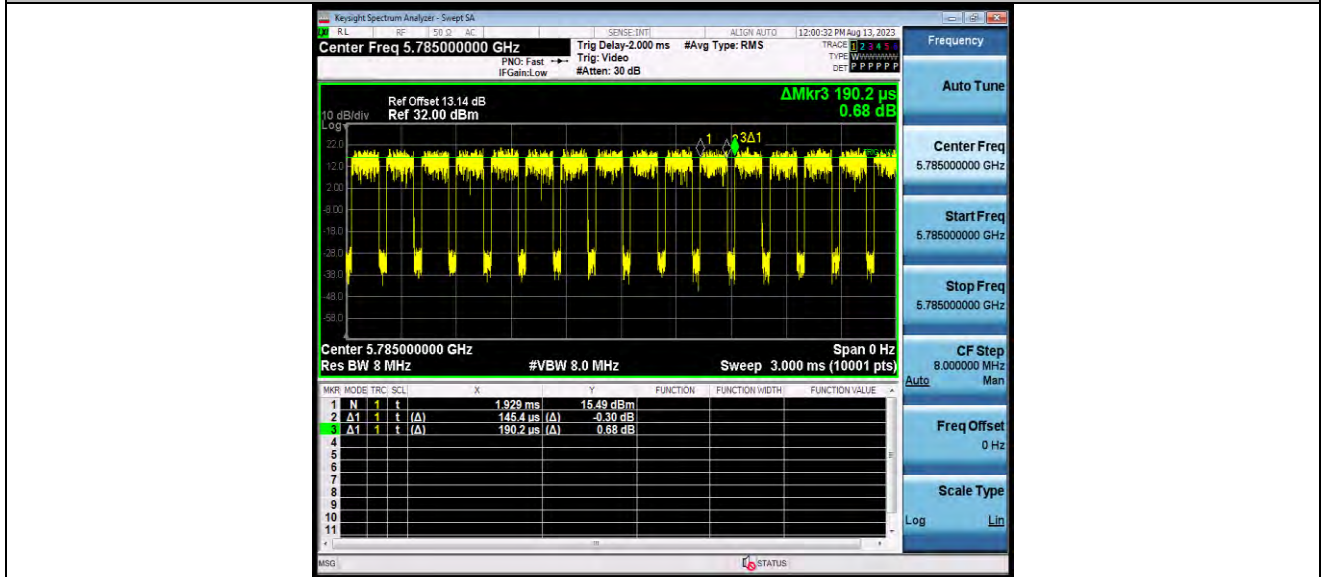


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC20SISO_Ant1_5785

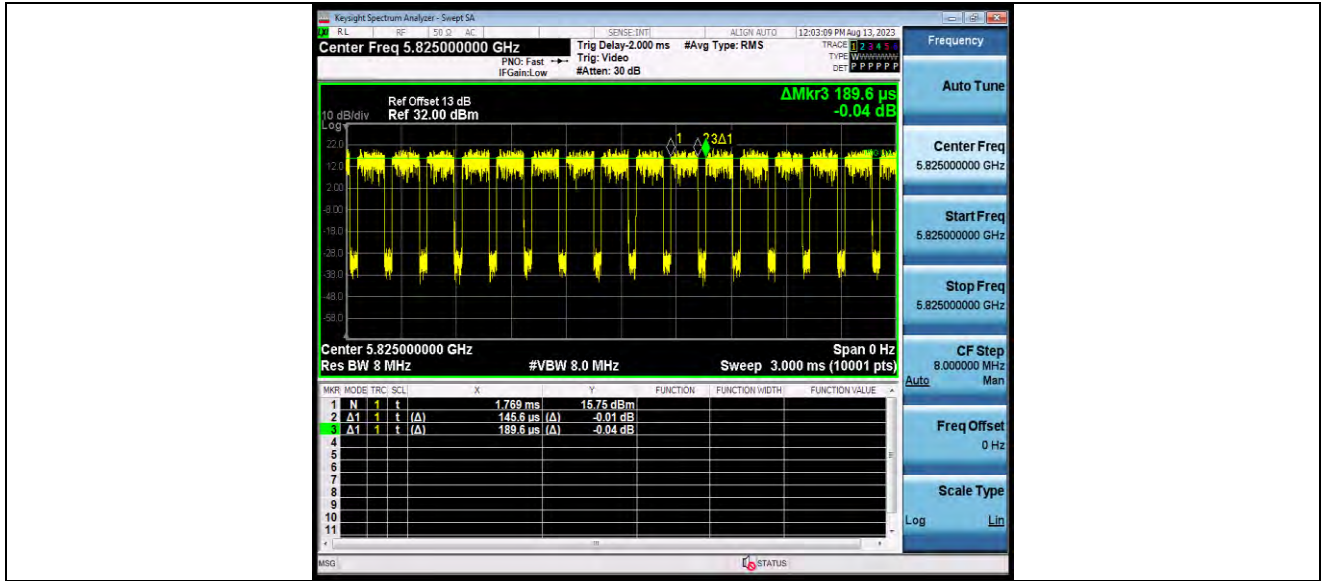


11AC20SISO_Ant1_5825

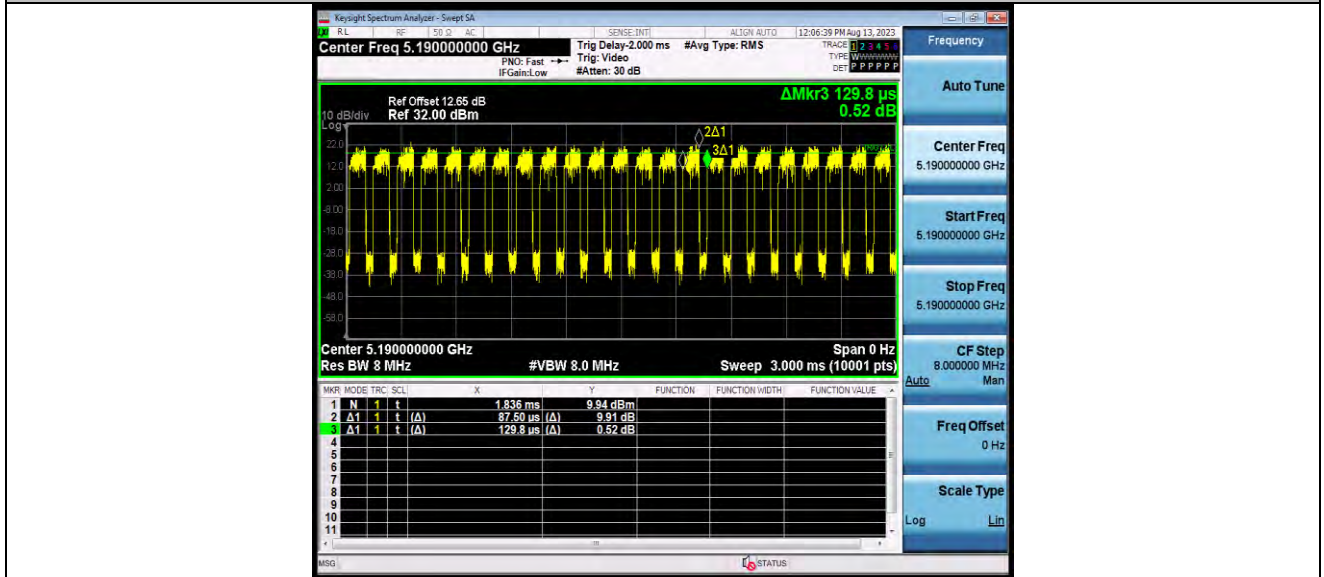


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC40SISO_Ant1_5190

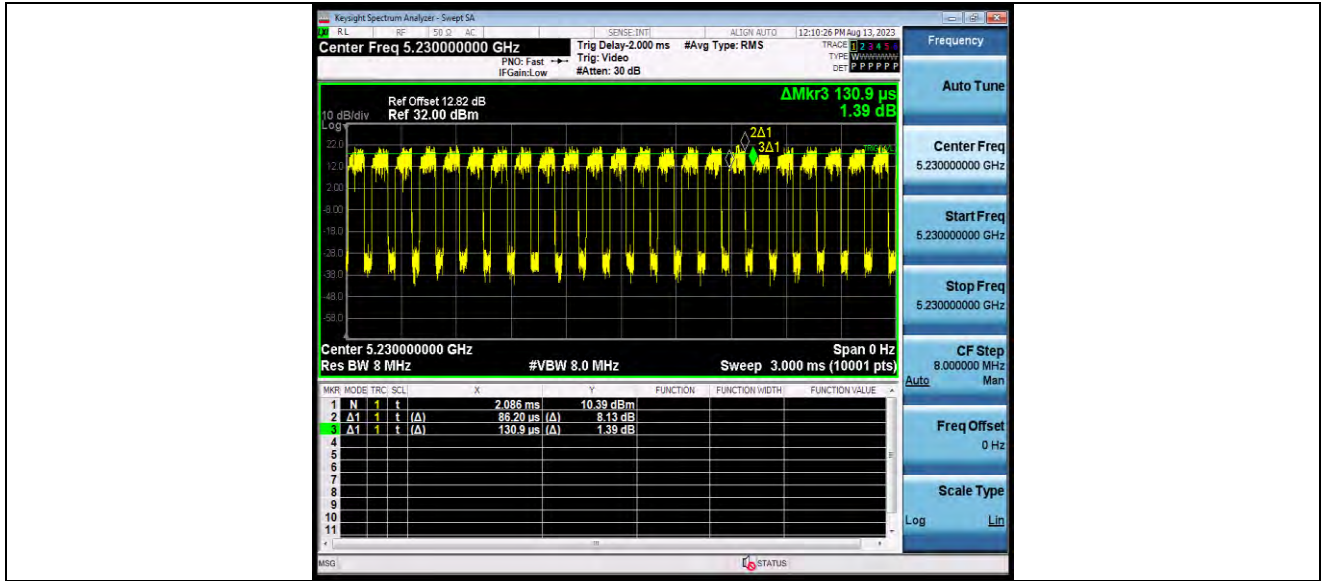


11AC40SISO_Ant1_5230

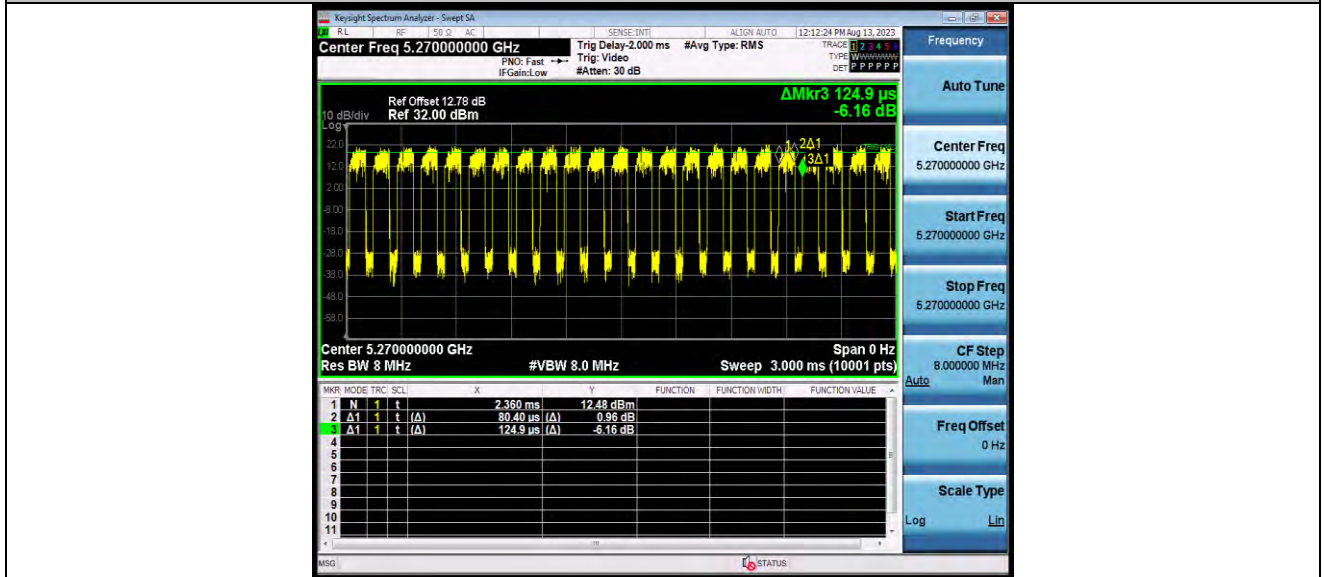


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC40SISO_Ant1_5270

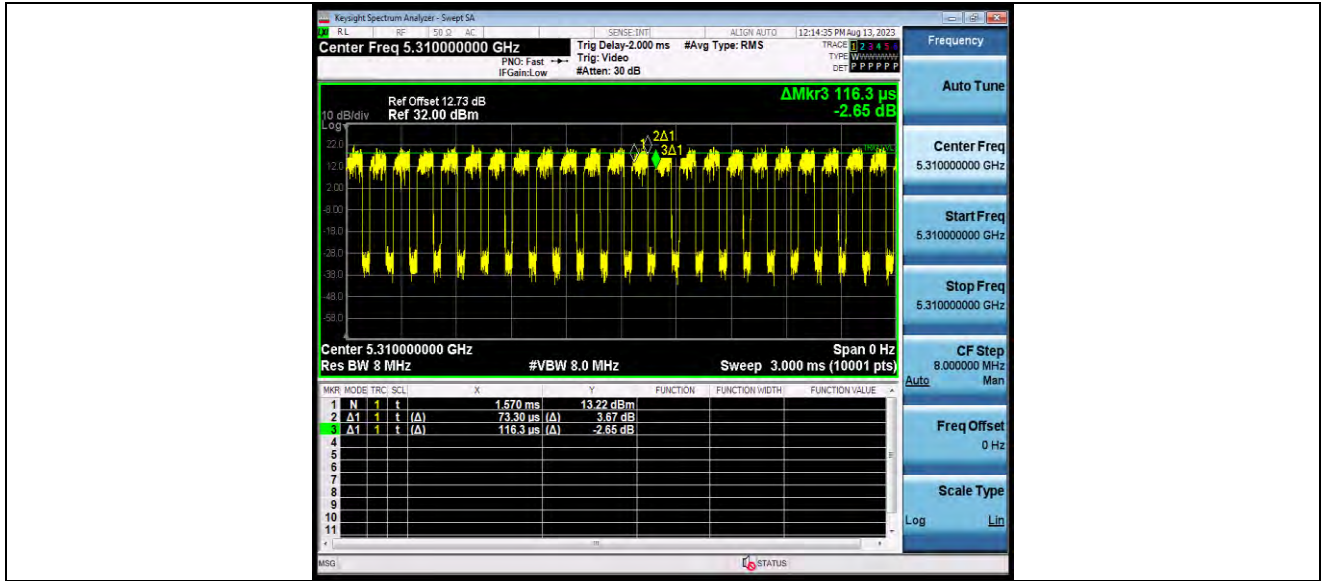


11AC40SISO_Ant1_5310

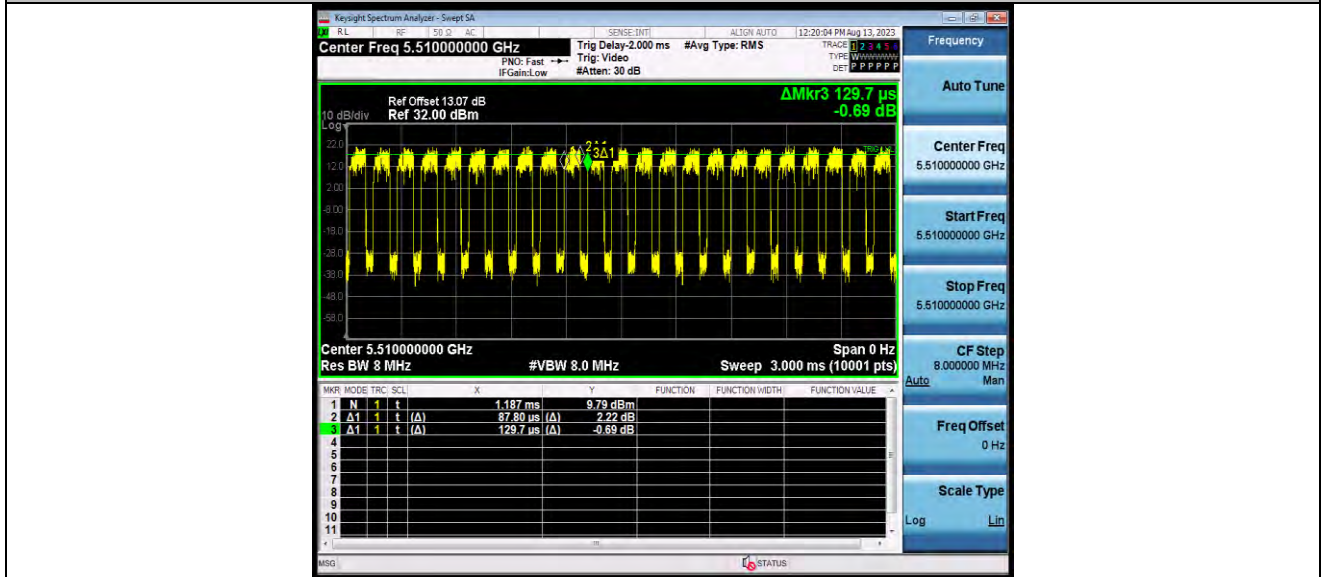


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC40SISO_Ant1_5510

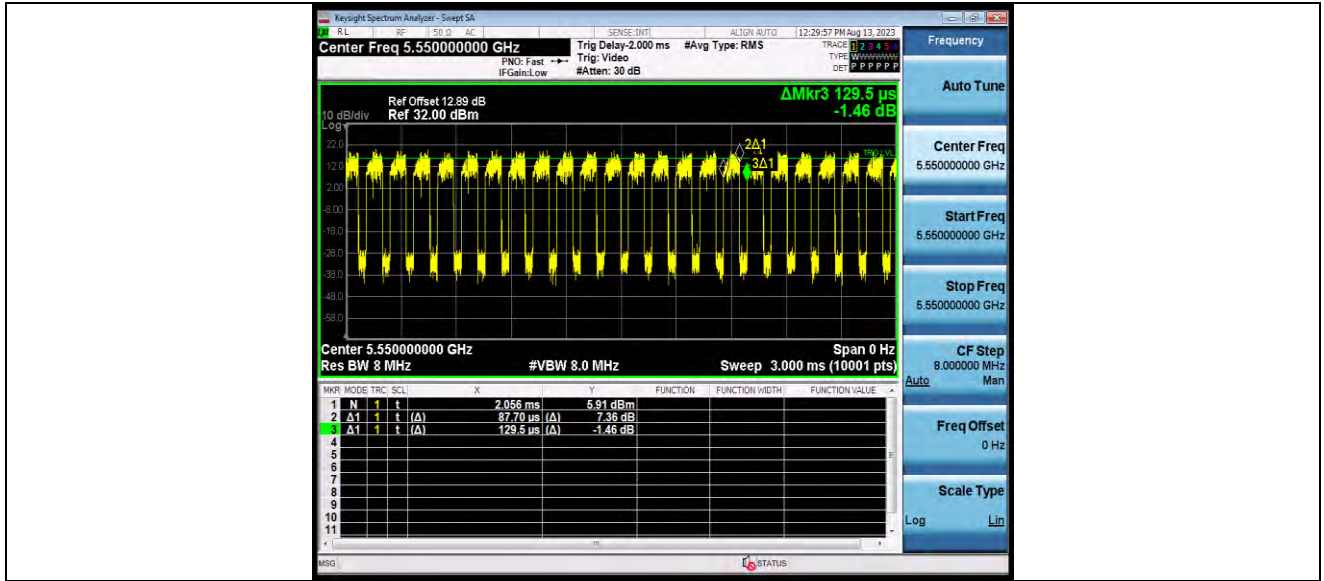


11AC40SISO_Ant1_5550

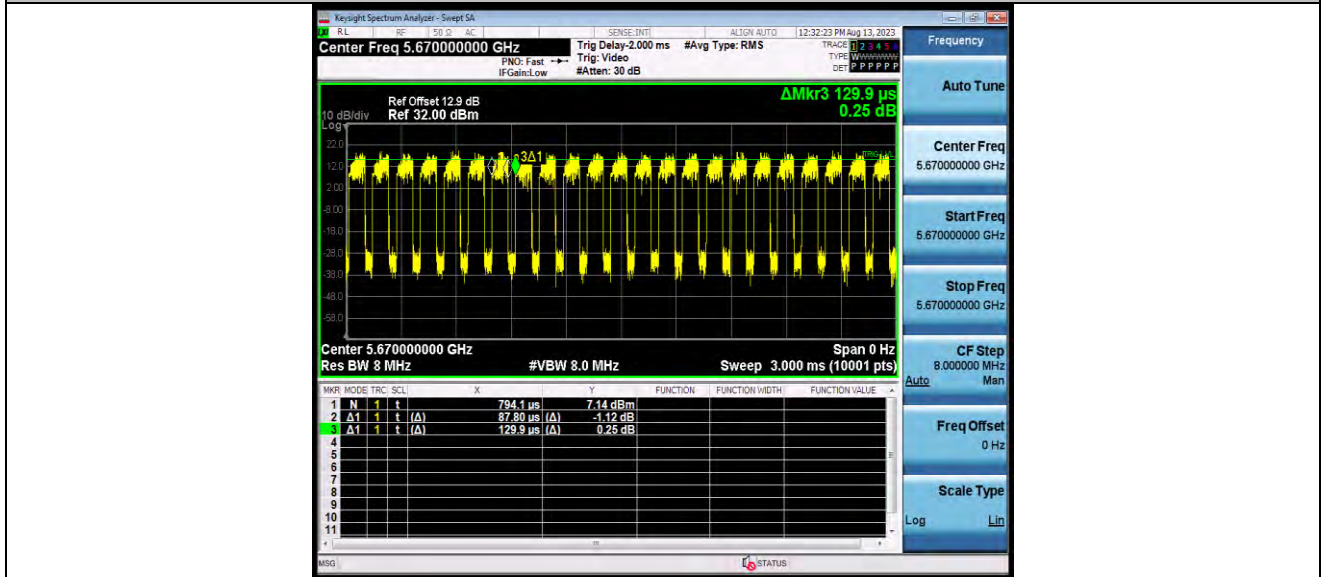


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC40SISO_Ant1_5670

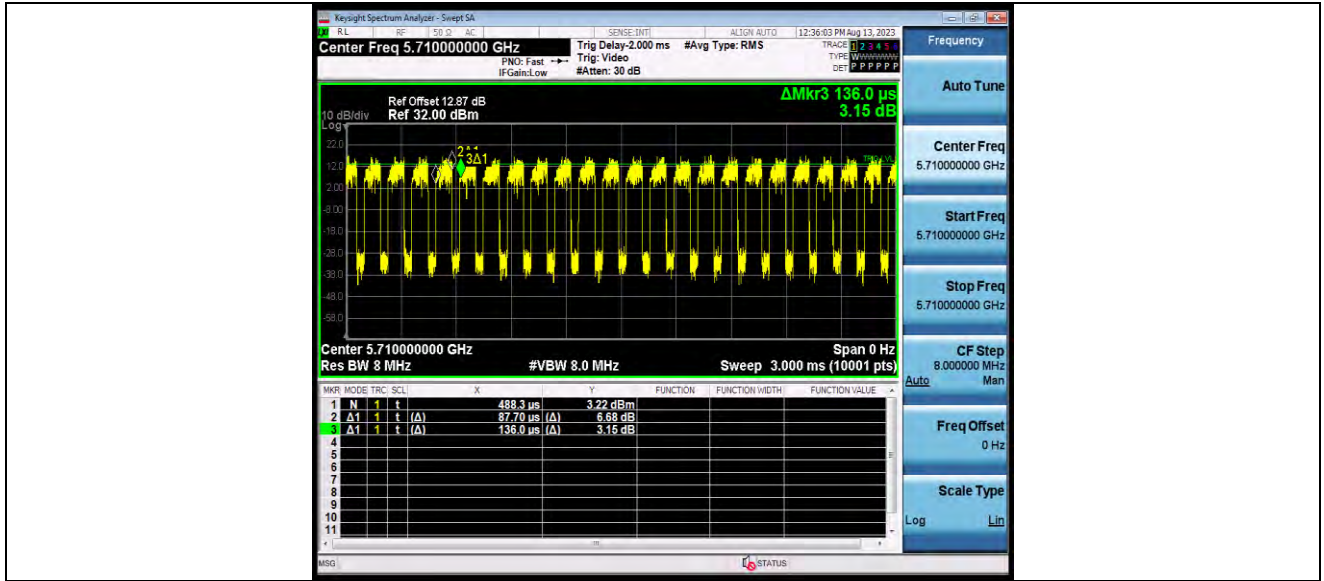


11AC40SISO_Ant1_5710

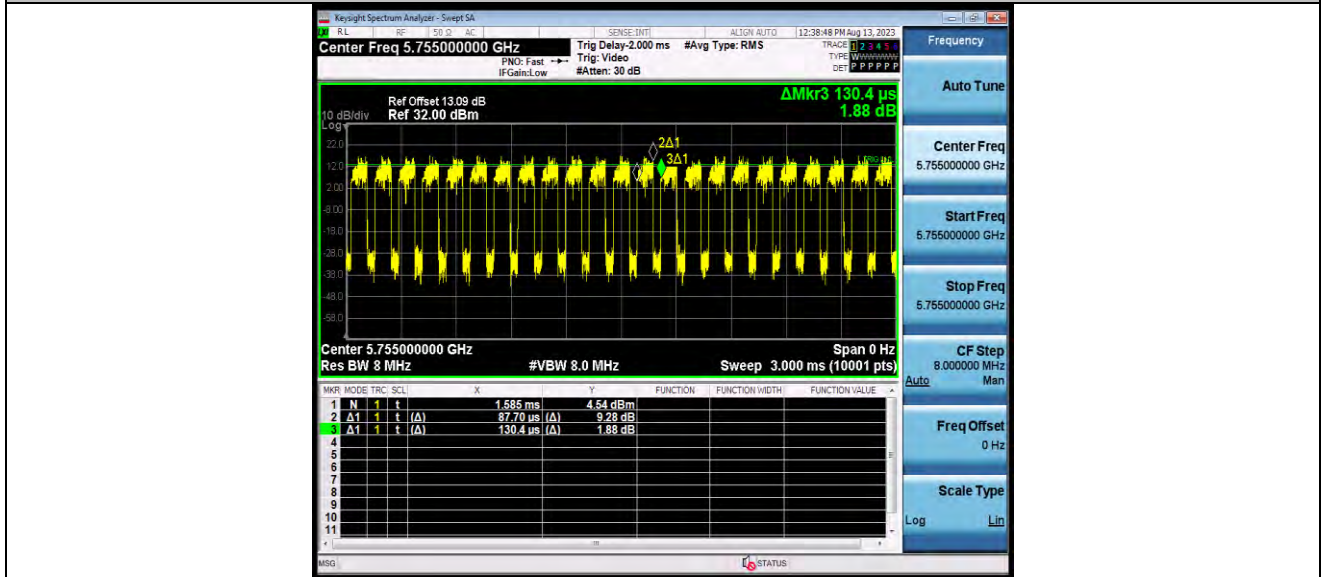


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC40SISO_Ant1_5755

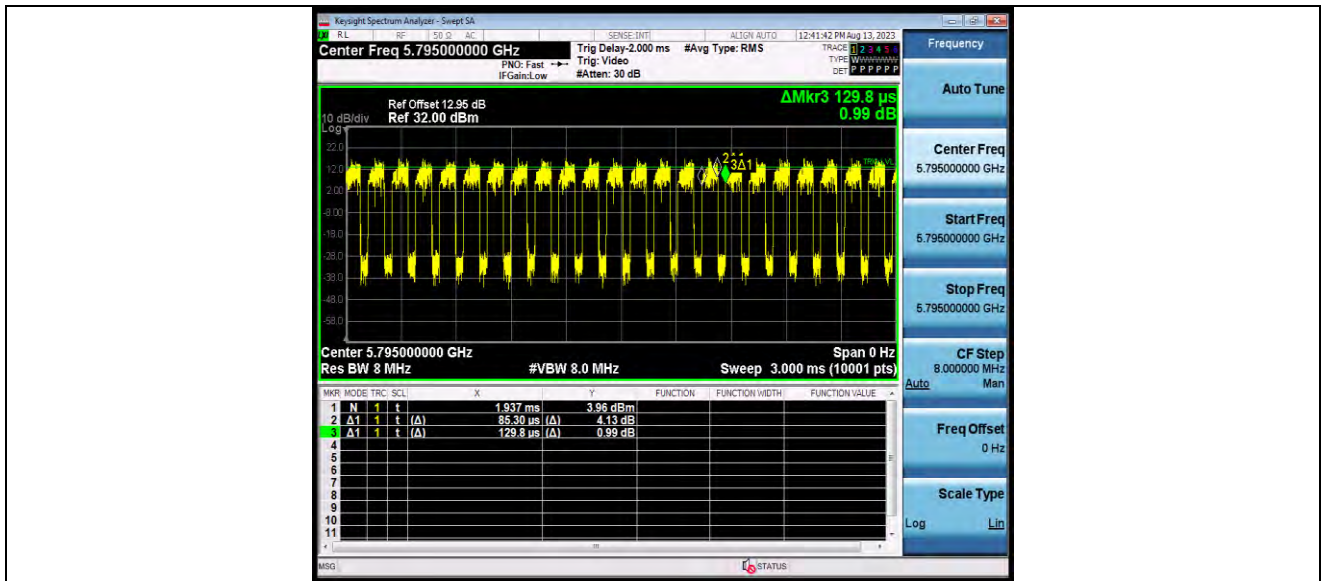


11AC40SISO_Ant1_5795

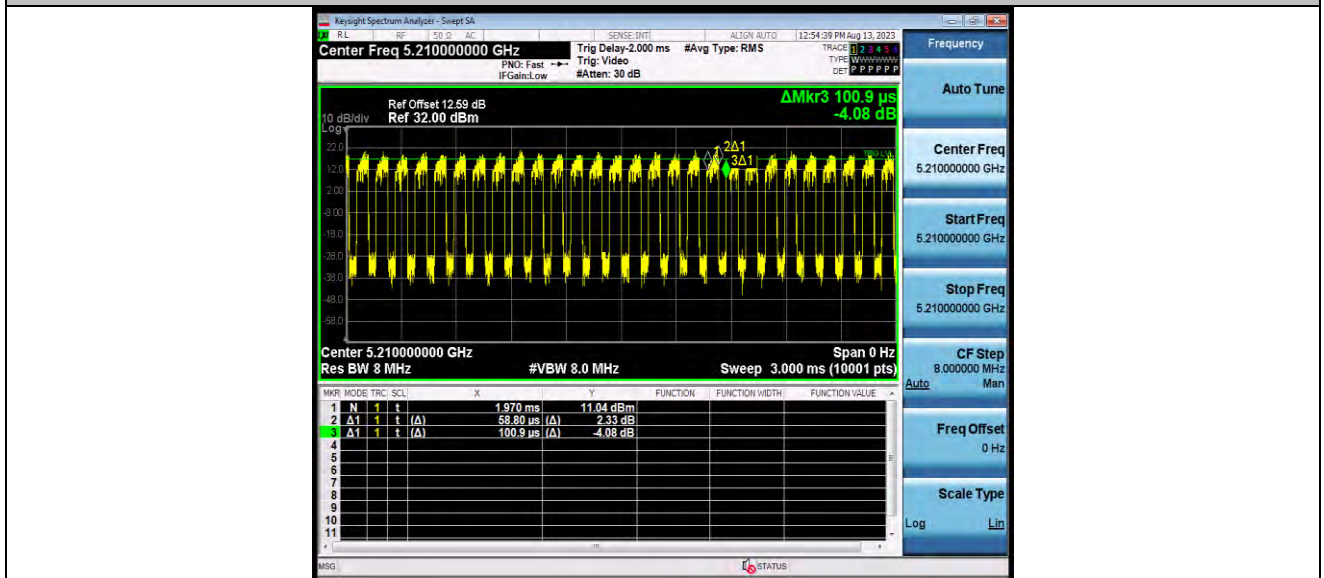


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC80SISO_Ant1_5210

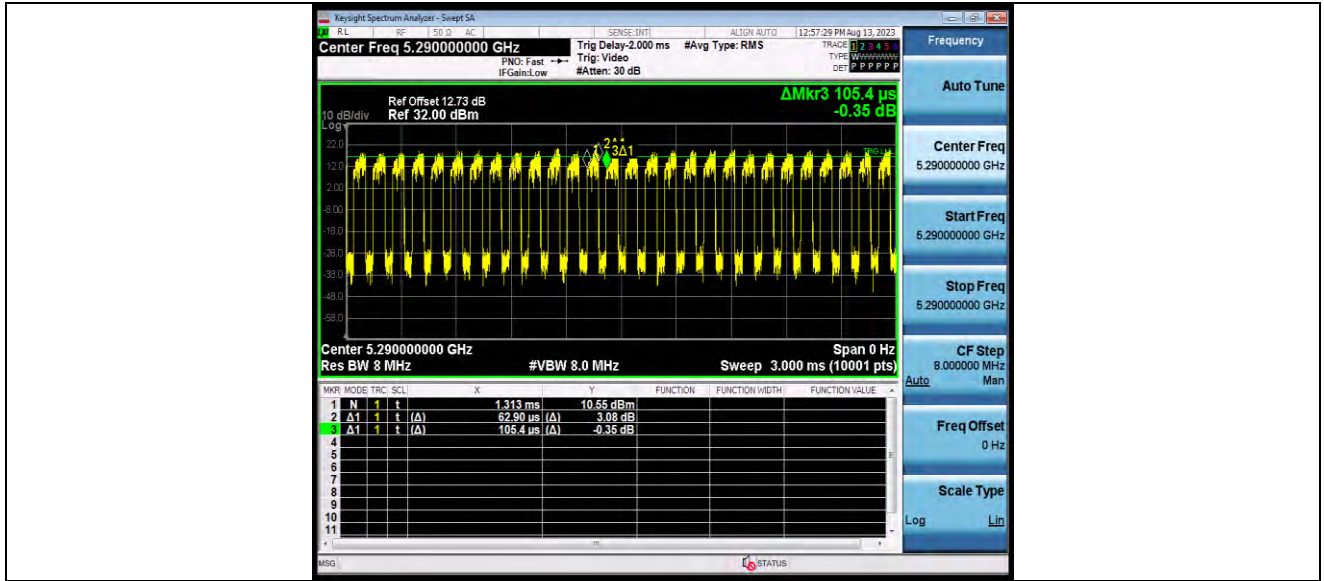


11AC80SISO_Ant1_5290

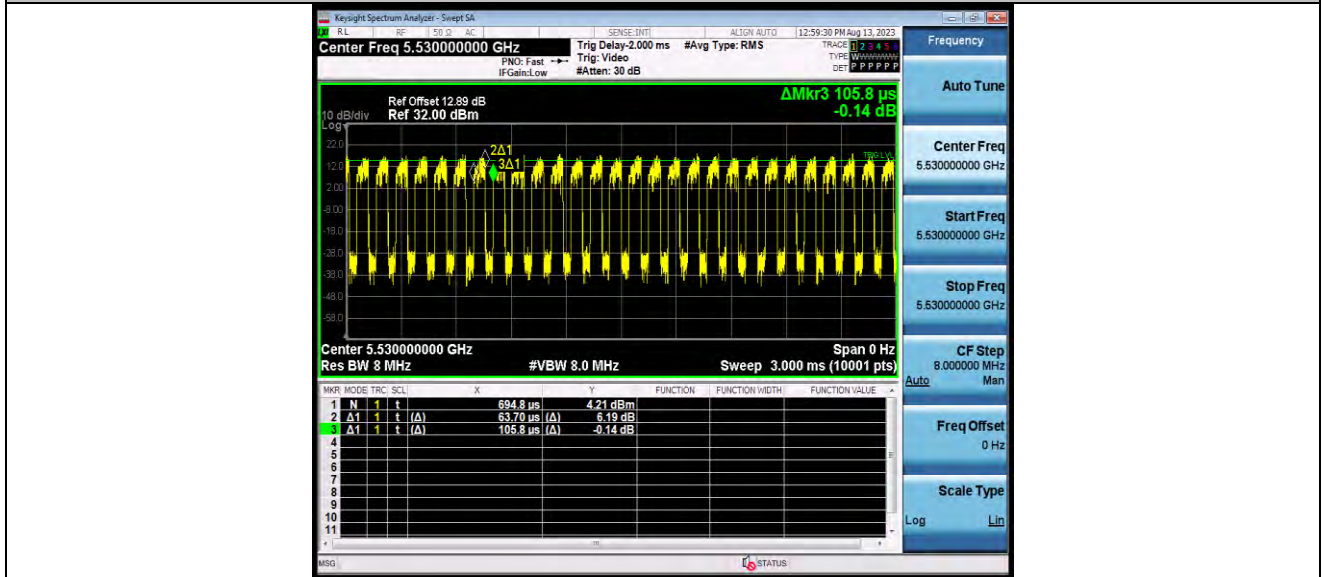


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC80SISO_Ant1_5530

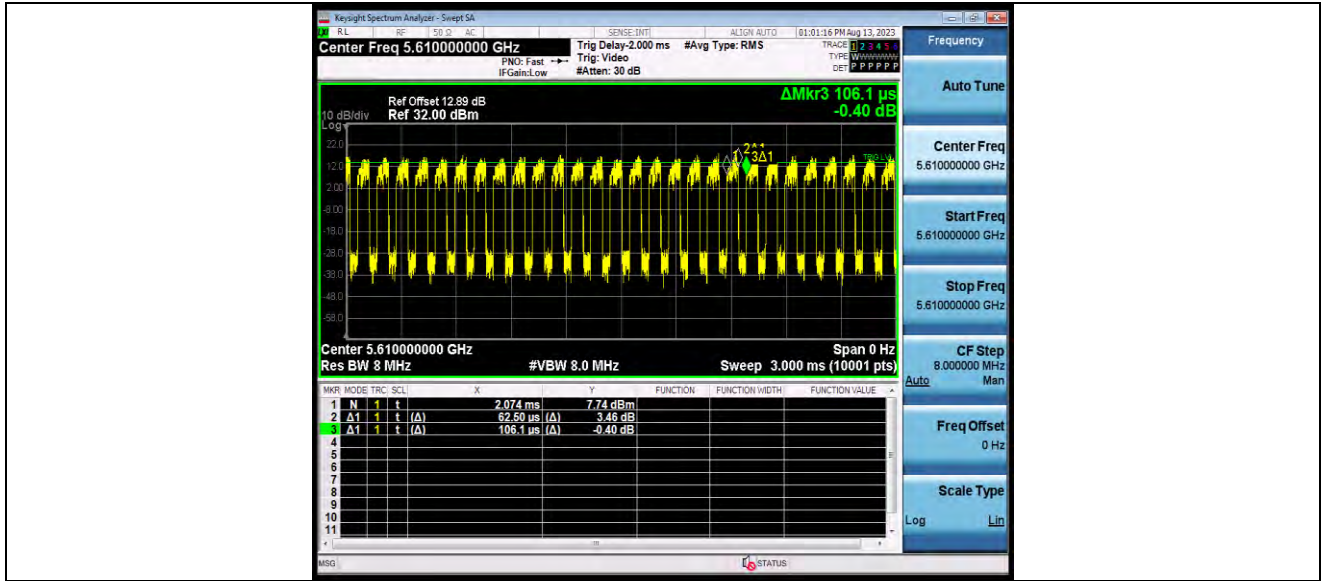


11AC80SISO_Ant1_5610

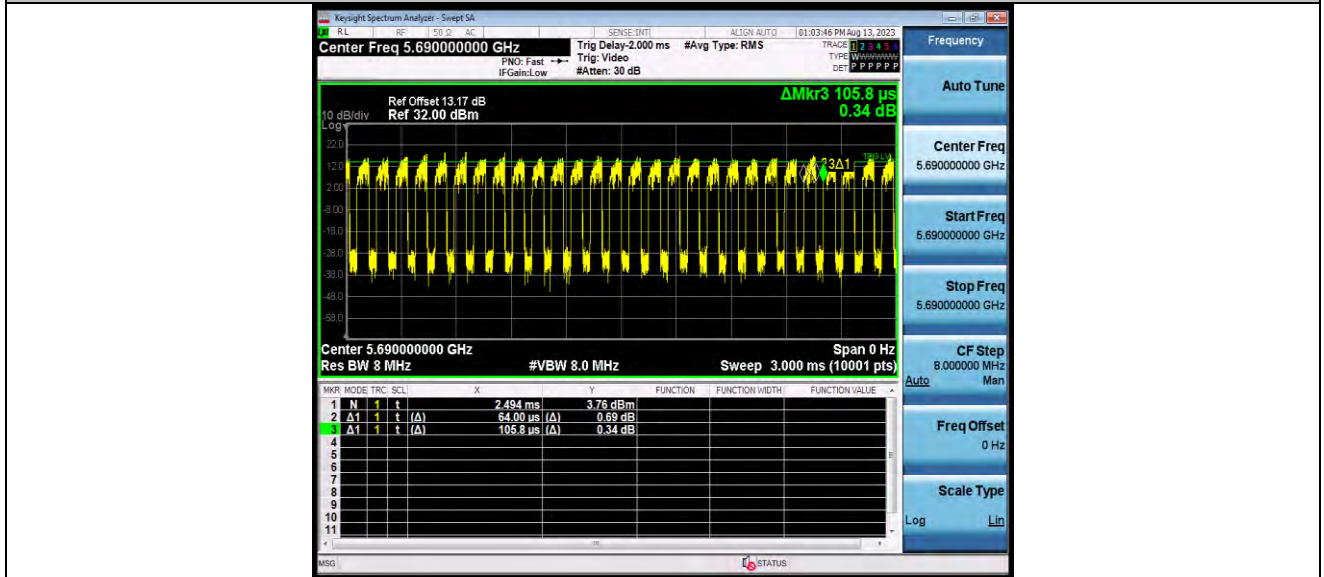


BUREAU VERITAS

Test Report No.: W7L-P23070005RF03



11AC80SISO_Ant1_5690

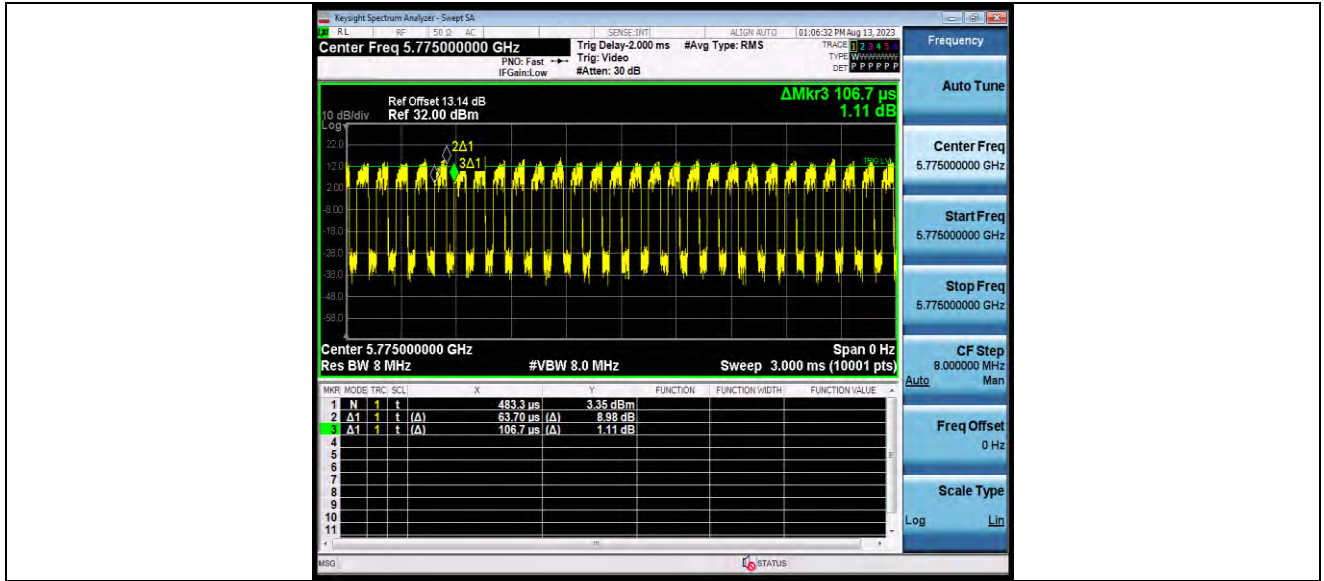


11AC80SISO_Ant1_5775



BUREAU VERITAS

Test Report No.: W7L-P23070005RF03





MAXIMUM CONDUCTED OUTPUT POWER

TEST RESULT

BV Power Table For_U-NII-1										
Test Mode	TX Mod.	Freq. (MHz)	Ant.	Maximum Conducted Power (dBm)	FCC Conducted Power Limit (dBm)	IC Conducted Power Limit (dBm)	EIRP (dBm)	IC EIRP Limit	Verdict	Power Setting
11A	SISO	5180	Ant1	16.74	≤24.00	---	18.19	≤23.01	Pass	17
		5200	Ant1	16.95	≤24.00	---	18.40	≤23.01	Pass	17
		5240	Ant1	16.65	≤24.00	---	18.10	≤22.72	Pass	17
11N20	SISO	5180	Ant1	16.74	≤24.00	---	18.19	≤22.91	Pass	17
		5200	Ant1	16.66	≤24.00	---	18.11	≤23.01	Pass	17
		5240	Ant1	16.49	≤24.00	---	17.94	≤22.80	Pass	17
11N40	SISO	5190	Ant1	17.89	≤24.00	---	19.34	≤23.01	Pass	18
		5230	Ant1	17.86	≤24.00	---	19.31	≤23.01	Pass	18
11AC20	SISO	5180	Ant1	16.60	≤24.00	---	18.05	≤22.70	Pass	17
		5200	Ant1	16.50	≤24.00	---	17.95	≤22.68	Pass	17
		5240	Ant1	16.83	≤24.00	---	18.28	≤22.65	Pass	17
11AC40	SISO	5190	Ant1	12.96	≤24.00	---	14.41	≤23.01	Pass	14
		5230	Ant1	17.54	≤24.00	---	18.99	≤23.01	Pass	18
11AC80	SISO	5210	Ant1	12.33	≤24.00	---	13.78	≤23.01	Pass	15

Note: The Maximum Conducted Power with duty cycle factor.



BV Power Table_U-NII-2A											
Test Mode	TX Mod.	Freq. (MHz)	Ant.	Maximum Conducted Power (dBm)	FCC Conducted Power Limit (dBm)	IC Conducted Power Limit (dBm)	EIRP (dBm)	FCC EIRP Limit	IC EIRP Limit	Verdict	Power Setting
11A	SISO	5260	Ant1	17.74	≤24.00	≤24.00	19.98	≤30.00	≤30.00	Pass	18
		5300	Ant1	18.00	≤24.00	≤23.89	20.24	≤30.00	≤29.89	Pass	19
		5320	Ant1	18.18	≤24.00	≤24.00	20.42	≤30.00	≤30.00	Pass	19
11N20	SISO	5260	Ant1	18.41	≤24.00	≤23.92	20.65	≤30.00	≤29.92	Pass	19
		5300	Ant1	19.08	≤24.00	≤24.00	21.32	≤30.00	≤30.00	Pass	20
		5320	Ant1	18.88	≤24.00	≤24.00	21.12	≤30.00	≤30.00	Pass	20
11N40	SISO	5270	Ant1	17.99	≤24.00	≤24.00	20.23	≤30.00	≤30.00	Pass	18
		5310	Ant1	15.83	≤24.00	≤24.00	18.07	≤30.00	≤30.00	Pass	17
11AC20	SISO	5260	Ant1	17.48	≤24.00	≤23.69	19.72	≤30.00	≤29.69	Pass	18
		5300	Ant1	17.11	≤24.00	≤23.67	19.35	≤30.00	≤29.67	Pass	18
		5320	Ant1	17.72	≤24.00	≤23.57	19.96	≤30.00	≤29.57	Pass	19
11AC40	SISO	5270	Ant1	17.24	≤24.00	≤24.00	19.48	≤30.00	≤30.00	Pass	18
		5310	Ant1	16.89	≤24.00	≤24.00	19.13	≤30.00	≤30.00	Pass	18
11AC80	SISO	5290	Ant1	14.16	≤24.00	≤24.00	16.40	≤30.00	≤30.00	Pass	17

Note: The Maximum Conducted Power with duty cycle factor.



BV Power Table_U-NII-2C											
Test Mode	TX Mod.	Freq. (MHz)	Ant.	Maximum Conducted Power (dBm)	FCC Conducted Power Limit (dBm)	IC Conducted Power Limit (dBm)	EIRP (dBm)	FCC EIRP Limit	IC EIRP Limit	Verdict	Power Setting
11A	SISO	5500	Ant1	18.53	≤24.00	≤23.78	20.41	≤30.00	≤29.78	Pass	19
		5580	Ant1	18.14	≤24.00	≤23.74	20.02	≤30.00	≤29.74	Pass	19
		5700	Ant1	18.29	≤24.00	≤23.67	20.17	≤30.00	≤29.67	Pass	19
		5720	Ant1	15.87	≤24.00	≤22.32	17.75	≤30.00	≤28.32	Pass	17
11N20	SISO	5500	Ant1	17.29	≤24.00	≤23.67	19.17	≤30.00	≤29.67	Pass	18
		5580	Ant1	18.04	≤24.00	≤24.00	19.92	≤30.00	≤29.97	Pass	19
		5700	Ant1	18.06	≤24.00	≤23.86	19.94	≤30.00	≤29.86	Pass	19
		5720	Ant1	16.75	≤24.00	≤22.55	18.63	≤30.00	≤28.55	Pass	18
11N40	SISO	5510	Ant1	16.53	≤24.00	≤24.00	18.41	≤30.00	≤30.00	Pass	17
		5550	Ant1	16.55	≤24.00	≤24.00	18.43	≤30.00	≤30.00	Pass	17
		5670	Ant1	15.55	≤24.00	≤24.00	17.43	≤30.00	≤30.00	Pass	16
		5710	Ant1	15.74	≤24.00	≤24.00	17.62	≤30.00	≤30.00	Pass	17
11AC20	SISO	5500	Ant1	16.15	≤24.00	≤23.62	18.03	≤30.00	≤29.62	Pass	17
		5580	Ant1	16.92	≤24.00	≤23.65	18.80	≤30.00	≤29.65	Pass	18
		5700	Ant1	15.55	≤24.00	≤23.61	17.43	≤30.00	≤29.61	Pass	17
		5720	Ant1	15.70	≤24.00	≤22.52	17.58	≤30.00	≤28.52	Pass	17
11AC40	SISO	5510	Ant1	17.31	≤24.00	≤24.00	19.19	≤30.00	≤30.00	Pass	18
		5550	Ant1	14.61	≤24.00	≤24.00	16.49	≤30.00	≤30.00	Pass	16
		5670	Ant1	14.13	≤24.00	≤24.00	16.01	≤30.00	≤30.00	Pass	16
		5710	Ant1	12.11	≤24.00	≤24.00	13.99	≤30.00	≤30.00	Pass	14
11AC80	SISO	5530	Ant1	16.57	≤24.00	≤24.00	18.45	≤30.00	≤30.00	Pass	18
	SISO	5690	Ant1	14.71	≤24.00	≤24.00	16.59	≤30.00	≤30.00	Pass	17

Note: The Maximum Conducted Power with duty cycle factor.



BV Power Table_U-NII-3									
Test Mode	TX Mod.	Freq. (MHz)	Ant.	Maximum Conducted Power (dBm)	Conducted Power Limit (dBm)	EIRP (dBm)	IC EIRP Limit	Verdict	Power Setting
11A	SISO	5745	Ant1	13.74	≤30.00	15.30	≤36.00	Pass	14
		5785	Ant1	13.78	≤30.00	13.21	≤36.00	Pass	14
		5825	Ant1	13.75	≤30.00	13.18	≤36.00	Pass	14
11N20	SISO	5745	Ant1	13.61	≤30.00	13.04	≤36.00	Pass	14
		5785	Ant1	13.61	≤30.00	13.04	≤36.00	Pass	14
		5825	Ant1	13.47	≤30.00	12.90	≤36.00	Pass	14
11N40	SISO	5755	Ant1	13.35	≤30.00	12.78	≤36.00	Pass	14
		5795	Ant1	13.32	≤30.00	12.75	≤36.00	Pass	14
11AC20	SISO	5745	Ant1	13.53	≤30.00	12.96	≤36.00	Pass	14
		5785	Ant1	13.54	≤30.00	12.97	≤36.00	Pass	14
		5825	Ant1	13.56	≤30.00	12.99	≤36.00	Pass	14
11AC40	SISO	5755	Ant1	12.34	≤30.00	11.77	≤36.00	Pass	13
		5795	Ant1	12.32	≤30.00	11.75	≤36.00	Pass	13
11AC80	SISO	5775	Ant1	12.61	≤30.00	12.04	≤36.00	Pass	14

Note: The Maximum Conducted Power with duty cycle factor.



MAXIMUM POWER SPECTRAL DENSITY

TEST RESULT

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit [dBm/MHz]	EIRP [dBm/MHz]	EIRP Limit [dBm/MHz]	Verdict
11A	Ant1	5180	8.24	≤11.00	9.69	≤10.00	PASS
		5200	8.24	≤11.00	9.69	≤10.00	PASS
		5240	8.33	≤11.00	9.78	≤10.00	PASS
		5260	10.04	≤11.00	12.28	---	PASS
		5300	9.78	≤11.00	12.02	---	PASS
		5320	9.76	≤11.00	12.00	---	PASS
		5500	10.74	≤11.00	12.98	---	PASS
		5580	10.58	≤11.00	12.82	---	PASS
		5700	9.7	≤11.00	11.94	---	PASS
		5720	7.56	≤11.00	9.80	---	PASS
		5745	2.83	≤30.00	5.07	---	PASS
		5785	2.12	≤30.00	4.36	---	PASS
5825	2.82	≤30.00	5.06	---	PASS		
11N20SISO	Ant1	5180	7.92	≤11.00	9.37	≤10.00	PASS
		5200	8.24	≤11.00	9.69	≤10.00	PASS
		5240	8.21	≤11.00	9.66	≤10.00	PASS
		5260	10.04	≤11.00	12.28	---	PASS
		5300	10.68	≤11.00	12.92	---	PASS
		5320	10.89	≤11.00	13.13	---	PASS
		5500	8.94	≤11.00	11.18	---	PASS
		5580	9.49	≤11.00	11.73	---	PASS
		5700	8.94	≤11.00	11.18	---	PASS
		5720	7.67	≤11.00	9.91	---	PASS
		5745	2.07	≤30.00	4.31	---	PASS
		5785	2.47	≤30.00	4.71	---	PASS
5825	1.65	≤30.00	3.89	---	PASS		
11N40SISO	Ant1	5190	5.65	≤11.00	7.89	≤10.00	PASS
		5230	5.75	≤11.00	7.99	≤10.00	PASS
		5270	5.94	≤11.00	8.18	---	PASS



		5310	3.78	≤11.00	6.02	---	PASS
		5510	4.52	≤11.00	6.76	---	PASS
		5550	4.38	≤11.00	6.62	---	PASS
		5670	2.05	≤11.00	4.29	---	PASS
		5710	4.52	≤11.00	6.76	---	PASS
		5755	-1.73	≤30.00	0.51	---	PASS
		5795	-0.8	≤30.00	1.44	---	PASS
11AC20SISO	Ant1	5180	7.87	≤11.00	9.32	≤10.00	PASS
		5200	8.21	≤11.00	9.66	≤10.00	PASS
		5240	7.65	≤11.00	9.10	≤10.00	PASS
		5260	8.64	≤11.00	10.88	---	PASS
		5300	8.38	≤11.00	10.62	---	PASS
		5320	9	≤11.00	11.24	---	PASS
		5500	7.37	≤11.00	9.61	---	PASS
		5580	8.24	≤11.00	10.48	---	PASS
		5700	6.42	≤11.00	8.66	---	PASS
		5720	6.01	≤11.00	8.25	---	PASS
		5745	2.14	≤30.00	4.38	---	PASS
		5785	2.75	≤30.00	4.99	---	PASS
		5825	1.8	≤30.00	4.04	---	PASS
11AC40SISO	Ant1	5190	6.46	≤11.00	8.70	≤10.00	PASS
		5230	5.44	≤11.00	7.68	≤10.00	PASS
		5270	5.48	≤11.00	7.72	---	PASS
		5310	5.27	≤11.00	7.51	---	PASS
		5510	5.71	≤11.00	7.95	---	PASS
		5550	2.82	≤11.00	5.06	---	PASS
		5670	3.24	≤11.00	5.48	---	PASS
		5710	-0.04	≤11.00	2.20	---	PASS
		5755	-2.13	≤30.00	0.11	---	PASS
		5795	-2.07	≤30.00	0.17	---	PASS
		11AC80SISO	Ant1	5210	3.96	≤11.00	6.20
5290	4.08			≤11.00	6.32	---	PASS
5530	0.72			≤11.00	2.96	---	PASS
5610	0.96			≤11.00	3.20	---	PASS
5690	0.46			≤11.00	2.70	---	PASS



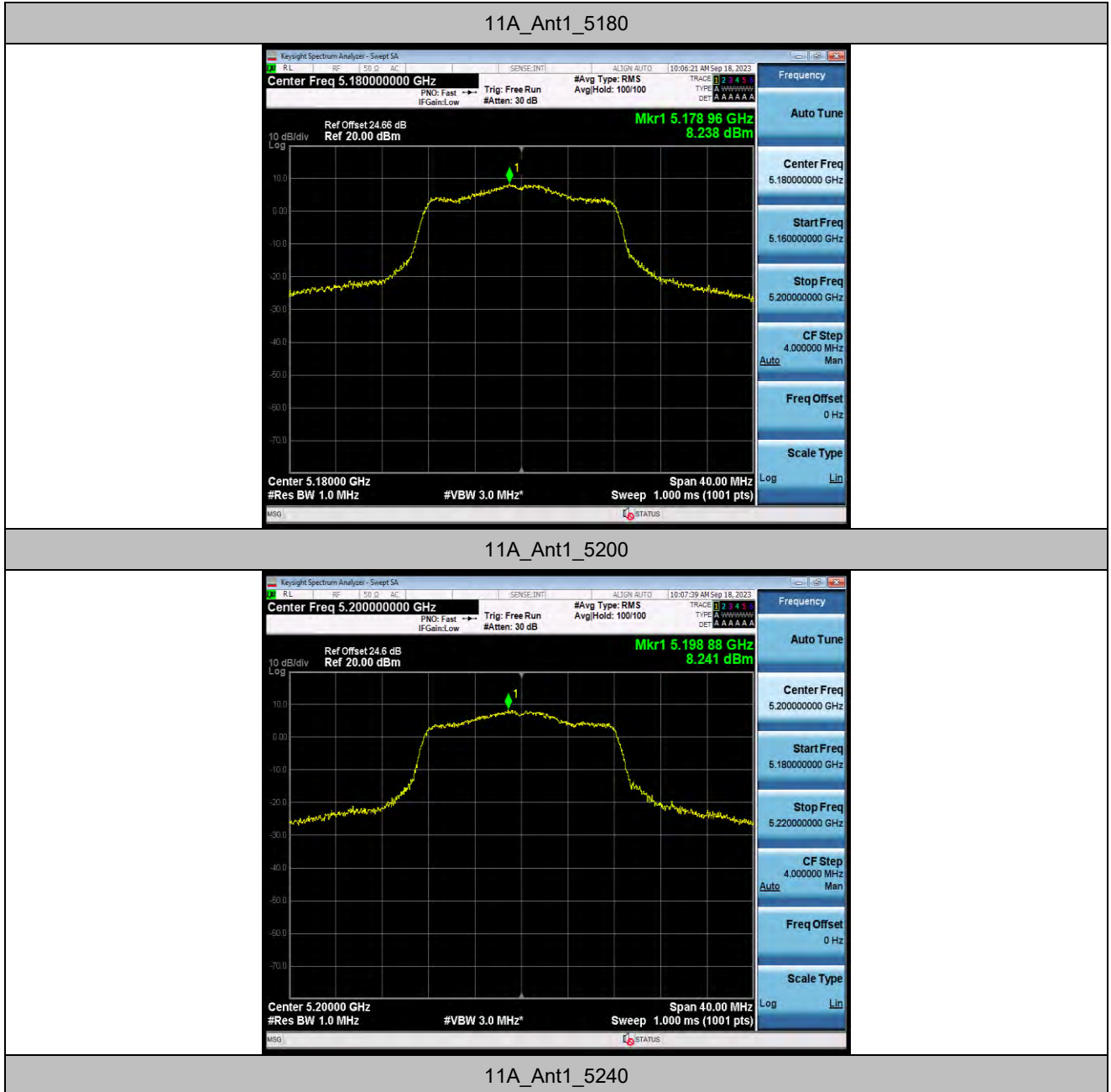
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		5775	-3.65	≤30.00	-1.41	---	PASS
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Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

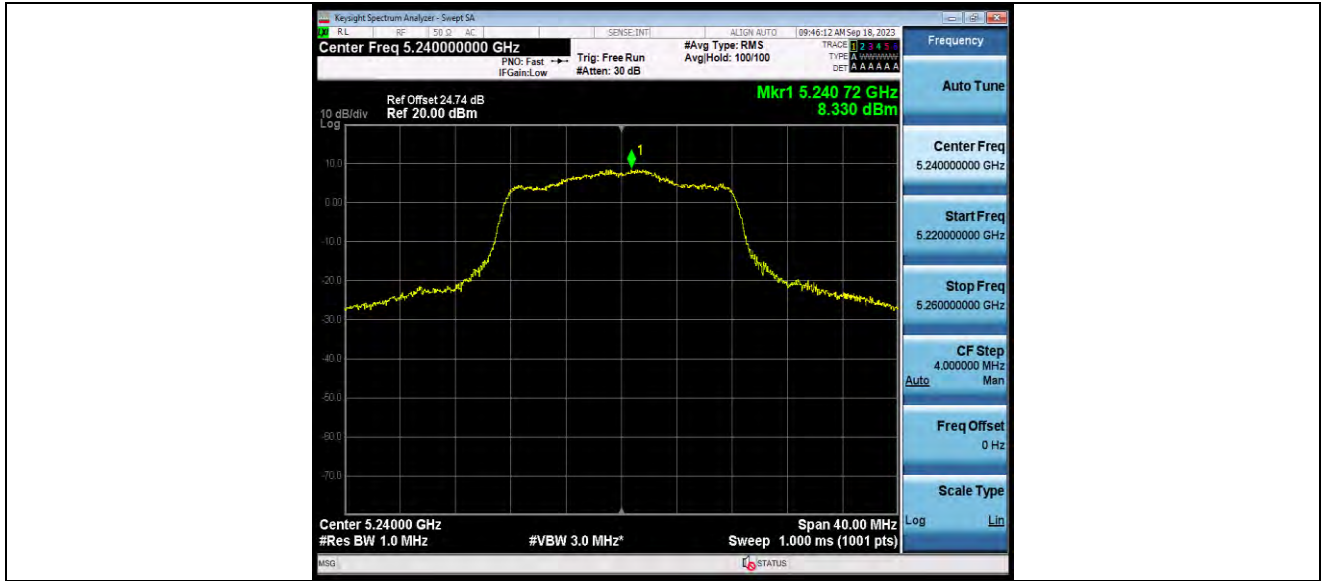
TEST GRAPHS





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11A_Ant1_5260



11A_Ant1_5300



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VERITAS

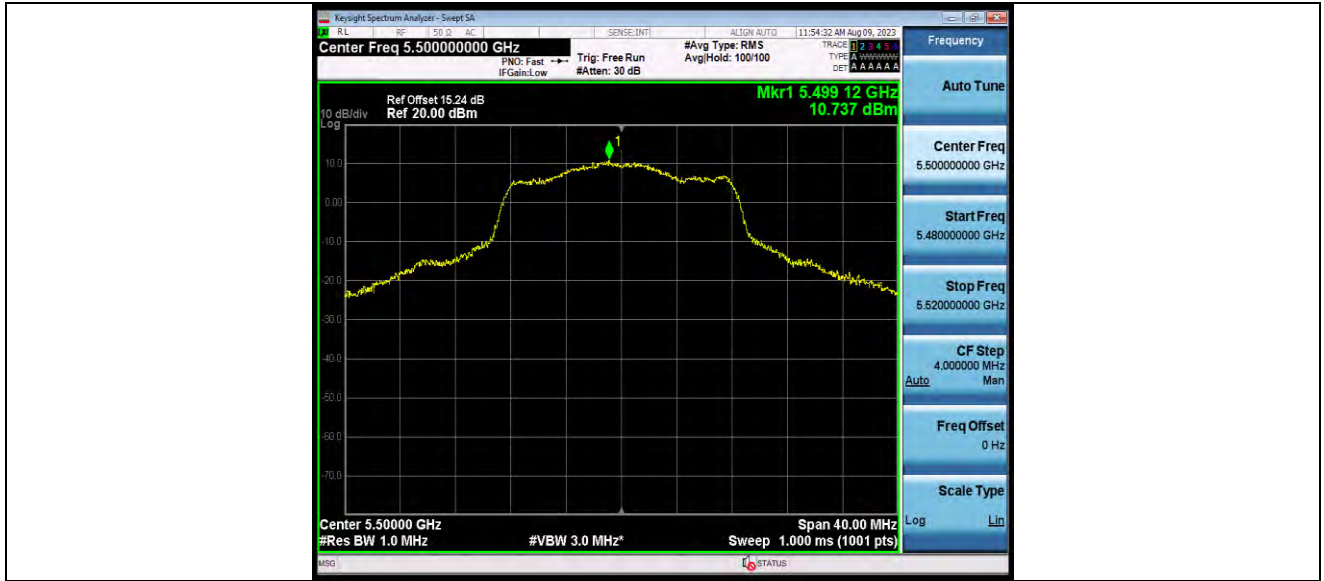
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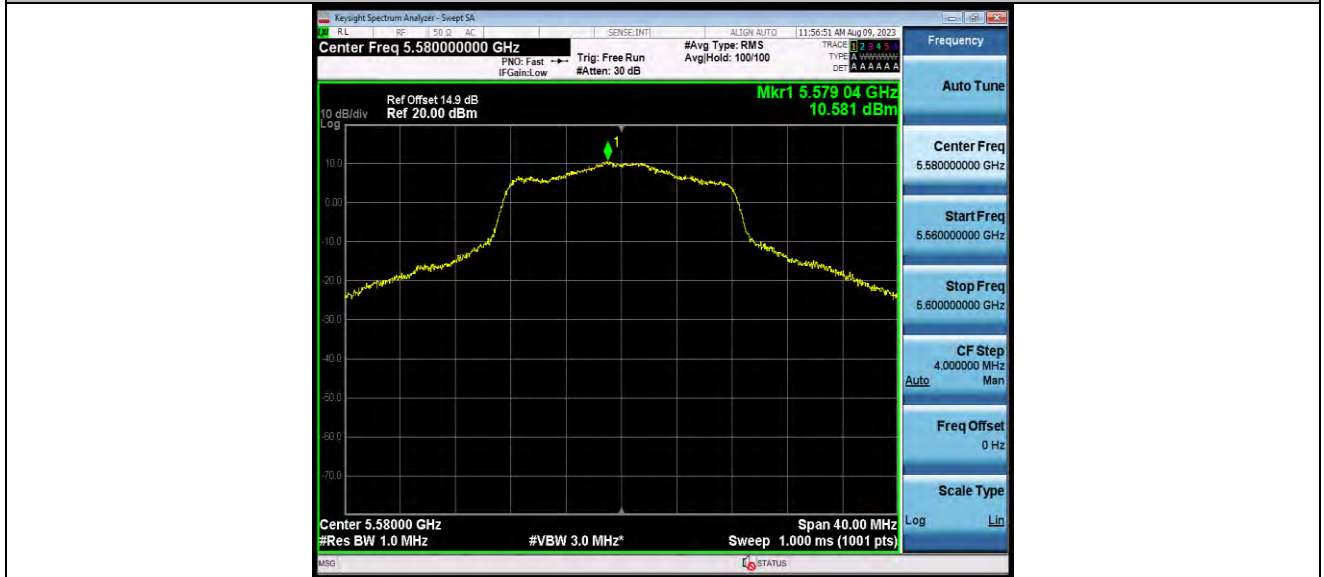


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Test Report No.: W7L-P23070005RF03



11A_Ant1_5580



11A_Ant1_5700



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Test Report No.: W7L-P23070005RF03



11A_Ant1_5720



11A_Ant1_5745



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Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5180

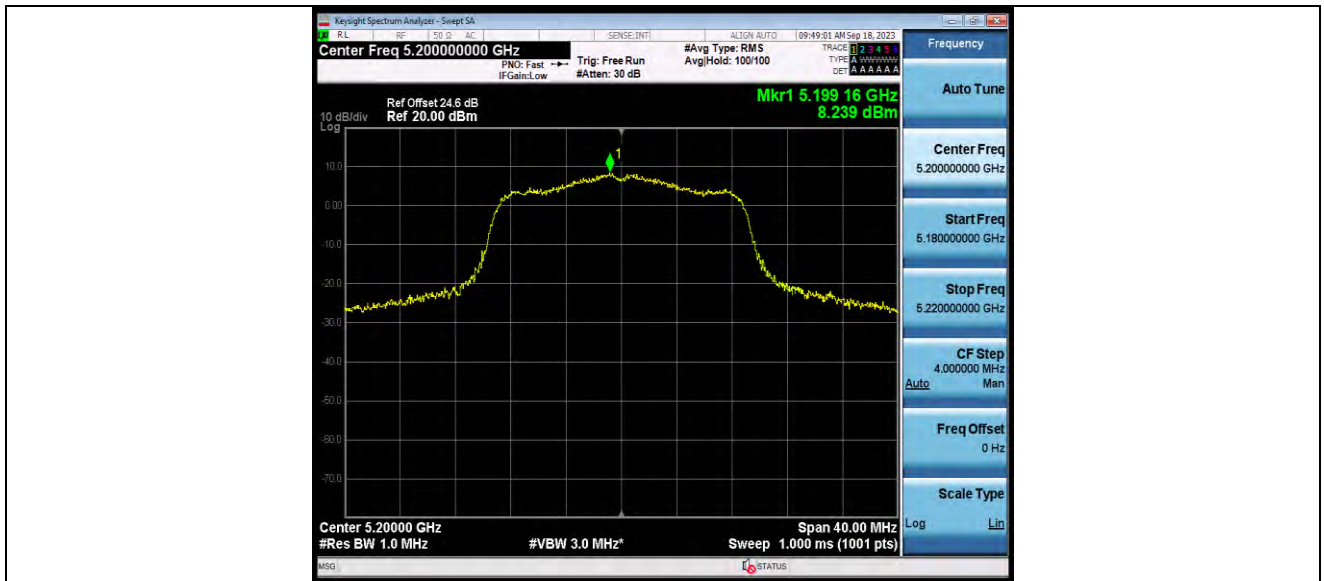


11N20SISO_Ant1_5200

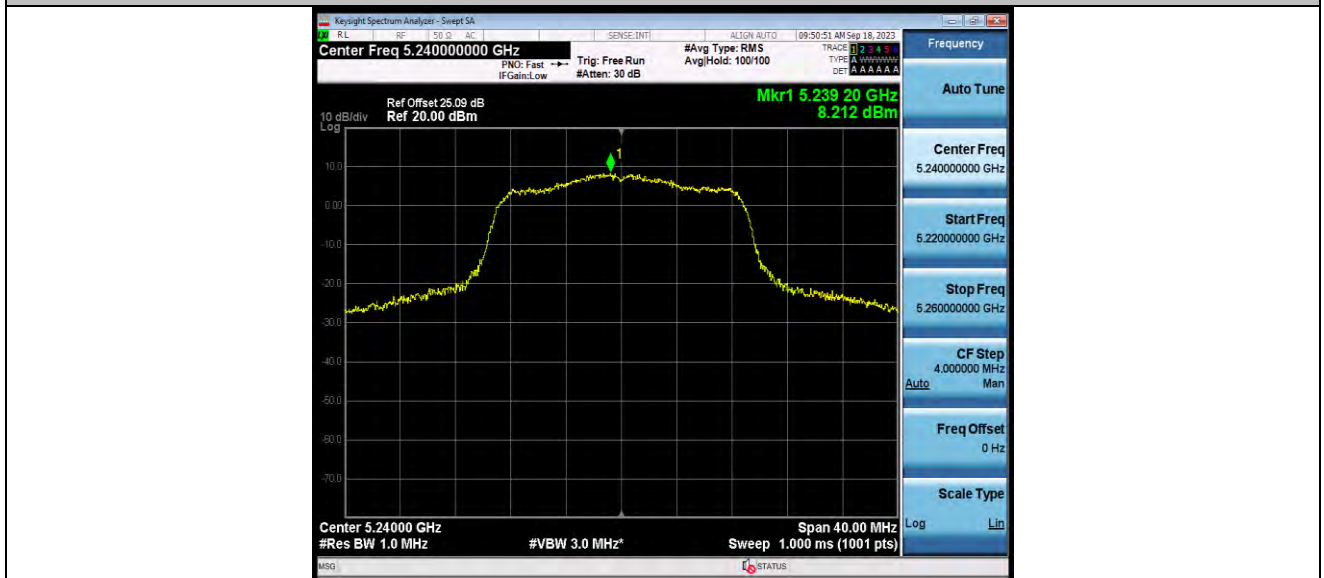


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11N20SISO_Ant1_5240



11N20SISO_Ant1_5260



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Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5500



11N20SISO_Ant1_5580



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Test Report No.: W7L-P23070005RF03



11N20SISO_Ant1_5700



11N20SISO_Ant1_5720



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11N20SISO_Ant1_5745



11N20SISO_Ant1_5785



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11N20SISO_Ant1_5825



11N40SISO_Ant1_5190



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Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5230



11N40SISO_Ant1_5270



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Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5310



11N40SISO_Ant1_5510



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11N40SISO_Ant1_5550



11N40SISO_Ant1_5670



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Test Report No.: W7L-P23070005RF03



11N40SISO_Ant1_5795



11AC20SISO_Ant1_5180



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Test Report No.: W7L-P23070005RF03

