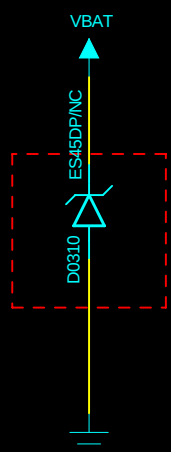


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LTN	ECNO	APPROVED	DATE

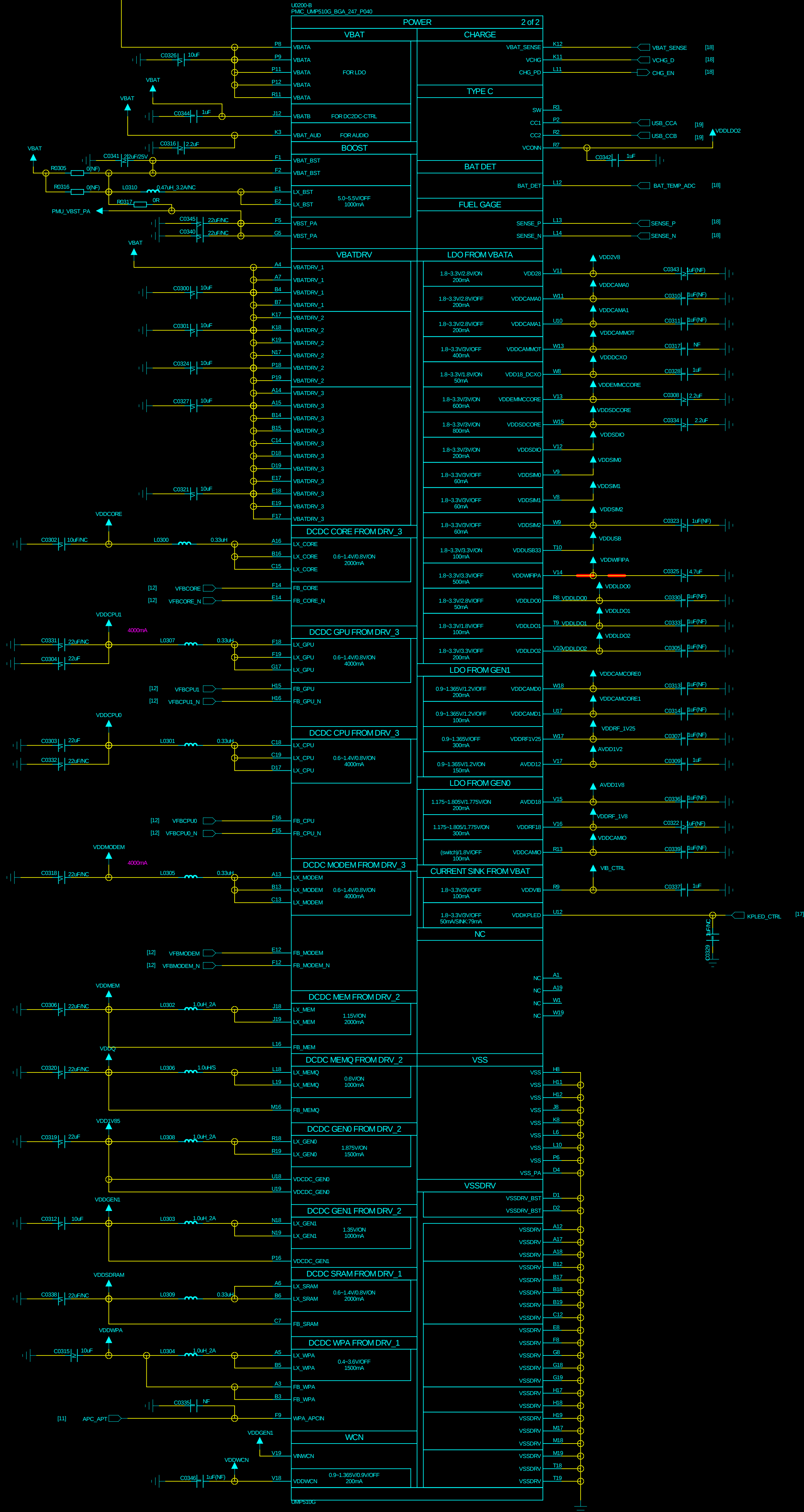
PMU_POWER

INPUT	Description
VBAT_ALD	VBAT power input for audio
VBAT_PA	VBAT power input for audio speaker PA
VBATA	VBAT power input for LDO
VBATB	VBAT power input for KP_LED,VIB,VBATBK,Flash,LED
VBATC	VBAT power input for LDO
VBAT_DRV	VBAT power input for DCDC



Close to PMU

PWR_SE platform	R031 R0311 VDDARM	VDDCORE
HIGH	SharkL3 00K	NF 0.9V 0.9V/0.7V
LOW	SharkL2 NF	0ohm 1.05V 1.05V/0.8V



SC2721 DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCCORE	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCMEM	1.1-1.4	800mA	1.2V	ON
	DCDCGEN1V85	1.6-2.1	1000mA	1.85V	ON
	DCDCWPA	0.4-3.6	1000mA		OFF

SC2721 LDO output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO from VBAT	VDDCAMA	1.8-3.3	150mA	2.8V	OFF
	VDDCAMMOT	1.8-3.3	100mA	2.8V	OFF
	VDDSIM0	1.8-3.3	50mA	1.8V	OFF
	VDDSIM1	1.8-3.3	50mA	1.8V	OFF
	VDDSIM2	1.8-3.3	200mA	1.8V	OFF
	VDD28	1.8-3.3	200mA	2.8V	ON
	VDDMEMCCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDIO	1.8-3.3	100mA	3.0V	ON
	VBATBK	2.6-3.2	2mA	3.0V	ON
	VDDVIB	1.8-3.3	100mA	3.3V	OFF
	VDDKPLED	1.8-3.3			
LDO from DCDCGEN	VDDWIFPA	1.8-3.3	400mA	3.3V	OFF
	VDDDCXO	1.8-3.3	10mA	1.8V	ON
	VDDUSB33	1.8-3.3	60mA	3.3V	ON
	VDDCAMD	1.0-1.4	400mA		OFF
	VDD_CON	1.1-1.6	400mA		OFF
	VDD_MEM	1.1-1.4	800mA	1.2V	ON
	VDDCAMIO	1.1-1.9	200mA		OFF
	AVDD18	1.1-1.9	200mA	1.8V	ON
	VDDRF18	1.1-1.9	200mA	1.8V	ON
	VDDRF1V25	1.1-1.4	200mA		OFF

EXT DCDC ARM

I2C Slave Address:0XC0

I2C-Compatible Interface Up to 3.4Mbps

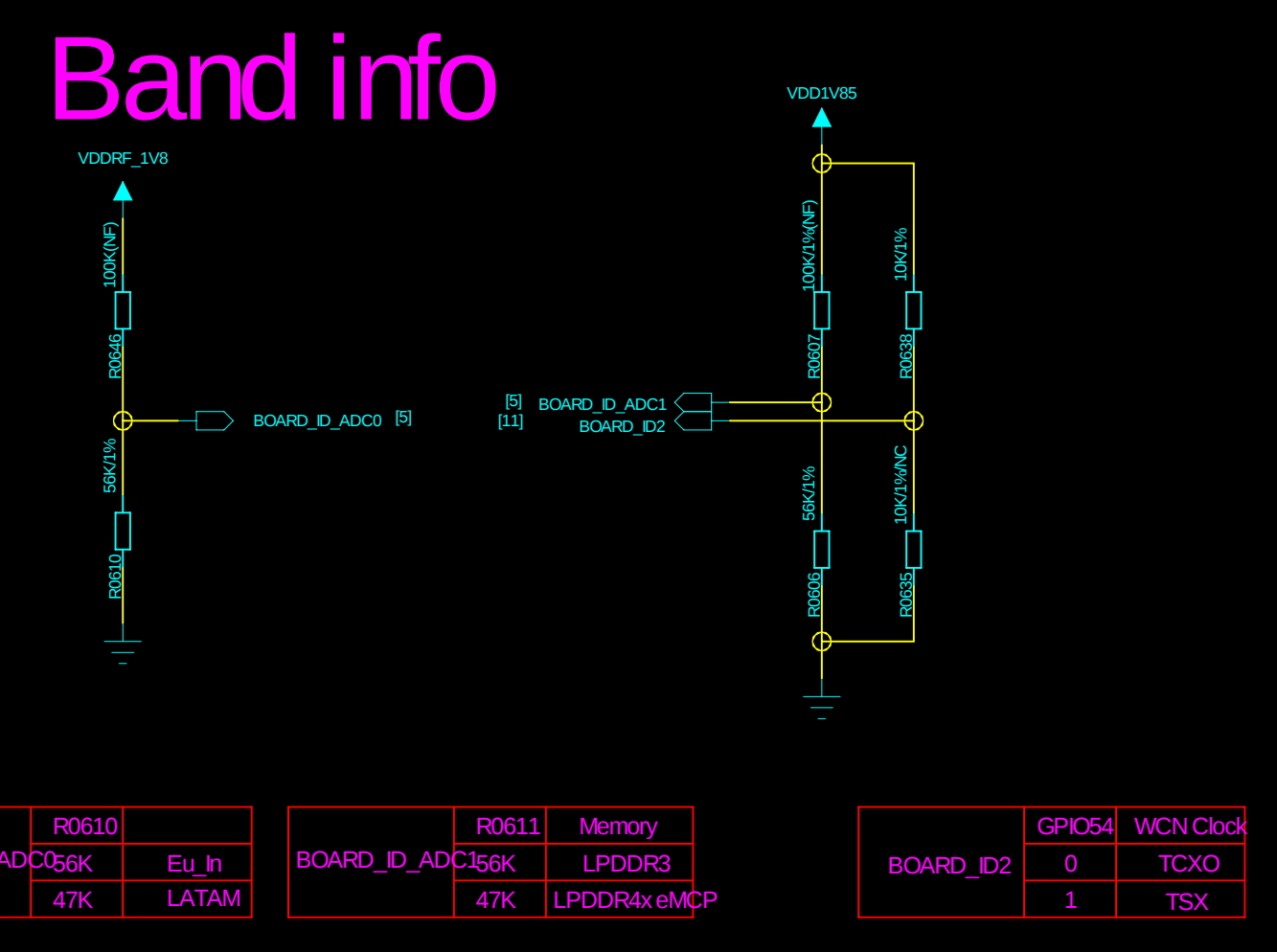
SC1803A(VDDARM) C0320=20UF C0328=20UF U0301=FAHS5655UC04X C0332=10UF R0311=10K/1% U0303=0.33uH_M R0322=0R SC18032E(VDDARM) C0323=1N5 C0328=1K U0301=NC C0330=1K R0311=1N5 U0303=1K R0322=1K

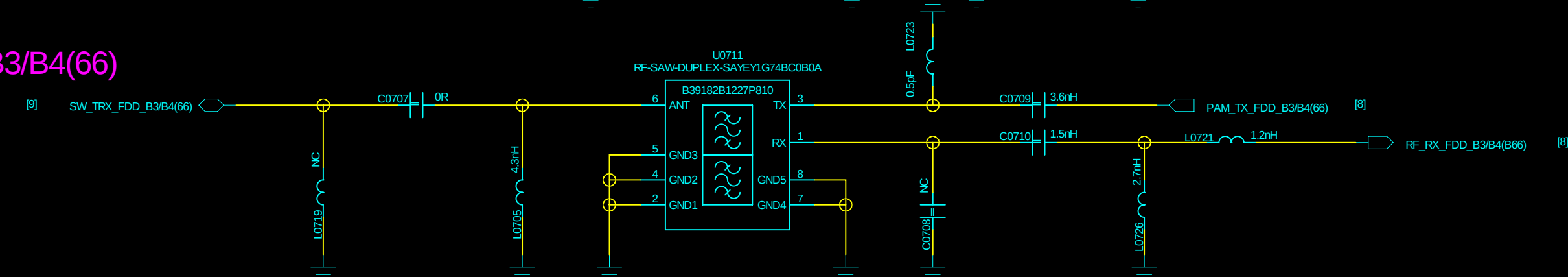
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		DOCUMENT NO.	
DESIGNER	DATED	DEPARTMENT	Hardware DEPT. Spreadtrum communications, Inc.
FOR	DATED	COMPANY	
APPROVED	DATED		
		CONFIDENTIAL AND PROPRIETARY	

eMMC+LPDDR3

eMMC/TLC

LTE/WCDMA PAM

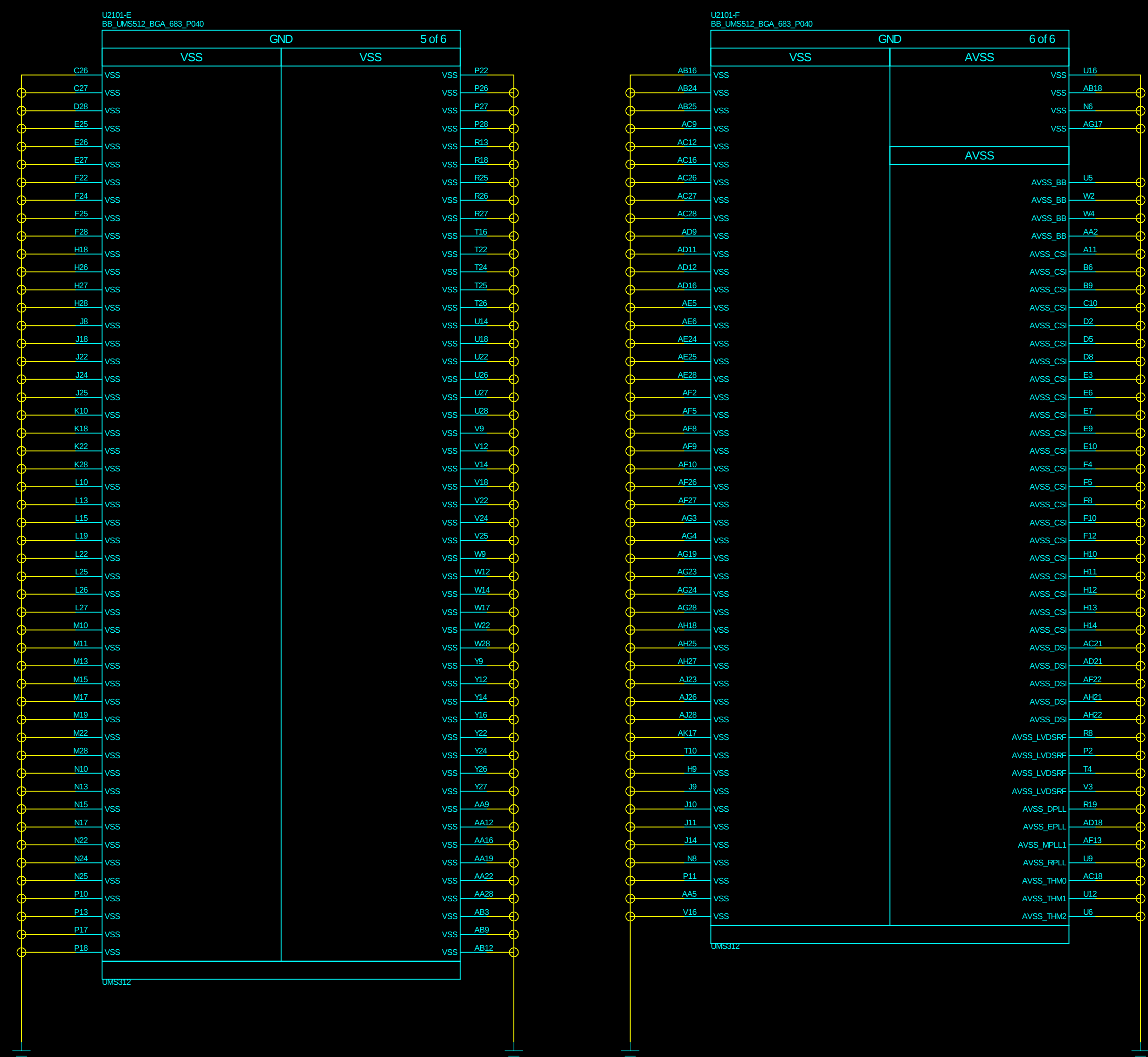




DIGITAL

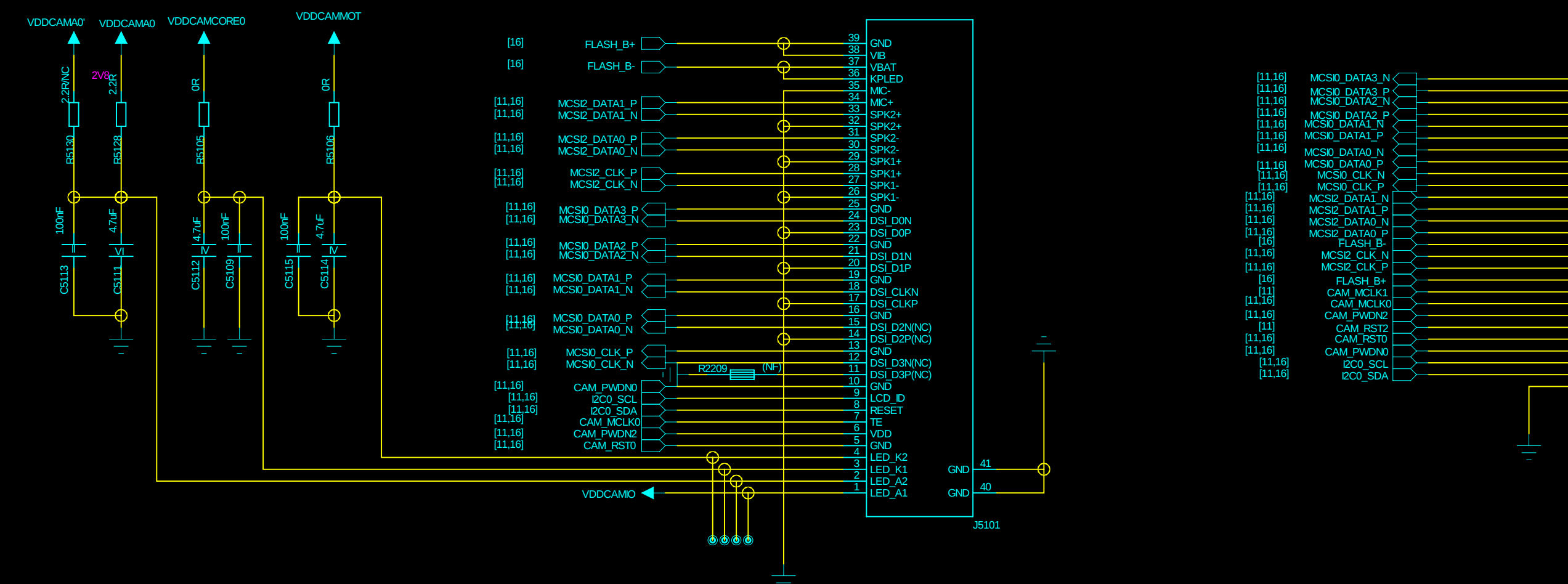
POWER

GND



Rear/Front Dual Camera

U5101:reserve for high PSRR VDDCAMA



Rear Daul Camera

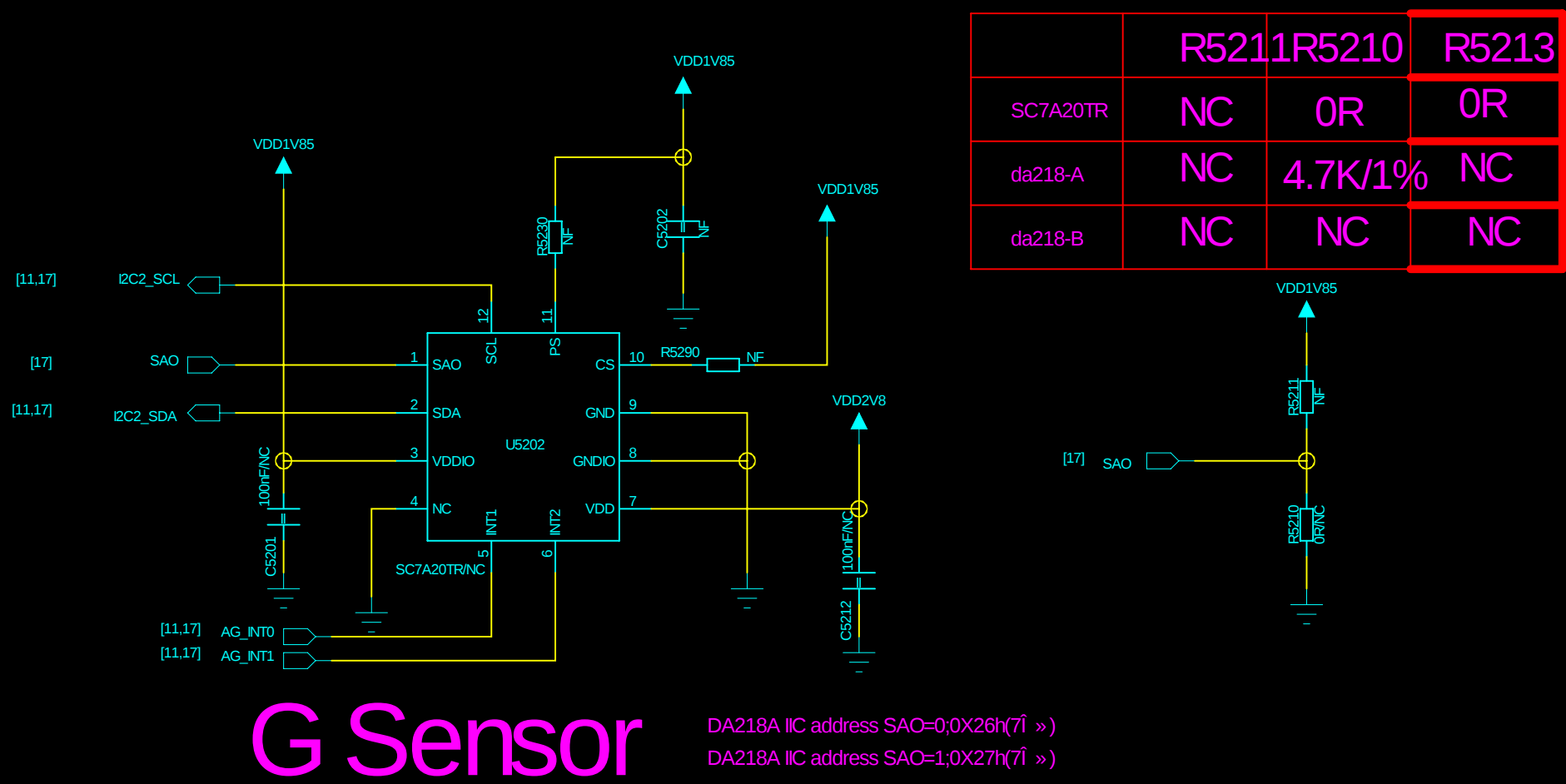
FLASHLED_BACK

BACK LED

Front LED

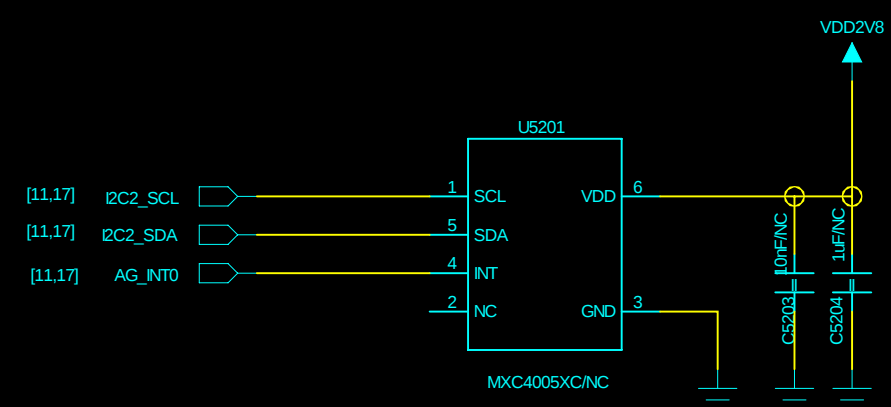
REVISION RECORD			
LTR	ECNO	APPROVED	DATE

Accelerometer

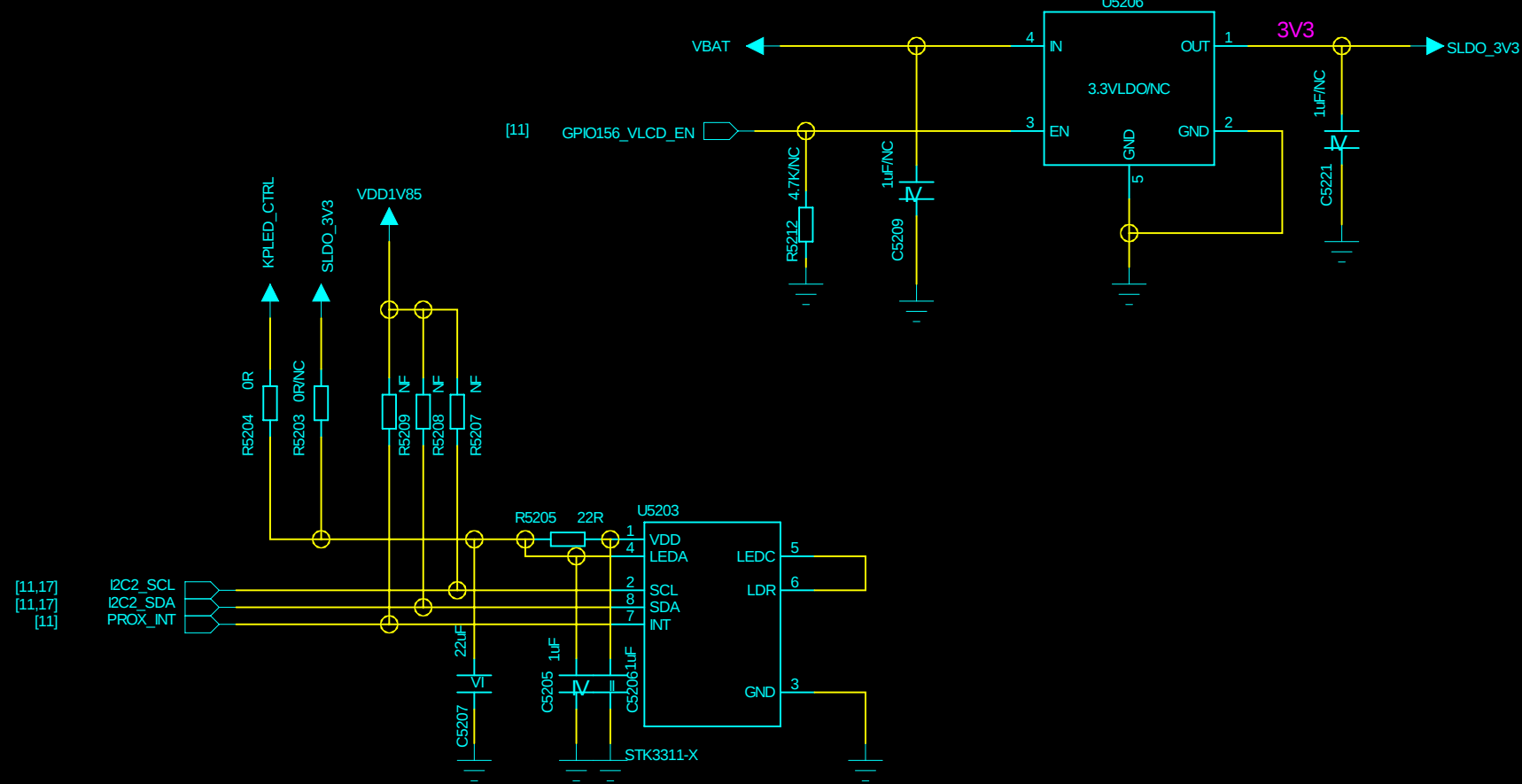


G Sensor

DA218A IC address SDA=00C3B7(7 =)
DA218A IC address SDA=10C274(7 =)

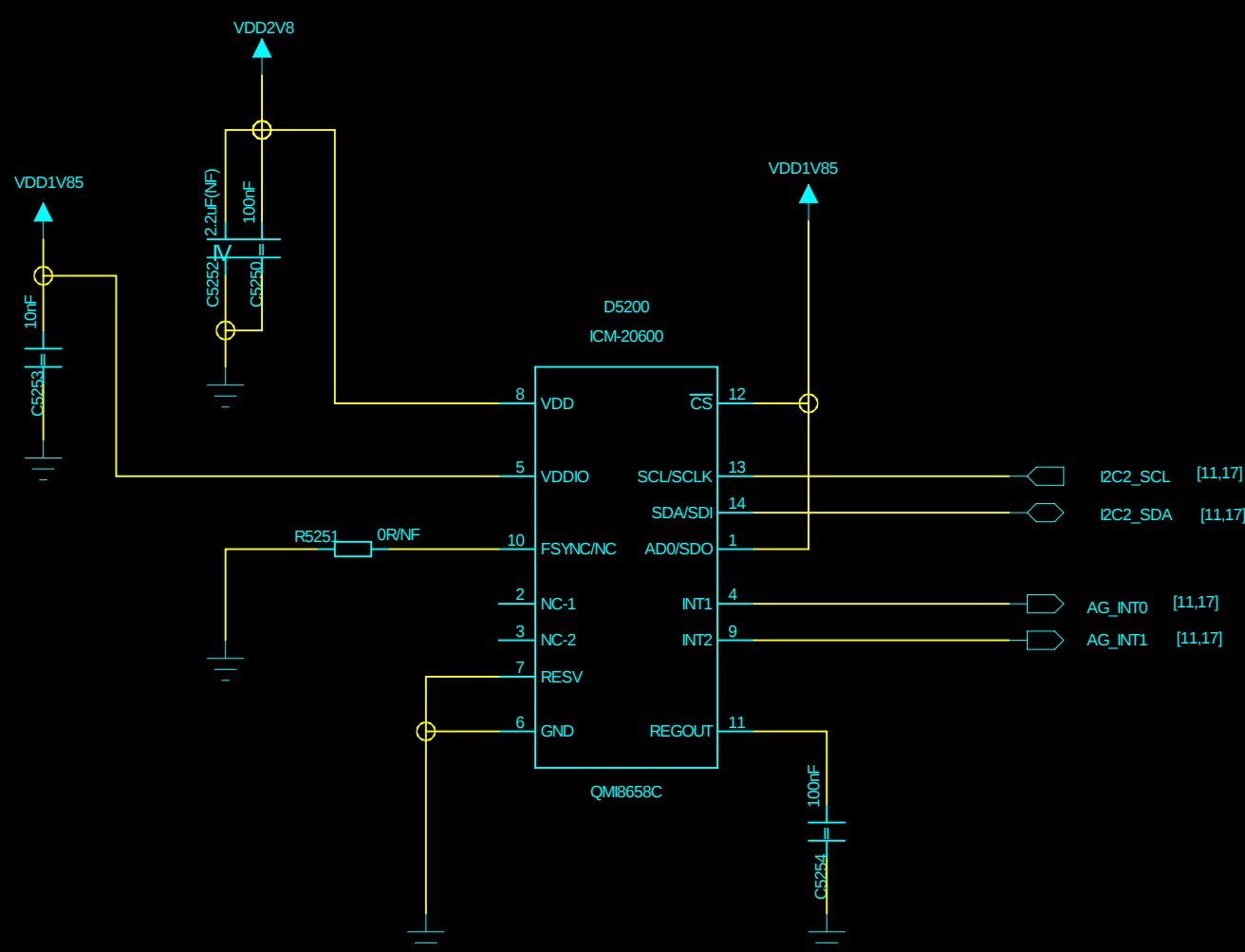


ALS & PROX

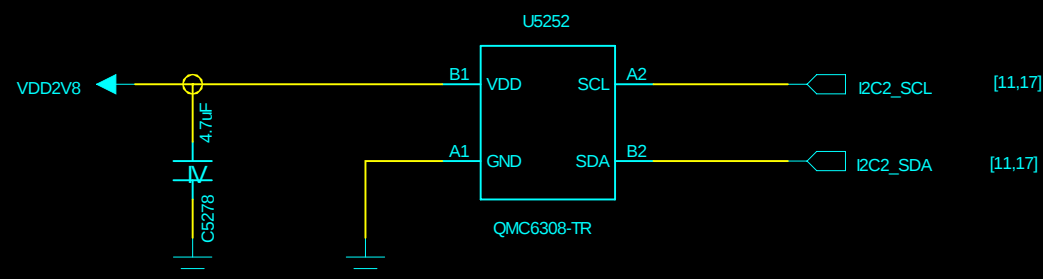


SIZE	LTR-553ALS-WA
1	3.94 x 2.36 x 1.35
2	SDA
3	INT
4	LDR
5	LED K
6	LED A
7	GND
8	SCL
9	VDD

Accelerometer&GYRO: ICM-20600



Magnetic Sensor:AK09911C



PIN	Definition
VDD	2.4V - 3.6V
VID	1.65V - VDD
CAD	Address select Note : connect to VDD,not VID

FingerPrint ID

NOTE:PCB LAYOUT LINE<0.1MM

(BACKUP)

I2C ADDRESS

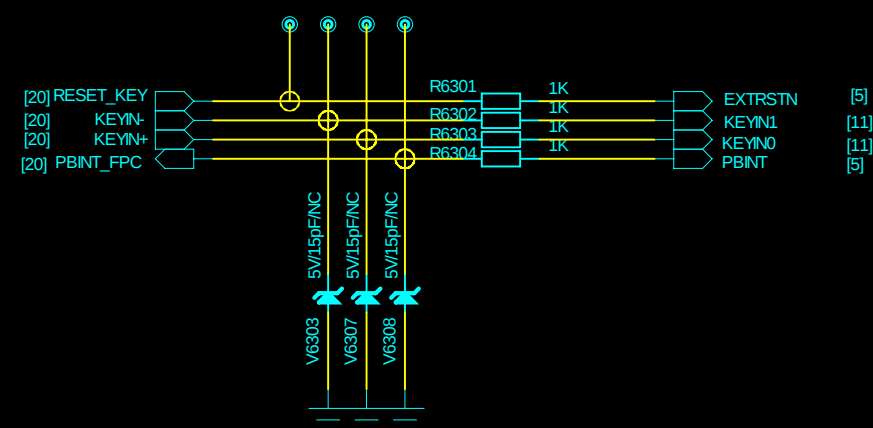
	Manufacturer	MPN	Address
A sensor	BOSCH	BMA253	0b0011001 (SDO=1)
M sensor	ASAHIKASE	AK09911	0b0001101 (CAD=1)
ALS & PROX	LITEON	LTR-553ALS-WA	0b0100011
A&G Sensor	InvenSense	ICM-20600	SA0=0,0b11010000 SA1=0,0b11010001
M sensor	AKM	AK09918C	0b0001100 (R/W)

TITLE & REV		
DOCUMENT NO.		
DESIGNER	DATE	DEPARTMENT
FOR	DATE	COMPANY
APPROVED	DATE	SPREADTRUM COMMUNICATIONS, INC.
CONFIDENTIAL AND PROPRIETARY		

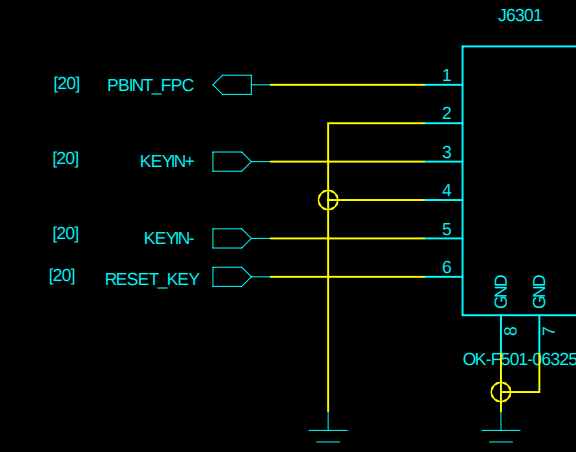
REVISION/RECORD			
LIB	ECNO	APPROVED	DATE

SIDEKEY

Only Keyin0USB Download with timeout
Keyin0+Keyin1 Boot with timeout



Side Key must be with ESD protect

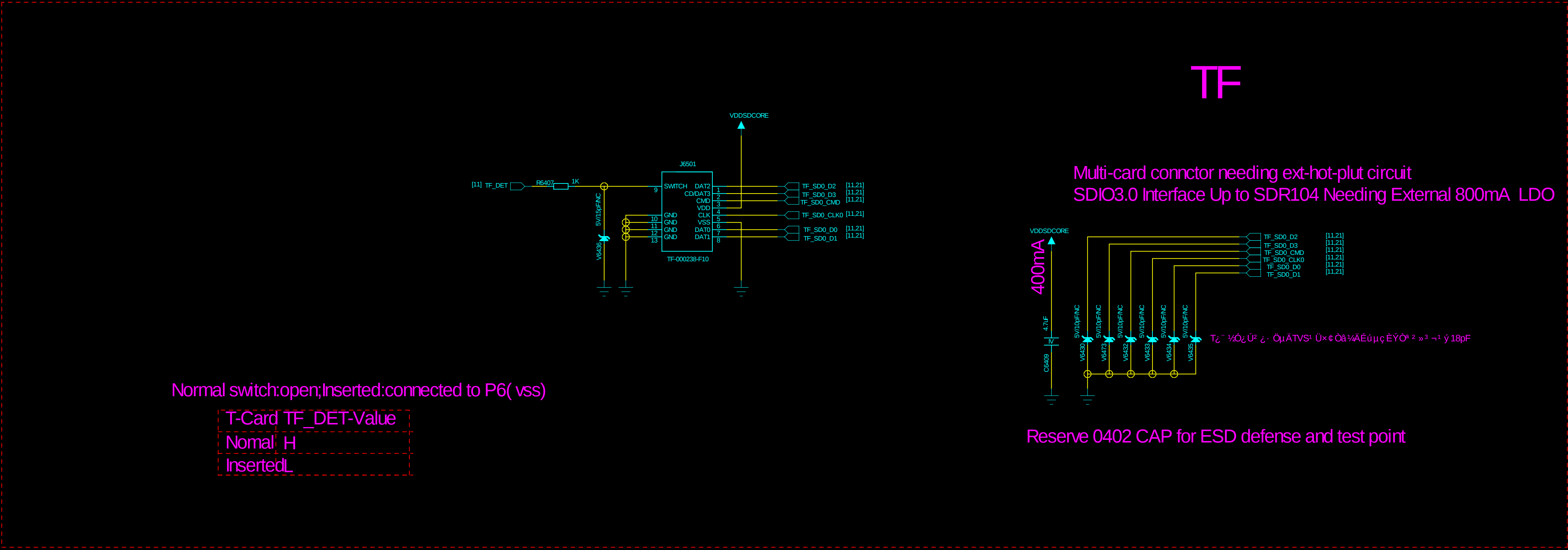
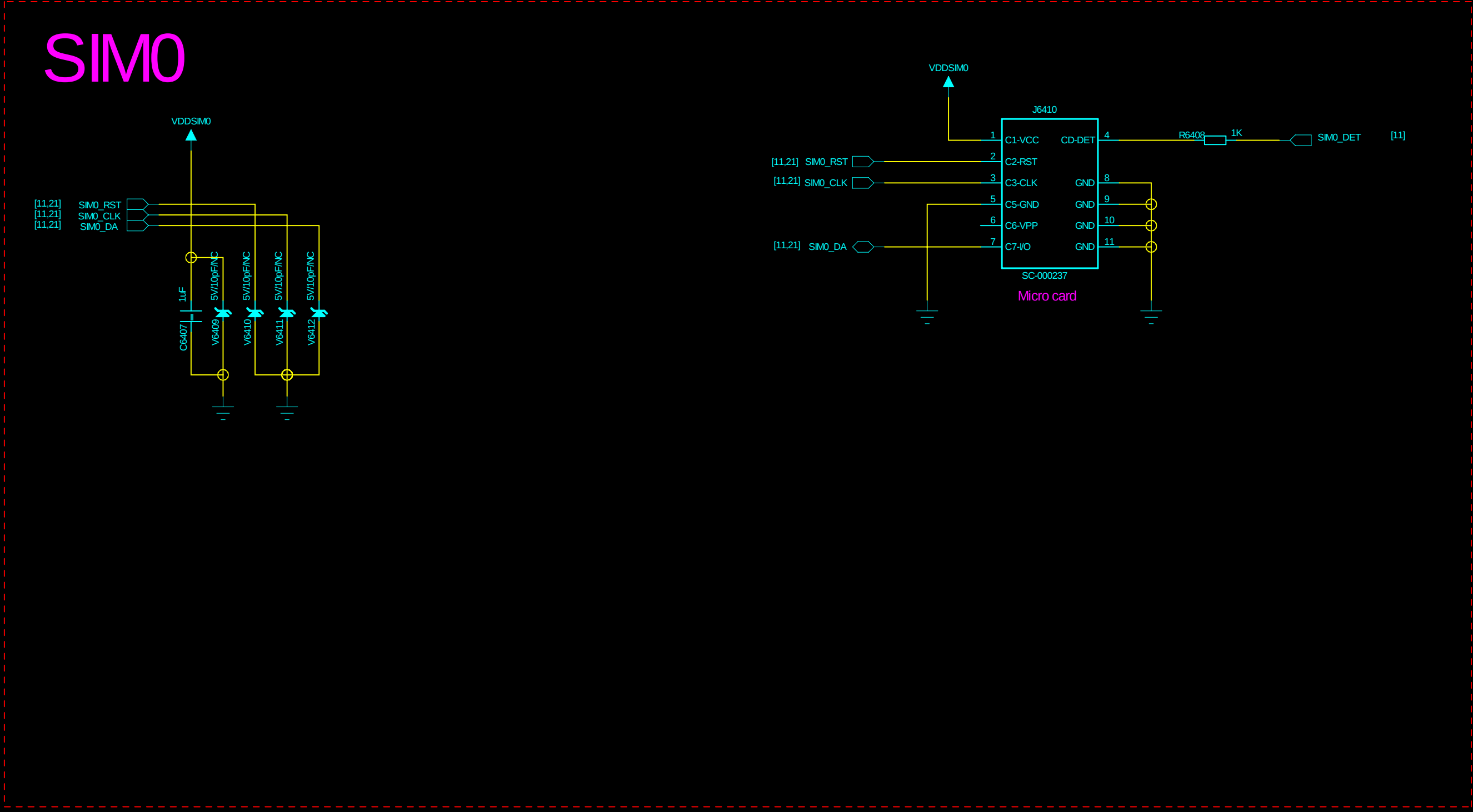


SharkL2 support two keys 7S reset ,also support single key(PBINT) 7S reset with SC2721.
Two keys Reset :EXTRSTN+PBINT

SIGN

TITLE & REV		
DOCUMENT NO.		
DESIGNER	DATED	DEPARTMENT
TEAM	DATED	Hardware DEPT.
APPROVED	DATED	COMPANY
Spreadtrum communications, Inc.		
CONFIDENTIAL AND PROPRIETARY		

REVISION/RECORD			
LTH	ECG NO.	APPROVED	DATE



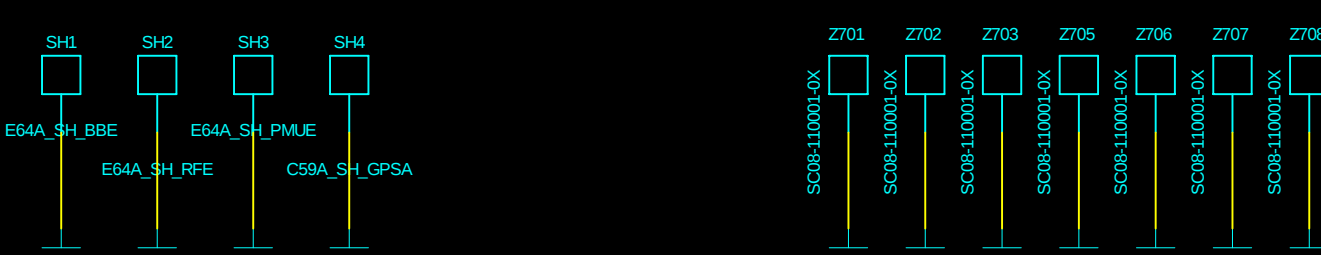
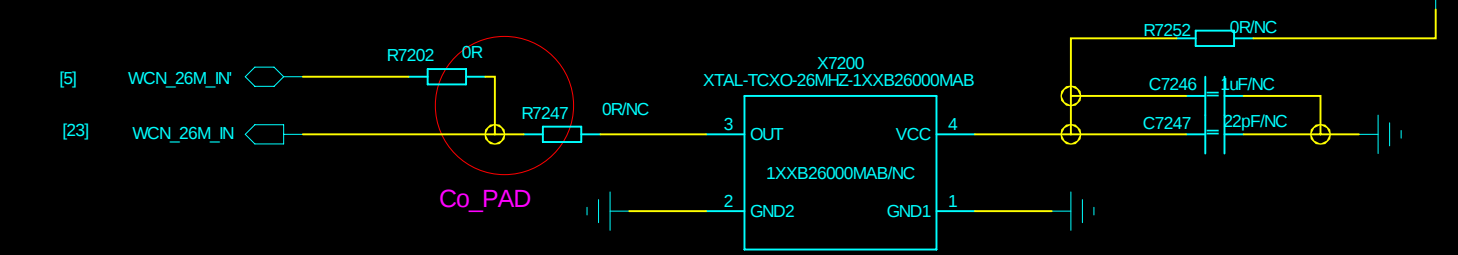
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EQA:		DATED:		DOCUMENT NO.:	
APPROVED:		DATED:		DEPARTMENT:	
				Hardware DEPT.	
				COMPANY:	
				Spreadtrum communications, Inc.	
				CONFIDENTIAL AND PROPRIETARY	

WIFI&BT&GPS ANT

2.4GW/FV/BT SAW

GPS SAW LNA

Strapping Pin



NC forever