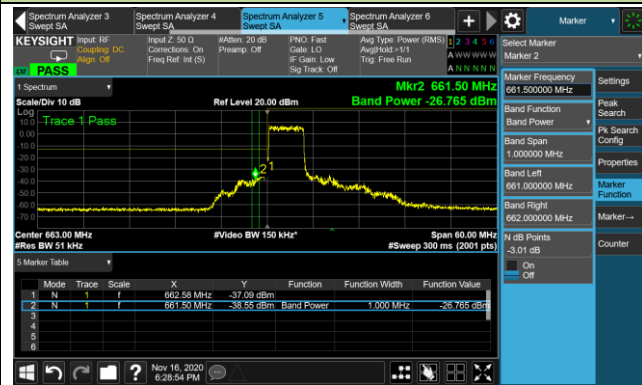
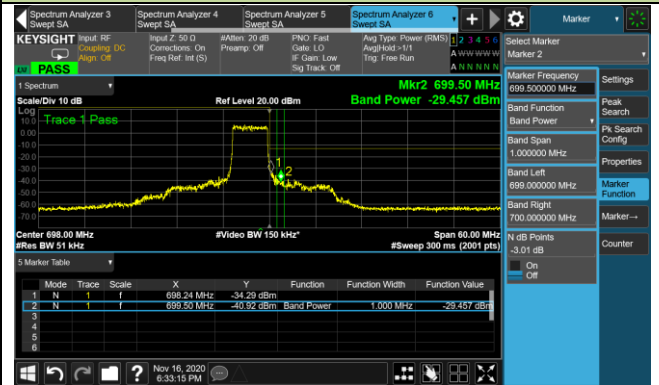


5MHz Channel Bandwidth - Full RB

Lower Band Edge

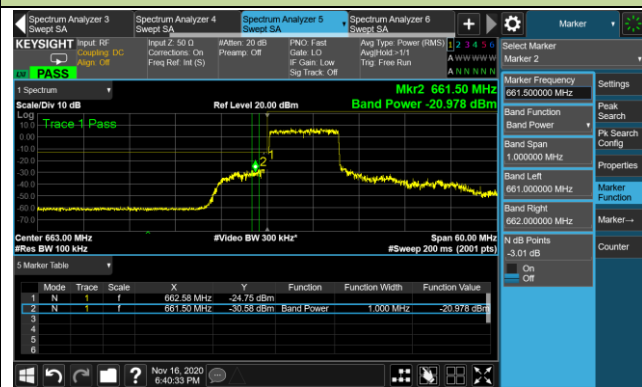


Upper Band Edge

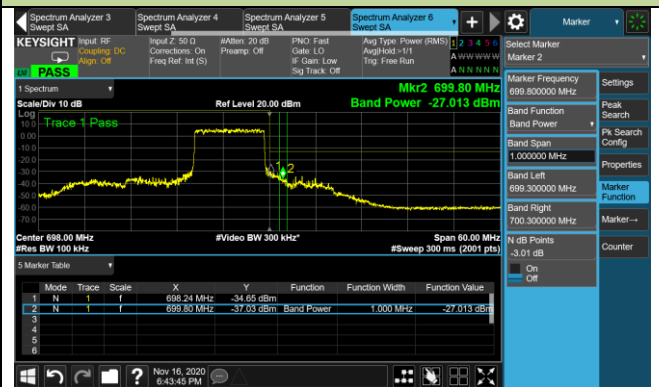


10MHz Channel Bandwidth - Full RB

Lower Band Edge

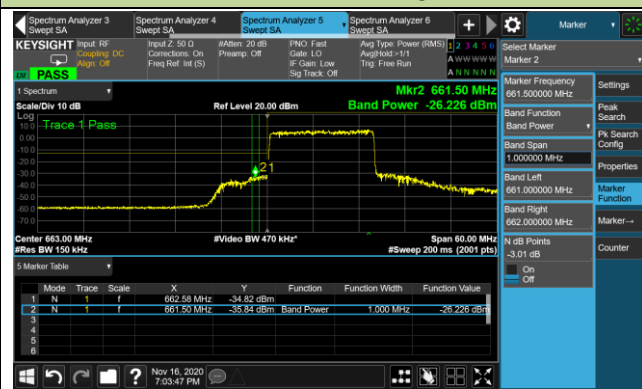


Upper Band Edge

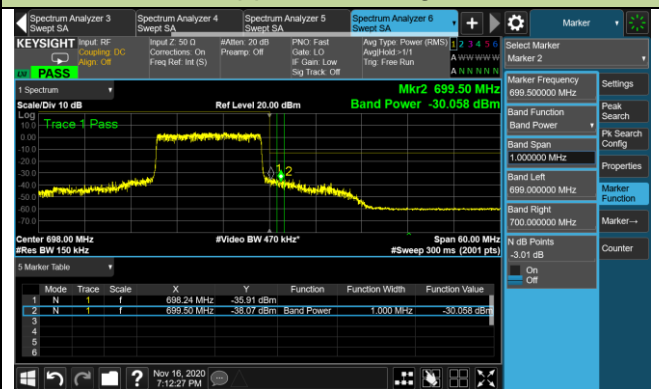


15MHz Channel Bandwidth - Full RB

Lower Band Edge

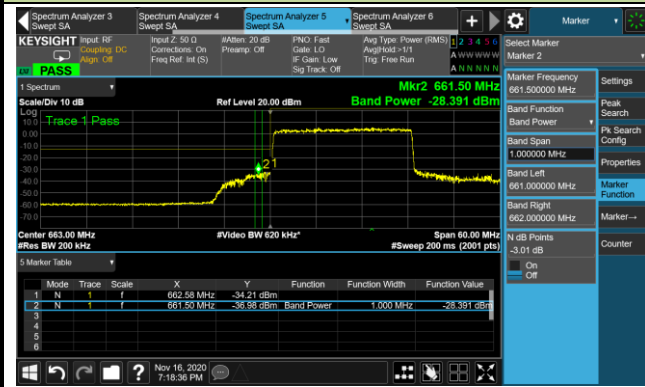


Upper Band Edge



20MHz Channel Bandwidth - Full RB

Lower Band Edge

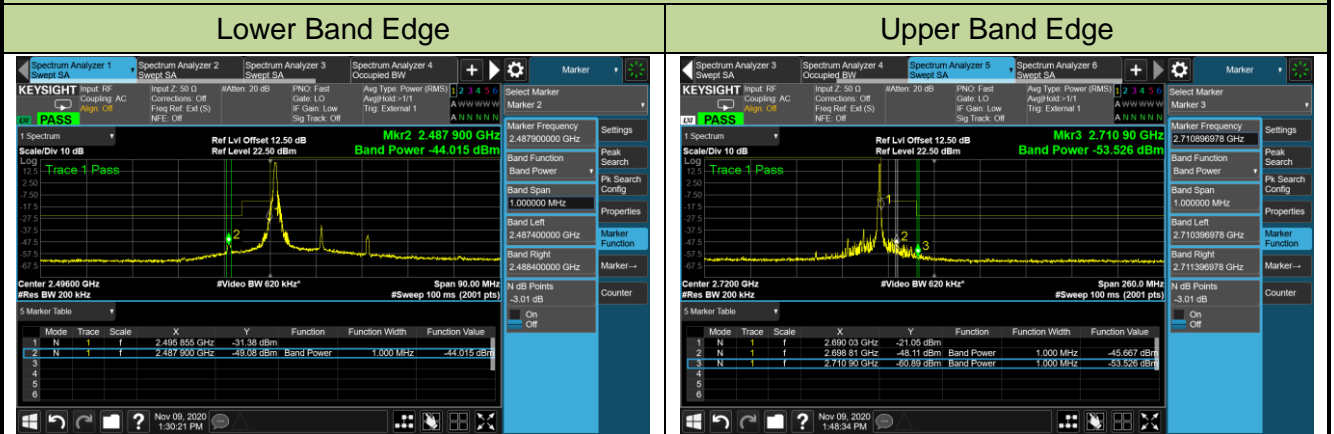


Upper Band Edge

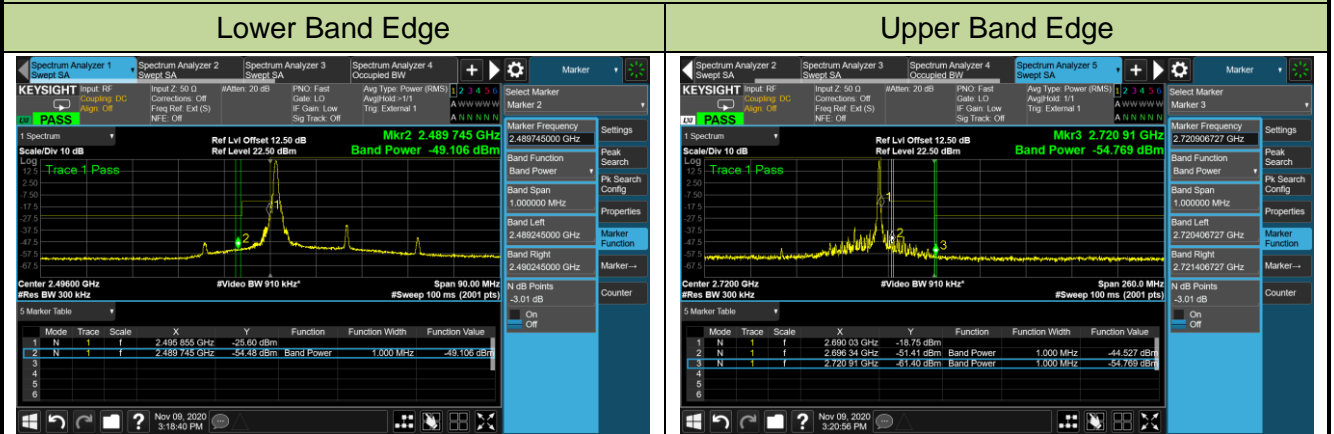


Product	5G Sub-6 GHz M.2 Module	Test Site	WZ-SR6
Test Engineer	Eric Xu	Test Date	2020/11/09
Test Band	n41_SA_HPUE	Test Result	Pass

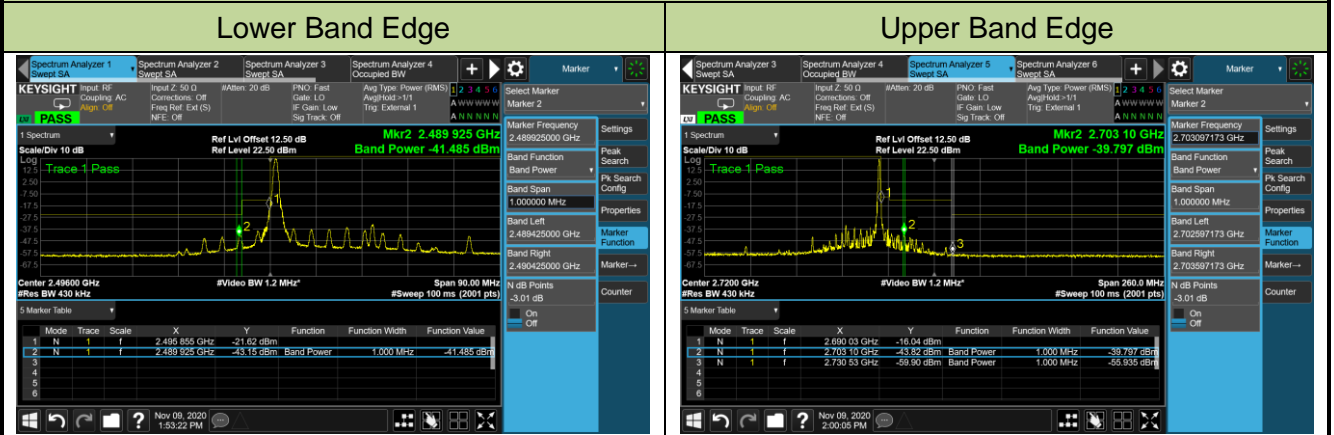
20MHz Channel Bandwidth - 1RB



30MHz Channel Bandwidth - 1RB

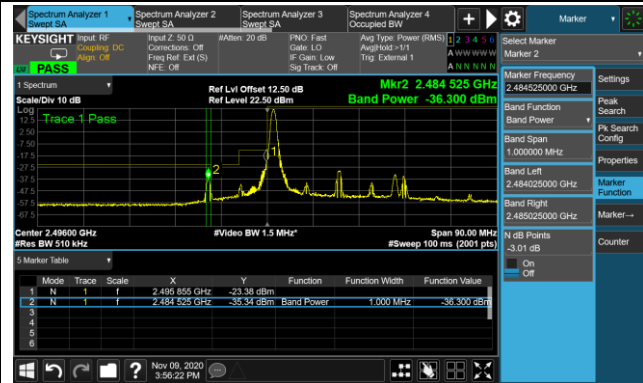


40MHz Channel Bandwidth - 1RB

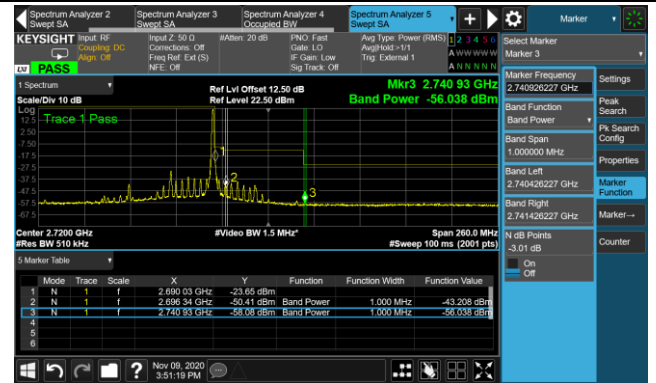


50MHz Channel Bandwidth - 1RB

Lower Band Edge

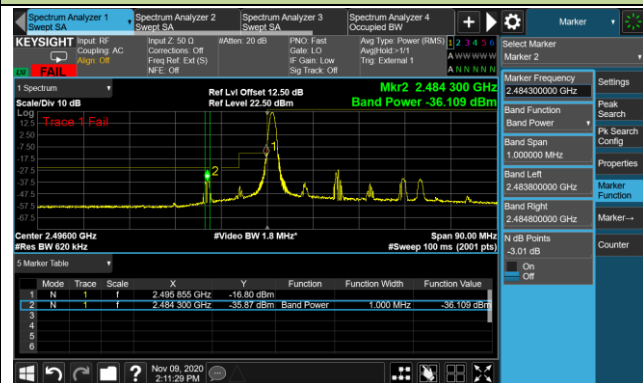


Upper Band Edge



60MHz Channel Bandwidth - 1RB

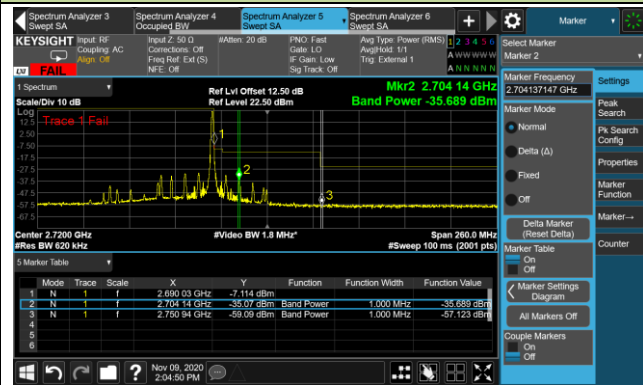
Lower Band Edge*



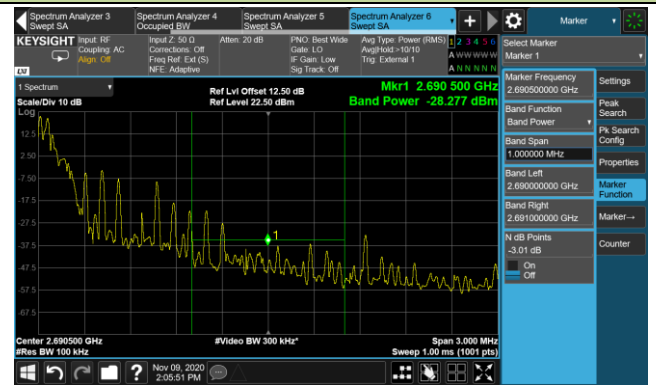
Channel Power < 13dBm Pass



Upper Band Edge*



Channel Power < 13dBm Pass

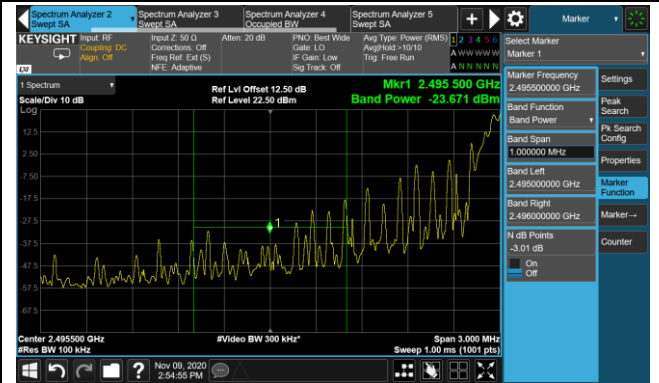


80MHz Channel Bandwidth - 1RB

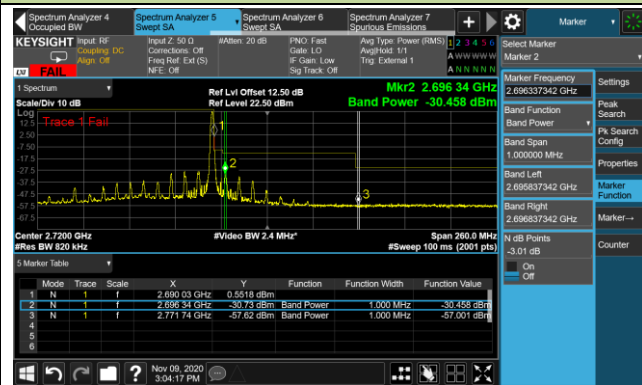
Lower Band Edge*



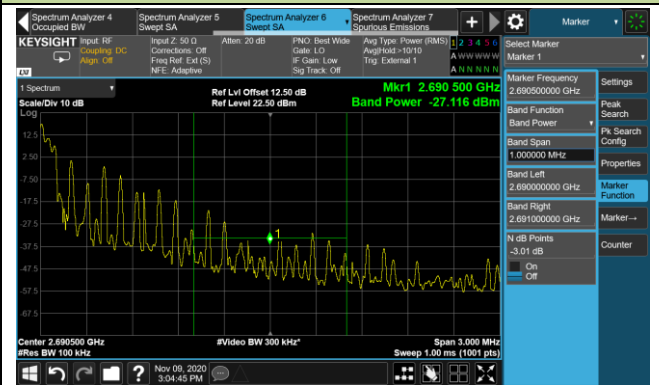
Channel Power < 13dBm Pass



Upper Band Edge*

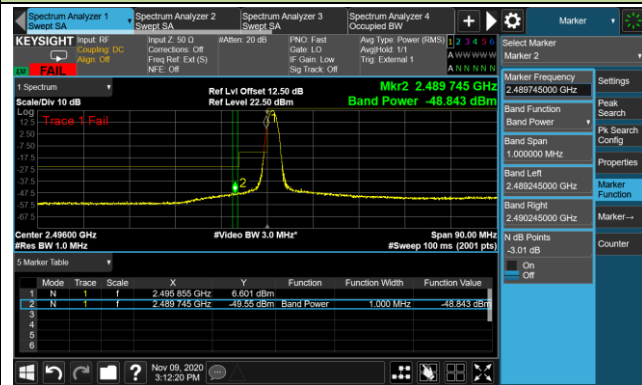


Channel Power < 13dBm Pass



100MHz Channel Bandwidth - 1RB

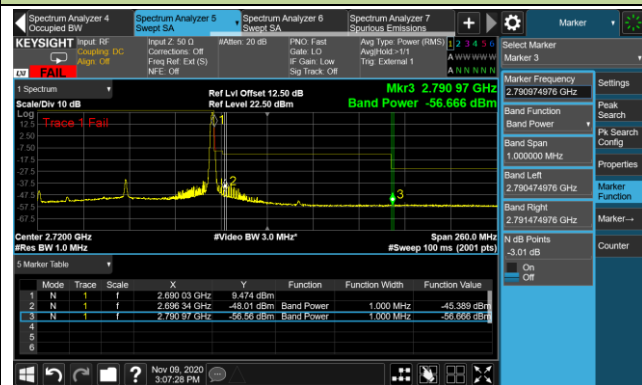
Lower Band Edge*



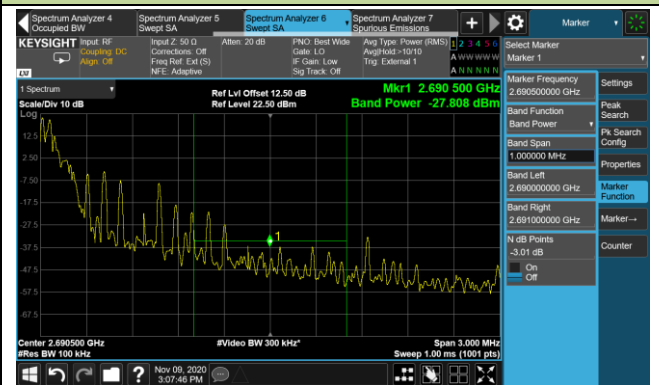
Channel Power < 13dBm Pass



Upper Band Edge*

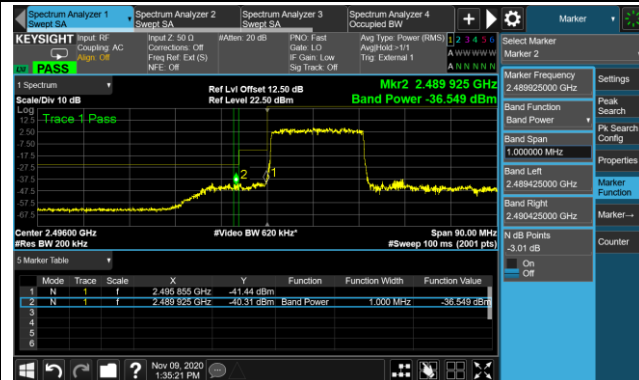


Channel Power < 13dBm Pass

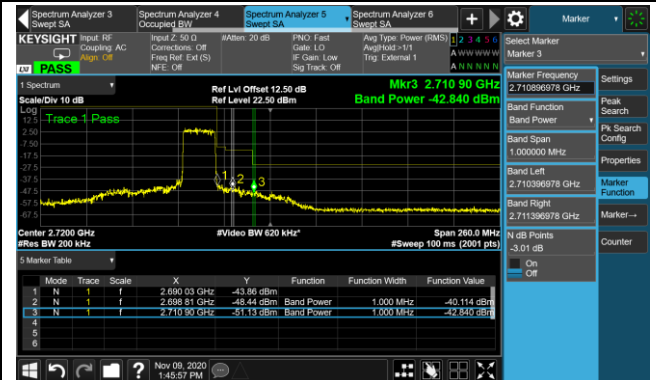


20MHz Channel Bandwidth - Full RB

Lower Band Edge



Upper Band Edge

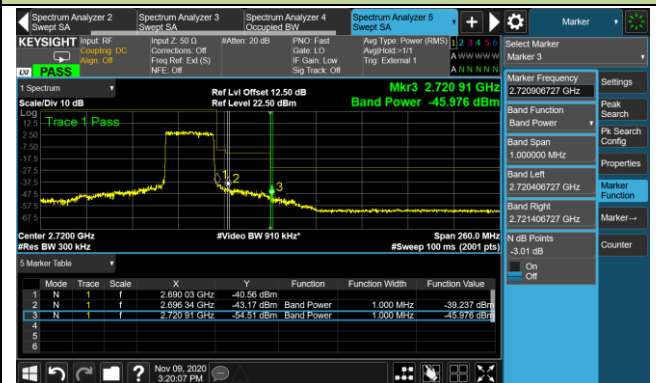


30MHz Channel Bandwidth - Full RB

Lower Band Edge

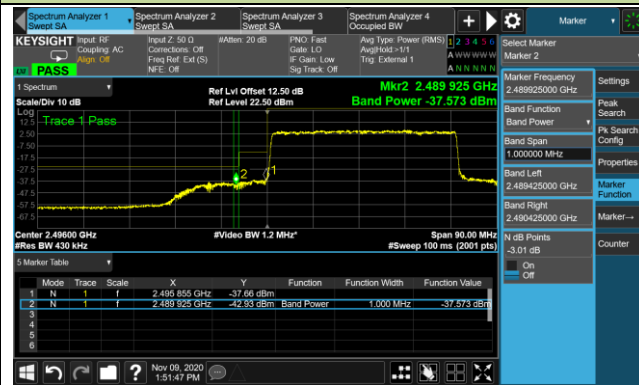


Upper Band Edge

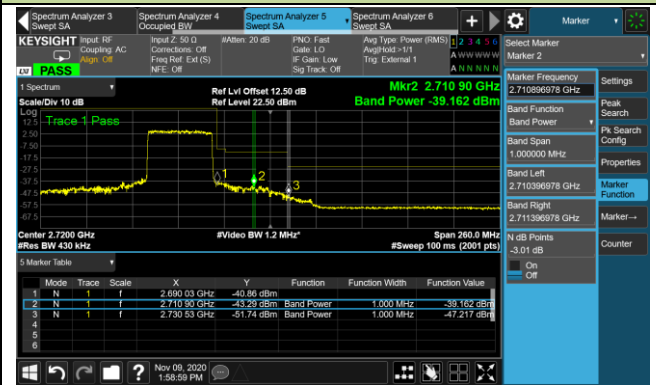


40MHz Channel Bandwidth - Full RB

Lower Band Edge

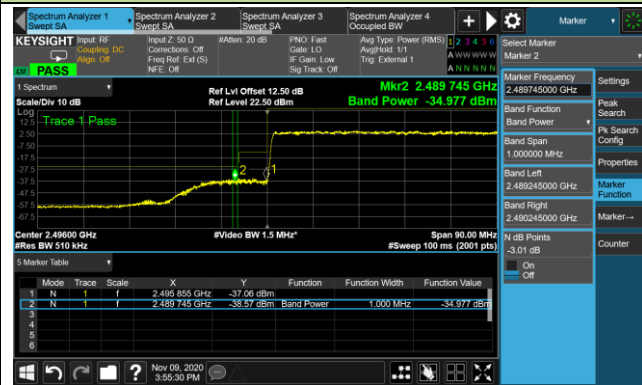


Upper Band Edge



50MHz Channel Bandwidth - Full RB

Lower Band Edge

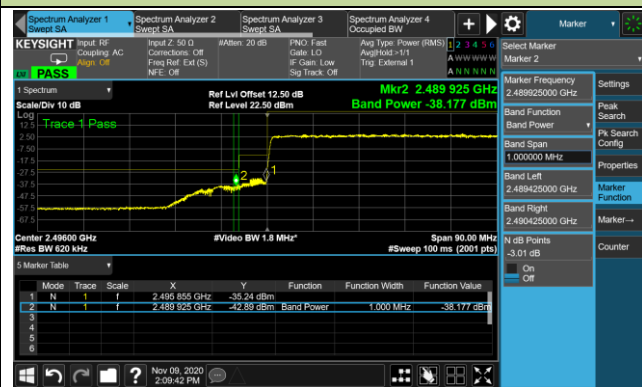


Upper Band Edge



60MHz Channel Bandwidth - Full RB

Lower Band Edge

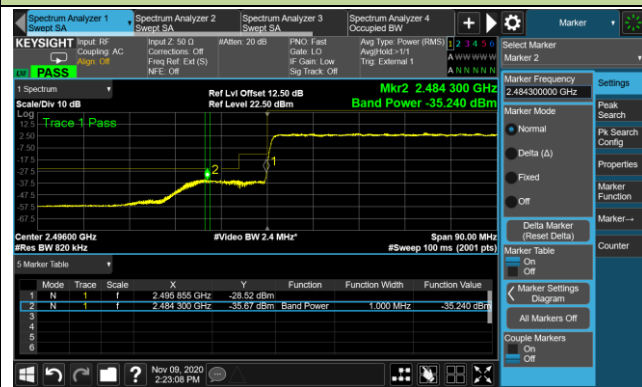


Upper Band Edge



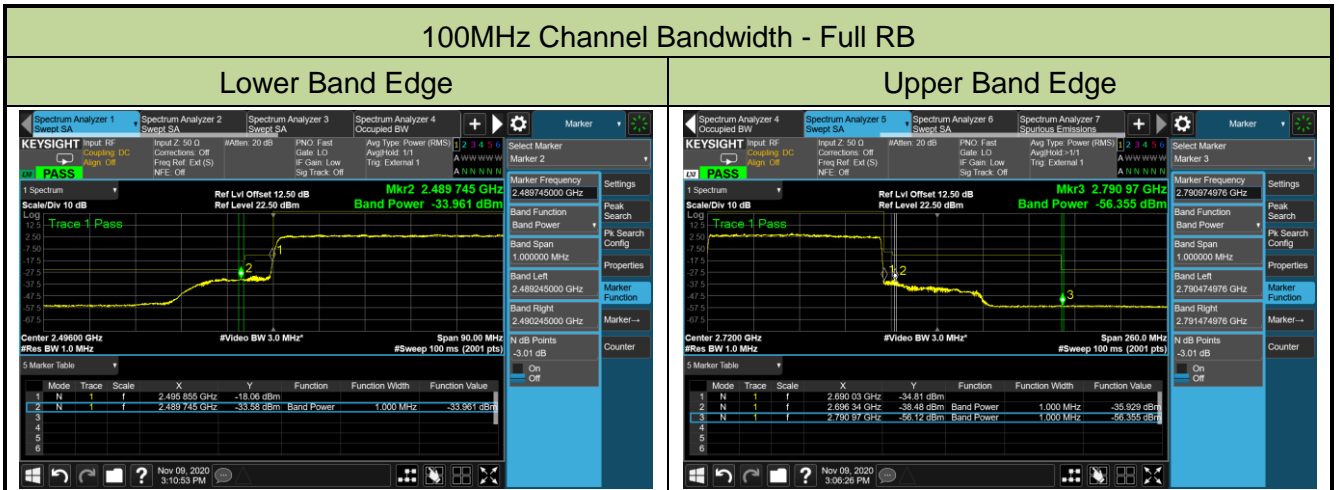
80MHz Channel Bandwidth - Full RB

Lower Band Edge



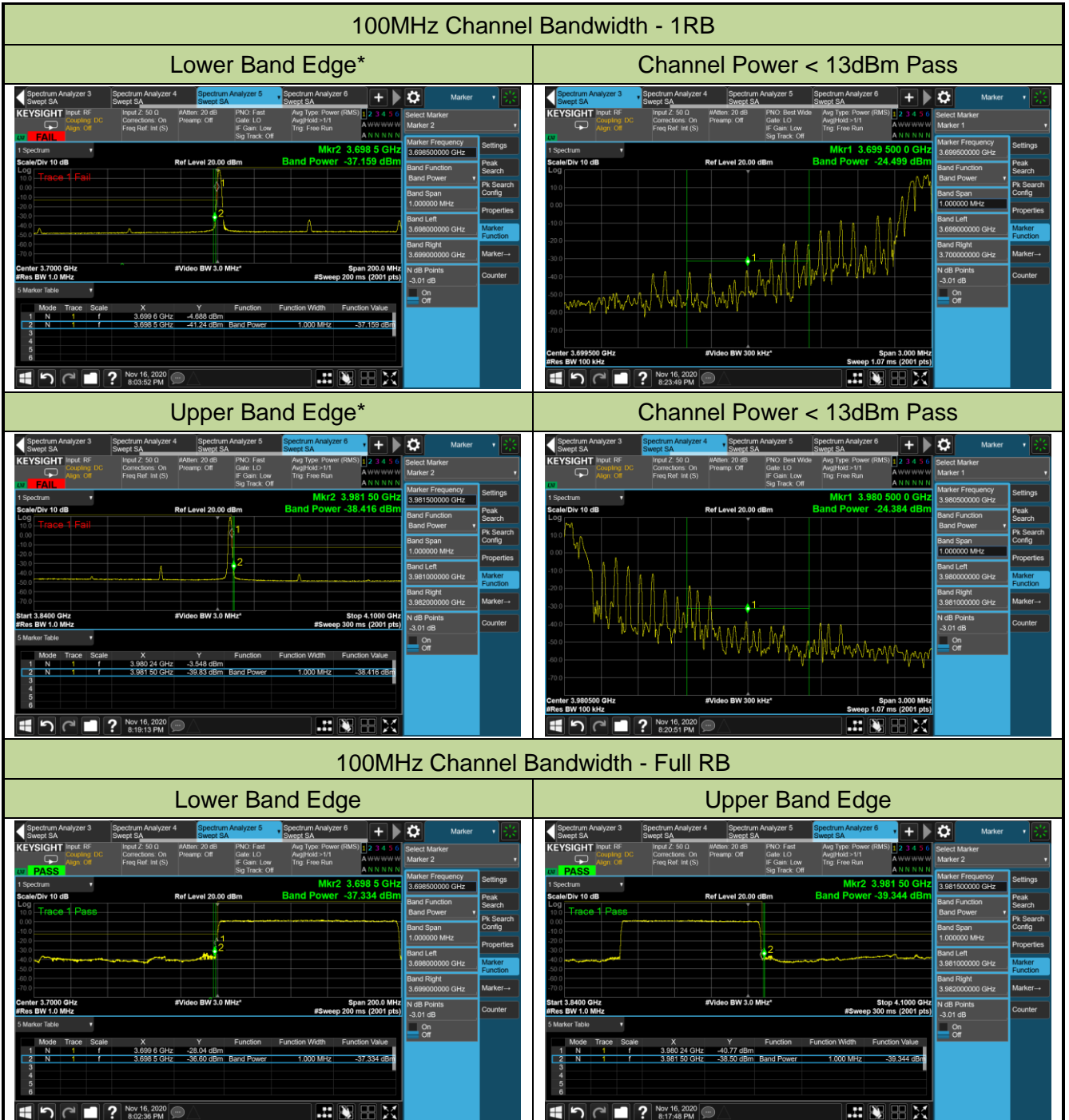
Upper Band Edge





Note: “*” means that the fail frequency has been verified by the plot of “Channel Power < 13dBm Pass”

Product	5G Sub-6 GHz M.2 Module	Test Site	WZ-SR6
Test Engineer	Eric Xu	Test Date	2020/10/28
Test Band	n77_SA_HPUE	Test Result	Pass

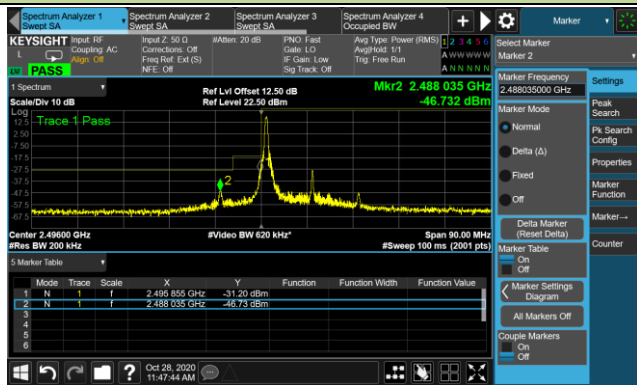


Note: “*” means that the fail frequency has been verified by the plot of “Channel Power < 13dBm Pass”

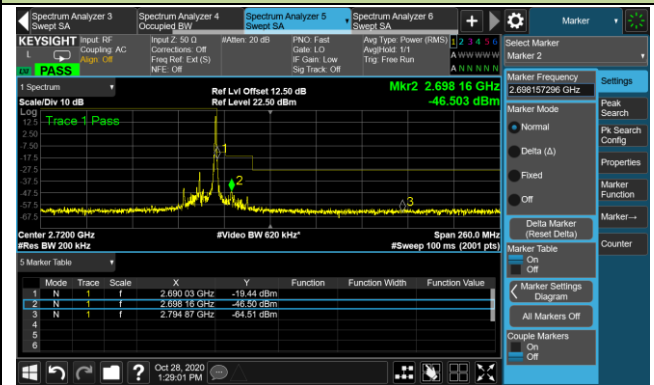
Product	5G Sub-6 GHz M.2 Module	Test Site	WZ-SR6
Test Engineer	Eric Xu	Test Date	2020/10/27 ~ 2020/10/30
Test Band	n41_SA_HPUE_MIMO (Port 0)	Test Result	Pass

20MHz Channel Bandwidth - 1RB

Lower Band Edge

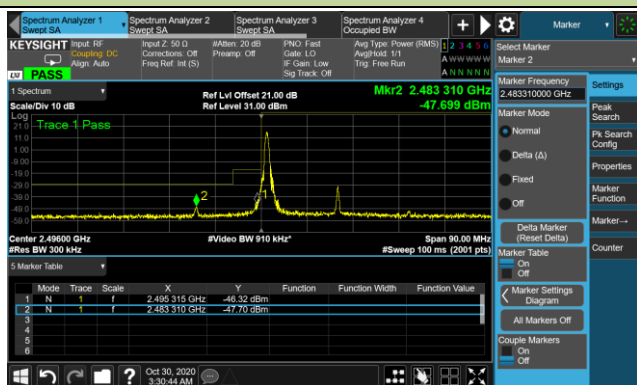


Upper Band Edge

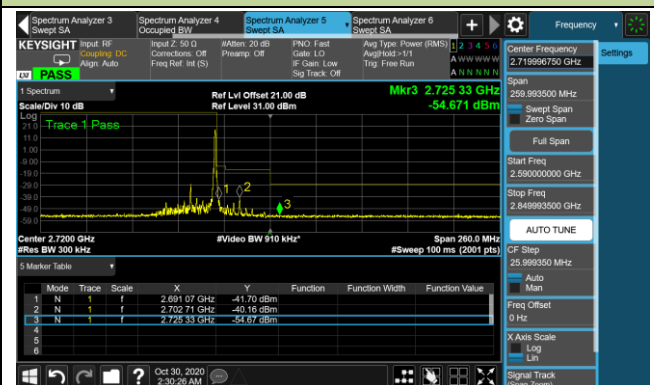


30MHz Channel Bandwidth - 1RB

Lower Band Edge

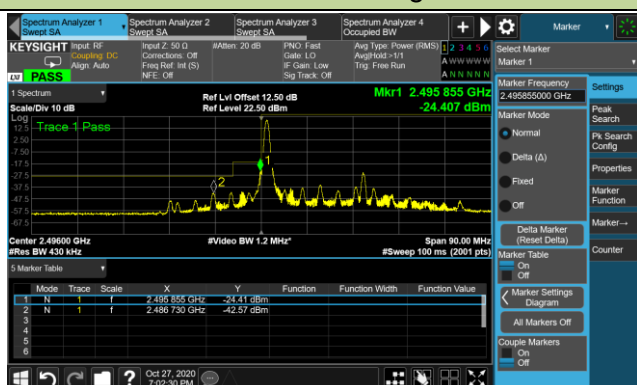


Upper Band Edge



40MHz Channel Bandwidth - 1RB

Lower Band Edge



Upper Band Edge

