

Appendix for 5A_N41A

Product Name: Zenius08

Model No: GMT-08QA-2301

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.82	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.66	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	24.18	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	24.25	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	23.72	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	23.48	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	24.16	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Inner_Full	24.19	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.78	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.22	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.39	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	24.37	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	23.67	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	24.04	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	24.54	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Inner_Full	24.34	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.89	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	25.07	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	25.40	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	25.47	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	24.89	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	24.94	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	24.77	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Inner_Full	25.37	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.73	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.97	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	24.25	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	24.28	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	23.69	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	23.85	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	24.36	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Inner_Full	24.24	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.72	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.07	PC3	PASS

DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.52	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	24.38	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	23.62	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	24.09	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	24.41	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Inner_Full	24.38	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.24	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	24.77	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	25.12	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.17	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	24.24	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	24.95	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	25.18	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Inner_Full	25.25	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.63	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.45	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.22	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	24.35	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	23.49	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	23.20	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	24.19	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Inner_Full	24.29	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.77	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.95	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	24.50	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	24.52	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	23.55	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	23.86	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	24.39	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Inner_Full	24.48	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.32	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	24.97	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	25.21	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	25.21	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	24.14	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	24.96	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	25.21	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Inner_Full	25.26	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.30	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.62	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	24.21	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	24.17	PC3	PASS

DC_5A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	23.31	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	23.52	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	24.14	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Inner_Full	23.98	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.27	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.23	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	24.24	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	24.27	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	23.06	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	24.29	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	24.29	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Inner_Full	24.33	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.47	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.65	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	25.53	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	24.74	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	23.29	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	24.69	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	24.98	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Inner_Full	24.78	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

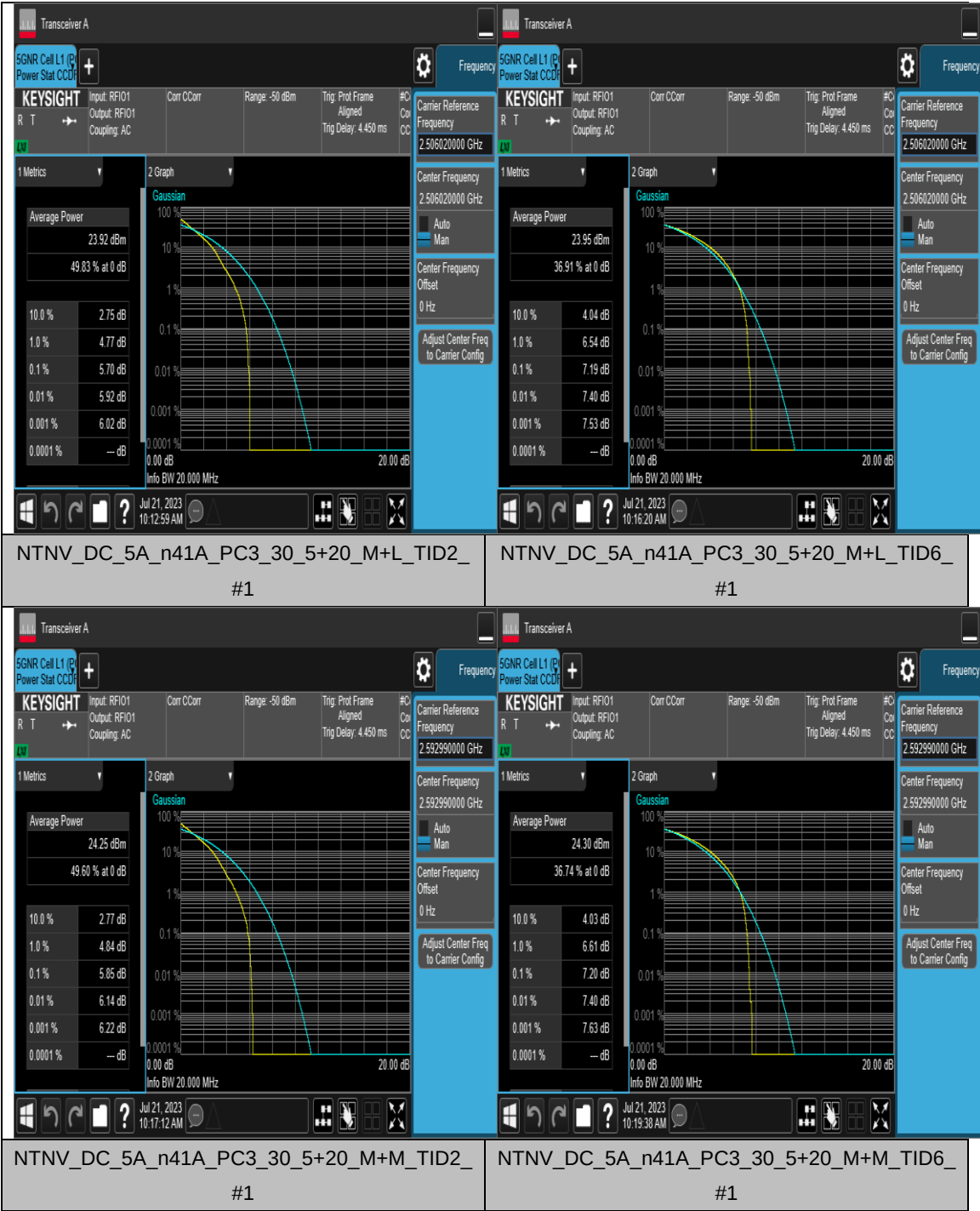
Peak-to-Average Ratio(CCDF)

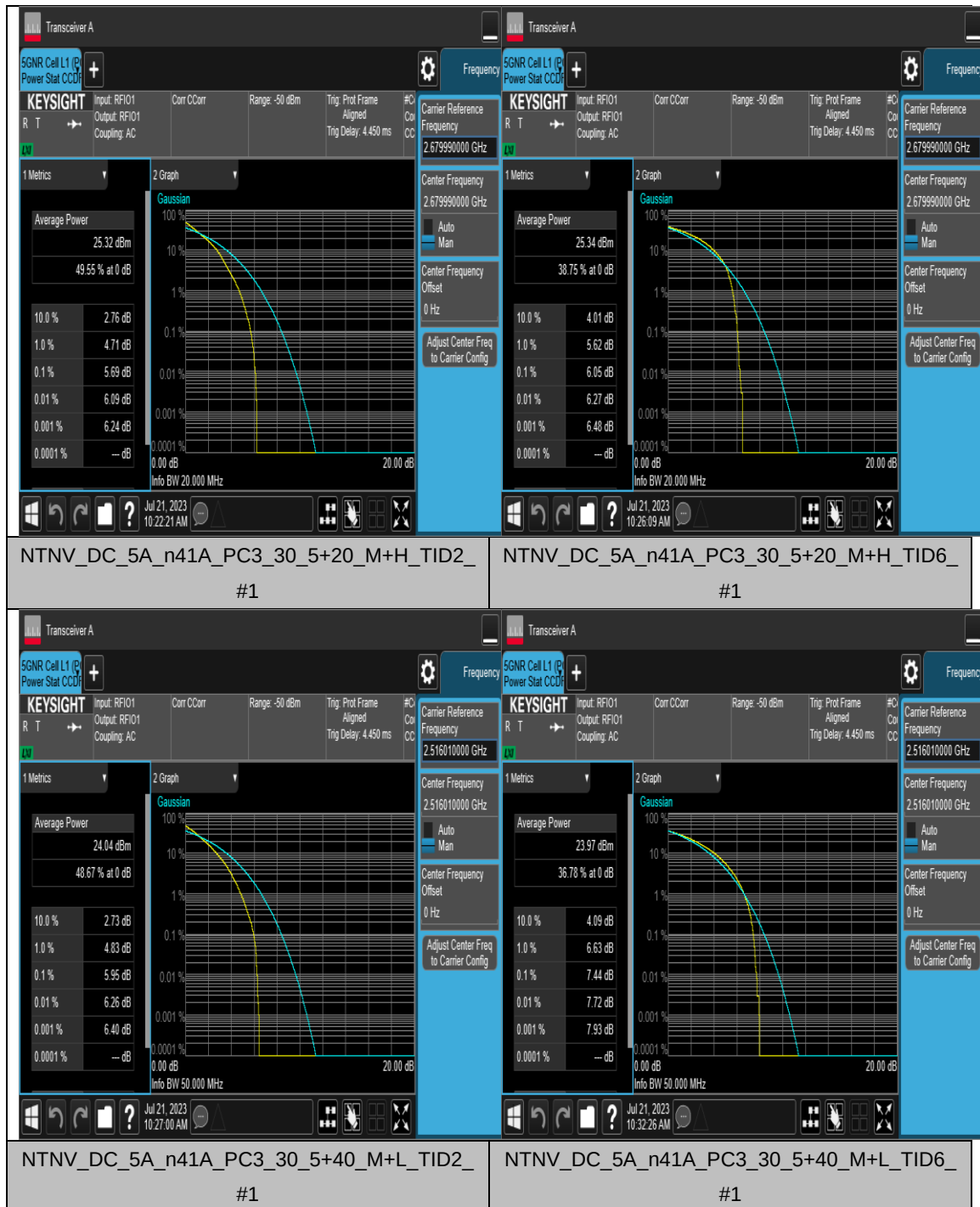
Test Result

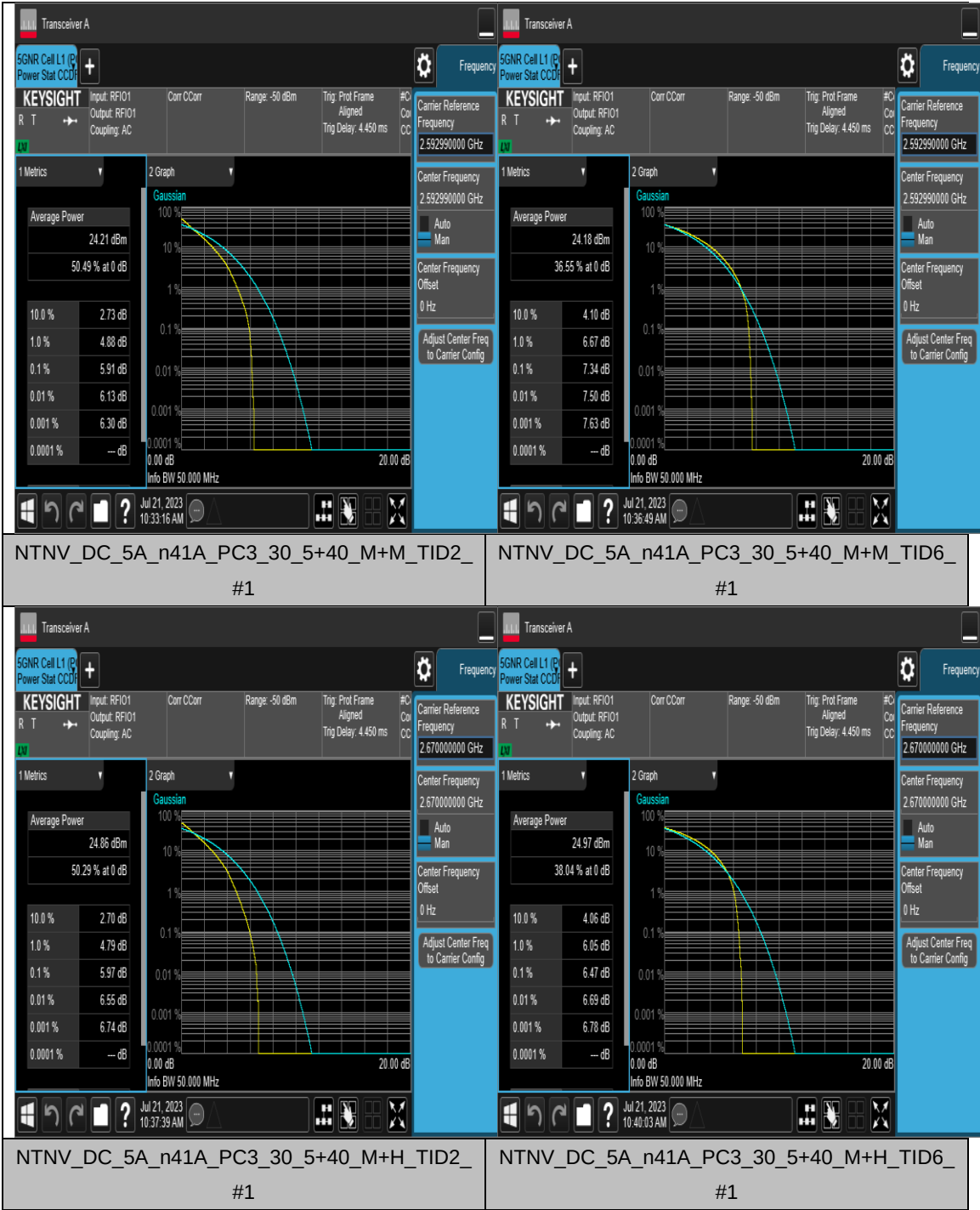
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	5.70	≤13	PASS
DC_5A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	7.19	≤13	PASS
DC_5A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.85	≤13	PASS
DC_5A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	7.20	≤13	PASS
DC_5A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	5.69	≤13	PASS
DC_5A_n4 1A	30	5+20	CP-QPSK	M+H	Outer_Full	6.05	≤13	PASS
DC_5A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	5.95	≤13	PASS
DC_5A_n4 1A	30	5+40	CP-QPSK	M+L	Outer_Full	7.44	≤13	PASS
DC_5A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.91	≤13	PASS
DC_5A_n4 1A	30	5+40	CP-QPSK	M+M	Outer_Full	7.34	≤13	PASS
DC_5A_n4 1A	30	5+40	DFT-QPSK	M+H	Outer_Full	5.97	≤13	PASS
DC_5A_n4 1A	30	5+40	CP-QPSK	M+H	Outer_Full	6.47	≤13	PASS
DC_5A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.68	≤13	PASS
DC_5A_n4 1A	30	5+60	CP-QPSK	M+L	Outer_Full	7.26	≤13	PASS
DC_5A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.95	≤13	PASS
DC_5A_n4 1A	30	5+60	CP-QPSK	M+M	Outer_Full	7.26	≤13	PASS
DC_5A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	5.74	≤13	PASS

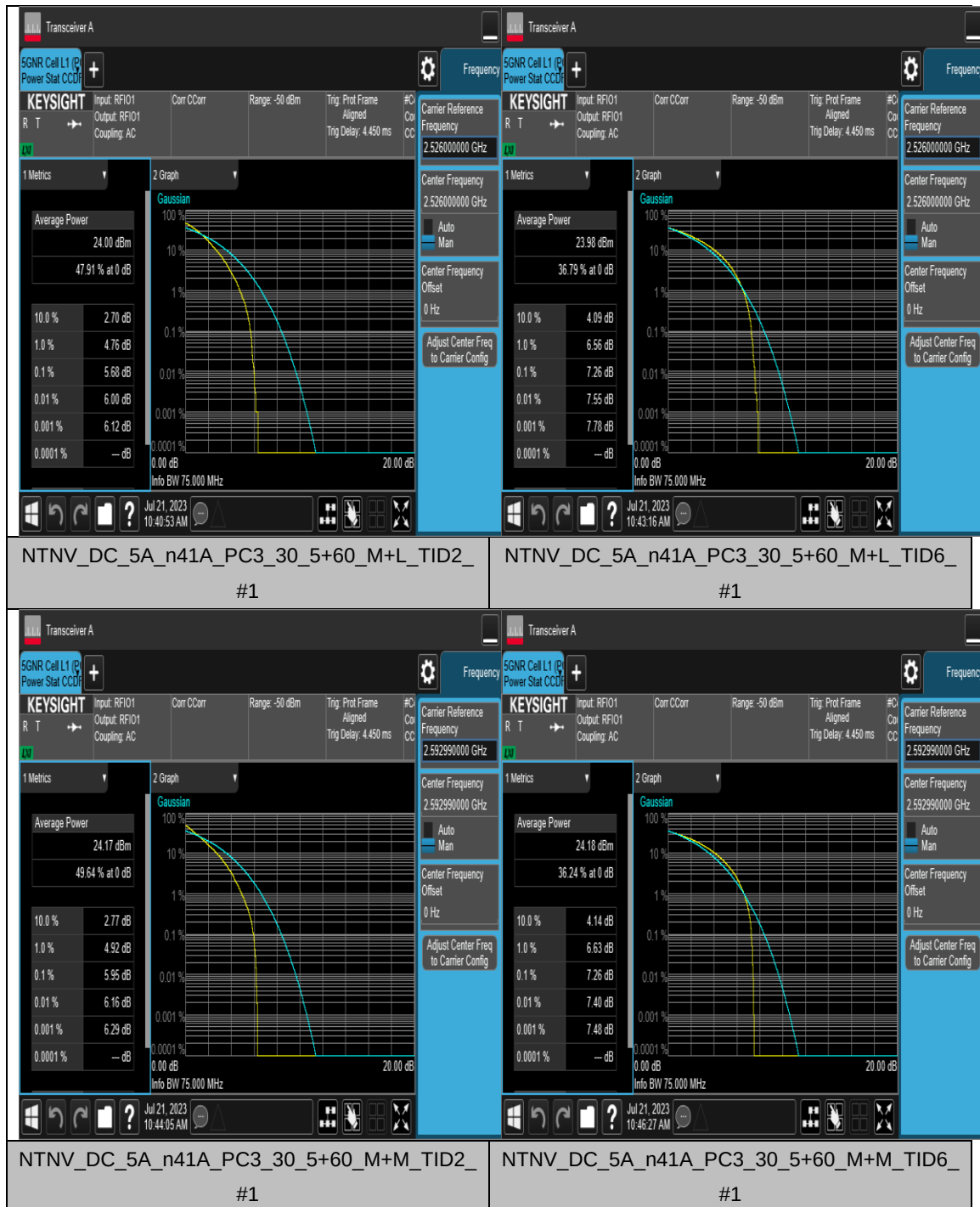
DC_5A_n4 1A	30	5+60	CP-QPSK	M+H	Outer_Full	6.50	≤13	PASS
DC_5A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.89	≤13	PASS
DC_5A_n4 1A	30	5+100	CP-QPSK	M+L	Outer_Full	7.72	≤13	PASS
DC_5A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	6.13	≤13	PASS
DC_5A_n4 1A	30	5+100	CP-QPSK	M+M	Outer_Full	7.53	≤13	PASS
DC_5A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	5.92	≤13	PASS
DC_5A_n4 1A	30	5+100	CP-QPSK	M+H	Outer_Full	7.33	≤13	PASS

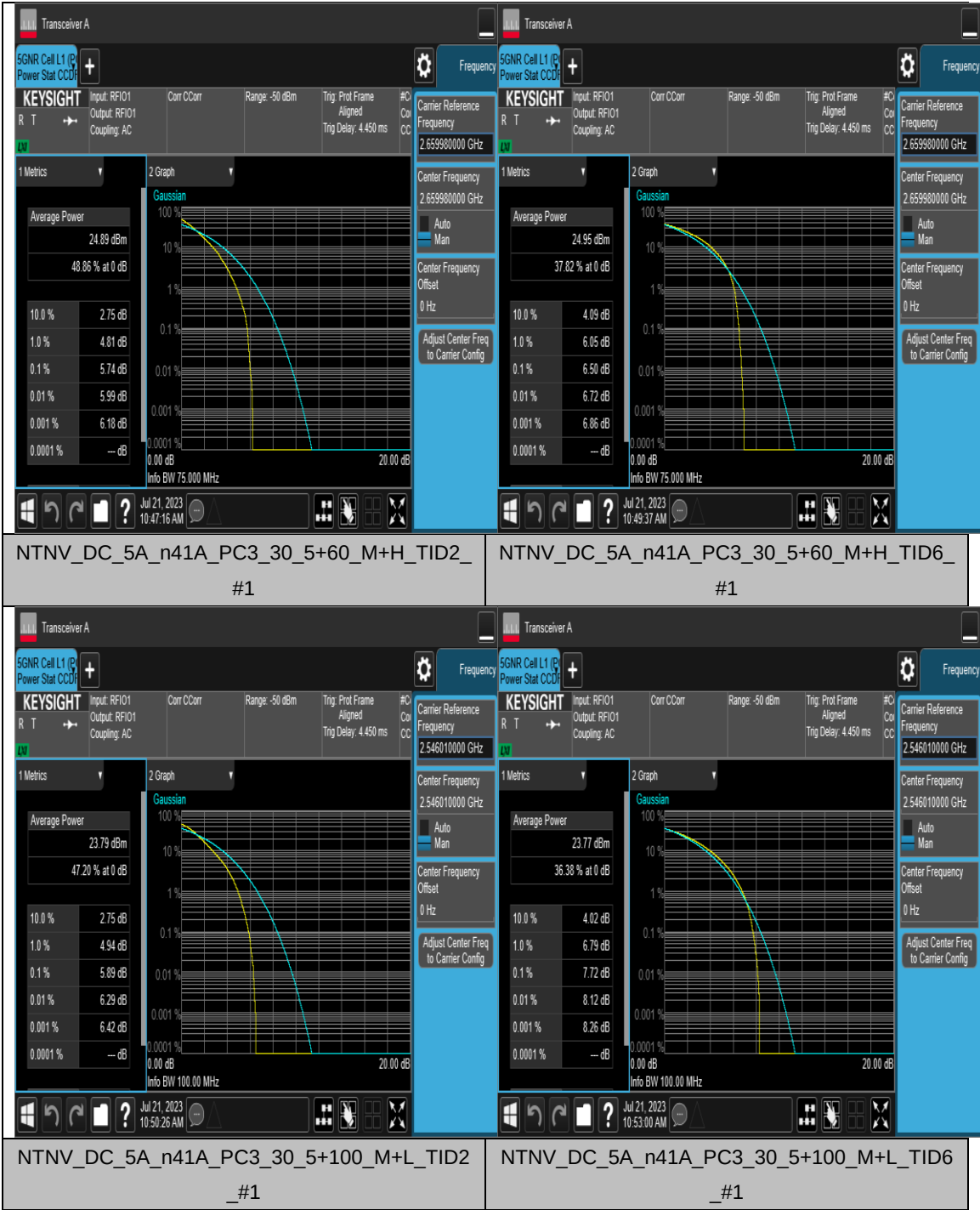
Test Graphs

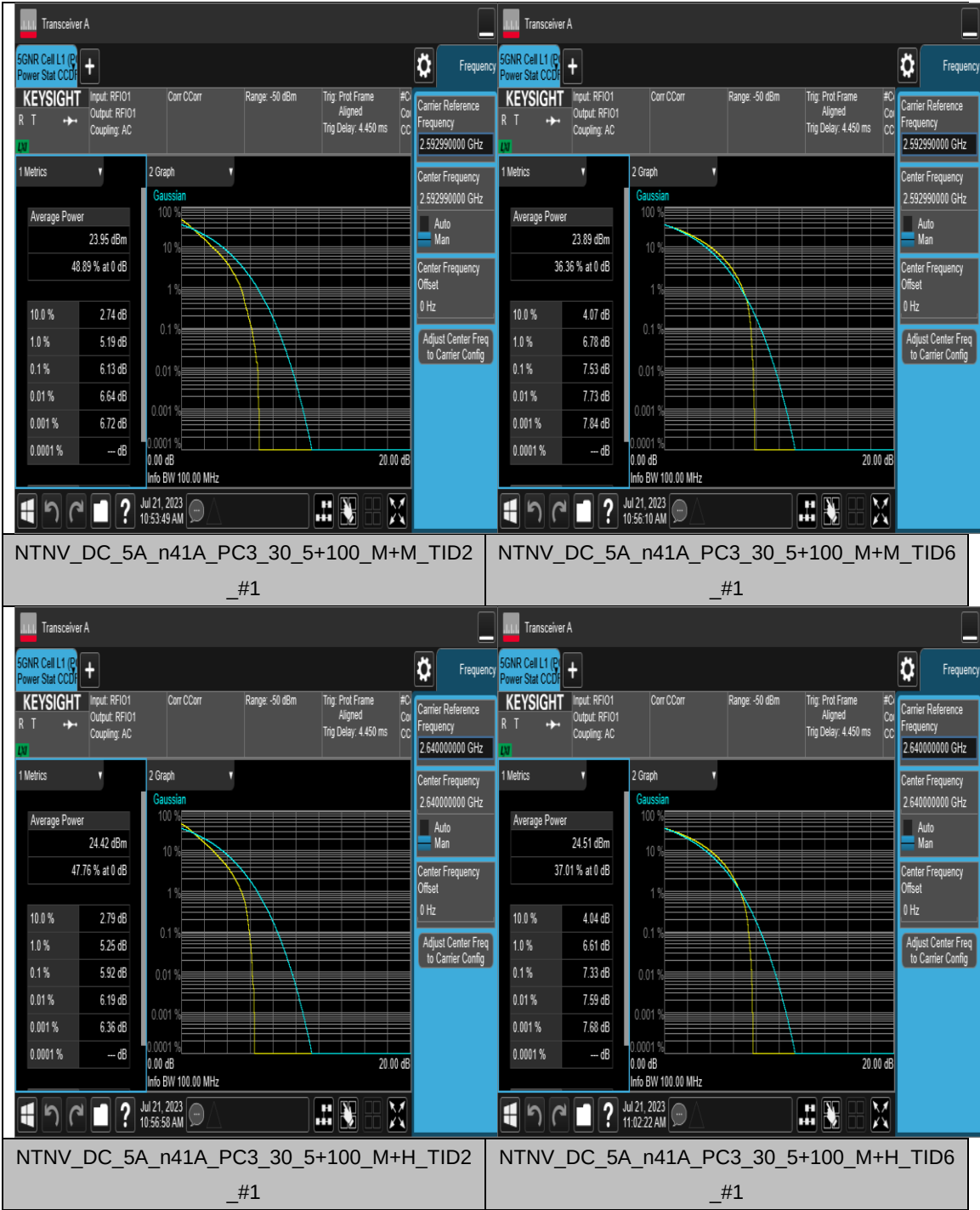












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	17.858	18.94	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18.233	20.10	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	17.900	19.09	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18.230	19.59	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	17.870	19.08	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18.471	38.88	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	26.779	28.22	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+L	Outer_Full	27.806	29.28	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	26.811	28.23	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+M	Outer_Full	27.846	29.34	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	26.957	53.77	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+H	Outer_Full	27.997	54.52	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	35.681	37.41	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	37.723	39.54	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	35.747	37.19	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	37.846	39.60	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	35.711	37.37	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	37.903	70.06	PASS
DC_5A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	45.646	47.44	PASS
DC_5A_n41A	30	5+50	CP-QPSK	M+L	Outer_Full	47.352	49.22	PASS
DC_5A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	45.560	47.40	PASS
DC_5A_n41A	30	5+50	CP-QPSK	M+M	Outer_Full	47.486	49.44	PASS
DC_5A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	45.590	47.38	PASS
DC_5A_n41A	30	5+50	CP-QPSK	M+H	Outer_Full	47.561	92.07	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	57.690	60.16	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	57.667	59.87	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	57.887	60.19	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	57.858	60.21	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	57.803	60.15	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	57.945	82.62	PASS
DC_5A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	63.960	66.45	PASS
DC_5A_n41A	30	5+70	CP-QPSK	M+L	Outer_Full	67.355	69.77	PASS
DC_5A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	64.116	66.49	PASS

DC_5A_n41A	30	5+70	CP-QPSK	M+M	Outer_Full	67.401	69.74	PASS
DC_5A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	63.992	66.53	PASS
DC_5A_n41A	30	5+70	CP-QPSK	M+H	Outer_Full	67.369	96.31	PASS
DC_5A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	76.804	79.71	PASS
DC_5A_n41A	30	5+80	CP-QPSK	M+L	Outer_Full	77.181	79.93	PASS
DC_5A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	77.034	79.97	PASS
DC_5A_n41A	30	5+80	CP-QPSK	M+M	Outer_Full	77.210	80.07	PASS
DC_5A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	76.892	79.62	PASS
DC_5A_n41A	30	5+80	CP-QPSK	M+H	Outer_Full	77.330	86.35	PASS
DC_5A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	86.371	89.54	PASS
DC_5A_n41A	30	5+90	CP-QPSK	M+L	Outer_Full	87.121	90.39	PASS
DC_5A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	86.578	89.61	PASS
DC_5A_n41A	30	5+90	CP-QPSK	M+M	Outer_Full	87.378	90.33	PASS
DC_5A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	86.323	89.51	PASS
DC_5A_n41A	30	5+90	CP-QPSK	M+H	Outer_Full	87.231	160.6	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	95.872	99.35	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	97.133	100.6	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	96.276	99.50	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	97.511	100.7	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	95.913	99.48	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	97.409	100.7	PASS

Test Graphs

