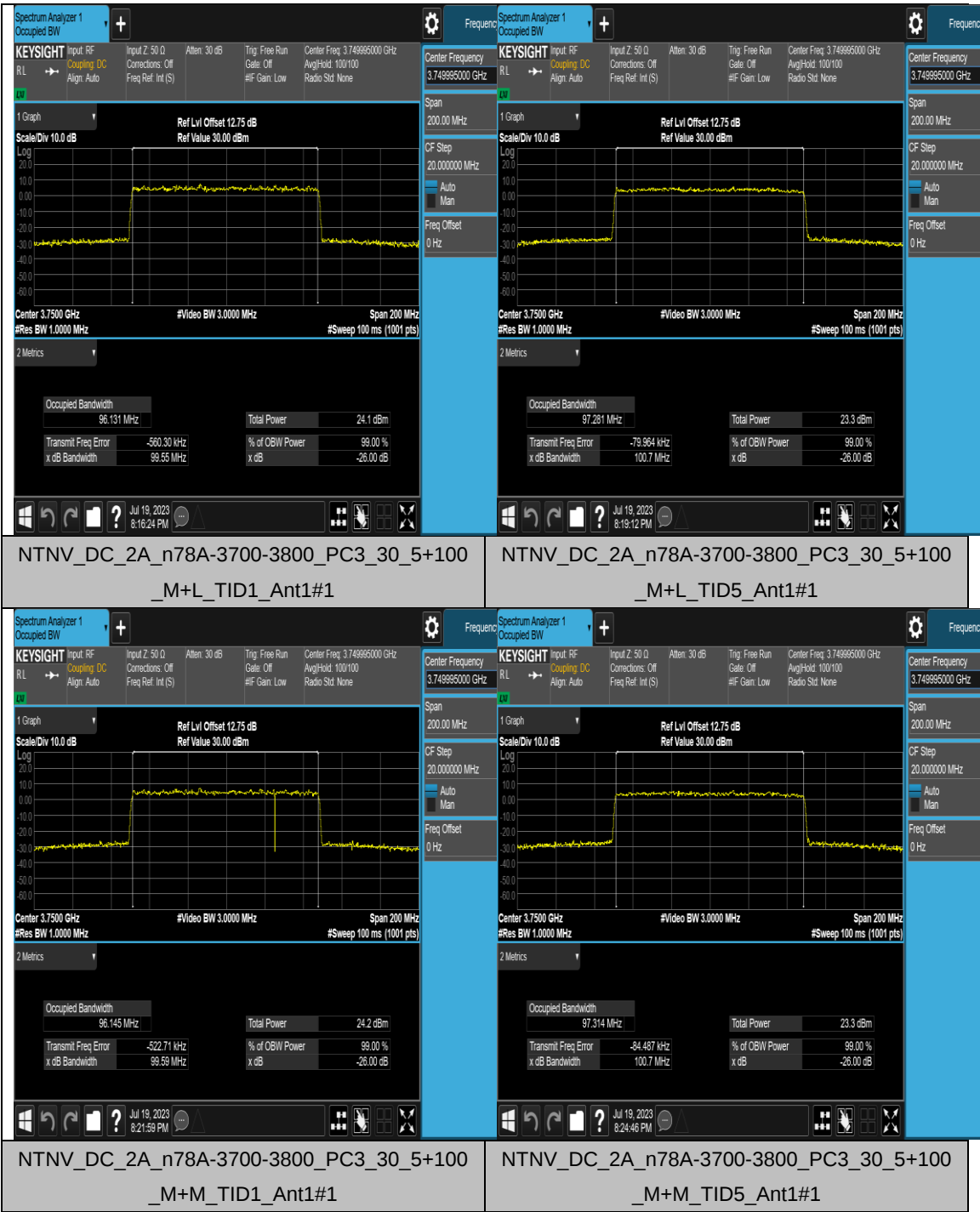
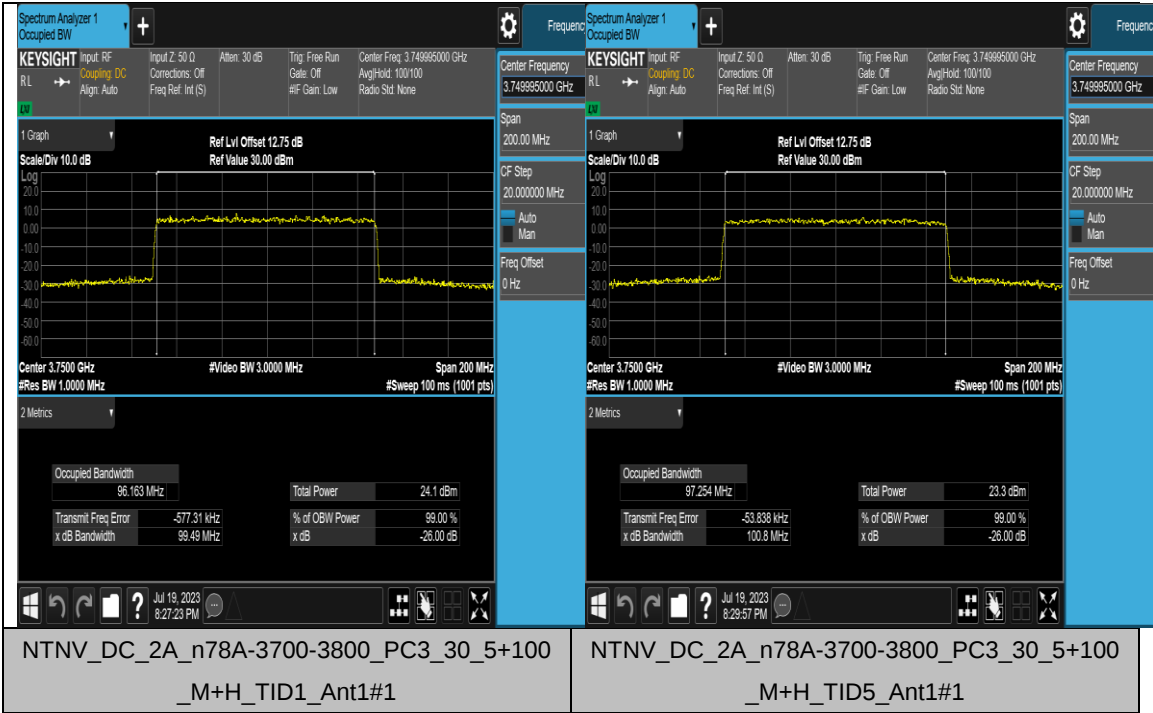






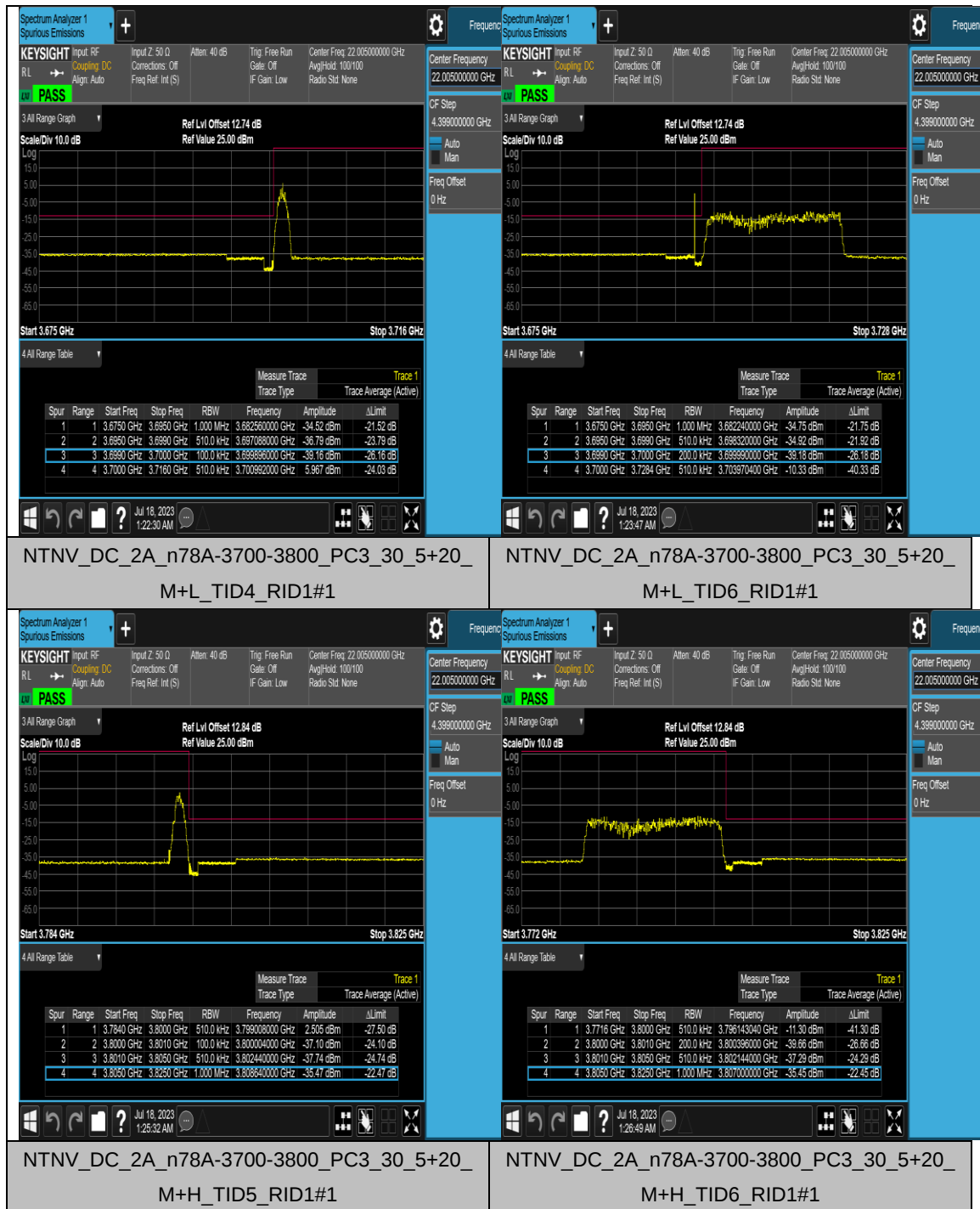
Appendix D: Band Edge for NSA

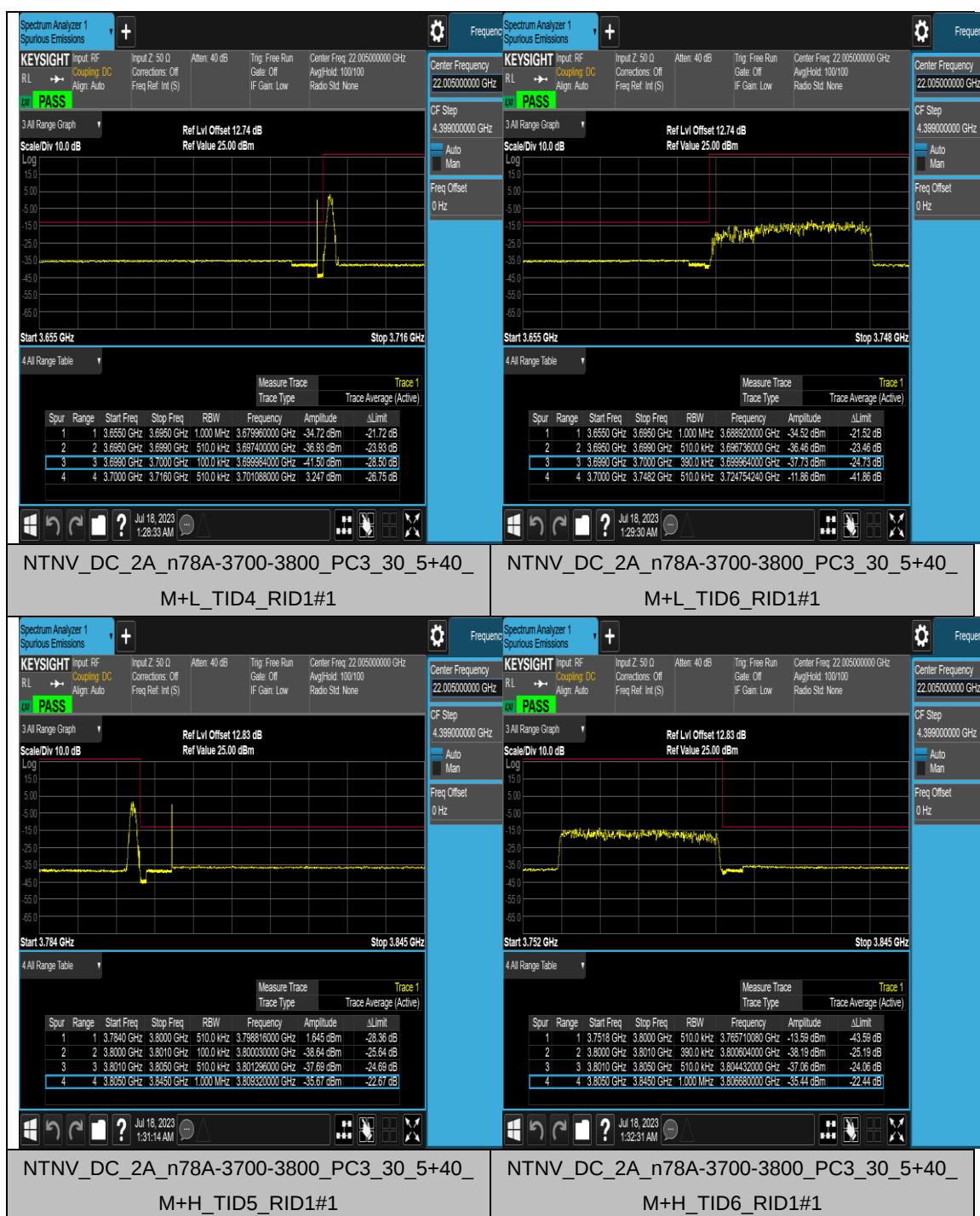
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS

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DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

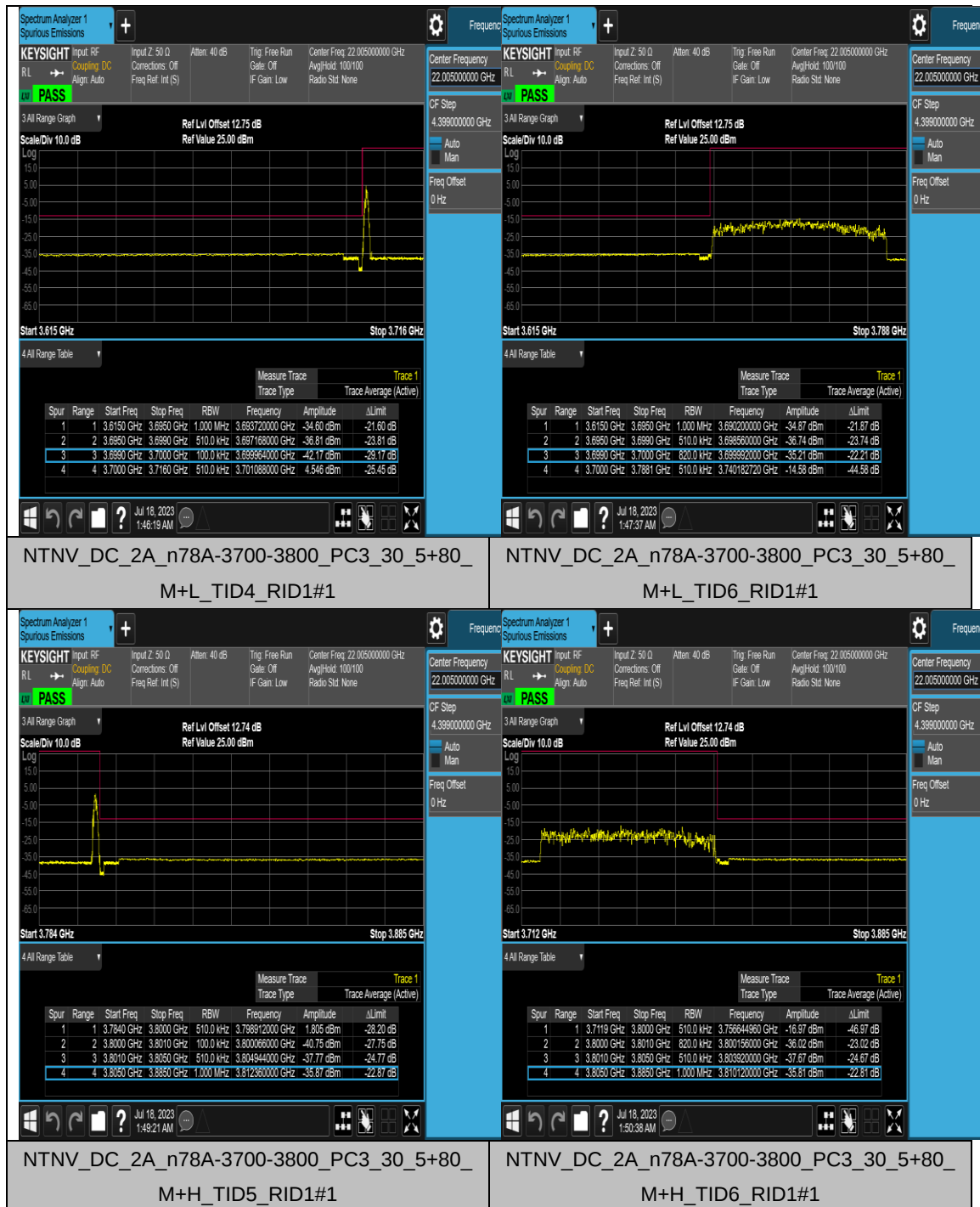
Test Graphs

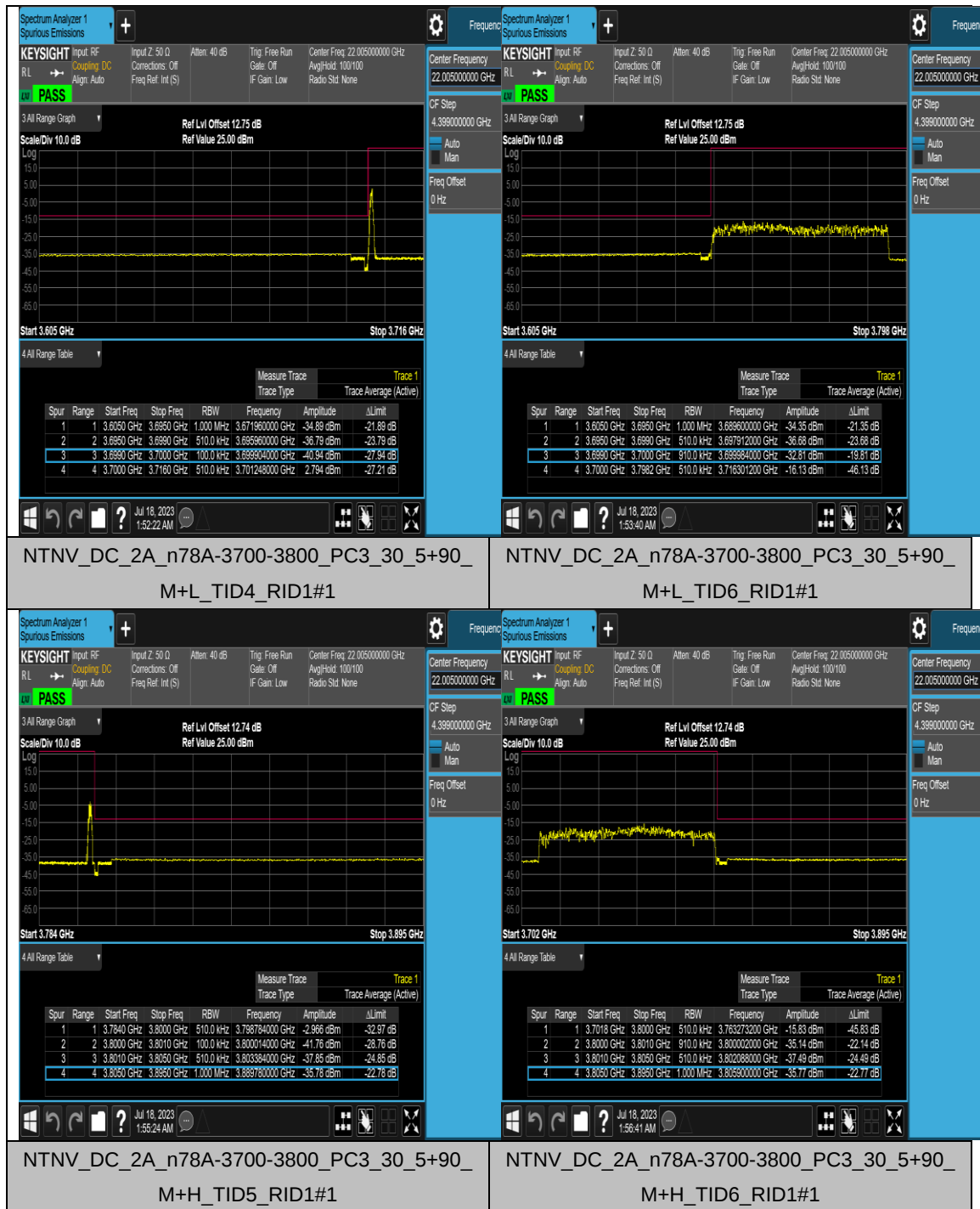


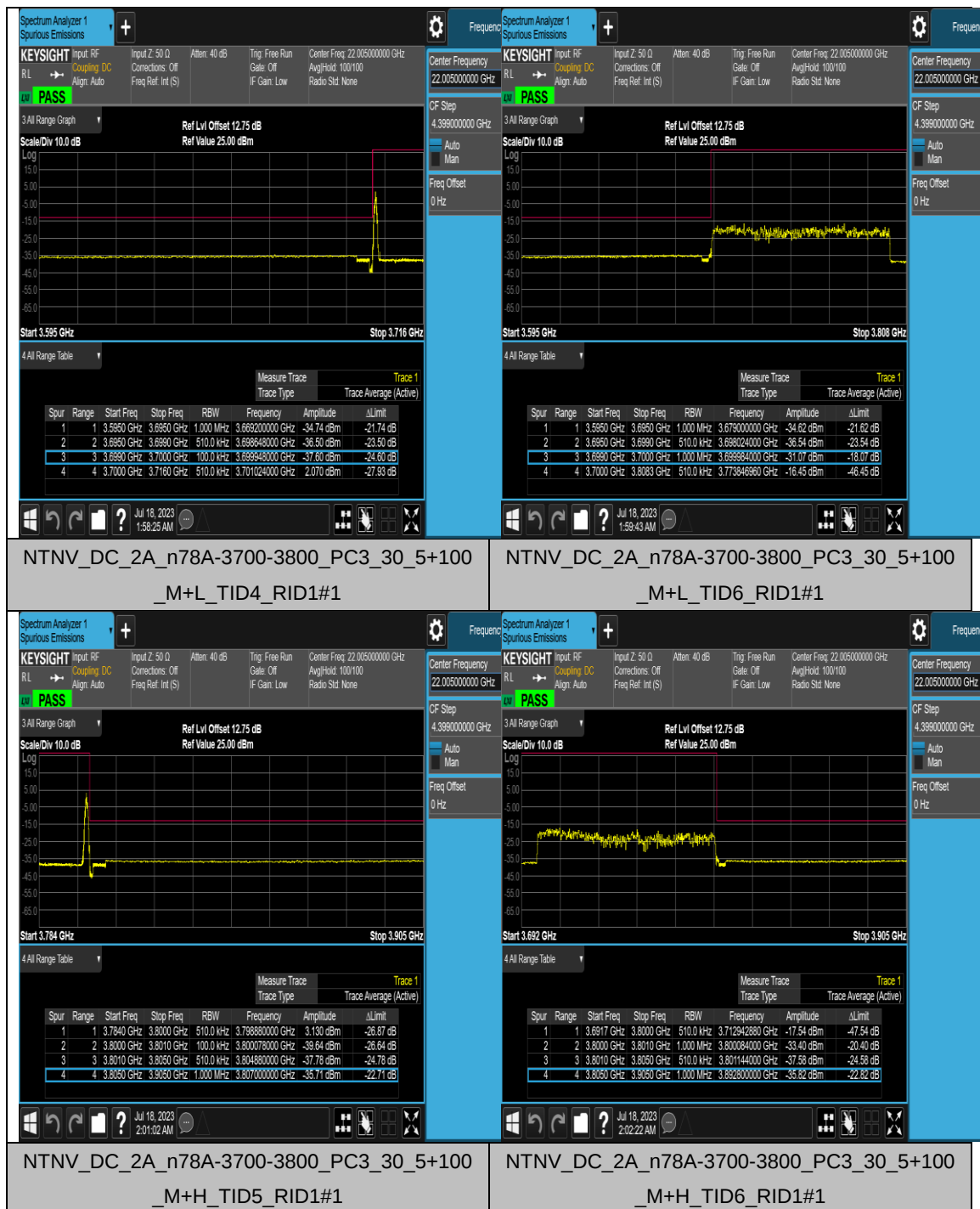












Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.28	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.51	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.49	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.45	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.54	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.82	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-32.95	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.61	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.20	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.47	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.66	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.26	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-75.22	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.59	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-70.48	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.54	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.12	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.77	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-69.97	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-32.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.97	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-79.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.16	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-75.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-77.50	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-42.13	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	3000	6000	-44.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-33.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.56	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.69	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.02	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.90	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.13	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-75.33	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.90	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-69.87	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.44	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.68	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.90	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-79.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.51	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-73.82	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-78.97	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-70.27	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-41.27	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	3000	6000	-46.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.53	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.47	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-69.59	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.76	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.99	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.40	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-75.80	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-69.91	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.95	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-73.78	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.46	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.16	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.19	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.86	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-69.83	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-75.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-79.70	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-41.67	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	3000	6000	-47.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.03	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.39	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.10	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-73.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.48	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-69.67	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-76.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-79.83	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-70.00	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.14	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.98	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-69.79	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.67	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.73	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.08	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-75.26	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-78.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-70.24	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-43.69	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	3000	6000	-46.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-76.15	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.14	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.98	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.62	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-75.20	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-70.59	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.25	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.34	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.55	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.07	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.63	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.20	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-73.10	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-78.53	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-70.33	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-39.06	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	3000	6000	-47.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.63	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.50	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.58	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.98	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-69.79	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-74.69	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-70.19	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.08	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.58	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.84	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.46	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-32.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.10	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.90	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.43	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-75.31	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-77.71	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-69.51	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-41.23	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	3000	6000	-47.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.00	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.97	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-69.72	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-70.49	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.04	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.83	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.37	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-32.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-75.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-77.30	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	1000	3000	-43.42	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	3000	6000	-46.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-29.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-32.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-76.03	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.68	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.31	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-69.99	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-43.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-73.17	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-80.12	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-70.25	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.37	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.70	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.36	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.42	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-48.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.34	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.78	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.09	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-69.37	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-78.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-70.07	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	1000	3000	-41.57	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	3000	6000	-46.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-79.35	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-32.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.69	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.11	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.26	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-70.15	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.16	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-70.38	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.74	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.36	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.43	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.56	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.34	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-63.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-77.65	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-70.73	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	1000	3000	-39.00	-13	PASS

DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	3000	6000	-47.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.53	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.09	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-22.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-48.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-70.39	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.65	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-72.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.96	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-70.21	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.92	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.10	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-69.57	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-32.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.34	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.39	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-72.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-79.24	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-70.65	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-36.66	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	3000	6000	-45.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-70.05	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.88	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.58	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-41.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.01	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-69.48	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.13	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-69.99	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.71	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.91	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.08	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-72.21	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.80	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-69.88	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-67.28	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-78.17	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-69.76	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-38.50	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	3000	6000	-47.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.80	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.57	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.43	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.21	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-78.70	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-69.80	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-43.23	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.58	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.33	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.11	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-32.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.72	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.08	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.36	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-77.25	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-69.87	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-42.47	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	3000	6000	-47.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.29	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-69.85	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.99	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.45	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-43.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-73.21	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-79.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-70.36	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.34	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-45.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.93	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.43	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-22.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.16	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.22	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-70.06	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-80.48	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-70.61	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	1000	3000	-40.57	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	3000	6000	-46.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-29.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-33.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.96	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-79.05	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-69.50	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.20	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-79.64	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.59	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-70.62	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-42.64	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-46.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-79.06	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.12	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.91	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-79.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.41	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-43.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-66.86	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-78.99	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-70.47	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	1000	3000	-39.70	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	3000	6000	-47.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-29.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-33.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.26	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.37	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-74.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.61	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.94	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-78.34	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-43.12	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.17	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.25	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-69.95	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.65	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-71.34	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-77.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-70.31	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	1000	3000	-42.48	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	3000	6000	-46.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-30.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.10	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-69.73	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.37	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-79.84	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.33	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-69.11	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.88	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-70.15	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.77	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.19	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.28	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-79.56	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.49	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-70.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-79.79	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-69.38	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	1000	3000	-41.86	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	3000	6000	-46.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-71.75	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.31	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-74.74	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.59	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.39	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-73.00	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-79.06	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.74	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.12	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.10	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.23	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.61	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.05	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-68.25	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-78.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-70.18	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	1000	3000	-43.40	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	3000	6000	-47.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.85	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-69.76	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.75	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.88	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.16	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.07	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.62	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-70.23	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.41	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.01	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.59	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.60	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.10	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.07	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-69.89	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-71.27	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-79.16	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-70.14	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-43.63	-13	PASS

DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-47.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.31	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.15	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.49	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.26	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.89	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.30	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-70.37	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-43.27	-13	PASS

DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-46.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.40	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-79.34	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.23	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.79	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.31	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-65.40	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-78.34	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-70.56	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-40.11	-13	PASS

DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	6000	-48.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-71.74	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.98	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.26	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.08	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.88	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-69.90	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-30.51	-13	PASS

DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.79	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.38	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-69.95	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-69.78	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.78	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.009	0.15	-73.87	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-77.29	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-70.47	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-39.53	-13	PASS

DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	6000	-48.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.47	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.03	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.46	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.14	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.50	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-69.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.99	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-70.13	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.83	-13	PASS

DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.54	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.61	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-68.36	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-75.53	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-70.07	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-39.59	-13	PASS

DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	6000	-48.66	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-29.56	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-33.41	-13	PASS

Test Graphs

