

Appendix for 2A_N78A 3700-3800MHz

Product Name: Zenius08

Model No: GMT-08QA-2301

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.73	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.84	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	24.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	24.21	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	22.76	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	22.81	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Outer_Full	23.20	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Inner_Full	24.13	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.13	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.84	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	24.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	24.19	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	23.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	22.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Outer_Full	23.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Inner_Full	24.56	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.32	PC3	PASS
DC_2A_n78A-37	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.68	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	23.39	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	23.44	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Outer_Full	22.47	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Inner_Full	23.42	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.37	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.48	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	23.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	23.90	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	22.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	22.27	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Outer_Full	22.82	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Inner_Full	23.97	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.86	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.33	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	24.11	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	24.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	22.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	22.26	PC3	PASS
DC_2A_n78A-37	30	5+40	CP-QPSK	M+M	Outer_Full	23.06	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Inner_Full	24.23	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	23.29	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	23.35	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	22.10	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	21.30	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Outer_Full	22.35	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Inner_Full	23.39	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.59	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	24.15	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	24.22	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	22.45	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	22.34	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Outer_Full	23.16	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Inner_Full	24.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.12	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	22.33	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	24.36	PC3	PASS
DC_2A_n78A-37	30	5+50	DFT-QPSK	M+M	Inner_Full	24.35	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	23.34	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	22.48	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Outer_Full	23.31	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Inner_Full	24.35	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	21.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	23.61	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	23.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	22.63	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	21.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Outer_Full	22.61	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Inner_Full	23.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.46	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.27	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	24.14	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	24.24	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	22.35	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	22.14	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Outer_Full	23.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Inner_Full	24.23	PC3	PASS
DC_2A_n78A-37	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.94	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.37	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	24.32	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	24.45	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	22.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	22.54	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Outer_Full	23.36	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Inner_Full	24.41	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.56	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	23.77	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	23.82	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	22.44	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	21.47	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Outer_Full	22.74	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Inner_Full	23.86	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.70	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	24.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	24.42	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	22.50	PC3	PASS
DC_2A_n78A-37	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	22.01	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Outer_Full	23.19	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Inner_Full	24.26	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	24.09	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	24.22	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	22.42	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	21.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Outer_Full	23.11	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Inner_Full	24.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.71	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	24.00	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	24.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	22.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	21.75	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Outer_Full	23.01	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Inner_Full	24.04	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.71	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.83	PC3	PASS
DC_2A_n78A-37	30	5+90	DFT-QPSK	M+L	Outer_Full	24.10	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	24.19	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	22.63	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	21.70	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Outer_Full	23.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Inner_Full	24.21	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.69	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	21.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	24.07	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	24.16	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	22.82	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	21.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Outer_Full	23.09	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Inner_Full	24.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	21.74	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	24.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	24.13	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	22.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	21.56	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Outer_Full	23.04	PC3	PASS
DC_2A_n78A-37	30	5+90	CP-QPSK	M+H	Inner_Full	24.13	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.51	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.57	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	24.01	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	24.16	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	22.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	21.50	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Outer_Full	23.04	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Inner_Full	24.18	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	24.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	22.78	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	21.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Outer_Full	22.98	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Inner_Full	24.14	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.55	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	24.04	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	24.16	PC3	PASS
DC_2A_n78A-37	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	22.80	PC3	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	21.42	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Outer_Full	22.99	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Inner_Full	24.13	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

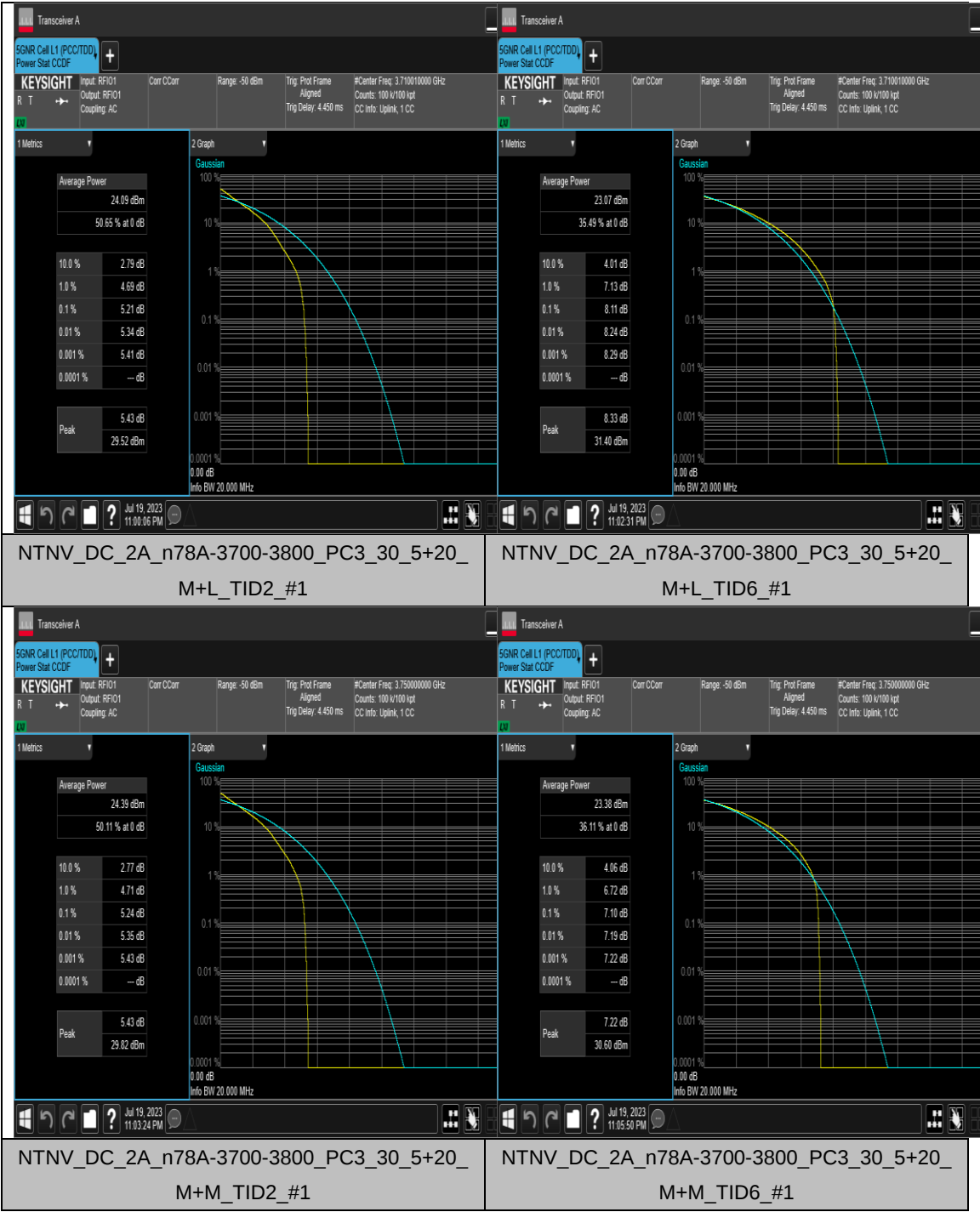
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+L	Outer_Full	5.21	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+L	Outer_Full	8.11	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+M	Outer_Full	5.24	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+M	Outer_Full	7.10	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+H	Outer_Full	5.74	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+H	Outer_Full	7.71	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+L	Outer_Full	5.39	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	CP-QPSK	M+L	Outer_Full	7.38	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	CP-QPSK	M+M	Outer_Full	7.41	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+H	Outer_Full	5.60	≤13	PASS
DC_2A_n7	30	5+40	CP-QPSK	M+H	Outer_Full	7.60	≤13	PASS

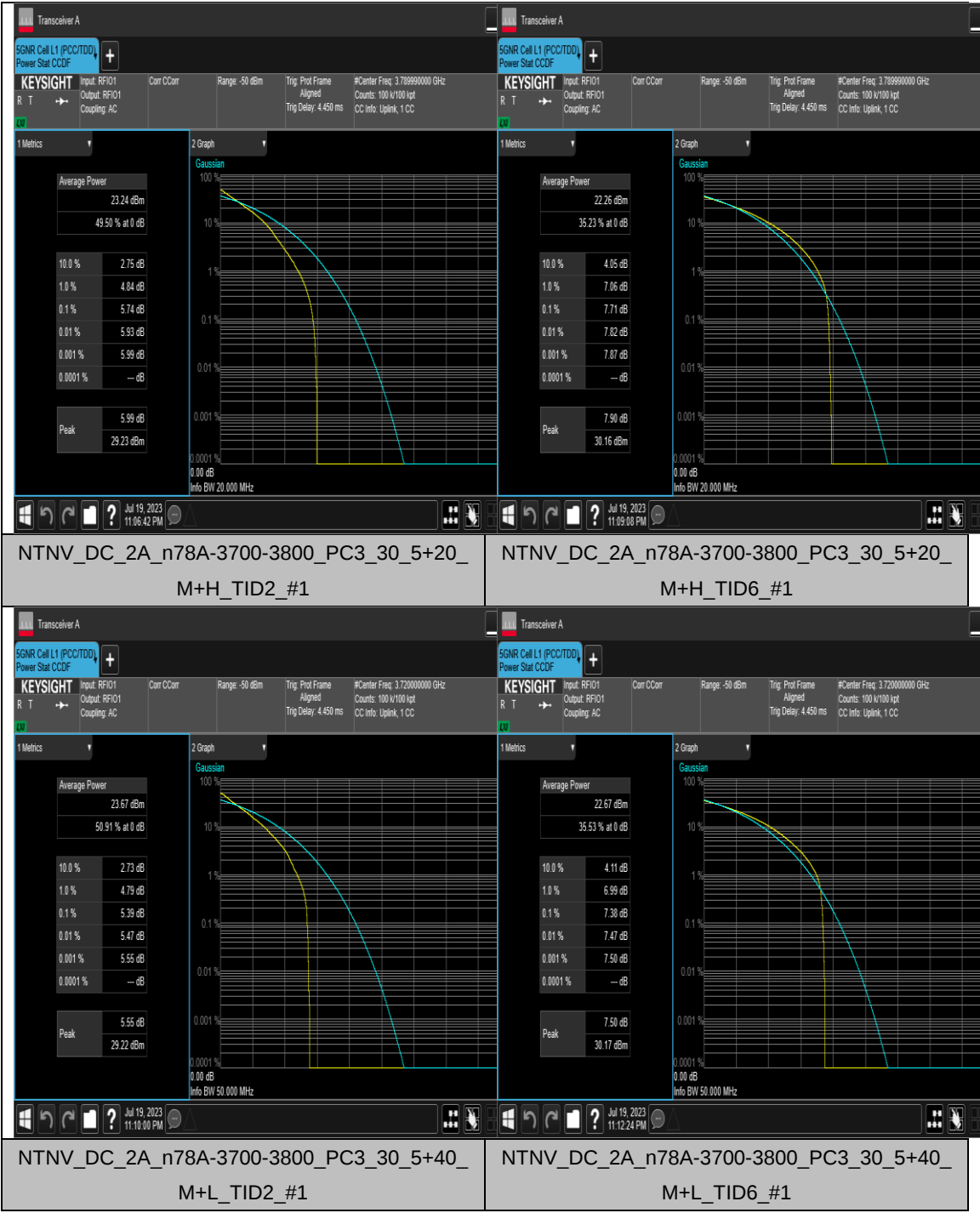
8A-3700-38 00								
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+L	Outer_Full	5.50	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+L	Outer_Full	7.82	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+M	Outer_Full	5.43	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+M	Outer_Full	7.83	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+H	Outer_Full	5.66	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+H	Outer_Full	7.29	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+L	Outer_Full	5.38	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+L	Outer_Full	7.98	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+M	Outer_Full	5.35	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+M	Outer_Full	8.00	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+H	Outer_Full	5.57	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+H	Outer_Full	7.39	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+L	Outer_Full	5.68	≤13	PASS
DC_2A_n7 8A-3700-38	30	5+80	CP-QPSK	M+L	Outer_Full	7.48	≤13	PASS

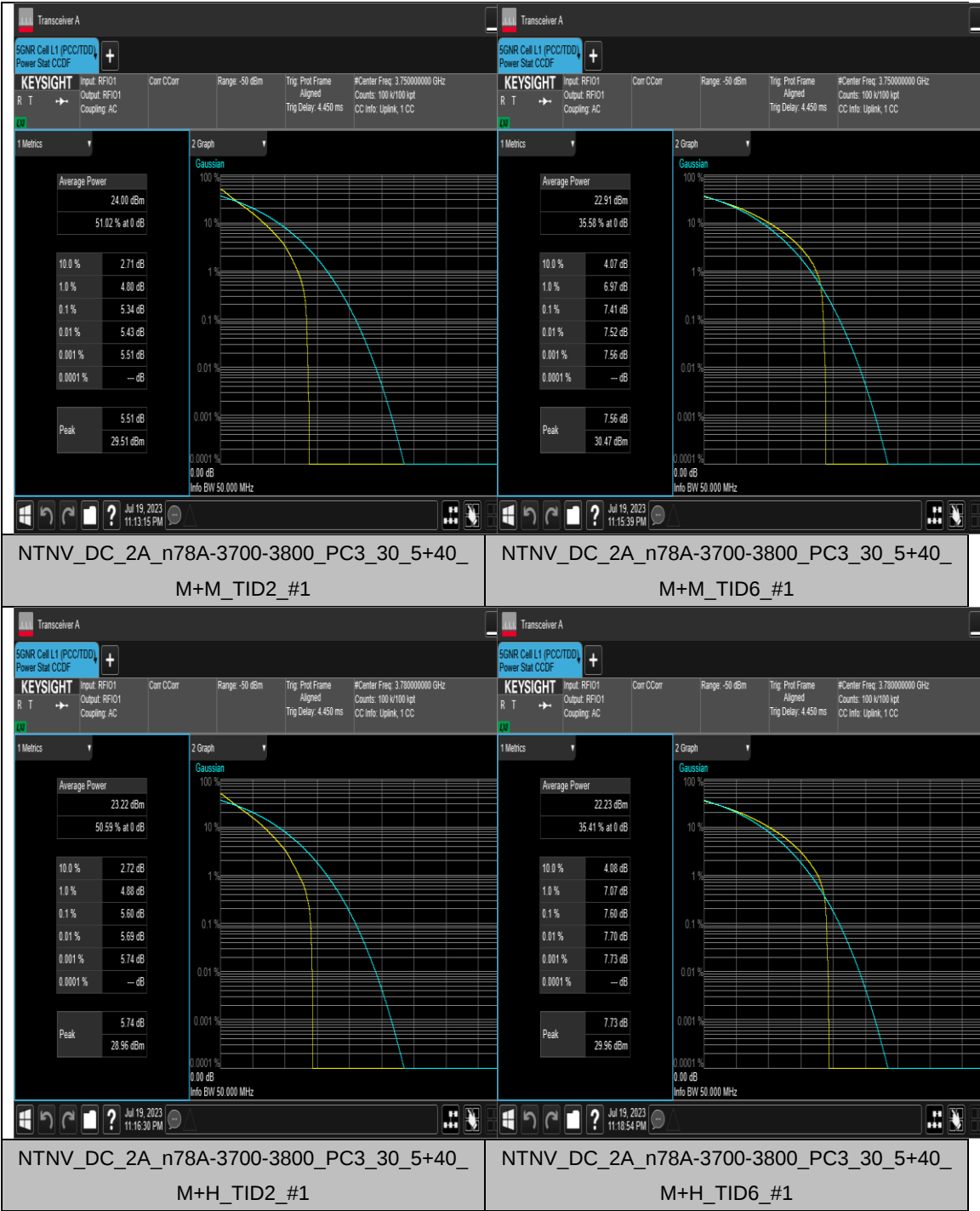
00								
DC_2A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+M	Outer_Full	5.63	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	CP-QPSK	M+M	Outer_Full	7.45	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	5.72	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	7.65	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	5.59	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	7.38	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	5.59	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+M	Outer_Full	7.41	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	5.54	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	7.42	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	5.56	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+L	Outer_Full	7.48	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	5.57	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+M	Outer_Full	7.43	≤13	PASS

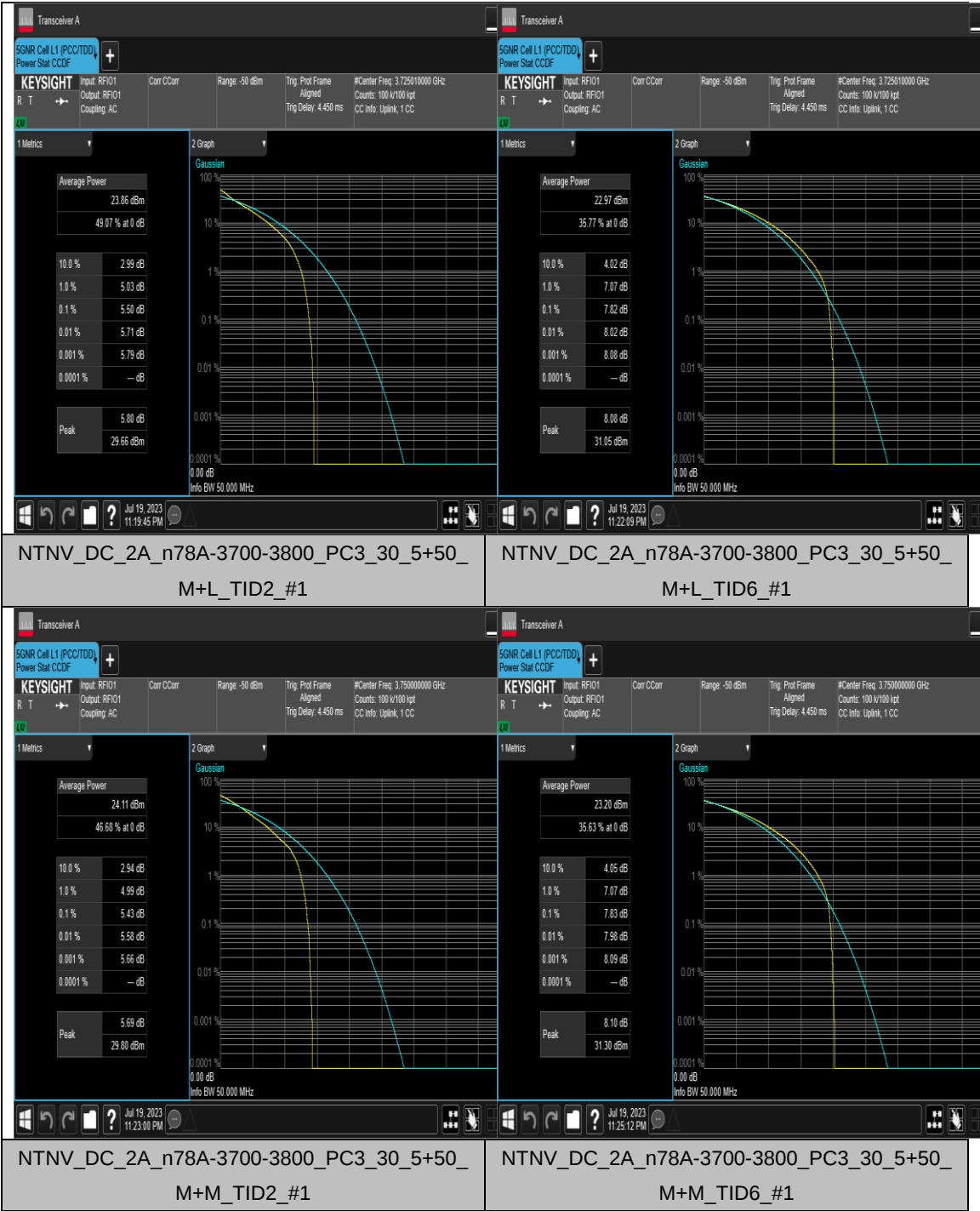
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	5.56	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	7.43	≤13	PASS

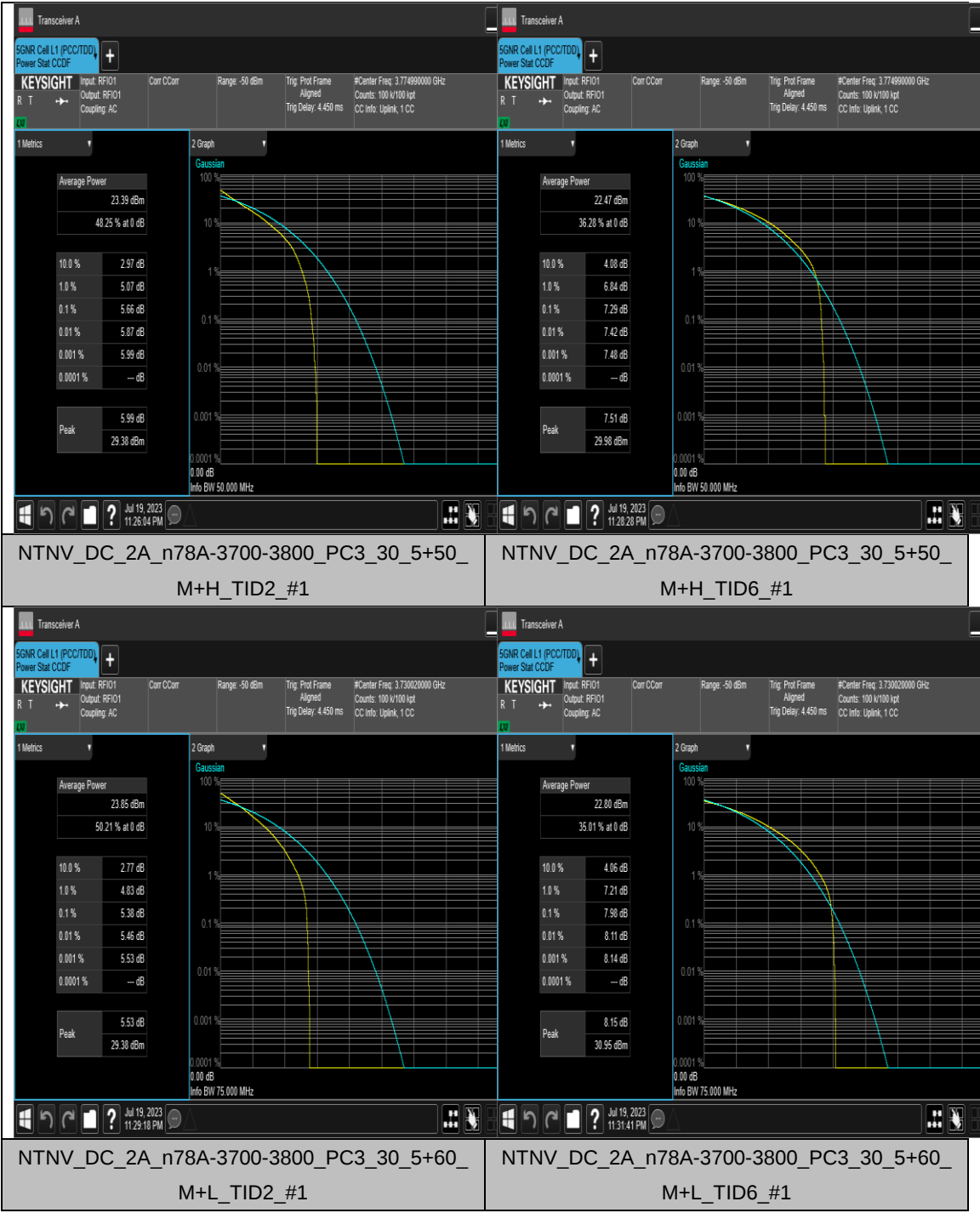
Test Graphs

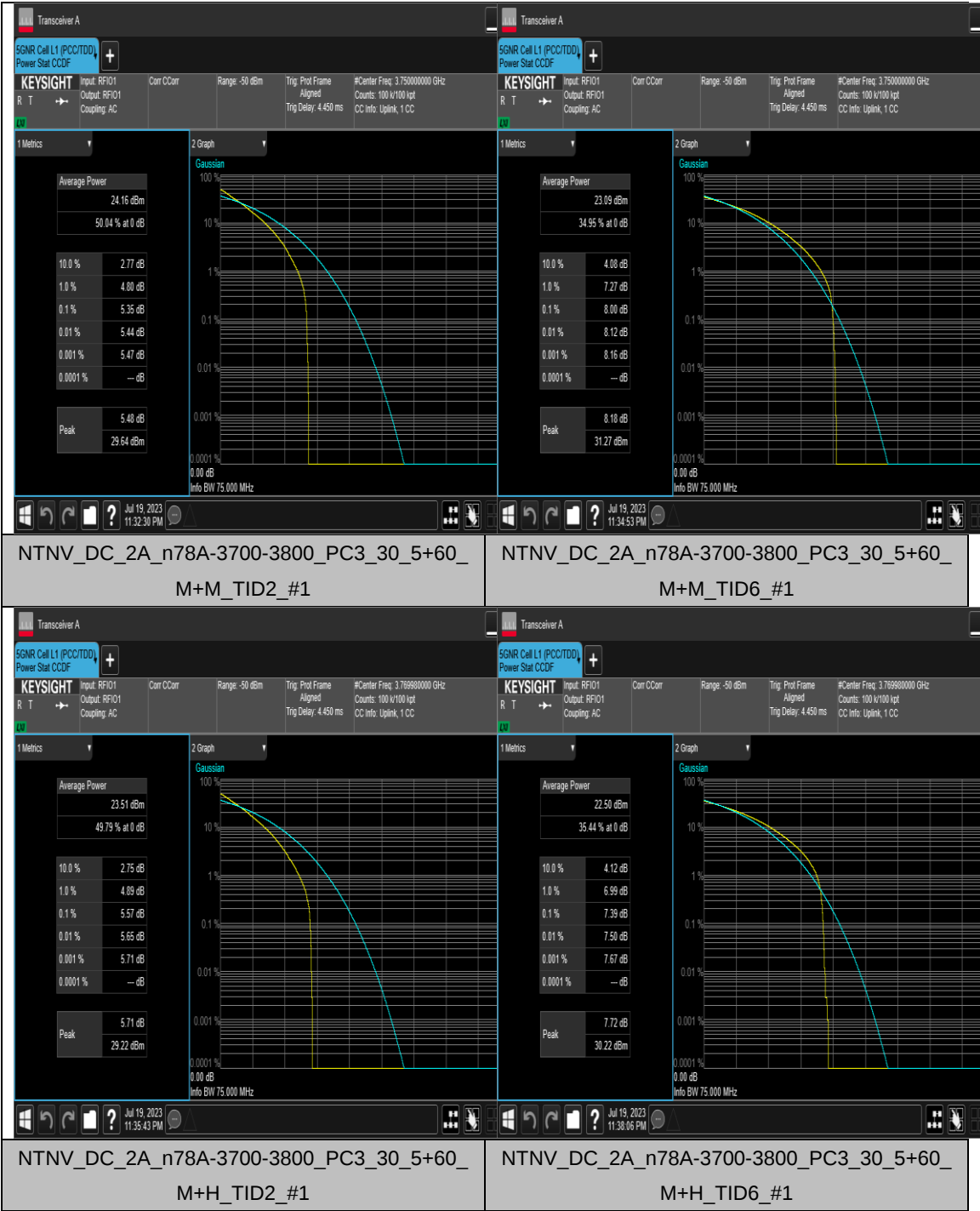


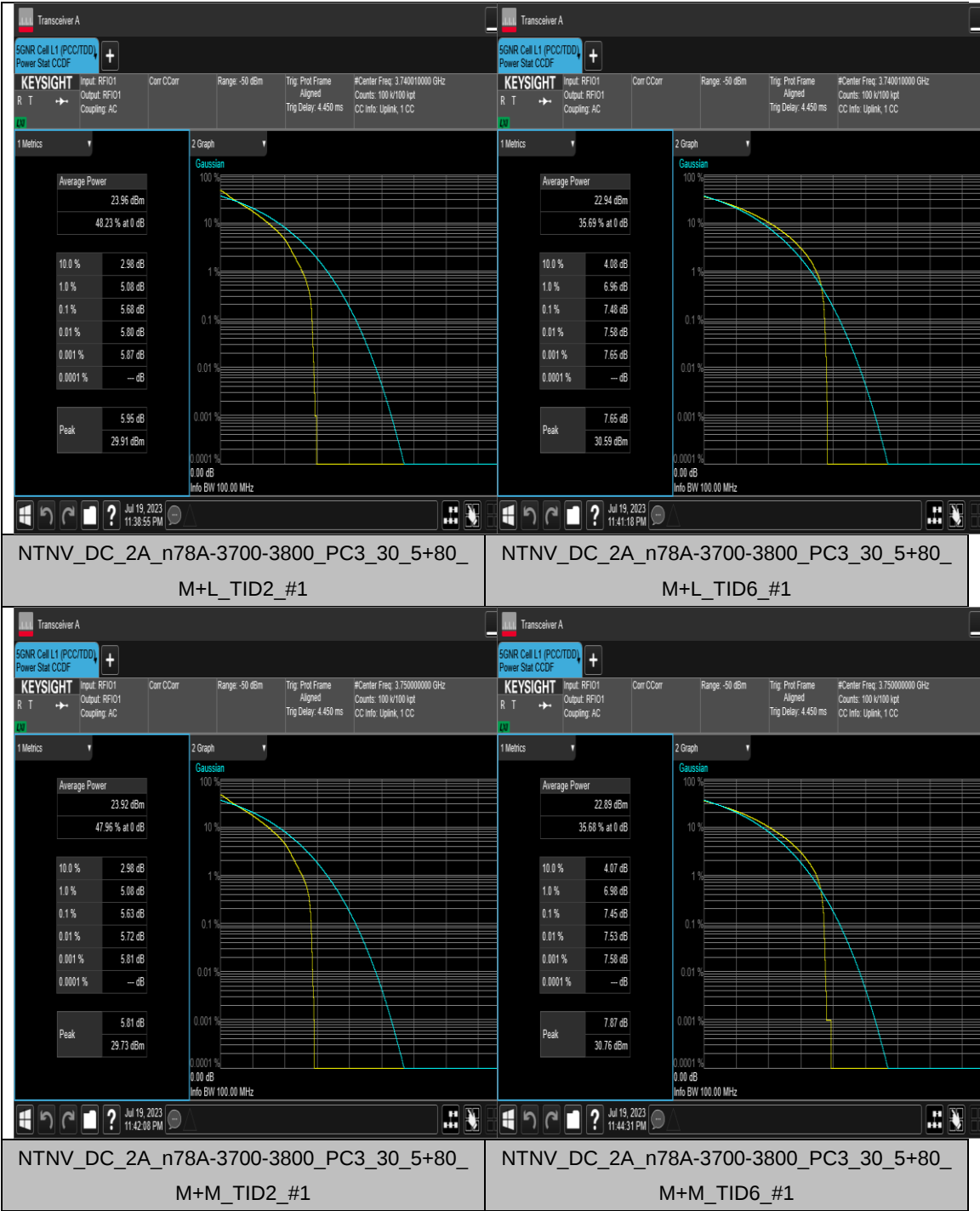


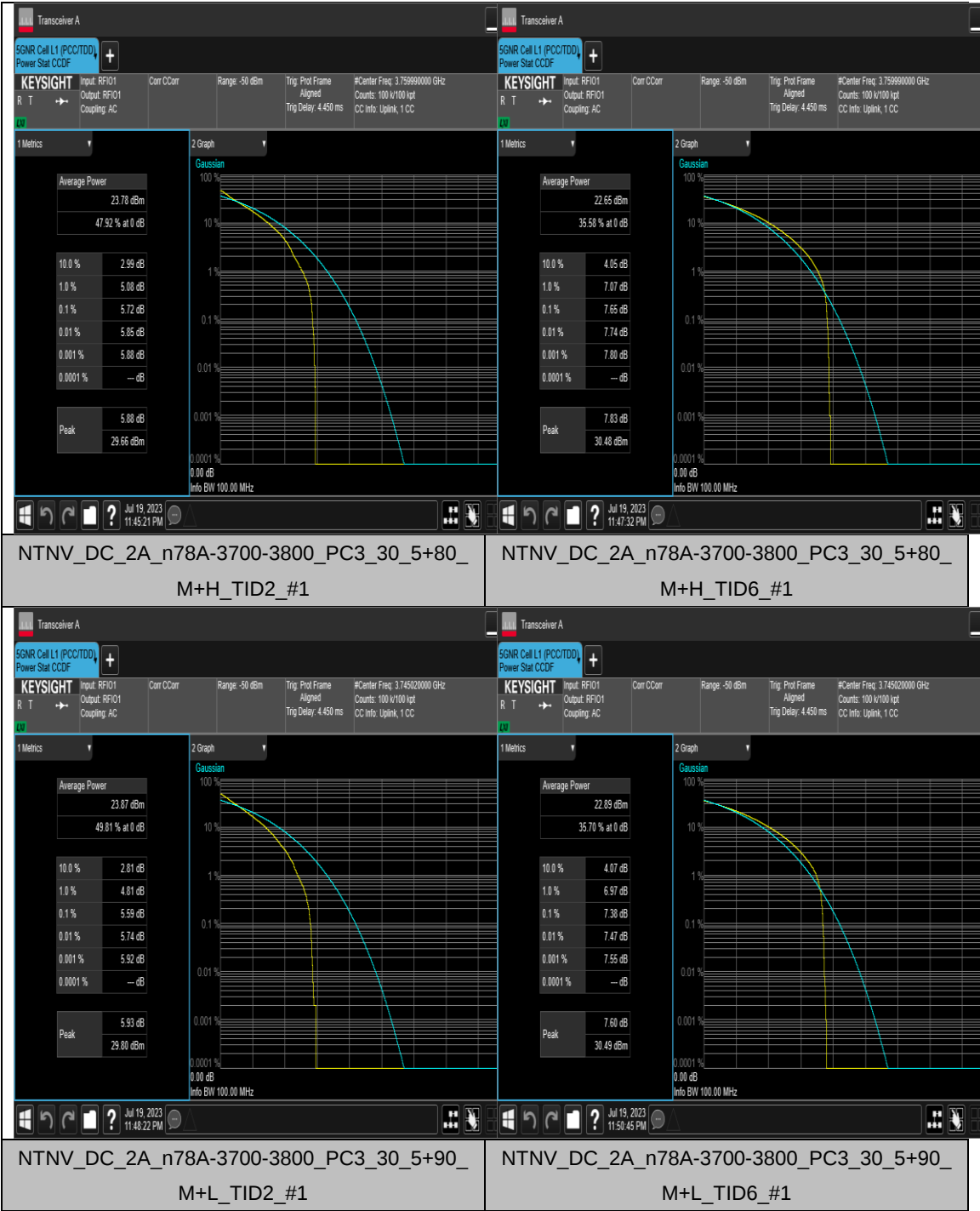


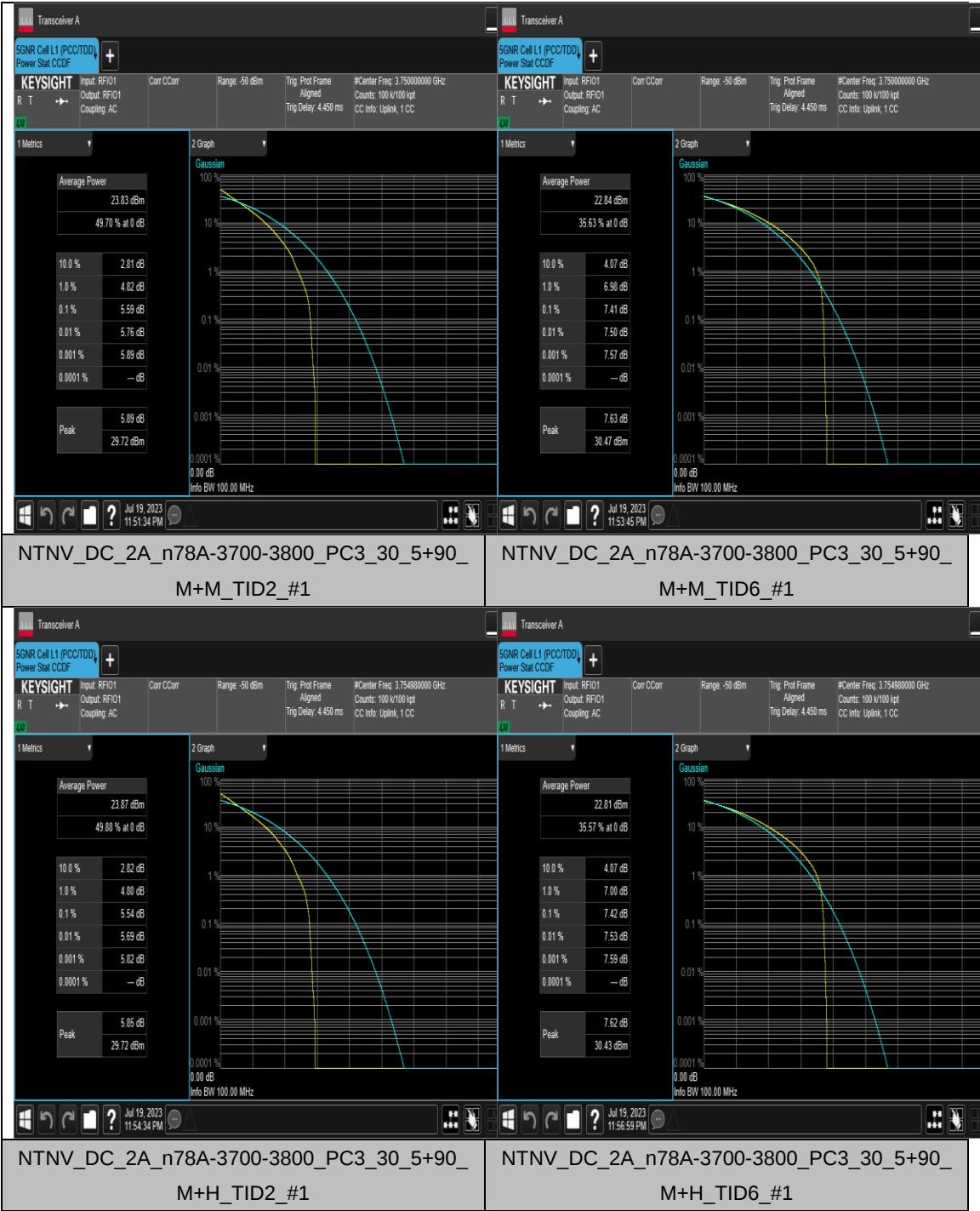


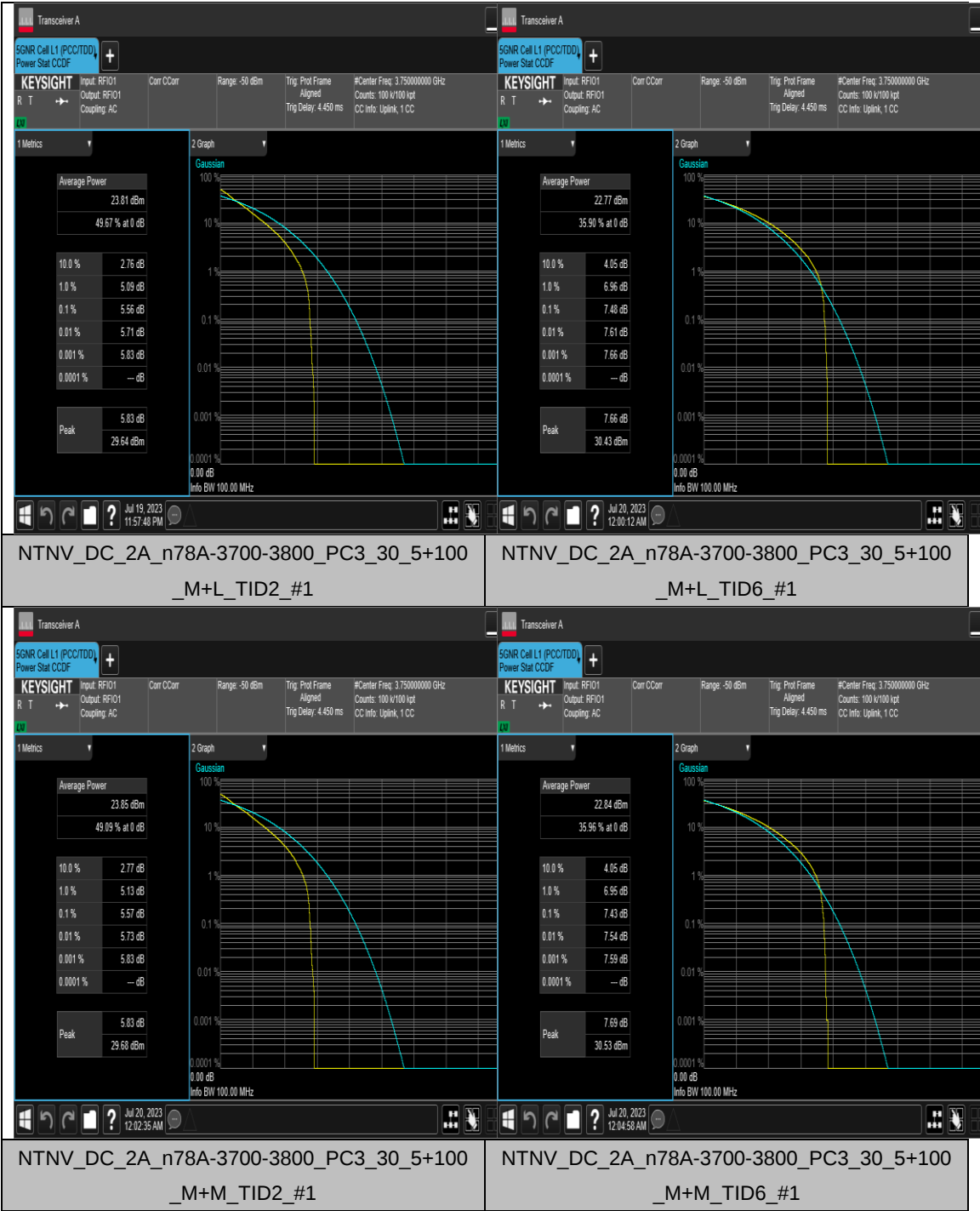


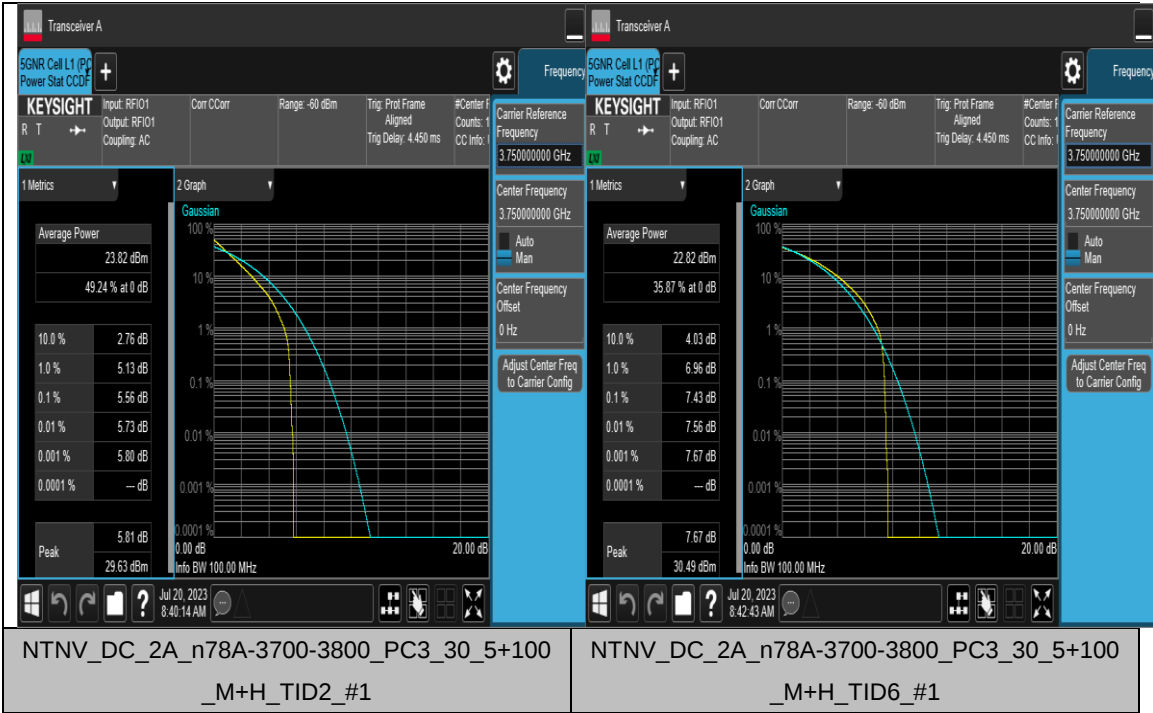












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	17.802	19.11	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	18.195	19.48	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	17.848	19.14	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	18.183	19.45	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	17.802	18.91	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	18.233	19.58	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	35.698	37.45	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	37.760	39.49	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	35.763	37.45	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	37.809	39.65	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	35.681	37.31	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	37.764	39.59	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	45.650	47.37	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	47.426	49.40	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	45.663	47.51	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	47.430	49.34	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	45.593	47.36	PASS

DC_2A_n78A-3700-38 00	30	5+50	CP-QPSK	M+H	Outer_Full	47.329	49.45	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+L	Outer_Full	57.823	60.07	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+L	Outer_Full	57.838	59.84	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+M	Outer_Full	57.780	60.15	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+M	Outer_Full	57.786	59.83	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+H	Outer_Full	57.825	60.03	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+H	Outer_Full	57.764	59.98	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+L	Outer_Full	77.111	79.70	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+L	Outer_Full	77.334	80.07	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+M	Outer_Full	77.080	80.04	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+M	Outer_Full	77.308	80.01	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	77.145	79.83	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	77.293	80.19	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	86.657	89.69	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	87.305	90.34	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	86.700	89.66	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+M	Outer_Full	87.359	90.38	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	86.535	89.75	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	87.354	90.55	PASS
DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	96.131	99.55	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+L	Outer_Full	97.281	100.7	PASS

DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	96.145	99.59	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+M	Outer_Full	97.314	100.7	PASS
DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	96.163	99.49	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	97.254	100.8	PASS

Test Graphs

