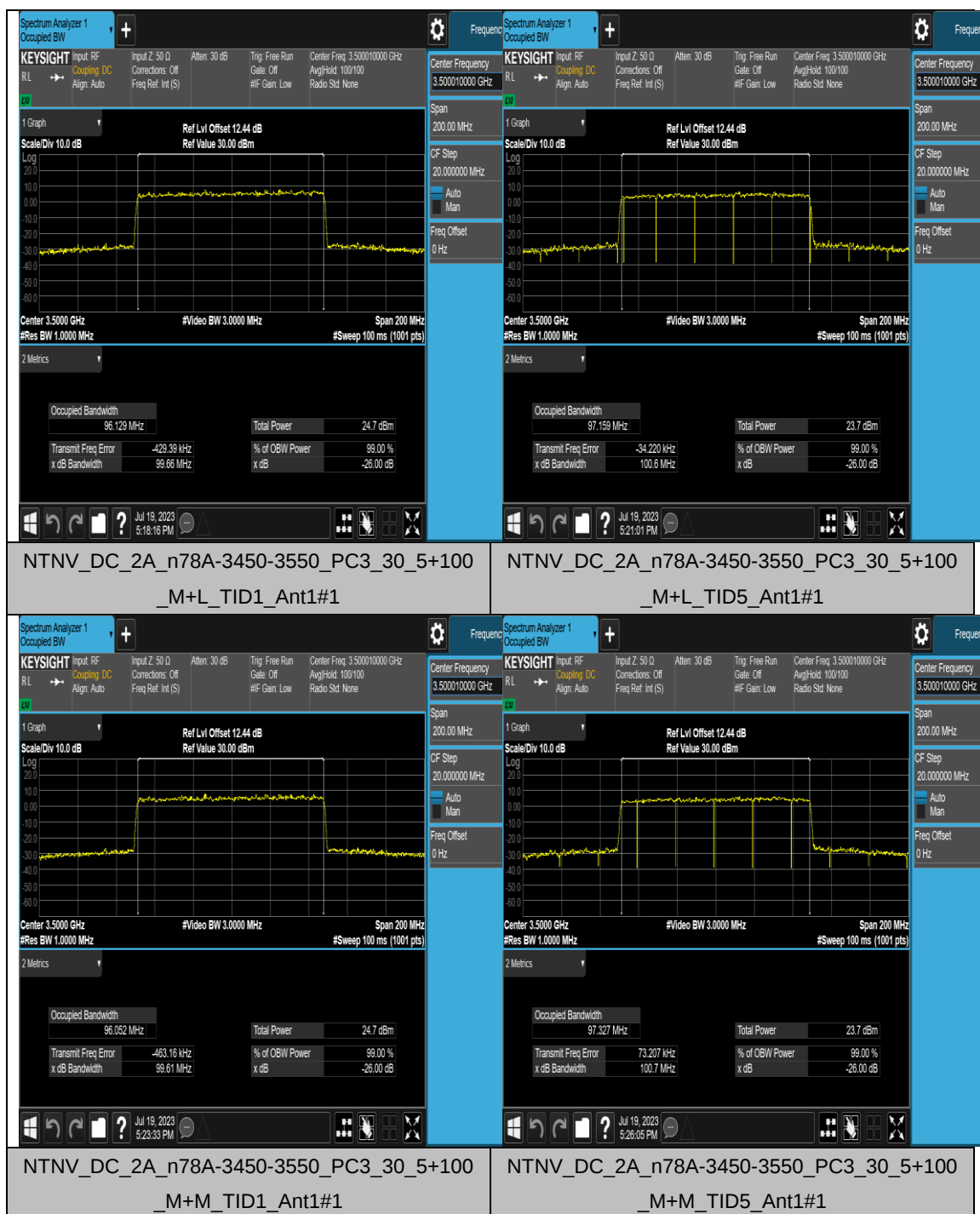
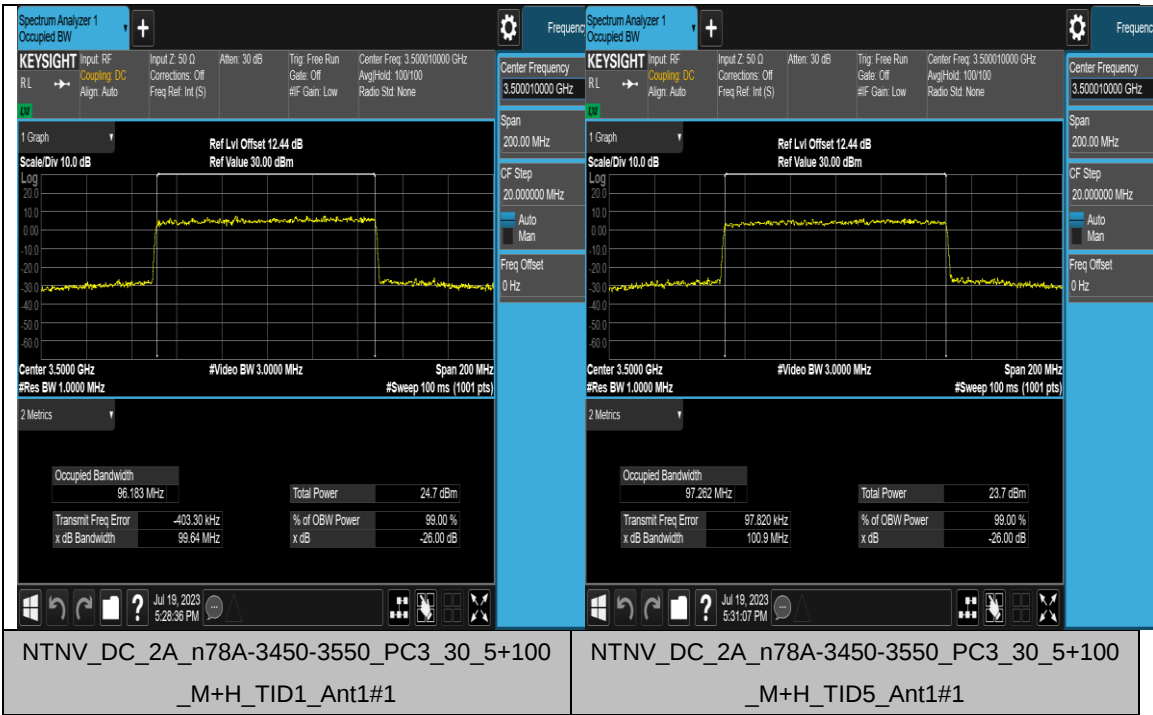










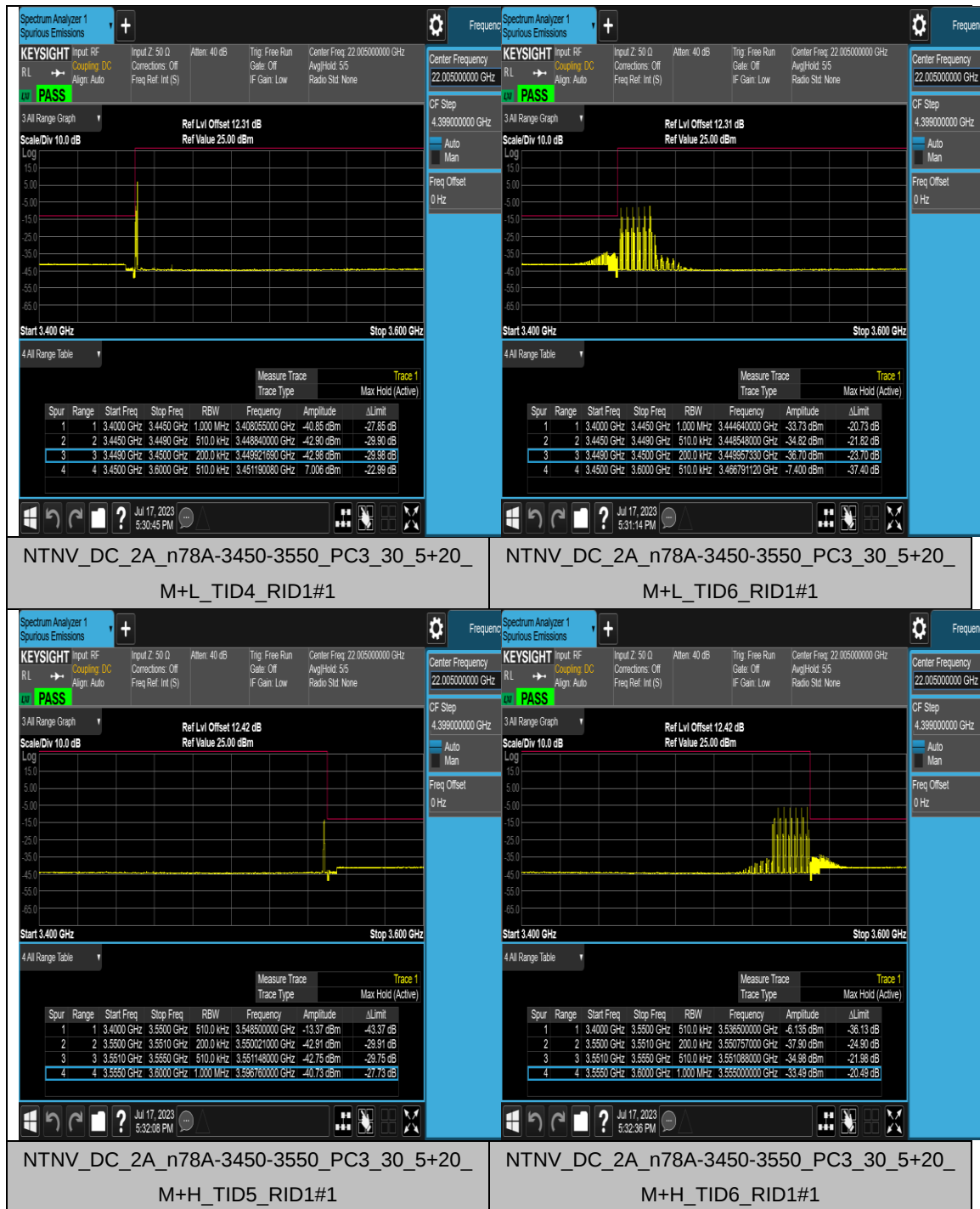
Appendix D: Band Edge for NSA

Test Result

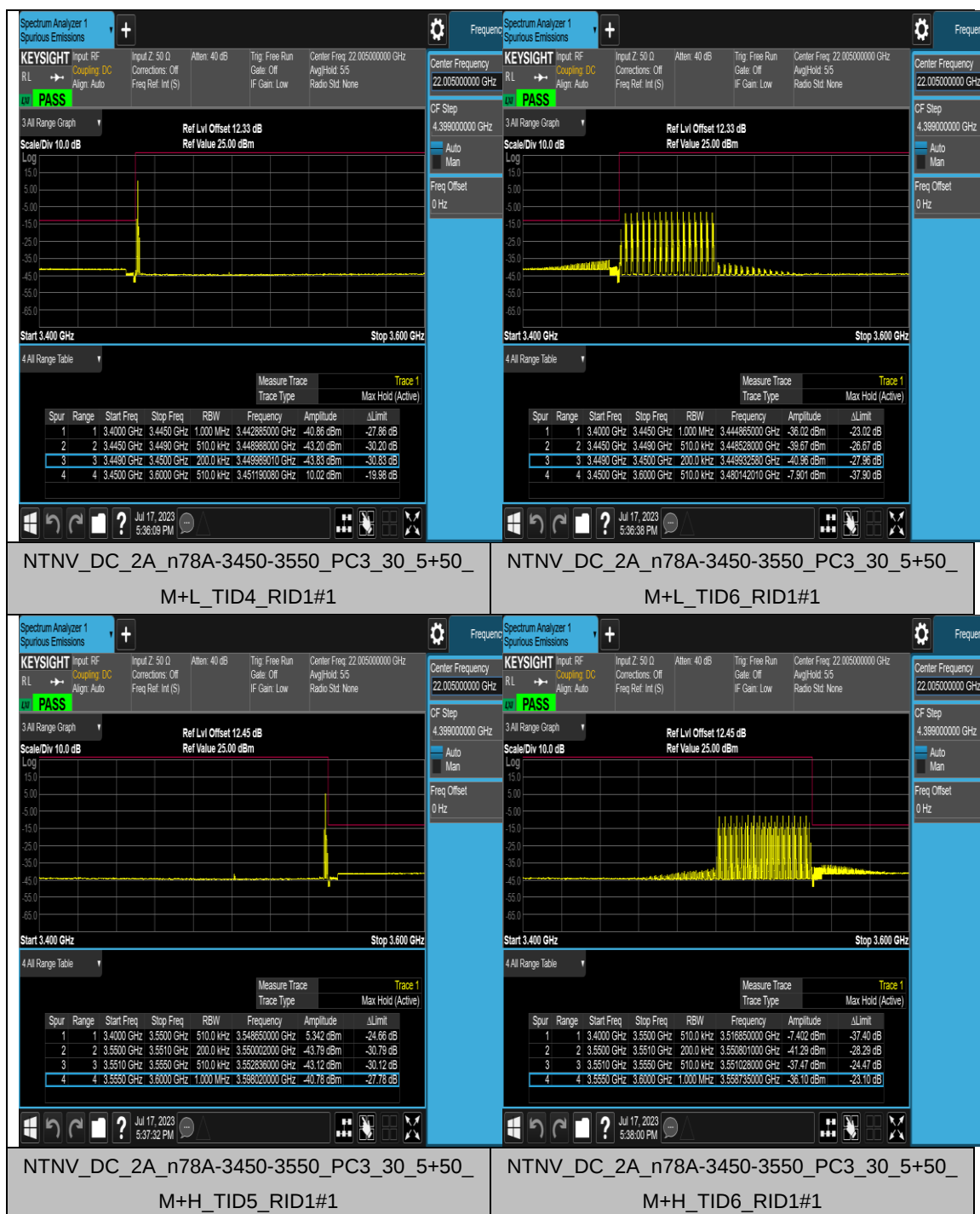
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS

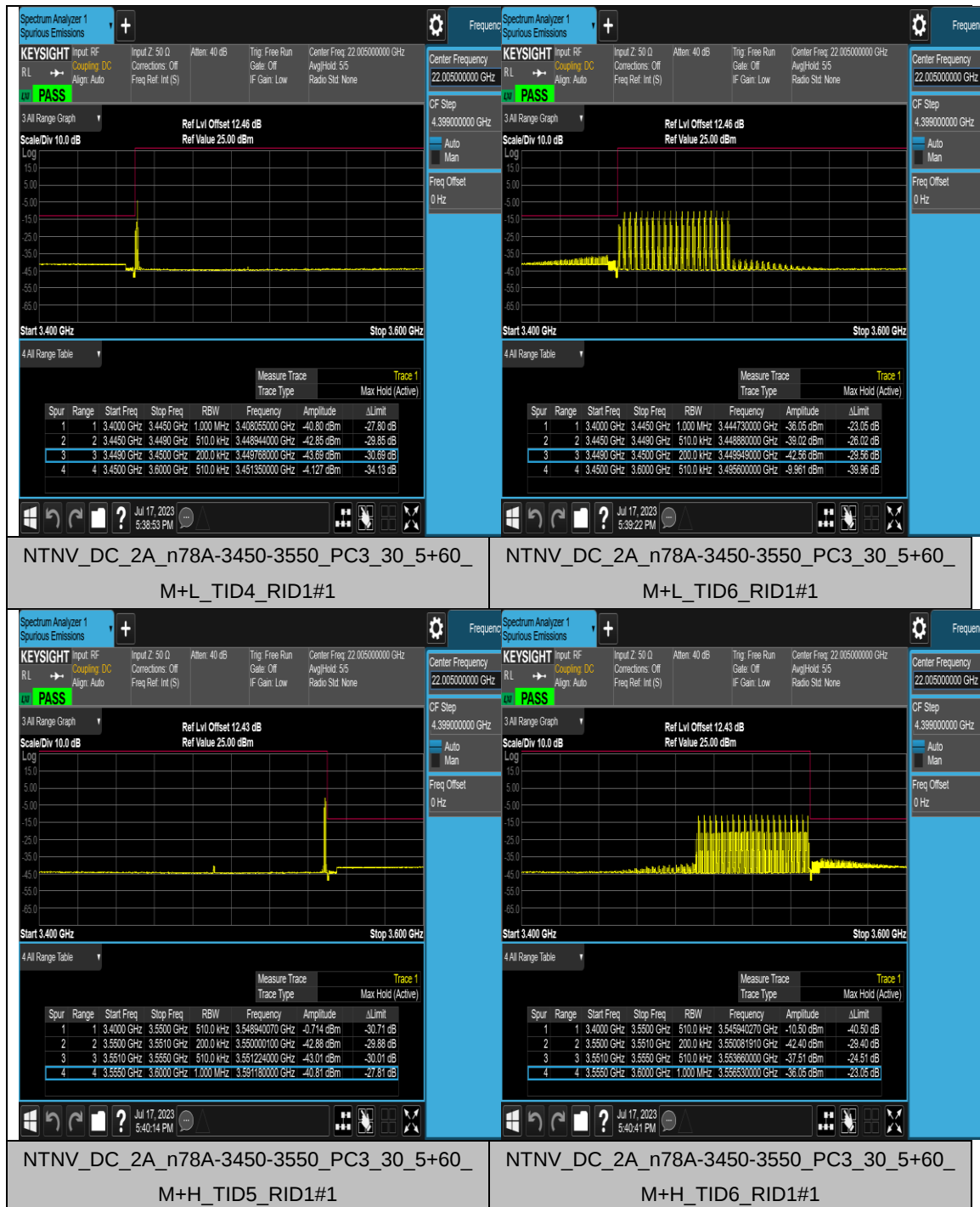
50							
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

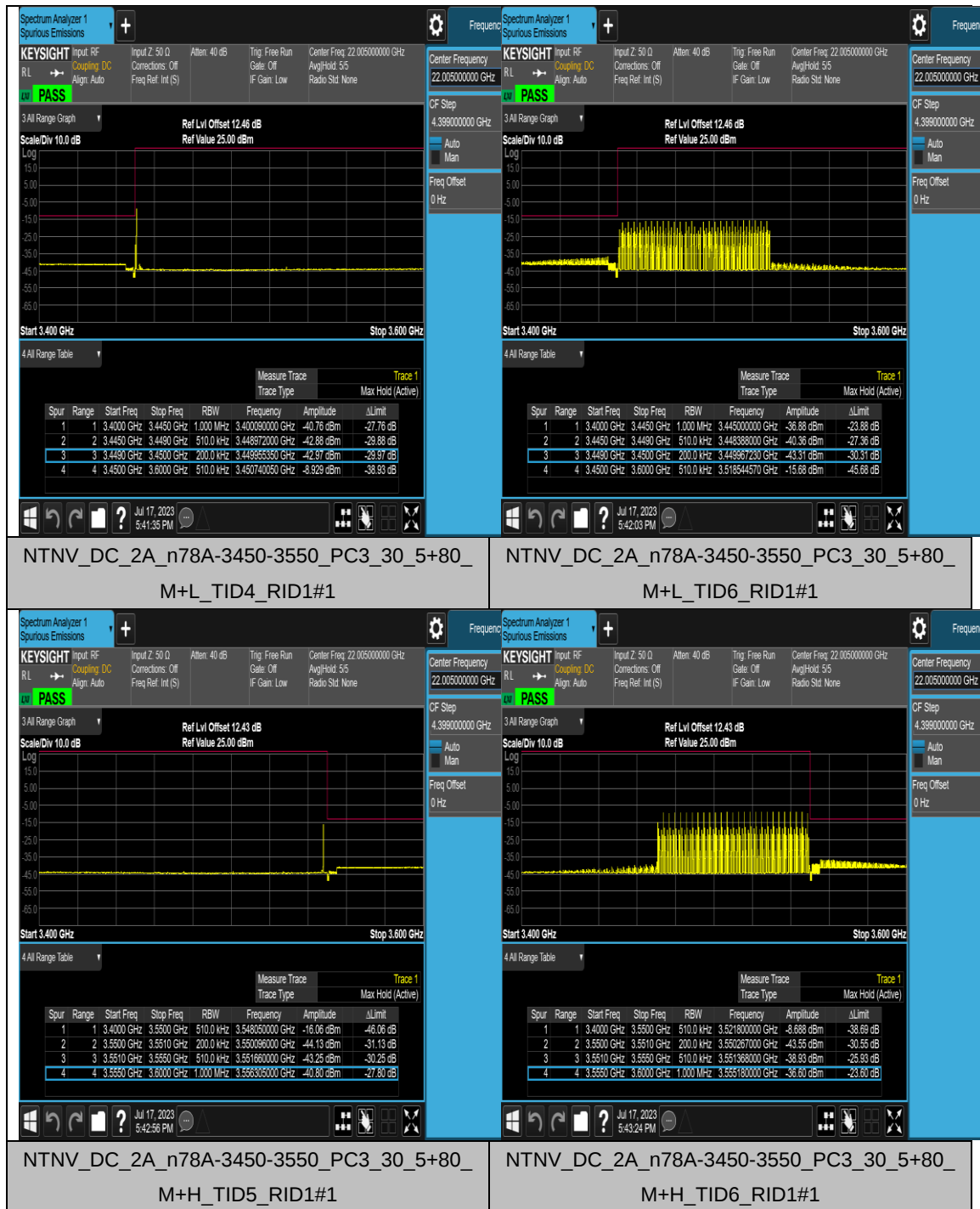
Test Graphs

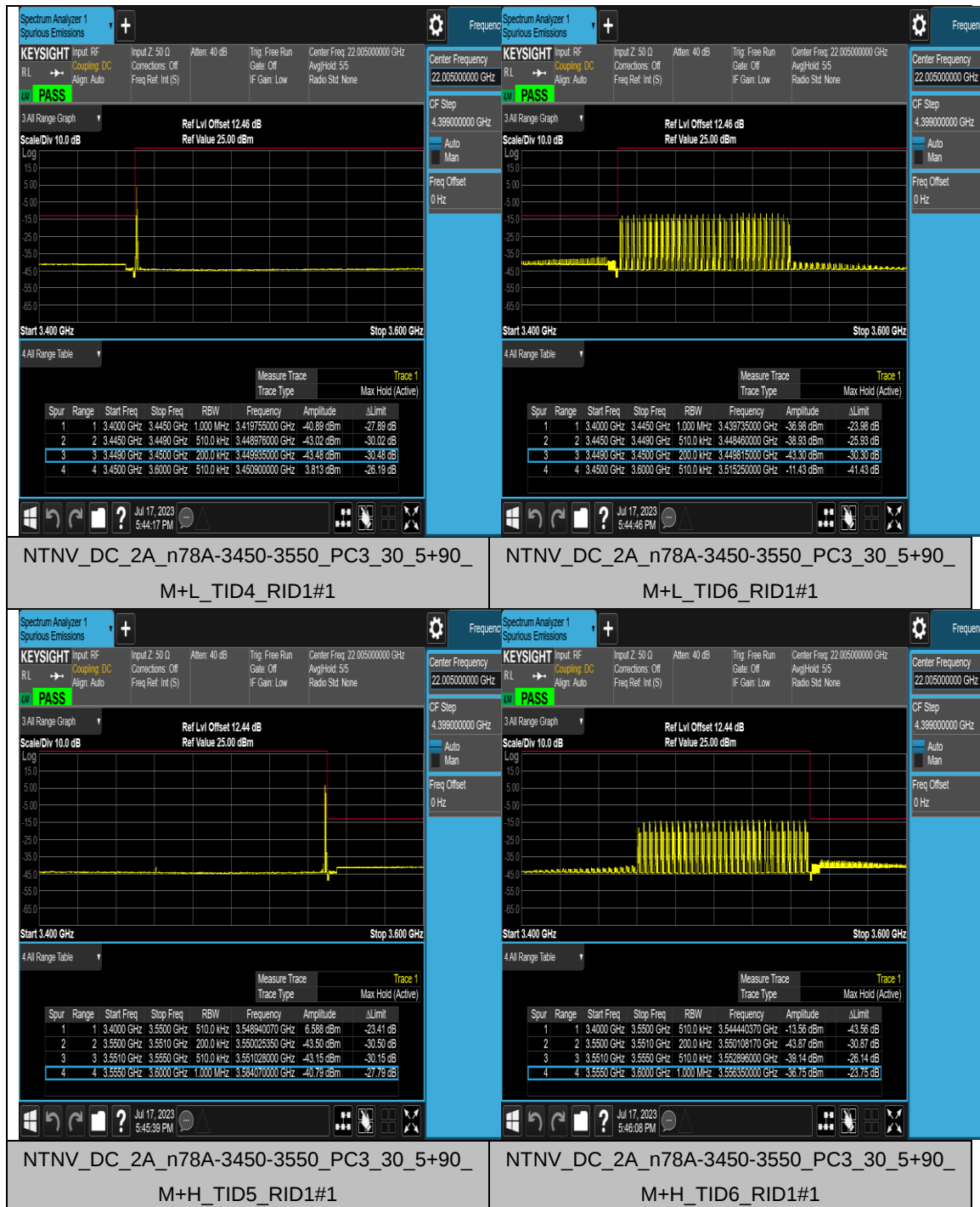


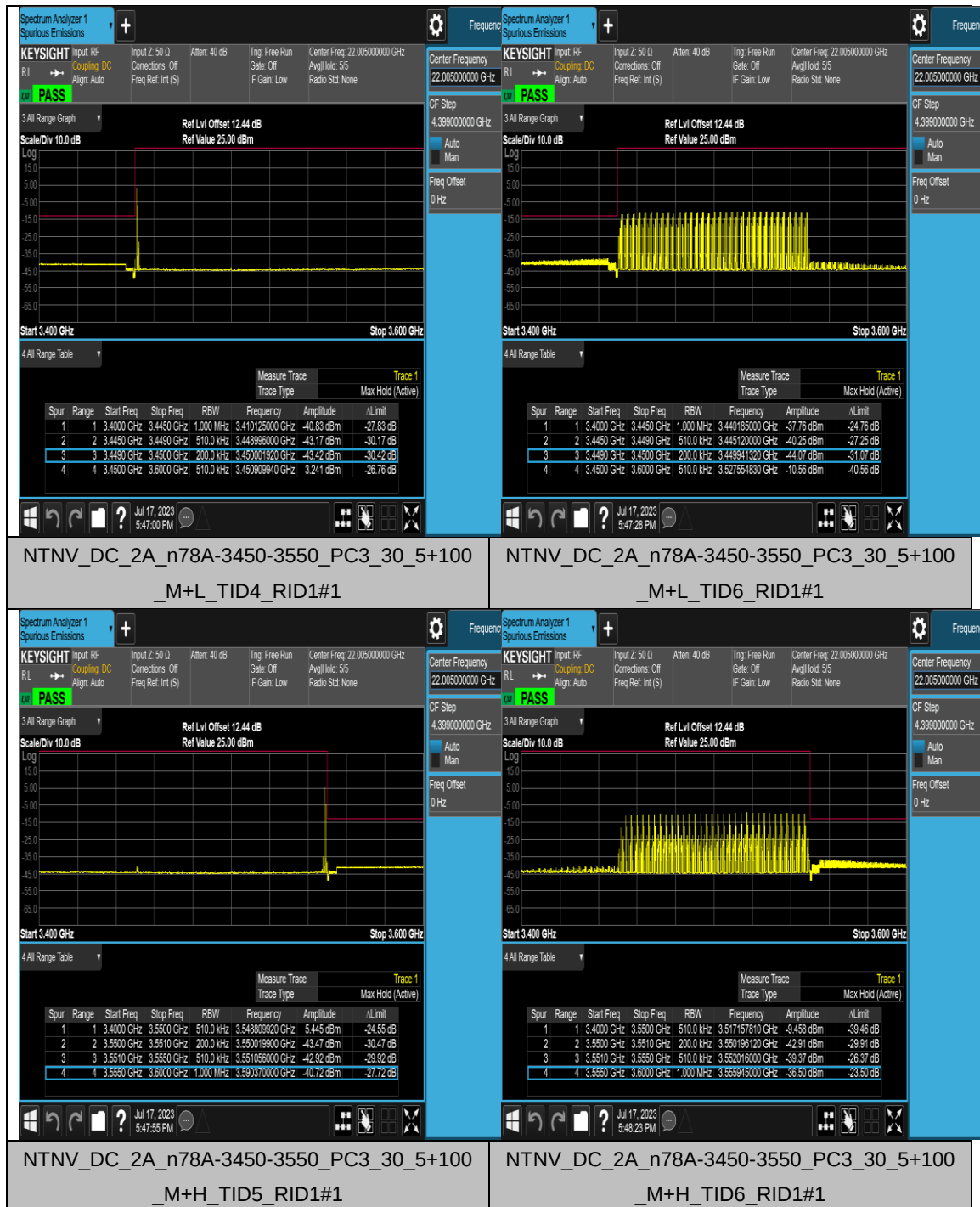












Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.53	-43	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.99	-33	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.27	-23	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.05	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.50	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.50	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.43	-43	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.20	-33	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.17	-23	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.76	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.79	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.21	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.29	-43	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.92	-33	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-70.35	-23	PASS
DC_2A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.58	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-46.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.18	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.49	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.28	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.38	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.15	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.14	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-74.80	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-77.59	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-70.47	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-42.08	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	3000	6000	-45.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.73	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.39	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.04	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-75.30	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.33	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-69.86	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.92	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.26	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.36	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-71.82	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.48	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.43	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-35.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-73.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-79.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-69.10	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-39.35	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	3000	6000	-49.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.39	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.86	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.81	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.92	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-74.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.30	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.43	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.94	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-69.93	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-32.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.09	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.29	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-76.04	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-80.03	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-69.86	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-43.73	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.03	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-29.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.87	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.88	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-44.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-73.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-70.08	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.32	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.64	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.24	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-69.59	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-76.00	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.90	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-75.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-80.42	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-69.40	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-43.77	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	3000	6000	-48.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-32.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-76.08	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.99	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.39	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.14	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.73	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-79.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-70.15	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.25	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.44	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.02	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.34	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-75.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-76.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-70.57	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-43.61	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	3000	6000	-46.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-30.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-33.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.30	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.39	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.52	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.07	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-75.76	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-69.92	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.53	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.52	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.03	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.56	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.17	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.42	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-73.49	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-78.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-70.35	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-28.35	-13	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	3000	6000	-47.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-30.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-33.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-76.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-76.55	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.31	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.45	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.50	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-80.11	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-69.98	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-41.29	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-49.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.21	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.91	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.88	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-43.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-73.21	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-77.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-70.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	1000	3000	-39.71	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	3000	6000	-46.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.08	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.21	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.44	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.63	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-69.98	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.51	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-70.60	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-42.87	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.83	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.70	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.51	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-43.11	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-74.44	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-77.81	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-70.41	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	1000	3000	-40.76	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	3000	6000	-46.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.41	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.00	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.04	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.09	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-68.94	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-75.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-78.06	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-70.60	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.79	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-73.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.00	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-69.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-74.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.26	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-76.00	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-79.11	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-70.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	1000	3000	-40.24	-13	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	3000	6000	-47.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.86	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.24	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.89	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-69.95	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-75.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-69.96	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-43.11	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.38	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.41	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-74.32	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-79.40	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-70.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-42.38	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	3000	6000	-46.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-33.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.92	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.16	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.47	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.71	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-69.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-73.53	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-80.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-70.17	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.93	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.11	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.50	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.05	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.72	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.06	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.07	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-34.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-32.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-74.91	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-77.77	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-70.44	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-40.54	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	3000	6000	-46.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.00	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.35	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.05	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.86	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.24	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-31.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-74.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-78.14	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-70.56	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.39	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-73.91	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.81	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.60	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.06	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-43.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-74.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-78.73	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-70.14	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-42.57	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	3000	6000	-46.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-32.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.58	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.64	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.54	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.79	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.92	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-75.72	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.26	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-70.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.99	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.86	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.50	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.76	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.47	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-74.79	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-77.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-70.46	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	1000	3000	-42.27	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	3000	6000	-46.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-30.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.40	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-69.85	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-17.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-74.78	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.21	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.19	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.08	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-78.70	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-69.86	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-31.17	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-46.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.76	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.07	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.10	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-74.30	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-79.34	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-70.29	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	1000	3000	-42.18	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	3000	6000	-48.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.60	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-79.11	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.69	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.18	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.10	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.96	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-79.04	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-70.04	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-42.36	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.30	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.34	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.53	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.15	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-73.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-76.98	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-70.12	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	1000	3000	-40.81	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	3000	6000	-47.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-33.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-76.60	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-73.71	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.74	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.22	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-76.22	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.91	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-70.24	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-41.19	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.78	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.33	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.90	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.94	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-75.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-78.97	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-70.57	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	1000	3000	-43.01	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	3000	6000	-47.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.81	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.72	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.33	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-41.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-74.99	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.17	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.16	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-76.24	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-79.53	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-70.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-42.42	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.77	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-74.69	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-79.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.44	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-73.73	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-76.68	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-69.99	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	1000	3000	-42.97	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	3000	6000	-48.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-76.25	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.97	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.49	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-76.38	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.45	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-69.87	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-75.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.66	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-70.39	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-42.09	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.82	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.44	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.29	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-32.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.50	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-75.12	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-76.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-70.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-39.72	-13	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-47.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-29.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-72.50	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-69.95	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.47	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.41	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.00	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-44.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.93	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.46	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.32	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.33	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.70	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.52	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.38	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-74.34	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-76.35	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-70.03	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-42.23	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	6000	-47.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-73.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-79.21	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.45	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-76.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.00	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.12	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.95	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-78.48	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-70.05	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-32.84	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-46.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-71.94	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.35	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.35	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.37	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.43	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.009	0.15	-76.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-78.51	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-70.09	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-39.76	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	6000	-47.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-30.11	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.86	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.71	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.43	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.27	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.46	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-75.73	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-77.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-70.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.42	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-46.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.15	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.01	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.49	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.85	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.74	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-75.29	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-79.02	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-70.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-42.11	-13	PASS

DC_2A_n78A- 3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	6000	-47.19	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-29.52	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-33.14	-13	PASS

Test Graphs

