

Appendix for 2A_N78A 3450-3550MHz

Product Name: Zenius08
Model No: GMT-08QA-2301

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.65	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.78	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.46	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.49	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	22.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	22.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Outer_Full	23.40	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Inner_Full	24.52	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.09	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.30	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	24.10	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	24.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	22.87	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	22.23	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Outer_Full	23.16	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Inner_Full	24.20	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.21	PC3	PASS
DC_2A_n78A-34	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.77	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	23.32	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	23.30	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	22.11	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.65	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Outer_Full	22.27	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Inner_Full	23.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.06	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	23.89	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	24.44	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	22.59	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	22.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Outer_Full	23.33	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Inner_Full	24.37	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.00	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	24.15	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	24.15	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	22.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	22.24	PC3	PASS
DC_2A_n78A-34	30	5+40	CP-QPSK	M+M	Outer_Full	23.16	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Inner_Full	24.16	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.14	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.80	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	23.53	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	23.61	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	21.93	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	21.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Outer_Full	22.53	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Inner_Full	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.52	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.49	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	24.32	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	24.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	22.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	22.29	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Outer_Full	23.37	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Inner_Full	24.31	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	22.67	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	22.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	23.97	PC3	PASS
DC_2A_n78A-34	30	5+50	DFT-QPSK	M+M	Inner_Full	24.06	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	22.44	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	22.29	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Outer_Full	23.00	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Inner_Full	24.06	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.25	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	21.60	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	23.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	21.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Outer_Full	22.53	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Inner_Full	23.65	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.57	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.92	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	24.09	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.31	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	22.85	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	21.75	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Outer_Full	23.14	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Inner_Full	24.27	PC3	PASS
DC_2A_n78A-34	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.79	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.97	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	24.02	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	24.02	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	22.52	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	21.86	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Outer_Full	22.98	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Inner_Full	23.99	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.66	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.54	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	23.67	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	23.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	23.38	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	21.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Outer_Full	22.66	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Inner_Full	23.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.92	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	23.82	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	24.01	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	22.34	PC3	PASS
DC_2A_n78A-34	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	21.79	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Outer_Full	22.80	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Inner_Full	23.90	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.75	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.43	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	23.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	23.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	22.55	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	21.26	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Outer_Full	22.80	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Inner_Full	23.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.59	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.45	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	23.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	22.42	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	21.25	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Outer_Full	22.60	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Inner_Full	23.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.31	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.37	PC3	PASS
DC_2A_n78A-34	30	5+90	DFT-QPSK	M+L	Outer_Full	23.76	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	23.88	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	22.06	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	21.22	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Outer_Full	22.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Inner_Full	23.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.55	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	21.37	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	23.74	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	23.95	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	22.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	21.55	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Outer_Full	22.80	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Inner_Full	23.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	21.45	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	23.67	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	23.89	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	22.63	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	21.24	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Outer_Full	22.74	PC3	PASS
DC_2A_n78A-34	30	5+90	CP-QPSK	M+H	Inner_Full	23.84	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.21	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.28	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	23.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	23.88	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	21.20	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Outer_Full	22.77	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Inner_Full	23.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.23	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.33	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	23.70	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	23.77	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	22.38	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	21.63	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Outer_Full	22.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Inner_Full	23.73	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.36	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	23.69	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	24.52	PC3	PASS
DC_2A_n78A-34	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	21.97	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	21.22	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Outer_Full	22.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Inner_Full	23.84	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

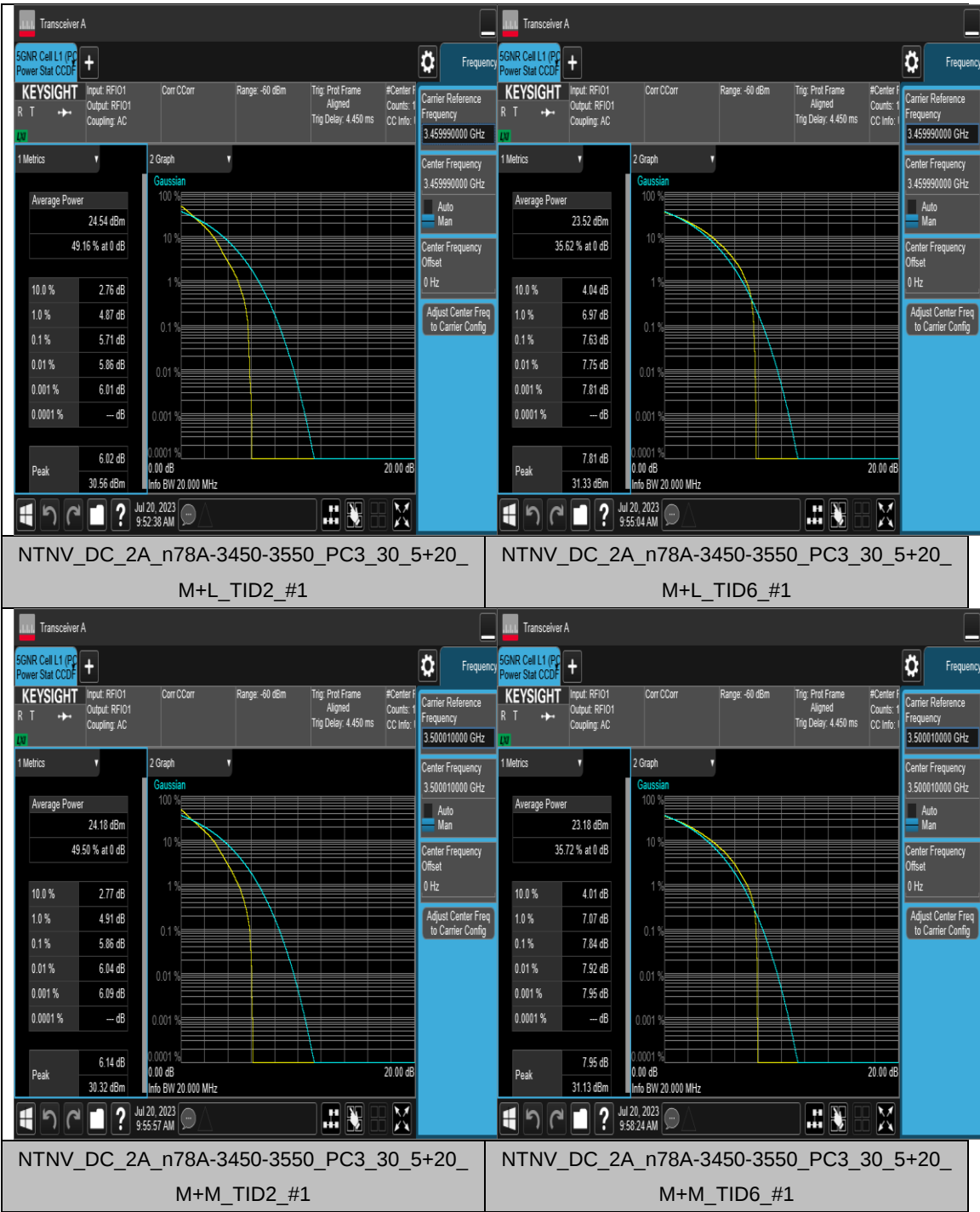
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	5.71	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	7.63	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	5.86	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Full	7.84	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	5.89	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	8.02	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	5.94	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	7.95	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	6.13	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Full	7.27	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Full	6.16	≤13	PASS
DC_2A_n7	30	5+40	CP-QPSK	M+H	Outer_Full	7.55	≤13	PASS

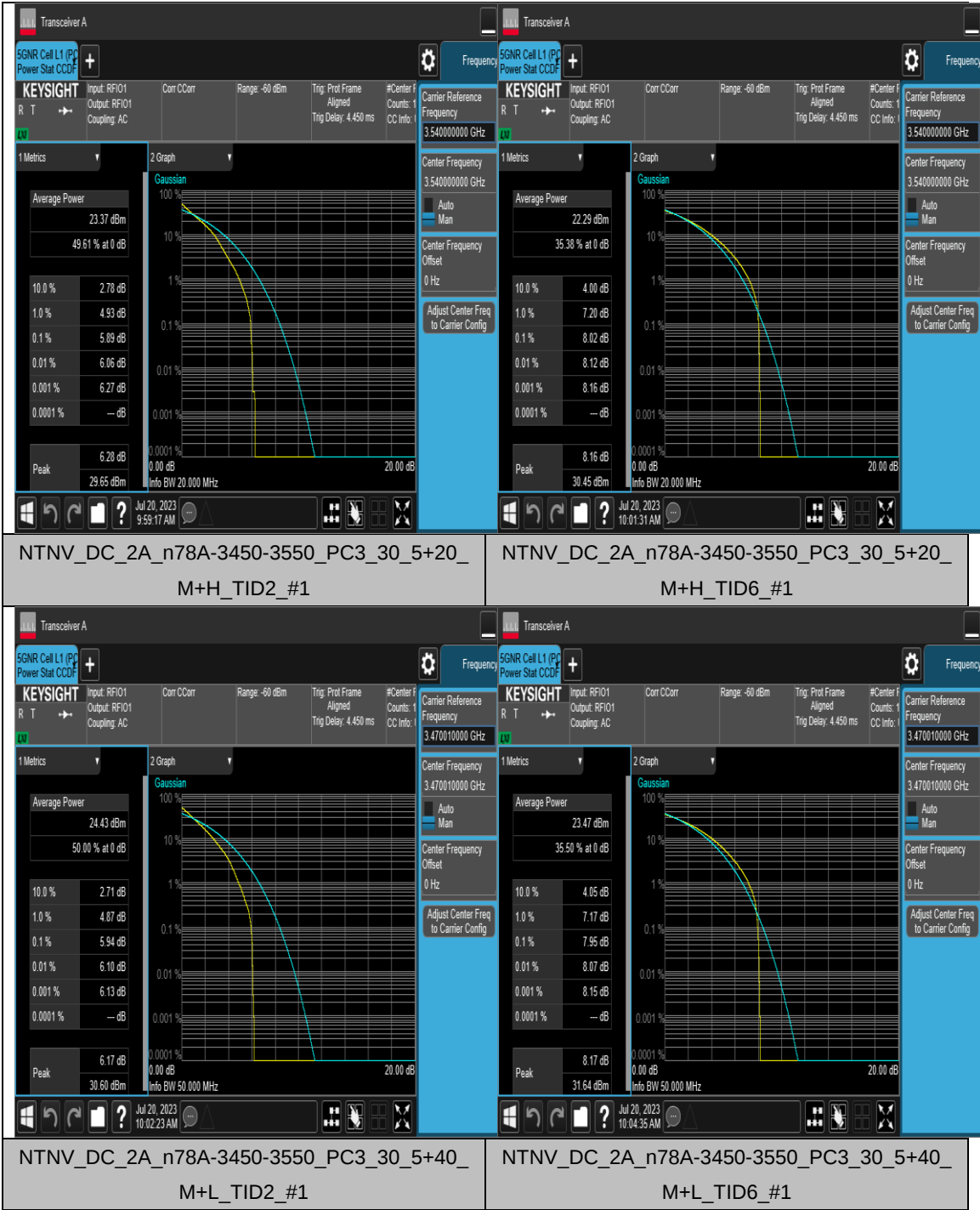
8A-3450-35 50								
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	7.28	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	5.50	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+M	Outer_Full	7.27	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+H	Outer_Full	5.74	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	7.62	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	5.40	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	7.42	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	5.53	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Full	7.43	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	5.69	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	7.66	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	5.61	≤13	PASS
DC_2A_n7 8A-3450-35	30	5+80	CP-QPSK	M+L	Outer_Full	7.48	≤13	PASS

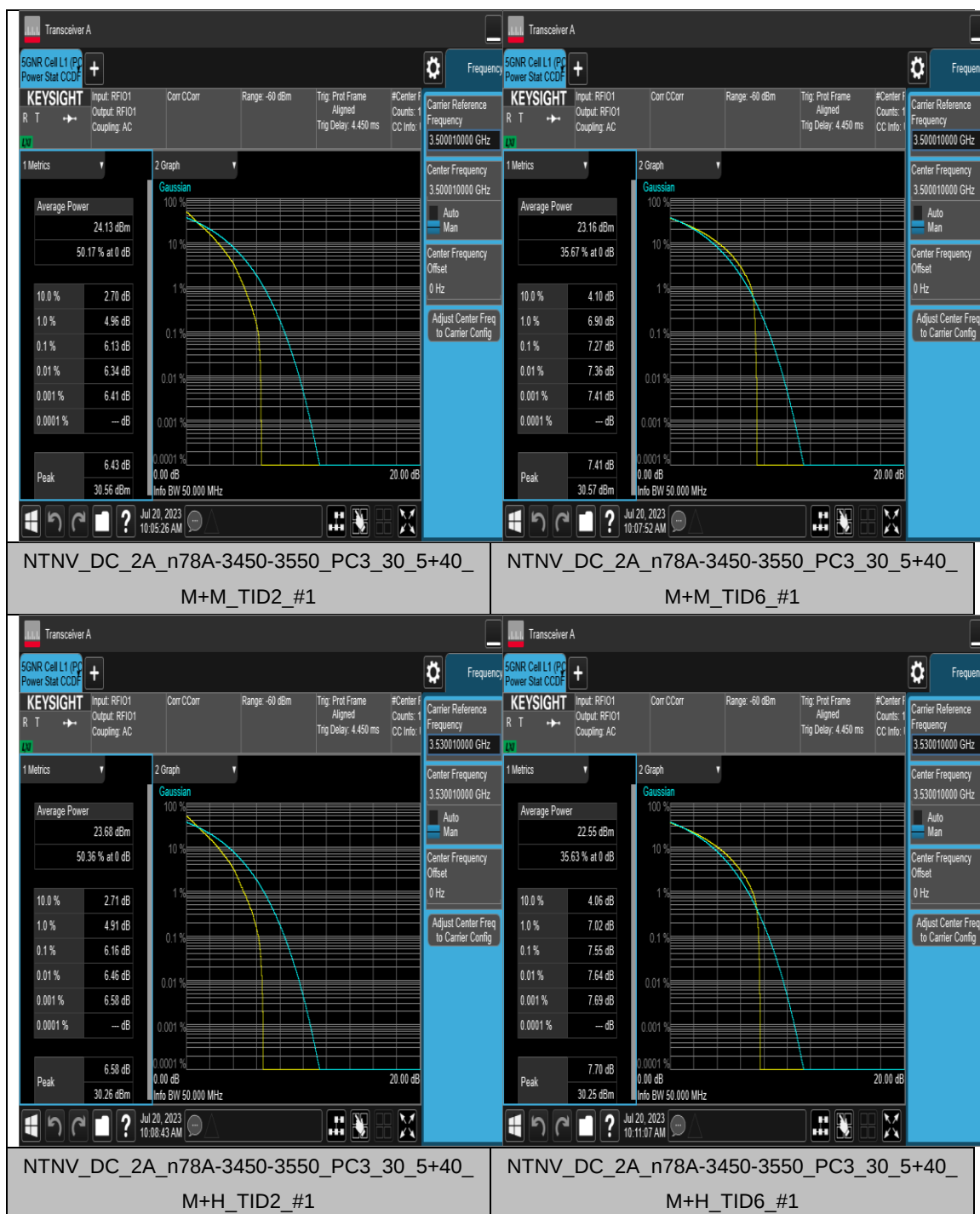
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DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	5.73	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Full	7.75	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+H	Outer_Full	5.93	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	7.95	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	5.53	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	7.52	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	5.59	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Full	7.59	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	5.70	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	7.79	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	5.53	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Full	7.67	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	5.60	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	CP-QPSK	M+M	Outer_Full	7.72	≤13	PASS

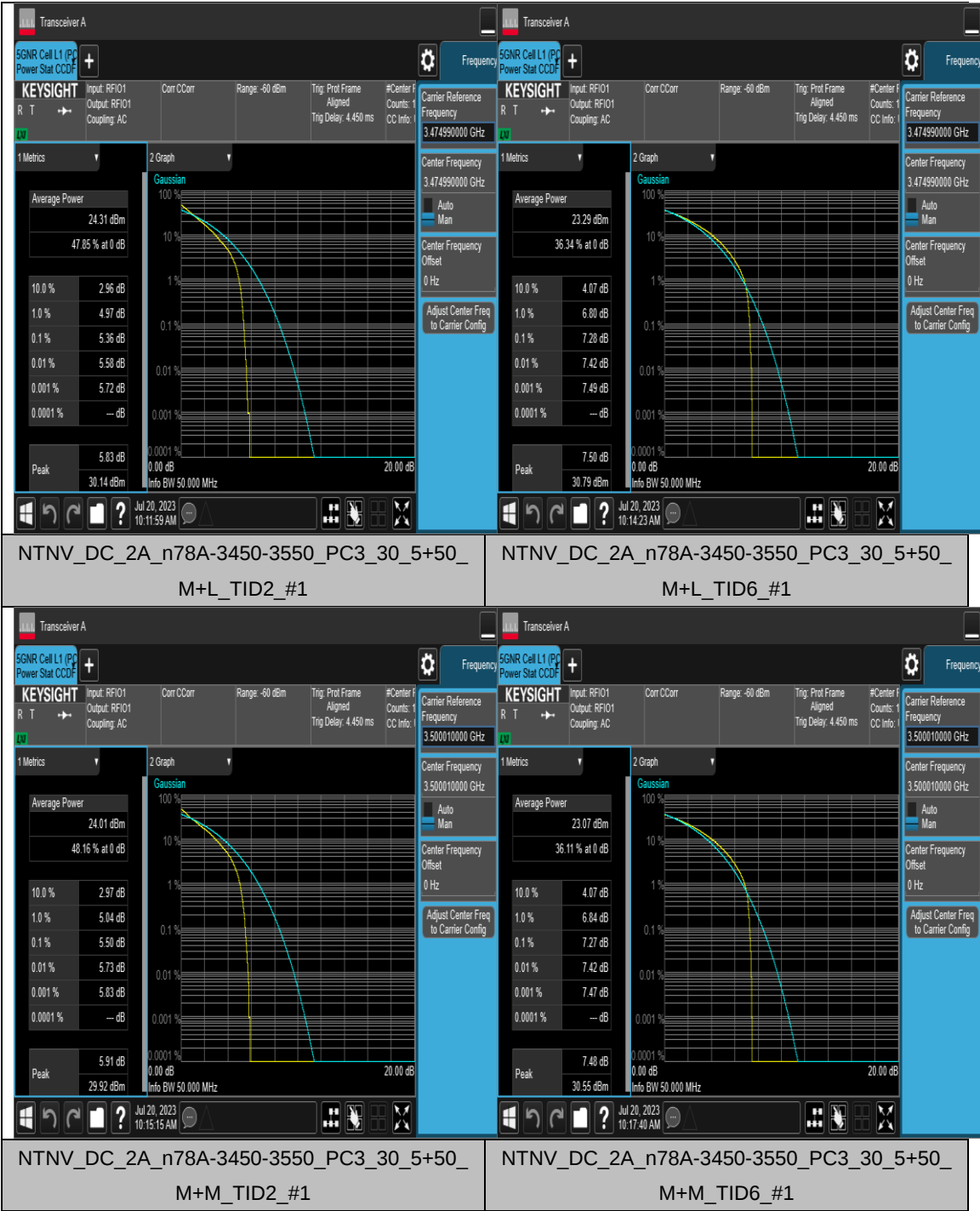
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	5.57	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	7.64	≤13	PASS

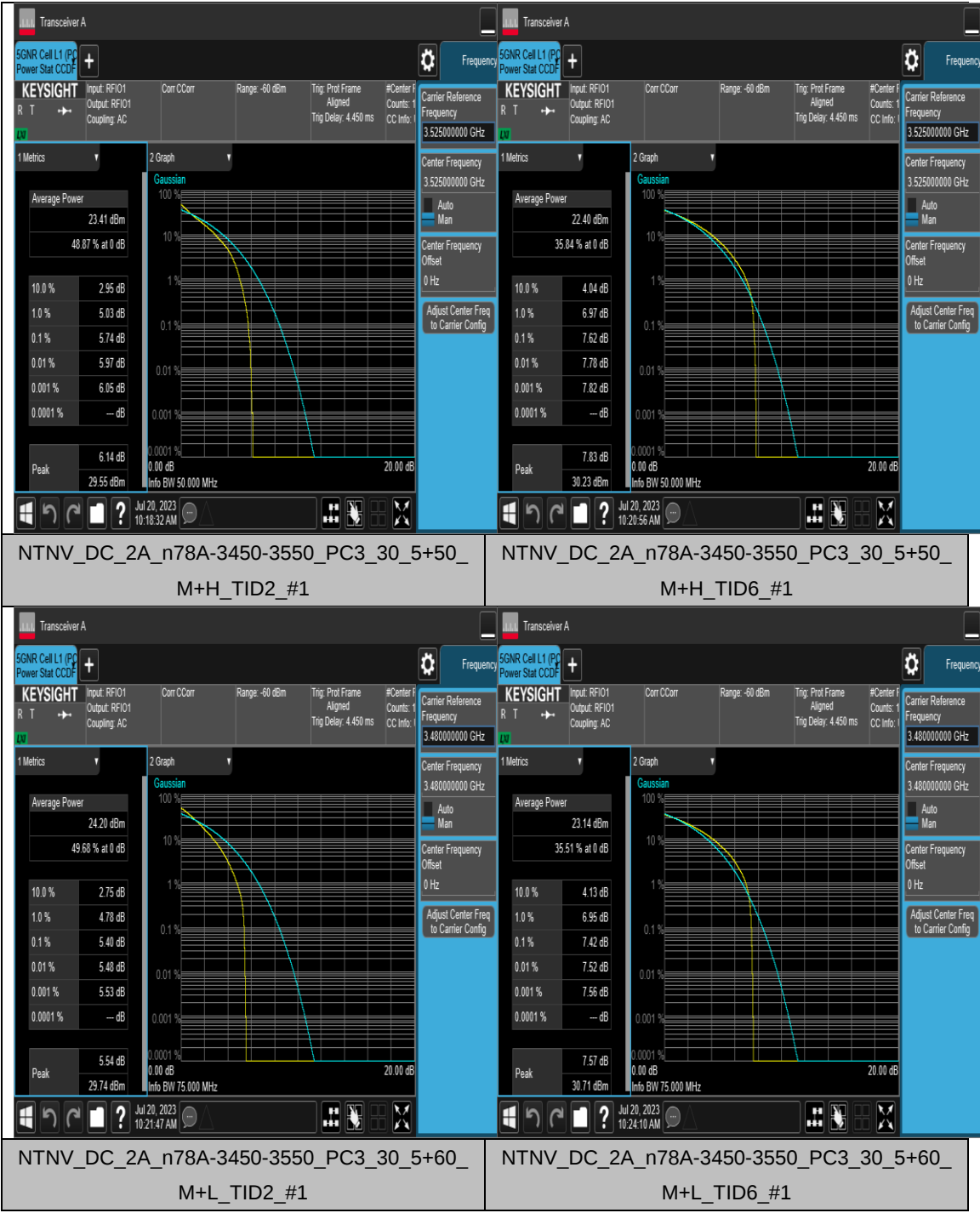
Test Graphs

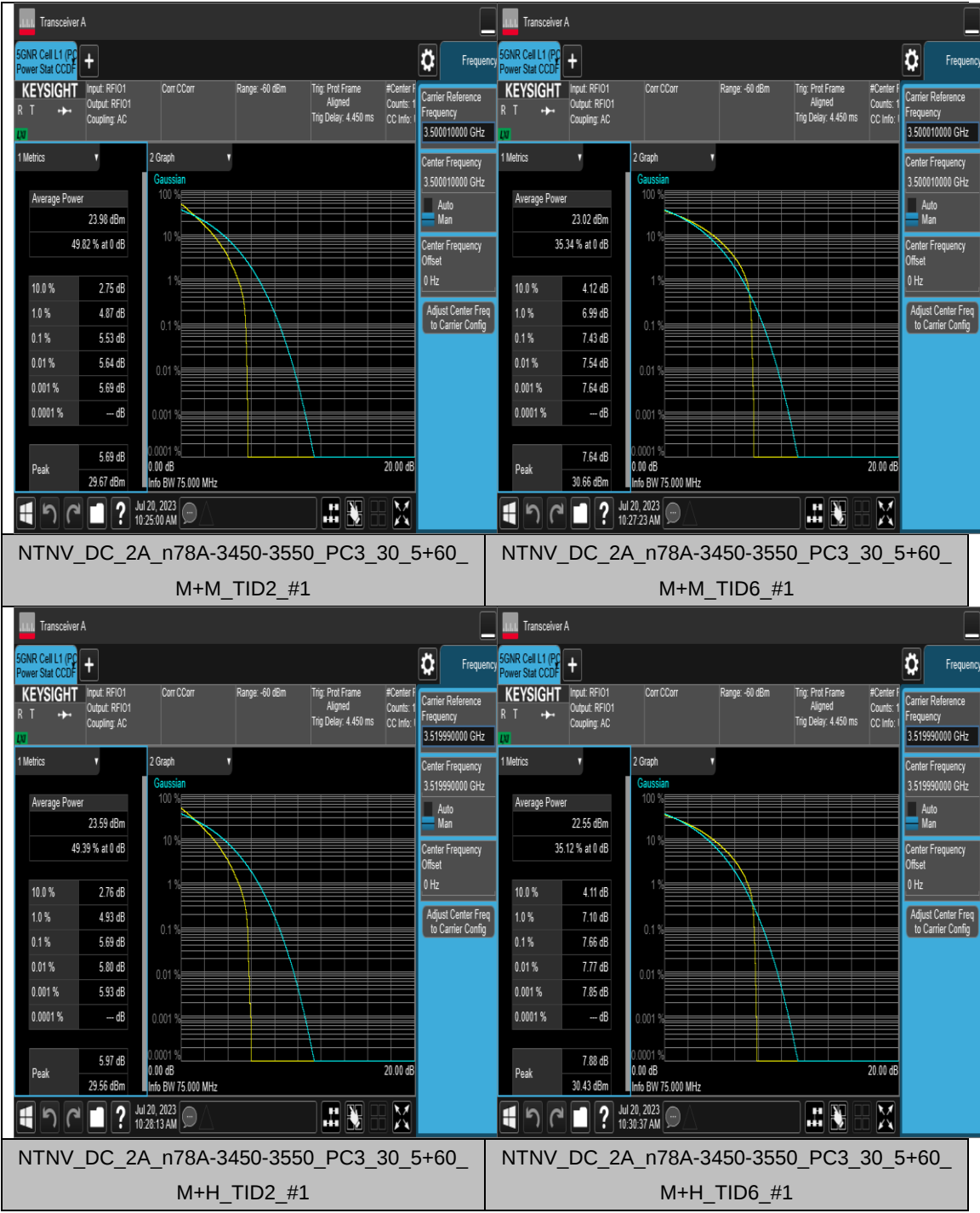


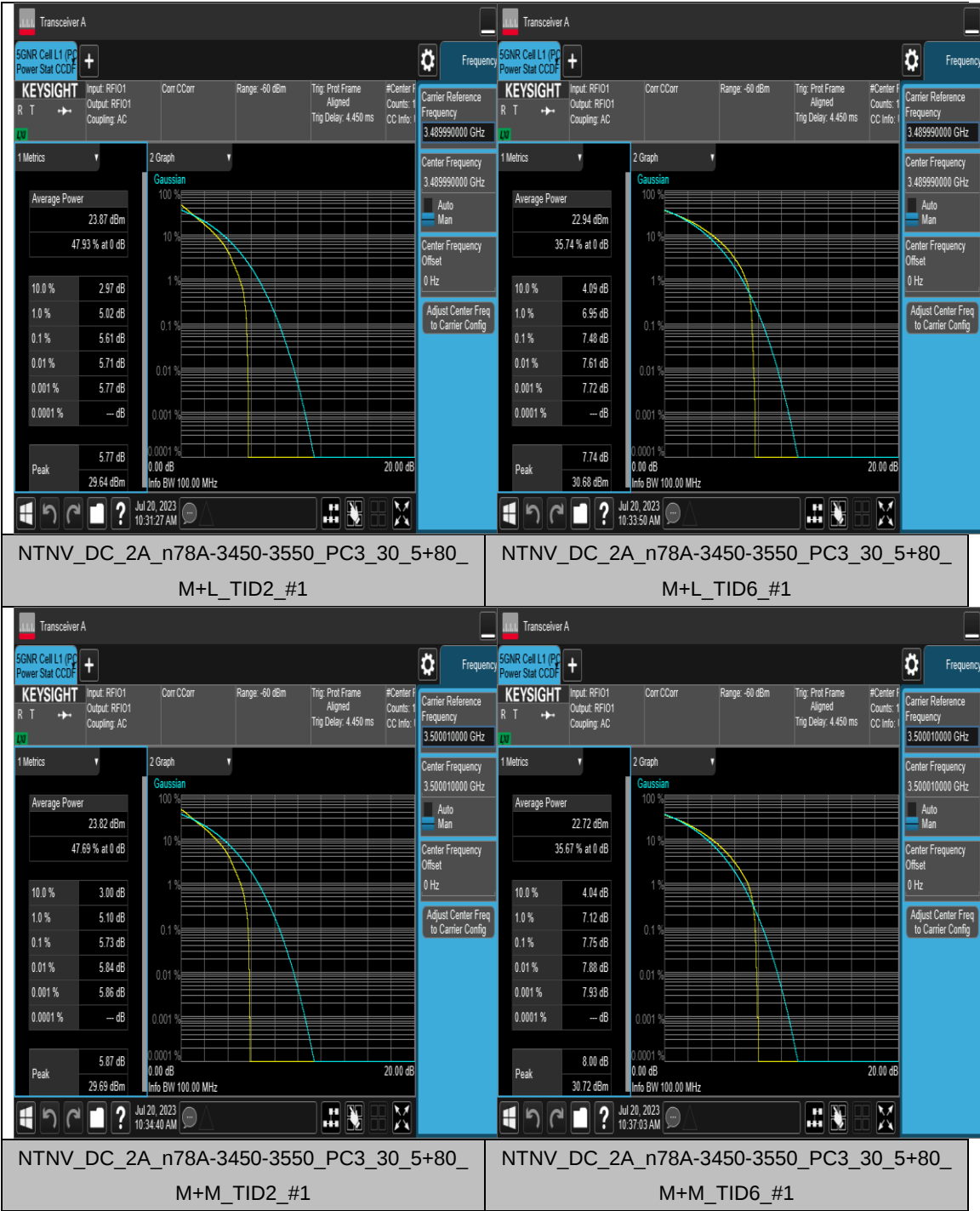


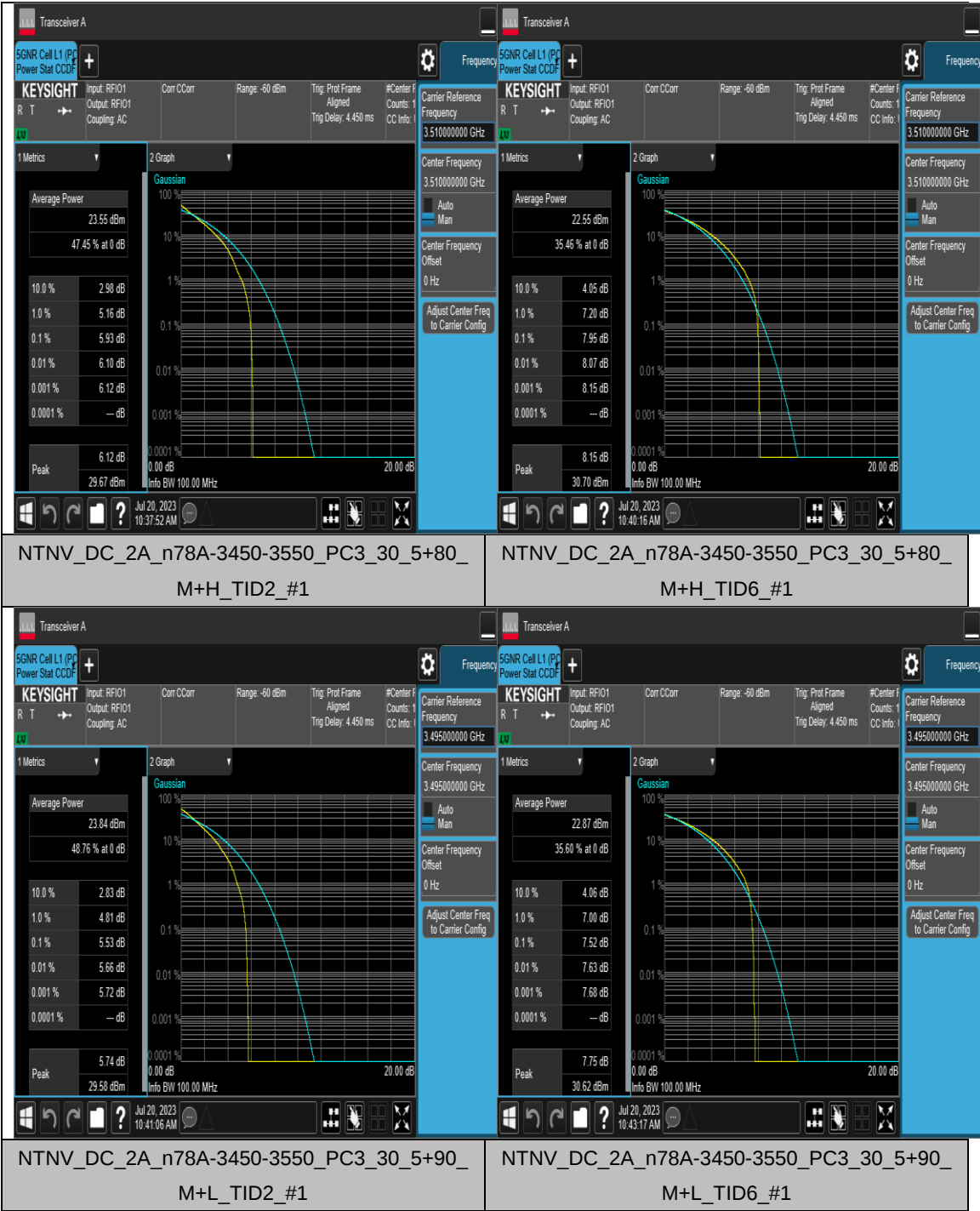


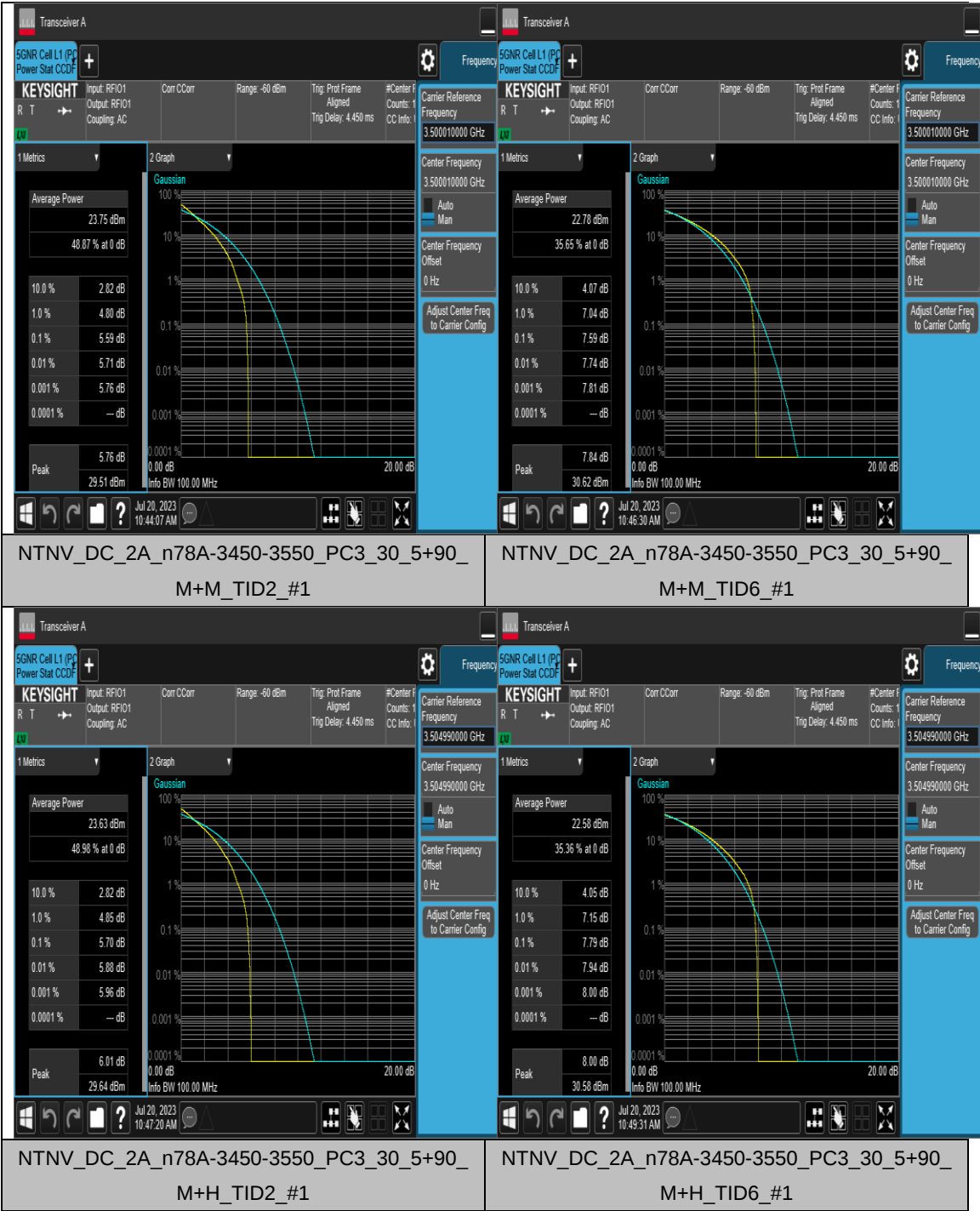


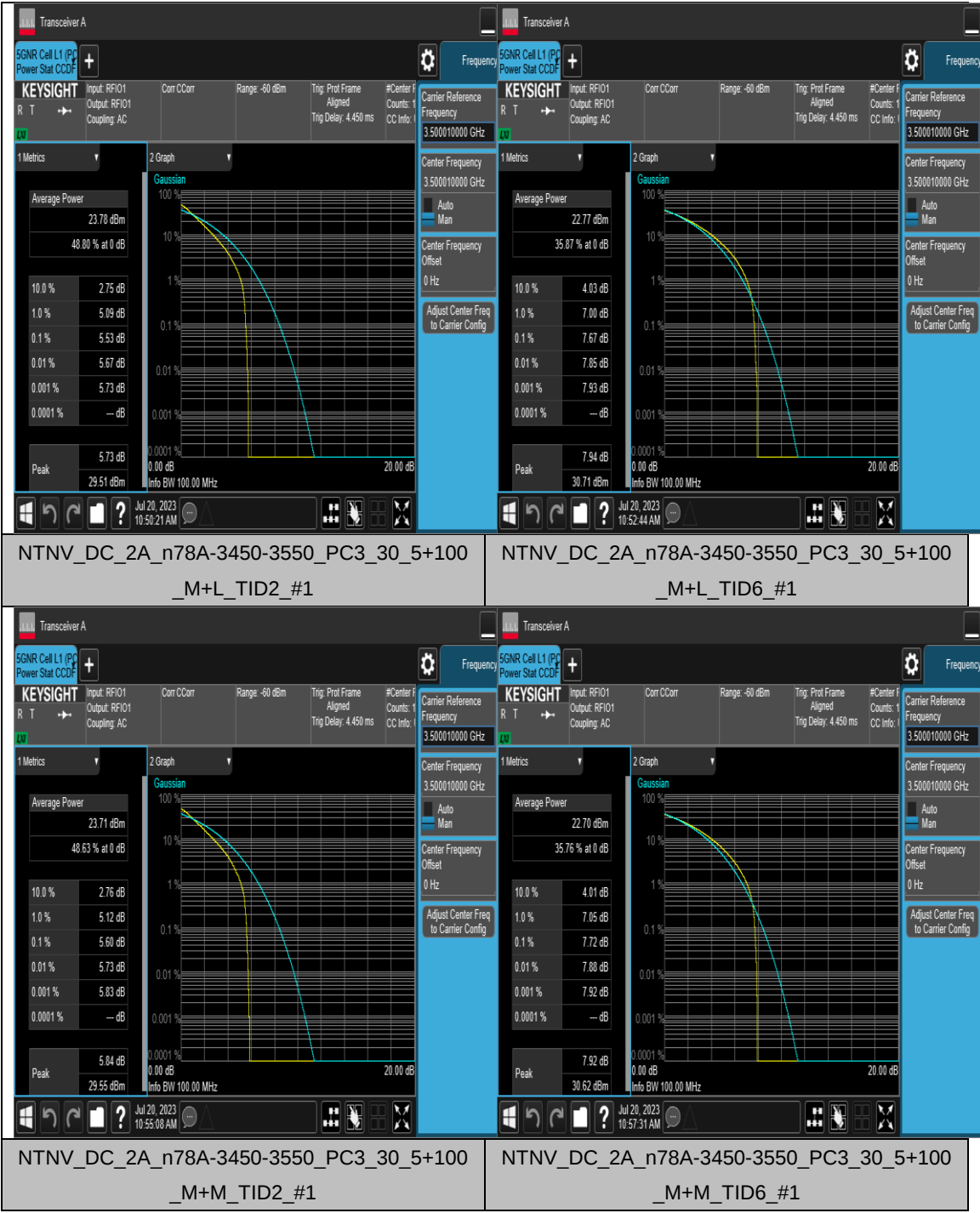


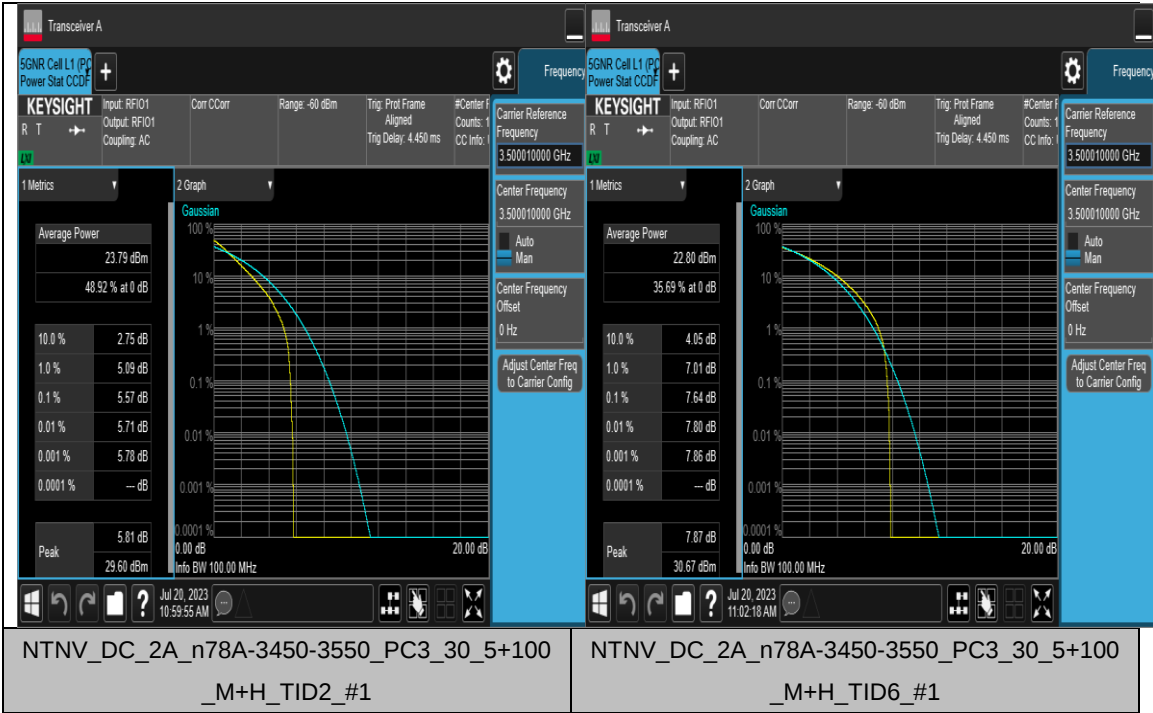












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	17.806	19.12	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	18.204	19.44	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	17.826	19.07	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Full	18.218	19.32	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	17.848	18.87	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	18.254	19.53	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	35.696	37.42	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	37.789	39.27	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	35.801	37.35	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Full	37.800	39.48	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Full	35.682	37.44	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	37.793	39.65	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	45.645	47.40	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	47.433	49.45	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	45.677	47.48	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+M	Outer_Full	47.365	49.55	PASS
DC_2A_n78A-3450-35	30	5+50	DFT-QPSK	M+H	Outer_Full	45.687	47.49	PASS

50								
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	47.415	49.44	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	57.724	60.20	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	57.838	60.19	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	57.797	60.19	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Full	57.905	59.91	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	57.820	59.93	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	57.739	60.15	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	77.077	79.80	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	77.304	80.09	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	76.984	79.86	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Full	77.343	80.00	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+H	Outer_Full	77.027	79.78	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	77.312	80.14	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	86.577	89.70	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	87.342	90.44	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	86.477	89.62	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Full	87.361	90.35	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	86.671	89.69	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	87.470	90.40	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	96.129	99.66	PASS
DC_2A_n78A-3450-35	30	5+100	CP-QPSK	M+L	Outer_Full	97.159	100.6	PASS

50								
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	96.052	99.61	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+M	Outer_Full	97.327	100.7	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	96.183	99.64	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	97.262	100.9	PASS

Test Graphs

