



SAR TEST REPORT

Product Name: Duet

Model Name: HM-1005

FCC ID: 2AUCLHM-1005

Issued For : FX TECHNOLOGY LIMITED

2 Stone Buildings, Lincoln's Inn, London, United Kingdom

Issued By : Shenzhen LGT Test Service Co., Ltd.

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Report Number: LGT23E031HA01

Sample Received Date: May. 18, 2023

Date of Test: June. 21, 2023 ~July. 26, 2023

Date of Issue: Aug. 02, 2023

Head: 1.102 W/kg

Max. SAR (1g):

Body: 1.188 W/kg

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Revision History

Rev.	Issue Date	Contents
00	Aug. 02, 2023	Initial Issue



TEST REPORT CERTIFICATION

Applicant FX TECHNOLOGY LIMITED
Address 2 Stone Buildings, Lincoln's Inn, London, England
Manufacture UWIN INNOVATIOIN (HONG KONG)LIMITED
Address ROOM D 10/F TOWER A BILLION CENTRE 1 WANG KWONG RD KOWLOON BAY KL
Product Name Duet
Trademark Linxdot
Model Name HM-1005
Sample number LGT2305027

APPLICABLE STANDARDS	
STANDARD	TEST RESULTS
ANSI/IEEE Std. C95.1-1992 FCC 47 CFR Part 2 (2.1093) IEEE 1528: 2013	PASS

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1. General Information

Environmental evaluation measurements of specific absorption rate (SAR) distributions in emulated human head and body tissues exposed to radio frequency (RF) radiation from wireless portable devices for compliance with the rules and regulations of the U.S. Federal Communications Commission (FCC).

1.1 EUT Description

Product Name	Duet		
Trademark	Linxdot		
Model Name	HM-1005		
Series Model	N/A		
Model Difference	N/A		
Device Category	Portable		
Product stage	Production unit		
RF Exposure Environment	General Population / Uncontrolled		
Hardware Version	N/A		
Software Version	N/A		
Frequency Range	GSM 850:824 MHz ~ 849 MHz PCS 1900:1850 MHz ~ 1910 MHz WCDMA Band II:1850 MHz ~ 1910 MHz WCDMA Band IV:1710 MHz ~ 1755 MHz WCDMA Band V:824 MHz ~ 849 MHz LTE Band 2:1850 MHz ~ 1910 MHz LTE Band 4:1710 MHz ~ 1755 MHz LTE Band 5:824 MHz ~ 849 MHz LTE Band 12:699 MHz ~ 716 MHz LTE Band 17:704 MHz ~ 716 MHz LTE Band 30:2305 MHz ~ 2315 MHz LTE Band 48:3550 MHz ~ 3700 MHz LTE Band 66:1710 MHz ~ 1780 MHz LTE Band 71:663 MHz ~ 698 MHz NR N5:824 MHz ~ 849 MHz NR N41:2496 MHz ~ 2690 MHz NR N48:3550 MHz ~ 3700 MHz NR N71:663 MHz ~ 698 MHz NR N77:3450 MHz ~ 3550 MHz, 3700 MHz ~ 3980 MHz NSA: B2+n41/B6+n41, B2+n71/B66+n71 WLAN 802.11b/g/n20:2412 MHz ~ 2462 MHz WLAN 802.11n40:2422 MHz ~ 2452 MHz WLAN 802.11a/n/ac20/40/80:5150 ~ 5250 MHz WLAN 802.11a/n/ac20/40/80:5250 ~ 5350 MHz WLAN 802.11a/n/ac20/40/80:5470 ~ 5725 MHz WLAN 802.11a/n/ac20/40/80:5725 ~ 5850 MHz Bluetooth:2402 ~ 2480 MHz		
Max. Reported SAR(1g): (Limit:1.6W/kg) Test distance: Head:0mm Body:10mm	Mode	Head (W/ kg)	Body Worn and Hotspot (W/ kg)
	GSM 850	0.217	0.205
	PCS 1900	0.194	0.515
	WCDMA Band II	0.349	0.636



	WCDMA Band IV	0.239	0.843
	WCDMA Band V	0.23	0.226
	LTE Band 2	0.31	0.667
	LTE Band 4	0.298	1.093
	LTE Band 5	0.264	0.224
	LTE Band 12	0.305	0.333
	LTE Band 17	0.302	0.348
	LTE Band 30	0.173	0.296
	LTE Band 48	1.102	0.658
	LTE Band 66	0.368	0.727
	LTE Band 71	0.209	0.314
	2.4G WLAN ANT A	0.155	0.130
	2.4G WLAN ANT B	0.117	0.169
	2.4G WLAN MIMO	0.236	0.202
	5.2G WLAN ANT A	0.316	0.207
	5.2G WLAN ANT B	0.355	0.336
	5.2G WLAN MIMO	0.326	0.280
	5.3G WLAN ANT A	0.269	0.180
	5.3G WLAN ANT B	0.123	0.386
	5.3G WLAN MIMO	0.412	0.264
	5.6G WLAN ANT A	0.328	0.191
	5.6G WLAN ANT B	0.232	0.313
	5.6G WLAN MIMO	0.442	0.179
	5.8G WLAN ANT A	0.311	0.332
	5.8G WLAN ANT B	0.339	0.329
	5.8G WLAN MIMO	0.351	0.316
	Bluetooth	0.05	0.097
	NR SA N5	0.155	0.193
	NR SA N41	0.21	0.461
	NR SA N48	0.992	0.674
	NR SA N71	0.154	0.167
	NR SA N77	0.889	0.689
	NSA N41+B2	0.52	1.128
	NSA N41+B66	0.578	1.188
	NSA N71+B2	0.464	0.834
	NSA N71+B66	0.522	0.894
1-g Sum SAR		1.544	1.574
Battery	Capacity: 5500mAh Rated Voltage: 3.85V		
Description test modes	SIM 1 and SIM 2 is a chipset unit and tested as single chipset, SIM 1 is used to tested.		



Operating Mode:	GSM: GSM Voice; GPRS/EGPRS Class 12 WCDMA: RMC, HSDPA, HSUPA Release 6 LTE: QPSK, 16QAM 5G NR: DFT-s-OFDM, CP-OFDM ($\pi/2$ shift BPSK, QPSK, 16QAM, 64QAM, 256QAM) 2.4G WLAN: 802.11b(DSSS): CCK, DQPSK, DBPSK 802.11n/g(OFDM): BPSK, QPSK, 16-QAM, 64-QAM 5G WLAN: 802.11a/n(OFDM): BPSK, QPSK, 16-QAM, 64-QAM 802.11ac (OFDM): BPSK, QPSK, 16-QAM, 64-QAM, 256-QAM Bluetooth: GFSK + $\pi/4$ DQPSK+8DPSK BLE: GFSK
Antenna Specification	GSM/WCDMA/CDMA/LTE/NR: PIFA Antenna Bluetooth: PIFA Antenna WLAN: PIFA Antenna
Operating Mode	Maximum continuous output
SIM Card	Support dual-SIM, dual standby, the multiple SIM card with two lines cannot transmitting at the same time
Hotspot Mode	Support
DTM Mode	Not Support
<p>Note:</p> <ol style="list-style-type: none"> 1. The dual SIM card mobile has 2 SIM slots and supports dual SIM dual standby. The WWAN radio transmission will be enabled by either one SIM at a time (Single active) 2. After pre-scan two SIM cards power, we found test result of the SIM1 was the worse, so we chose SIM1 card to perform all tests. 3. The EUT battery must be fully charged and checked periodically during the test to ascertain uniform power 	

EN-DC SAR test summary:

Band	Mode	Head Max SAR	Head Test sum	Body Max SAR	Body Test sum
		(W/Kg)		(W/Kg)	
NSA N41+B2	SA N41	0.210	0.52	0.461	1.128
	LTE B2	0.310		0.667	
NSA N41+B66	SA N41	0.210	0.578	0.461	1.188
	LTE B66	0.368		0.727	
NSA N71+B2	SA N71	0.154	0.464	0.167	0.834
	LTE B2	0.310		0.667	
NSA N71+B66	SA N41	0.154	0.522	0.167	0.894
	LTE B66	0.368		0.727	



1.2 Test Environment

Ambient conditions in the SAR laboratory:

Items	Required
Temperature (°C)	18-25
Humidity (%RH)	30-70

1.3 Test Factory

Company Name:	Shenzhen LGT Test Service Co., Ltd.
Address:	Room 205, Building 13, Zone B, Chen Hsong Industrial Park, No.177 Renmin West Road, Jinsha Community, Kengzi Street, Pingshan New District, Shenzhen, China
Accreditation Certificate	FCC Registration No.: 746540
	A2LA Certificate No.: 6727.01
	IC Registration No.: CN0136



2. Test Standards and Limits

No.	Identity	Document Title
1	47 CFR Part 2	Frequency Allocations and Radio Treaty Matters; General Rules and Regulations
2	ANSI/IEEE Std. C95.1-1992	IEEE Standard for Safety Levels with Respect to Human Exposure to Radio Frequency Electromagnetic Fields, 3 kHz to 300 GHz
3	IEEE Std. 1528-2013	Recommended Practice for Determining the Peak Spatial-Average Specific Absorption Rate (SAR) in the Human Head from Wireless Communications Devices: Measurement Techniques
4	FCC KDB 447498 D01 v06	Mobile and Portable Device RF Exposure Procedures and Equipment Authorization Policies
5	FCC KDB 865664 D01 v01r04	SAR Measurement 100 MHz to 6 GHz
6	FCC KDB 865664 D02 v01r02	RF Exposure Reporting
7	FCC KDB 941225 D01 v03r01	SAR Measurement Procedures for 3G Devices
8	FCC KDB 941225 D05 v02r05	SAR for LTE Devices
9	FCC KDB 941225 D06 v02r01	Hotspot Mode SAR
10	FCC KDB 648474 D04 v01r03	SAR Evaluation Considerations for Wireless Handsets
11	FCC KDB 248227 D01 Wi-Fi SAR v02r02	SAR Considerations for 802.11 Devices

(A). Limits for Occupational/Controlled Exposure (W/kg)

<u>Whole-Body</u>	<u>Partial-Body</u>	<u>Hands, Wrists, Feet and Ankles</u>
0.4	8.0	20.0

(B). Limits for General Population/Uncontrolled Exposure (W/kg)

<u>Whole-Body</u>	<u>Partial-Body</u>	<u>Hands, Wrists, Feet and Ankles</u>
0.08	1.6	4.0

NOTE: Whole-Body SAR is averaged over the entire body, partial-body SAR is averaged over any 1 gram of tissue defined as a tissue volume in the shape of a cube. SAR for hands, wrists, feet and ankles is averaged over any 10 grams of tissue defined as a tissue volume in the shape of a cube.

Population/Uncontrolled Environments:

Are defined as locations where there is the exposure of individuals who have no knowledge or control of their exposure.

Occupational/Controlled Environments:

Are defined as locations where there is exposure that may be incurred by people who are aware of the potential for exposure, (i.e. as a result of employment or occupation).

<p>NOTE</p> <p>GENERAL POPULATION/UNCONTROLLED EXPOSURE</p> <p>PARTIAL BODY LIMIT</p> <p>1.6 W/kg</p>



3. SAR Measurement System

3.1 Definition of Specific Absorption Rate (SAR)

SAR is related to the rate at which energy is absorbed per unit mass in an object exposed to a radio field. The SAR distribution in a biological body is complicated and is usually carried out by experimental techniques or numerical modeling. The standard recommends limits for two tiers of groups, occupational/controlled and general population/uncontrolled, based on a person's awareness and ability to exercise control over his or her exposure. In general, occupational/controlled exposure limits are higher than the limits for general population/uncontrolled.

The SAR definition is the time derivative (rate) of the incremental energy (dW) absorbed by (dissipated in) an incremental mass (dm) contained in a volume element (dv) of a given density (ρ). The equation description is as below:

$$SAR = \frac{d}{dt} \left(\frac{dW}{dm} \right) = \frac{d}{dt} \left(\frac{dW}{\rho dv} \right)$$

SAR is expressed in units of Watts per kilogram (W/kg) SAR measurement can be related to the electrical field in the tissue by

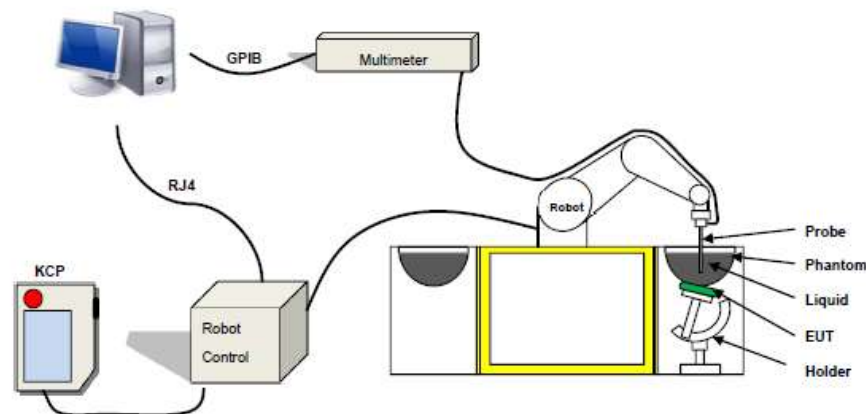
$$SAR = \frac{\sigma E^2}{\rho}$$

Where: σ is the conductivity of the tissue;

ρ is the mass density of the tissue and E is the RMS electrical field strength.

3.2 SAR System

MVG SAR System Diagram:



COMOSAR is a system that is able to determine the SAR distribution inside a phantom of human being according to different standards. The COMOSAR system consists of the following items:

- Main computer to control all the system
- 6 axis robot
- Data acquisition system
- Miniature E-field probe
- Phone holder
- Head simulating tissue



The following figure shows the system.



The EUT under test operating at the maximum power level is placed in the phone holder, under the phantom, which is filled with head simulating liquid. The E-Field probe measures the electric field inside the phantom. The OpenSAR software computes the results to give a SAR value in a 1g or 1g mass.

3.2.1 Probe

For the measurements the Specific Dosimetric E-Field Probe SN 04/22 EPGO364 with following specifications is used

- Probe Length: 330 mm
- Length of Individual Dipoles: 2mm
- Maximum external diameter: 8 mm
- Probe Tip External Diameter: 2.5 mm
- Distance between dipole/probe extremity: 1 mm
- Dynamic range: 0.01-100 W/kg
- Probe linearity: 3%
- Axial Isotropy: < 0.10 dB
- Spherical Isotropy: < 0.10 dB
- Calibration range: 600 MHz to 6 GHz for head & body simulating liquid.
- Angle between probe axis (evaluation axis) and surface normal line: less than 30°



Figure 1-MVG COMOSAR Dosimetric E field Probe



3.2.2 Phantom

For the measurements the Specific Anthropomorphic Mannequin (SAM) defined by the IEEE SCC-34/SC2 group is used. The phantom is a polyurethane shell integrated in a wooden table. The thickness of the phantom amounts to 2mm +/- 0.2mm. It enables the dosimetric evaluation of left and right phone usage and includes an additional flat phantom part for the simplified performance check. The phantom set-up includes a cover, which prevents the evaporation of the liquid.



Figure-SN 06/22 SAM 148



Figure-SN 06/22 ELLI 51

3.2.3 Device Holder



The SAR in the phantom is approximately inversely proportional to the square of the distance between the source and the liquid surface. For a source at 5 mm distance, a positioning uncertainty of ± 0.5 mm would produce a SAR uncertainty of ± 20 %. Accurate device positioning is therefore crucial for accurate and repeatable measurements. The positions in which the devices must be measured are defined by the standards.



4. Tissue Simulating Liquids

4.1 Simulating Liquids Parameter Check

The simulating liquids should be checked at the beginning of a series of SAR measurements to determine if the dielectric parameters are within the tolerances of the specified target values

The uncertainty due to the liquid conductivity and permittivity arises from two different sources. The first source of error is the deviation of the liquid conductivity from its target value (max _ 5 %) and the second source of error arises from the measurement procedures used to assess conductivity. The uncertainty shall be assessed using a rectangular probability For 1 g averaging, the maximum weighting coefficient for SAR is 0,5.

IEEE SCC-34/SC-2 RECOMMENDED TISSUE DIELECTRIC PARAMETERS

The head and body tissue dielectric parameters recommended by the IEEE SCC-34/SC-2 have been incorporated in the following table.

Frequency	ϵ_r	σ 10g S/m
300	45.3	0.87
450	43.5	0.87
750	41.9	0.89
835	41.5	0.90
900	41.5	0.97
1450	40.5	1.20
1800 to 2000	40.0	1.40
2100	39.8	1.49
2450	39.2	1.80
2600	39.0	1.96
3000	38.5	2.40
3500	37.9	2.91
4000	37.4	3.43
4500	36.8	3.94
5000	36.2	4.45
5200	36.0	4.66
5400	35.8	4.86
5600	35.5	5.07
5800	35.3	5.27



LIQUID MEASUREMENT RESULTS

Date	Ambient		Simulating Liquid		Parameters	Target	Measured	Deviation %	Limited %
	Temp. [°C]	Humidity %	Frequency (MHz)	Temp. [°C]					
2023-06-21	23.1	50	750	22.8	Permittivity	41.90	42.24	0.81	±5
					Conductivity	0.89	0.85	-4.49	±5
2023-07-09	21.6	57	750	21.3	Permittivity	41.90	42.61	1.69	±5
					Conductivity	0.89	0.91	2.25	±5
2023-07-10	20.5	47	835	20.3	Permittivity	41.50	41.69	0.46	±5
					Conductivity	0.90	0.88	-2.22	±5
2023-07-17	21.4	53	835	21.1	Permittivity	41.50	41.66	0.39	±5
					Conductivity	0.90	0.92	2.22	±5
2023-07-11	21	48	1800	20.7	Permittivity	40.00	40.86	2.15	±5
					Conductivity	1.40	1.44	2.86	±5
2023-07-12	20.2	42	1900	19.9	Permittivity	40.00	40.57	1.43	±5
					Conductivity	1.40	1.39	-0.71	±5
2023-07-13	21.6	57	2300	21.3	Permittivity	39.47	39.79	0.82	±5
					Conductivity	1.67	1.71	2.60	±5
2023-07-14	21.4	50	2450	21.1	Permittivity	39.20	39.76	1.43	±5
					Conductivity	1.80	1.85	2.78	±5
2023-07-18	22.3	49	2450	22.0	Permittivity	39.20	38.59	-1.56	±5
					Conductivity	1.80	1.75	-2.78	±5
2023-07-13	21.6	57	2600	21.3	Permittivity	39.00	40.58	4.05	±5
					Conductivity	1.96	1.92	-2.04	±5
2023-07-09	21.6	57	3500	21.3	Permittivity	37.90	38.13	0.61	±5
					Conductivity	2.91	2.96	1.72	±5
2023-07-15	23.4	51	3700	23.1	Permittivity	37.67	38.52	2.25	±5
					Conductivity	3.12	3.20	2.72	±5
2023-07-17	21.4	53	3500	21.1	Permittivity	37.90	37.42	-1.27	±5
					Conductivity	2.91	2.85	-2.06	±5
2023-07-18	24	55	5200	23.7	Permittivity	36.00	36.37	1.03	±5
					Conductivity	4.66	4.68	0.43	±5
2023-07-23	21.8	45	5200	21.5	Permittivity	36.00	37.16	3.22	±5
					Conductivity	4.66	4.68	0.43	±5
2023-07-21	21.8	45	5300	21.5	Permittivity	35.90	36.31	1.14	±5
					Conductivity	4.76	4.81	1.05	±5
2023-07-22	22.6	45	5300	22.3	Permittivity	35.90	37.13	3.43	±5
					Conductivity	4.76	4.78	0.42	±5
2023-07-24	21.9	60	5600	21.6	Permittivity	35.55	36.73	3.32	±5
					Conductivity	5.07	5.09	0.49	±5
2023-07-25	23.8	60	5600	23.5	Permittivity	35.55	36.09	1.52	±5
					Conductivity	5.07	5.06	-0.10	±5
2023-07-26	23.3	45	5800	23	Permittivity	35.30	35.87	1.61	±5
					Conductivity	5.27	5.23	-0.76	±5
2023-07-08	23.8	62	5800	23.5	Permittivity	35.30	35.61	0.88	±5
					Conductivity	5.27	5.30	0.57	±5

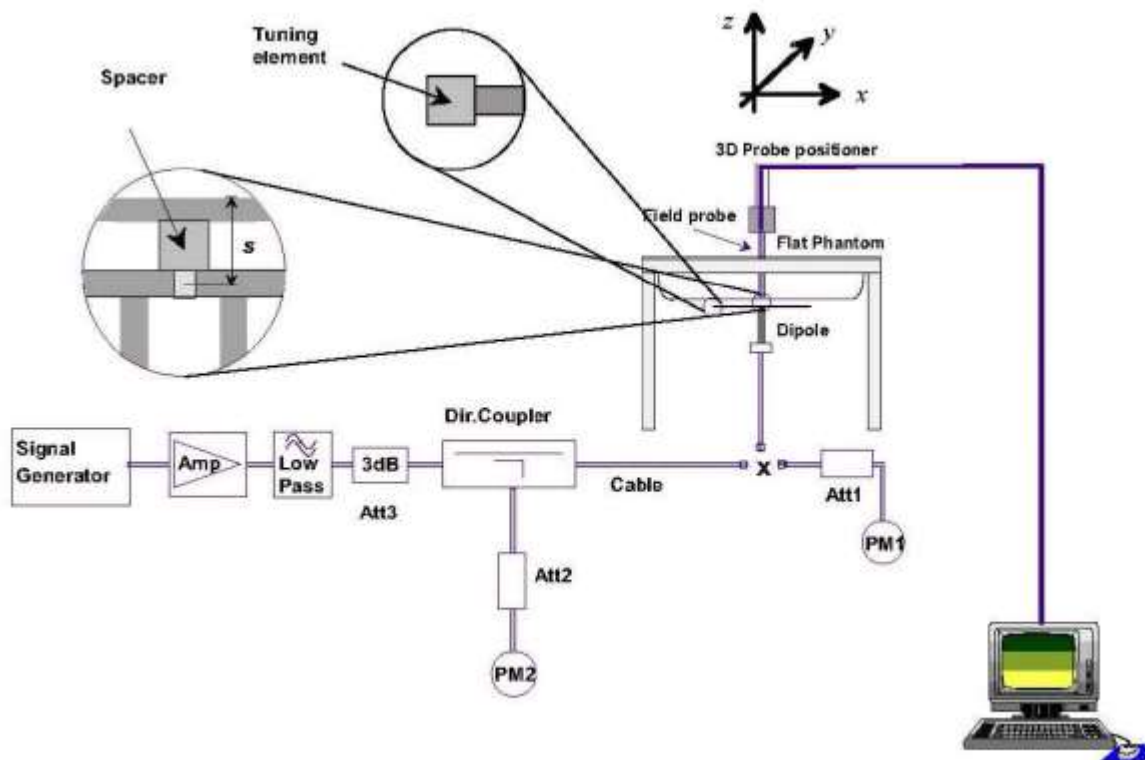


5. SAR System Validation

5.1 Validation System

Each MVG system is equipped with one or more system validation kits. These units, together with the predefined measurement procedures within the MVG software, enable the user to conduct the system performance check and system validation. System kit includes a dipole, and dipole device holder.

The system check verifies that the system operates within its specifications. It's performed daily or before every SAR measurement. The system check uses normal SAR measurement in the flat section of the phantom with a matched dipole at a specified distance. The system validation setup is shown as below.





5.2 Validation Result

Comparing to the original SAR value provided by MVG, the validation data should be within its specification of $\pm 10\%$.

Date	Freq.	Power	Tested Value	Normalized SAR	Target SAR	Tolerance	Limit
	(MHz)	(mW)	(W/Kg)	(W/kg)	1g(W/kg)	(%)	(%)
2023-06-21	750	100	0.866	8.66	8.27	4.72	10
2023-07-09	750	100	0.802	8.02	8.27	-3.02	10
2023-07-10	835	100	0.991	9.91	9.75	1.64	10
2023-07-17	835	100	0.969	9.69	9.75	-0.62	10
2023-07-11	1800	100	3.910	39.10	39.06	0.10	10
2023-07-12	1900	100	4.094	40.94	40.85	0.22	10
2023-07-13	2300	100	5.131	51.31	50.94	0.73	10
2023-07-14	2450	100	5.461	54.61	54.28	0.61	10
2023-07-16	2450	100	5.389	53.89	54.28	-0.72	10
2023-07-13	2600	100	5.657	56.57	56.58	-0.02	10
2023-07-09	3500	100	6.627	66.27	69.87	-5.15	10
2023-07-15	3700	100	6.598	65.98	69.81	-5.49	10
2023-07-17	3500	100	6.696	66.96	69.87	-4.16	10
2023-07-18	5200	100	7.773	77.73	77.64	0.12	10
2023-07-23	5200	100	7.275	72.75	77.64	-6.30	10
2023-07-21	5300	100	8.048	80.48	80.27	0.26	10
2023-07-22	5300	100	7.790	77.90	80.27	-2.95	10
2023-07-24	5600	100	7.857	78.57	78.35	0.28	10
2023-07-25	5600	100	8.306	83.06	78.35	6.01	10
2023-07-26	5800	100	7.503	75.03	74.92	0.15	10
2023-07-08	5800	100	7.275	72.75	74.92	-2.90	10

Note:

1. The tolerance limit of System validation $\pm 10\%$.
2. The dipole input power (forward power) was 100 mW.
3. The results are normalized to 1 W input power.



6. SAR Evaluation Procedures

The procedure for assessing the average SAR value consists of the following steps:

The following steps are used for each test position

- Establish a call with the maximum output power with a base station simulator. The connection between the mobile and the base station simulator is established via air interface
- Measurement of the local E-field value at a fixed location. This value serves as a reference value for calculating a possible power drift.
- Measurement of the SAR distribution with a grid of 8 to 16mm * 8 to 16 mm and a constant distance to the inner surface of the phantom. Since the sensors cannot directly measure at the inner phantom surface, the values between the sensors and the inner phantom surface are extrapolated. With these values the area of the maximum SAR is calculated by an interpolation scheme.
- Around this point, a cube of 30 * 30 * 30 mm or 32 * 32 * 32 mm is assessed by measuring 5 or 8 * 5 or 8*4 or 5 mm. With these data, the peak spatial-average SAR value can be calculated.

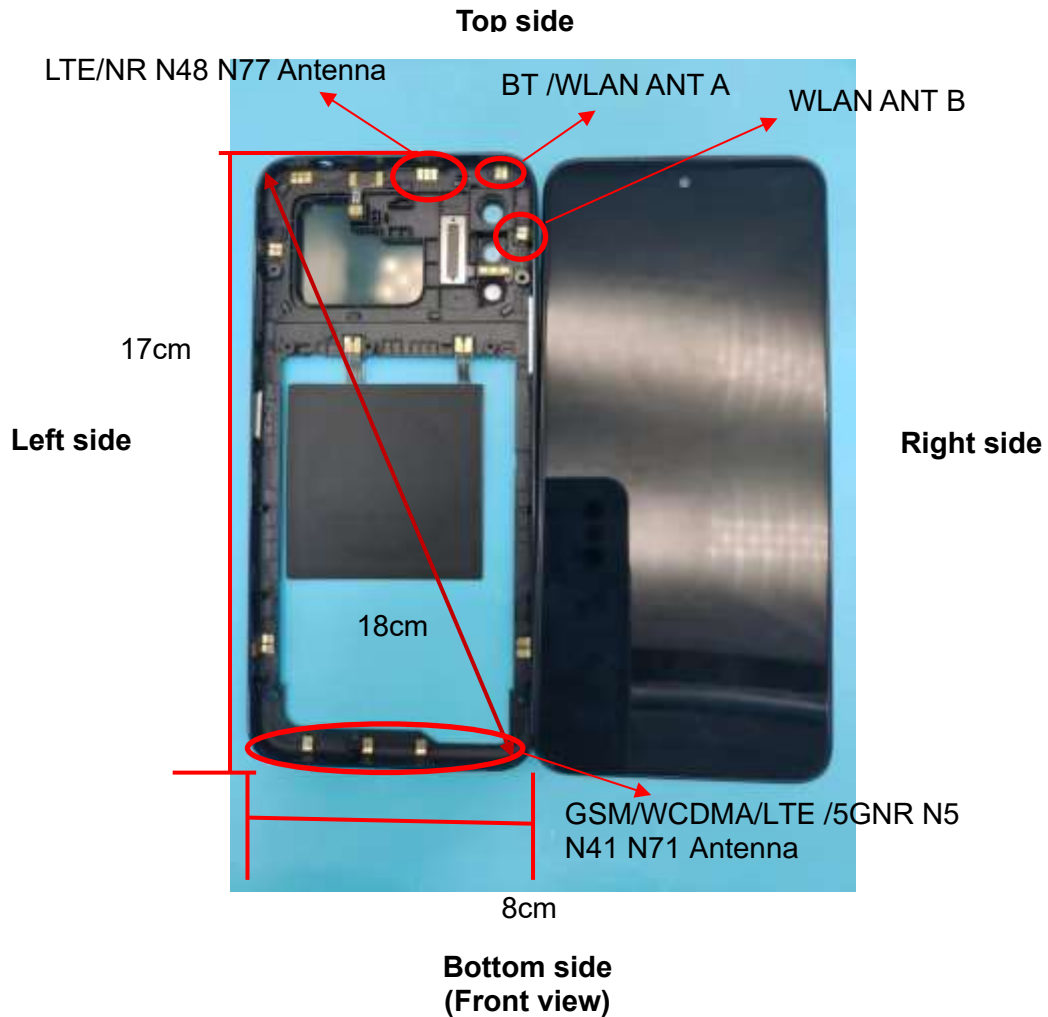
➤ Area Scan & Zoom Scan

First Area Scan is used to locate the approximate location(s) of the local peak SAR value(s). The measurement grid within an Area Scan is defined by the grid extent, grid step size and grid offset. Next, in order to determine the EM field distribution in a three-dimensional spatial extension, Zoom Scan is required. The Zoom Scan is performed around the highest E-field value to determine the averaged SAR-distribution over 10 g. Area scan and zoom scan resolution setting follows KDB 865664 D01 quoted below.

When the 1-g SAR of the highest peak is within 2 dB of the SAR limit, additional zoom scans are required for other peaks within 2 dB of the highest peak that have not been included in any zoom scan to ensure there is no increase in SAR.

7. EUT Antenna Location Sketch

It is a Smart phone, support GSM/WCDMA/CDMA/LTE/NR/WLAN/BT mode.



ANT	Transmitting antenna located(mm)					
	Back Side	Front Side	Left Side	Right Side	Top Side	Bottom Side
GSM/WCDMA/LTE/5G NR Antenna	≤5	≤5	≤5	≤5	160	≤5
BT/ WLAN ANT A	≤5	≤5	61	≤5	≤5	161
WLAN ANT B	≤5	≤5	72	≤5	≤5	134
LTE/NR N48 N77 Antenna	≤5	≤5	41	2.2	≤5	161

Note 1: The antenna information refer the manufacturer provide report, applicable only to the tested sample identified in the report.



7.1 SAR test exclusion consider table

The WWAN/WLAN/BT SAR evaluation of Maximum power (dBm) summing tolerance.

Exposure Position	Wireless Interface	GSM850	PCS1900	WCDMA II	WCDMA IV	WCDMA V
	Calculated Frequency (MHz)	848.8	1909.8	1880	1740	826.4
	Maximum Turn-up power (dBm)	33	31	24	23.5	23.5
	Maximum rated power(mW)	1995.26	1258.93	251.19	223.87	223.87
Back Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	16.28	10.85	10.94	11.37	16.50
	Testing required?	YES	YES	YES	YES	YES
Front Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	16.28	10.85	10.94	11.37	16.50
	Testing required?	YES	YES	YES	YES	YES
Left Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	16.28	10.85	10.94	11.37	16.50
	Testing required?	YES	YES	YES	YES	YES
Right Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	16.28	10.85	10.94	11.37	16.50
	Testing required?	YES	YES	YES	YES	YES
Top Edge	Separation distance (mm)	160	160	160	160	160
	exclusion threshold(mW)	785.27	1208.54	1209.40	1213.71	771.03
	Testing required?	YES	YES	NO	NO	NO
Bottom Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	16.28	10.85	10.94	11.37	16.50
	Testing required?	YES	YES	YES	YES	YES



Exposure Position	Wireless Interface	LTE Band 2	LTE Band 4	LTE Band 5	LTE Band 12	LTE Band 17
	Calculated Frequency (MHz)	1860	1732	844	704	711
	Maximum Turn-up power (dBm)	25	24.5	24.5	23	25.5
	Maximum rated power(mW)	316.23	281.84	281.84	199.53	354.81
Back Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	11.00	11.40	16.33	17.88	17.79
	Testing required?	YES	YES	YES	YES	YES
Front Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	11.00	11.40	16.33	17.88	17.79
	Testing required?	YES	YES	YES	YES	YES
Left Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	11.00	11.40	16.33	17.88	17.79
	Testing required?	YES	YES	YES	YES	YES
Right Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	11.00	11.40	16.33	17.88	17.79
	Testing required?	YES	YES	YES	YES	YES
Top Edge	Separation distance (mm)	160	160	160	160	160
	exclusion threshold(mW)	1209.99	1213.98	782.21	695.04	699.29
	Testing required?	NO	NO	NO	NO	NO
Bottom Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	11.00	11.40	16.33	17.88	17.79
	Testing required?	YES	YES	YES	YES	YES



Exposure Position	Wireless Interface	LTE Band 30	LTE Band 48	LTE Band 66	LTE Band 71	BT
	Calculated Frequency (MHz)	2310	3625	1745	681.000	2441
	Maximum Turn-up power (dBm)	23.5	23	25	24	7
	Maximum rated power(mW)	223.87	199.53	316.23	251.19	5.01
Back Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.87	7.88	11.36	18.18	9.60
	Testing required?	YES	YES	YES	YES	NO
Front Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.87	7.88	11.36	18.18	9.60
	Testing required?	YES	YES	YES	YES	NO
Left Edge	Separation distance (mm)	5	5	5	5	61
	exclusion threshold(mW)	9.87	7.88	11.36	18.18	206.01
	Testing required?	YES	YES	YES	YES	NO
Right Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.87	7.88	11.36	18.18	9.60
	Testing required?	YES	YES	YES	YES	NO
Top Edge	Separation distance (mm)	160	160	160	160	5
	exclusion threshold(mW)	1198.69	1178.78	1213.55	681.17	9.60
	Testing required?	NO	NO	NO	NO	NO
Bottom Edge	Separation distance (mm)	5	5	5	5	161
	exclusion threshold(mW)	9.87	7.88	11.36	18.18	1206.01
	Testing required?	YES	YES	YES	YES	NO



Exposure Position	Wireless Interface	2.4G WLAN ANT A	5.2G WLAN ANT A	5.3G WLAN ANT A	5.6G WLAN ANT A	5.8G WLAN ANT A
	Calculated Frequency (MHz)	2412	5190	5320	5700	5745
	Maximum Turn-up power (dBm)	15	10.5	5.5	5	2
	Maximum rated power(mW)	31.62	11.22	3.55	3.16	1.58
Back Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.50	6.28	6.26
	Testing required?	YES	YES	NO	NO	NO
Front Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.50	6.28	6.26
	Testing required?	YES	YES	NO	NO	NO
Left Edge	Separation distance (mm)	61	61	61	61	61
	exclusion threshold(mW)	206.58	175.84	175.03	172.83	172.58
	Testing required?	NO	NO	NO	NO	NO
Right Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.50	6.28	6.26
	Testing required?	YES	YES	NO	NO	NO
Top Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.50	6.28	6.26
	Testing required?	YES	YES	NO	NO	NO
Bottom Edge	Separation distance (mm)	161	161	161	161	161
	exclusion threshold(mW)	1206.58	1175.84	1175.03	1172.83	1172.58
	Testing required?	NO	NO	NO	NO	NO



Exposure Position	Wireless Interface	2.4G WLAN ANT B	5.2G WLAN ANT B	5.3G WLAN ANT B	5.6G WLAN ANT B	5.8G WLAN ANT B
	Calculated Frequency (MHz)	2412	5190	5260	5770	5745
	Maximum Turn-up power (dBm)	15	12	6.5	7	5
	Maximum rated power(mW)	31.62	15.85	4.47	5.01	3.16
Back Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.54	6.24	6.26
	Testing required?	YES	YES	NO	NO	NO
Front Side	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.54	6.24	6.26
	Testing required?	YES	YES	NO	NO	NO
Left Edge	Separation distance (mm)	72	72	72	72	72
	exclusion threshold(mW)	316.58	285.84	285.40	282.45	282.58
	Testing required?	NO	NO	NO	NO	NO
Right Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.54	6.24	6.26
	Testing required?	YES	YES	NO	NO	NO
Top Edge	Separation distance (mm)	5	5	5	5	5
	exclusion threshold(mW)	9.66	6.58	6.54	6.24	6.26
	Testing required?	YES	YES	NO	NO	NO
Bottom Edge	Separation distance (mm)	134	134	134	134	134
	exclusion threshold(mW)	936.58	905.84	905.40	902.45	902.58
	Testing required?	NO	NO	NO	NO	NO



Exposure Position	Wireless Interface	SA N5	SA N41
	Calculated Frequency (MHz)	844	2592.99
	Maximum Turn-up power (dBm)	23.5	24
	Maximum rated power(mW)	223.87	251.19
Back Side	Separation distance (mm)	5	5
	exclusion threshold(mW)	16.33	9.32
	Testing required?	YES	YES
Front Side	Separation distance (mm)	5	5
	exclusion threshold(mW)	16.33	9.32
	Testing required?	YES	YES
Left Edge	Separation distance (mm)	5	5
	exclusion threshold(mW)	16.33	9.32
	Testing required?	YES	YES
Right Edge	Separation distance (mm)	5	5
	exclusion threshold(mW)	16.33	9.32
	Testing required?	YES	YES
Top Edge	Separation distance (mm)	161	161
	exclusion threshold(mW)	787.84	1203.15
	Testing required?	NO	NO
Bottom Edge	Separation distance (mm)	5	5
	exclusion threshold(mW)	16.33	9.32
	Testing required?	YES	YES



Exposure Position	Wireless Interface	SA N48	SA N71	SA N77
	Calculated Frequency (MHz)	3649.8	688	3500
	Maximum Turn-up power (dBm)	23	24	24
	Maximum rated power(mW)	199.53	251.19	251.19
Back Side	Separation distance (mm)	5	5	5
	exclusion threshold(mW)	7.85	18.08	8.02
	Testing required?	YES	YES	YES
Front Side	Separation distance (mm)	5	5	5
	exclusion threshold(mW)	7.85	18.08	8.02
	Testing required?	YES	YES	YES
Left Edge	Separation distance (mm)	41	5	41
	exclusion threshold(mW)	64.38	18.08	65.75
	Testing required?	YES	YES	YES
Right Edge	Separation distance (mm)	22	5	25
	exclusion threshold(mW)	34.55	18.08	40.09
	Testing required?	YES	YES	YES
Top Edge	Separation distance (mm)	5	161	5
	exclusion threshold(mW)	7.85	689.96	8.02
	Testing required?	YES	NO	YES
Bottom Edge	Separation distance (mm)	161	5	161
	exclusion threshold(mW)	1188.52	18.08	1190.18
	Testing required?	NO	YES	NO

Note:

1. maximum power is the source-based time-average power and represents the maximum RF output power among production units.
2. per KDB 447498 D01, for larger devices, the test separation distance of adjacent edge configuration is determined by the closest separation between the antenna and the user.
3. per KDB 447498 D01, standalone SAR test exclusion threshold is applied; if the distance of the antenna to the user is <25mm,25mm is user to determine SAR exclusion threshold
4. per KDB 447498 D01, the 1-g and 10-g SAR test exclusion thresholds for 100 MHz to 6 GHz at test separation distance $\leq 50\text{mm}$ are determined by:

$$\frac{[(\text{max.power of channel, including tune-up tolerance, mW})/(\text{min. test separation distance,}]}{}$$



mm)]* $\sqrt{f(\text{GHz})} \leq 3.0$ for 1-g SAR and ≤ 7.5 for 10-g extremity SAR, $f(\text{GHz})$ is the RF channel transmit frequency in GHz. Power and distance are rounded to the nearest mW and mm before calculation.

The result is rounded to one decimal place for comparison

For <50mm distance, we just calculate mW of the exclusion threshold value(3.0)to do compare

5. per KDB 447498 D01, at 100 MHz to 6GHz and for test separation distances >50mm, the SAR test exclusion threshold is determined according to the following
 - a) [threshold at 50mm in step 1]+(test separation distance -50mm)*(f (MHz)/150)]mW, at 100 MHz to 1500 MHz
 - b) [threshold at 50mm in step1]+(test separation distance -50mm) *10]mW at > 1500MHz and \leq 6GHz
6. Per KDB 248227 D01, choose the highest output power channel to test SAR and determine further SAR exclusion 8.for each frequency band ,testing at higher data rates and higher order modulations is not required when the maximum average output power for each of each of these configurations is less than 1/4db higher than those measured at the lower data rate than 11b mode ,thus the SAR can be excluded.

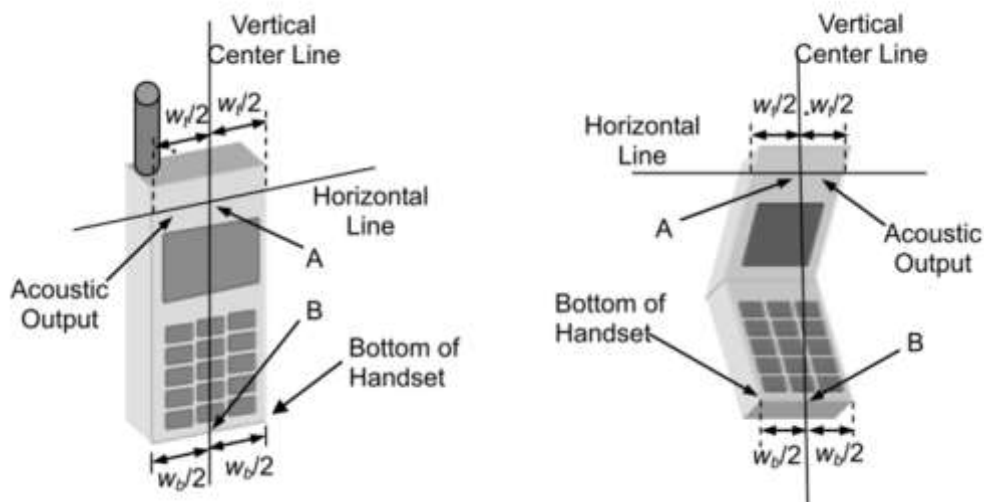


8. EUT Test Position

This EUT was tested in Right Cheek, Right Titled, Left Cheek, Left Titled, Front Face and Rear Face.

8.1 Define Two Imaginary Lines on the Handset

- (1) The vertical centerline passes through two points on the front side of the handset: the midpoint of the width w_t of the handset at the level of the acoustic output, and the midpoint of the width w_b of the handset.
- (2) The horizontal line is perpendicular to the vertical centerline and passes through the center of the acoustic output. The horizontal line is also tangential to the face of the handset at point A.
- (3) The two lines intersect at point A. Note that for many handsets, point A coincides with the center of the acoustic output; however, the acoustic output may be located elsewhere on the horizontal line. Also note that the vertical centerline is not necessarily to the front face of the handset, especially for clamshell handsets, handsets with flip covers, and other irregularly shaped handsets.



Cheek Position

- 1) To position the device with the vertical center line of the body of the device and the horizontal line crossing the center piece in a plane parallel to the sagittal plane of the phantom. While maintaining the device in this plane, align the vertical center line with the reference plane containing the ear and mouth reference point (M: Mouth, RE: Right Ear, and LE: Left Ear) and align the center of the ear piece with the line RE-LE.
- 2) To move the device towards the phantom with the ear piece aligned with the line LE-RE until the phone touched the ear. While maintaining the device in the reference plane and maintaining the phone contact with ear, move the bottom of the phone until any point on the front side is in contact with the cheek of the phantom or until contact with the ear is lost.





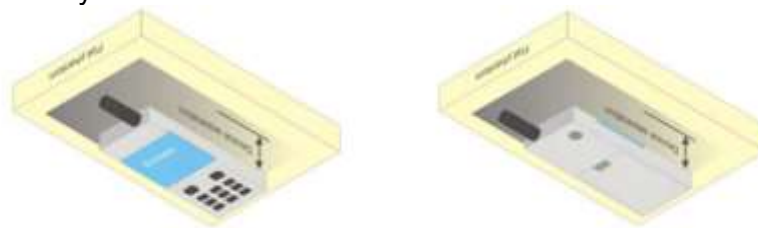
Title Position

- (1) To position the device in the “cheek” position described above.
- (2) While maintaining the device in the reference plane described above and pivoting against the ear, moves it outward away from the mouth by an angle of 15 degrees or until with the ear is lost.



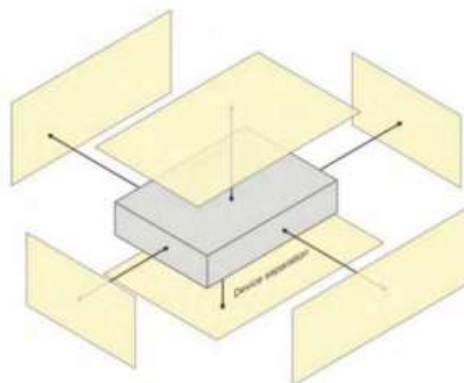
Body-worn Position Conditions:

Body-worn accessory exposure is typically related to voice mode operations when handsets are carried in body-worn accessories. The body-worn accessory procedures in KDB Publication 447498 D01 should be used to test for body-worn accessory SAR compliance, without a headset connected to it. When the same wireless transmission configuration is used for testing body-worn accessory and hotspot mode SAR, respectively, in voice and data mode, SAR results for the most conservative *test separation distance* configuration may be used to support both SAR conditions. When the *reported SAR* for a body-worn accessory, measured without a headset connected to the handset, is $> 1.2 \text{ W/kg}$, the highest *reported SAR* configuration for that wireless mode and frequency band should be repeated for the body-worn accessory with a headset attached to the handset.



8.2 Hotspot mode exposure position condition

For handsets that support hotspot mode operations, with wireless router capabilities and various web browsing function, the relevant hand and body exposure condition are tested according to the hotspot SAR procedures in KDB 941225. A test separation distance of 10 mm is required between the phantom and all surface and edges with a transmitting antenna located within 25 mm from that surface or edge. When form factor of a handset is smaller than 9cm x 5cm, a test separation distance of 5mm (instead of 10mm) is required for testing hotspot mode. When the separate distance required for body-worn accessory testing is larger than or equal to that tested for hotspot mode, in the same wireless mode and for the same surface of the phone, the hotspot mode SAR data may be used to support body-worn accessory SAR compliance for that particular configuration (surface).





9. Measurement Uncertainty

The following measurement uncertainty levels have been estimated for tests performed on the EUT as specified in EN/IEC IEE 62209-1528. This uncertainty represents an expanded uncertainty expressed at approximately the 95% confidence level using a coverage factor of $k=2$.

Symbol	Uncertainty Component	Prob. Dist.	Unc. $a(x_i)$	Div. q_i	$u(x_i) = a(x_i)/q_i$	C_i	$u(y) = C_i * u(x_i)$	ν_i
Measurement system errors								
CF	Probe calibration	N ($k = 2$)	5.8	2	2.90	1	2.90	∞
CF _{drift}	Probe calibration drift	R	0.12	$\sqrt{3}$	0.07	1	0.07	∞
LIN	Probe linearity and detection limit	R	1.91	$\sqrt{3}$	1.10	1	1.10	∞
BBS	Broadband signal	R	0.15	$\sqrt{3}$	0.09	1	0.09	∞
ISO	Probe isotropy	R	0.18	$\sqrt{3}$	0.10	1	0.10	∞
DAE	Other probe and data acquisition errors	N	2.7	1	2.70	1	2.70	∞
AMB	RF ambient and noise	N	1.73	1	1.73	1	1.73	∞
Δ_{xyz}	Probe positioning errors	N	0.81	1	0.81	$2/\delta$	0.81	
DAT	Data processing errors	N	2.5	1	2.50	1	2.50	∞
Phantom and device (DUT or validation antenna) errors								
LIQ(σ)	Measurement of phantom conductivity(σ)	N	4.4	1	4.4	$c\epsilon, c\sigma$	4.40	∞
LIQ(T_c)	Temperature effects (medium)	R	2.9	$\sqrt{3}$	1.67	$c\epsilon, c\sigma$	1.67	∞
EPS	Shell permittivity	R	3.4	$\sqrt{3}$	1.96	See 8.4.2.3	0.49	∞
DIS	Distance between the radiating element of the DUT and the phantom medium	N	0.8	1	0.8	2	1.60	∞
D _{xyz}	Repeatability of positioning the DUT or source against the phantom	N	1.5	1	1.5	1	1.50	5
H	Device holder effects	N	3	1	3	1	3.00	
MOD	Effect of operating mode on probe sensitivity	R	3.59	$\sqrt{3}$	2.07	1	2.07	∞
TAS	Time-average SAR	R	1.73	$\sqrt{3}$	1.00	1	1.00	∞
RF _{drift}	Variation in SAR due to drift in output of DUT	N	2.89	1	2.89	1	2.89	
VAL	Validation antenna uncertainty (validation measurement only)	N	1.45	1	1.45	1	1.45	
P _{in}	Uncertainty in accepted power (validation measurement only)	N	2.5	1	2.5	1	2.50	
Corrections to the SAR result (if applied)								
C(ϵ', σ)	Phantom deviation from target (ϵ', σ)	N	2.31	1	2.31	1	2.31	
C(R)	SAR scaling	R	1.15	$\sqrt{3}$	0.66	1	0.66	
u(Δ SAR)	Combined uncertainty						9.53	
U	Expanded uncertainty and effective degrees of freedom					U =	19.06	



10. Conducted Power Measurement

10.1 Test Result

Burst Average Power (dBm)						
Band	GSM 850			PCS 1900		
Channel	128	190	251	512	661	810
Frequency (MHz)	824.2	836.6	848.8	1850.2	1880.0	1909.8
GSM (GMSK, 1-Slot)	32.18	32.42	32.69	30.42	30.58	30.64
GPRS (GMSK, 1-Slot)	27.03	27.24	27.71	27.69	27.07	27.31
GPRS (GMSK, 2-Slot)	26.40	26.67	26.72	26.09	26.41	26.34
GPRS (GMSK, 3-Slot)	24.33	24.71	24.35	24.40	24.46	24.55
GPRS (GMSK, 4-Slot)	23.48	23.17	23.78	23.11	23.05	23.06
EGPRS (8PSK, 1-Slot)	27.56	27.68	27.96	27.51	27.48	27.33
EGPRS (8PSK, 2-Slot)	26.48	26.44	27.39	26.62	27.36	26.36
EGPRS (8PSK, 3-Slot)	24.60	24.38	24.83	24.02	24.58	24.34
EGPRS (8PSK, 4-Slot)	23.39	23.39	23.70	23.04	22.99	23.15

Remark: GPRS, CS4 coding scheme. EGPRS, MCS5 coding scheme.
 Multi-Slot Class 8, Support Max 4 downlink, 1 uplink, 5 working link
 Multi-Slot Class 10, Support Max 4 downlink, 2 uplink, 5 working link
 Multi-Slot Class 12, Support Max 4 downlink, 4 uplink, 5 working link

Frame- Average Power(dBm)						
Band	GSM 850			PCS 1900		
Channel	128	190	251	512	661	810
Frequency (MHz)	824.2	836.6	848.8	1850.2	1880.0	1909.8
GSM (GMSK, 1-Slot)	23.15	23.39	23.66	21.39	21.55	21.61
GPRS (GMSK, 1-Slot)	18.00	18.21	18.68	18.66	18.04	18.28
GPRS (GMSK, 2-Slot)	20.38	20.65	20.70	20.07	20.39	20.32
GPRS (GMSK, 3-Slot)	20.07	20.45	20.09	20.14	20.20	20.29
GPRS (GMSK, 4-Slot)	20.47	20.16	20.77	20.10	20.04	20.05
EGPRS (8PSK, 1-Slot)	18.53	18.65	18.93	18.48	18.45	18.30
EGPRS (8PSK, 2-Slot)	20.46	20.42	21.37	20.60	21.34	20.34
EGPRS (8PSK, 3-Slot)	20.34	20.12	20.57	19.76	20.32	20.08
EGPRS (8PSK, 4-Slot)	20.38	20.38	20.69	20.03	19.98	20.14

Remark:
 1. SAR testing was performed on the maximum frame-averaged power mode.
 2. The frame-averaged power is linearly proportion to the slot number configured and it is linearly scaled the maximum
 Burst - averaged power based on time slots. The calculated method is shown as below:
 Frame-averaged power = Burst averaged power (1 TX Slot) – 9.03 dB
 Frame-averaged power = Burst averaged power (2 TX Slots) – 6.02 dB
 Frame-averaged power = Burst averaged power (3 TX Slots) - 4.26 dB
 Frame-averaged power = Burst averaged power (4 TX Slots) – 3.01 dB



WCDMA

Band	WCDMA Band 2			WCDMA Band 4			WCDMA Band 5		
Channel	9262	9400	9538	1312	1450	1513	4132	4182	4233
Frequency (MHz)	1852.4	1880	1907.6	1712.6	1740	1752.4	826.4	836.4	846.6
RMC 12.2Kbps	23.61	23.61	23.6	22.8	23.19	23.16	23.06	22.84	23.06
HSDPA Subtest-1	22.63	22.64	22.61	21.84	22.22	22.21	22.1	22	22.19
HSDPA Subtest-2	22.26	22.26	22.12	21.48	21.48	21.7	21.78	21.79	21.86
HSDPA Subtest-3	21.36	20.87	20.91	20.4	20.43	20.44	20.51	20.2	21.06
HSDPA Subtest-4	21.2	21.16	20.87	20.28	20.8	20.64	20.58	20.27	20.38
HSUPA Subtest-1	21.61	22.45	22.46	20.85	22.09	22.04	20.37	21.85	21.91
HSUPA Subtest-2	22.57	22.48	22.46	21.66	22.19	22.08	21.93	21.81	22.19
HSUPA Subtest-3	20.51	21.14	21.67	20.17	20.97	20.86	20.49	20.48	20.65
HSUPA Subtest-4	22.59	22.61	22.6	21.83	22.23	22.22	22.08	22	22.17
HSUPA Subtest-5	21	21.95	21.86	20.32	21.31	21.54	20.53	21.37	21.31

According to 3GPP 25.101 sub-clause 6.2.2, the maximum output power is allowed to be reduced by following the table.

Table 6.1A: UE maximum output power with HS-DPCCH and E-DCH

UE Transmit Channel Configuration	CM (db)	MPR (db)
For all combinations of ,DPDCH,DPCCH HS-DPDCH,E-DPDCH and E-DPCCH	$0 \leq CM \leq 3.5$	MAX(CM-1,0)
Note: CM=1 for $\beta_c/\beta_d=12/15$, $\beta_{hs}/\beta_c=24/15$. For all other combinations of DPDCH, DPCCH, HS-DPCCH, E-DPDCH and E-DPCCH the MPR is based on the relative CM difference.		

The device supports MPR to solve linearity issues (ACLR or SEM) due to the higher peak-to average ratios (PAR) of the HSUPA signal. This prevents saturating the full range of the TX DAC inside of device and provides a reduced power output to the RF transceiver chip according to the Cubic Metric (a function of the combinations of DPDCH, DPCCH, HS-DPCCH, E-DPDCH and E-DPCCH).

When E-DPDCH channels are present the beta gains on those channels are reduced firsts to try to get the power under the allowed limit. If the beta gains are lowered as far as possible, then a hard limiting is applied at the maximum allowed level.

The SW currently recalculates the cubic metric every time the beta gains on the E-DPDCH are reduced. The cubic metric will likely get lower each time this is done .However, there is no reported reduction of maximum output power in the HSUPA mode since the device also provides a compensation for the power back-off by increasing the gain of TX_AGC in the transceiver (PA) device.

The end effect is that the DUT output power is identical to the case where there is no MPR in the device.



2.4G WLAN

2.4GWIFI					
Mode	Channel Number	Frequency (MHz)	ANT A Power (dBm)	ANT B Power (dBm)	MIMO Power (dBm)
802.11b	1	2412	14.59	14.68	N/A
	7	2437	13.60	14.03	N/A
	11	2462	14.00	14.26	N/A
802.11g	1	2412	16.68	16.51	N/A
	7	2437	15.75	15.55	N/A
	11	2462	16.21	16.49	N/A
802.11n-HT20	1	2412	16.71	17.04	19.89
	7	2437	15.79	16.39	19.11
	11	2462	16.22	16.89	19.58
802.11n-HT40	3	2422	15.99	16.76	19.40
	6	2437	16.00	16.56	19.30
	9	2452	16.66	16.59	19.61

Bluetooth

BT				
Mode	Channel Number	Frequency (MHz)	Average Power (dBm)	Output Power (mW)
GFSK(1Mbps)	0	2402	5.73	3.74
	39	2441	6.42	4.39
	78	2480	4.67	2.93
$\pi/4$ -QPSK(2Mbps)	0	2402	5.33	3.41
	39	2441	6.02	4.00
	78	2480	4.48	2.81
8DPSK(3Mbps)	0	2402	5.78	3.78
	39	2441	6.38	4.35
	78	2480	4.84	3.05

BLE

BLE				
Mode	Channel Number	Frequency (MHz)	Average Power (dBm)	Output Power (mW)
GFSK(1Mbps)	0	2402	1.62	1.45
	19	2440	1.71	1.48
	39	2480	0.46	1.11
GFSK(2Mbps)	0	2402	1.53	1.42
	19	2440	1.67	1.47
	39	2480	0.43	1.10



WLAN (5.2Gband)

5.2G WLAN					
Mode	Channel Number	Frequency (MHz)	ANT A Power (dBm)	ANT B Power (dBm)	MIMO Power (dBm)
802.11a	36	5180	4.24	6.44	N/A
	40	5200	4.46	4.22	N/A
	48	5240	5.09	5.13	N/A
802.11n-HT20	36	5180	4.37	5.68	8.08
	40	5200	4.53	5.64	8.13
	48	5240	5.00	6.19	8.65
802.11n-HT40	38	5190	9.98	11.78	13.98
	46	5230	3.26	4.85	7.14
802.11ac-VHT80	42	5210	1.41	2.06	4.76

WLAN (5.3Gband)

5.3G WLAN					
Mode	Channel Number	Frequency (MHz)	ANT A Power (dBm)	ANT B Power (dBm)	MIMO Power (dBm)
802.11a	52	5260	4.91	6.09	N/A
	60	5300	4.47	4.81	N/A
	64	5320	5.15	5.30	N/A
802.11n-HT20	52	5260	4.86	5.69	8.31
	60	5300	4.66	5.81	8.27
	64	5320	4.85	5.93	8.41
802.11n-HT40	54	5270	3.35	5.62	7.64
	62	5310	3.84	5.48	7.75
802.11ac-VHT80	58	5290	0.54	2.47	4.39

WLAN (5.6G band)

5.6G WLAN					
Mode	Channel Number	Frequency (MHz)	ANT A Power (dBm)	ANT B Power (dBm)	MIMO Power (dBm)
802.11a	100	5500	4.06	1.54	N/A
	116	5580	4.42	5.12	N/A
	140	5700	4.21	4.96	N/A
802.11n-HT20	100	5500	3.83	3.44	6.65
	116	5580	4.19	5.11	7.61
	140	5700	4.55	6.58	8.69
802.11n-HT40	102	5510	1.64	1.87	4.75
	110	5550	2.47	2.60	5.45
802.11ac-VHT80	106	5530	0.37	0.63	3.71
	122	5610	0.96	1.06	3.91



WLAN (5.8G band)

5.8G WLAN					
Mode	Channel Number	Frequency (MHz)	ANT A Power (dBm)	ANT B Power (dBm)	MIMO Power (dBm)
802.11a	149	5745	1.81	1.45	N/A
	157	5785	1.06	2.04	N/A
	165	5825	0.40	1.69	N/A
802.11n-HT20	149	5745	1.12	4.54	6.02
	157	5785	1.48	4.33	6.15
	165	5825	0.75	4.30	5.81
802.11n-HT40	151	5755	0.25	2.60	3.76
	159	5795	-1.19	2.66	3.70
802.11ac-VHT80	155	5775	-2.17	0.28	2.24



LTE Conducted Power

General Note:

1. Anritsu CMW500 base station simulator was used to setup the connection with EUT; the frequency band, channel bandwidth, RB allocation configuration, modulation type are set in the base station simulator to configure EUT transmitting at maximum power and at different configurations which are requested to be reported to FCC, for conducted power measurement and SAR testing.
2. Per KDB 941225 D05, when a properly configured base station simulator is used for the SAR and power measurements, spectrum plots for each RB allocation and offset configuration is not required.
3. Per KDB 941225 D05, start with the largest channel bandwidth and measure SAR for QPSK with 1 RB allocation, using the RB offset and required test channel combination with the highest maximum output power for RB offsets at the upper edge, middle and lower edge of each required test channel.
4. Per KDB 941225 D05, 50% RB allocation for QPSK SAR testing follows 1RB QPSK allocation procedure.
5. Per KDB 941225 D05, For QPSK with 100% RB allocation, SAR is not required when the highest maximum output power for 100 % RB allocation is less than the highest maximum output power in 50% and 1 RB allocations and the highest reported SAR for 1 RB and 50% RB allocation are ≤ 0.8 W/kg. Otherwise, SAR is measured for the highest output power channel; and if the reported SAR is > 1.45 W/kg, the remaining required test channels must also be tested.
6. Per KDB 941225 D05, 16QAM output power for each RB allocation configuration is $>$ not $\frac{1}{2}$ dB higher than the same configuration in QPSK and the reported SAR for the QPSK configuration is ≤ 1.45 W/kg; Per KDB 941225 D05, 16QAM SAR testing is not required.
7. Per KDB 941225 D05, Smaller bandwidth output power for each RB allocation configuration is $>$ not $\frac{1}{2}$ dB higher than the same configuration in the largest supported bandwidth, and the reported SAR for the largest supported bandwidth is ≤ 1.45 W/kg; Per KDB 941225 D05, smaller bandwidth SAR testing is not required.



LTE Band 2 Maximum Average Power [dBm]							
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest	
1.4	1	0	QPSK	24.46	24.55	24.4	
1.4	1	2		24.53	24.59	24.4	
1.4	1	5		24.51	24.56	24.14	
1.4	3	0		24.52	24.53	24.51	
1.4	3	1		24.55	24.52	24.54	
1.4	3	2		24.55	24.51	24.52	
1.4	6	0		23.55	23.6	23.54	
1.4	1	0		23.81	23.5	23.27	
1.4	1	2	16-QAM	23.79	23.53	23.35	
1.4	1	5		23.8	23.51	23.28	
1.4	3	0		23.79	23.71	23.77	
1.4	3	1		23.79	23.68	23.76	
1.4	3	2		23.79	23.73	23.77	
1.4	6	0		22.73	22.7	22.68	
3	1	0		QPSK	24.54	24.23	23.96
3	1	7			24.61	24.1	23.96
3	1	14	24.65		24.1	23.93	
3	8	0	23.55		23.47	23.46	
3	8	4	23.58		23.5	23.51	
3	8	7	23.58		23.49	23.47	
3	15	0	23.58		23.49	23.48	
3	1	0	16-QAM		23.42	23.45	23.25
3	1	7		23.38	23.46	23.29	
3	1	14		23.42	23.49	23.26	
3	8	0		22.59	22.54	22.57	
3	8	4		22.58	22.56	22.58	
3	8	7		22.57	22.54	22.54	
3	15	0		22.62	22.54	22.48	



LTE Band 2 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	24.4	24.56	24.19
5	1	12		24.31	24.57	24.12
5	1	24		24.26	24.64	24.18
5	12	0		23.6	23.56	23.59
5	12	6		23.6	23.52	23.56
5	12	11		23.62	23.58	23.53
5	25	0		23.6	23.55	23.57
5	1	0	16-QAM	23.57	23.43	23.54
5	1	12		23.57	23.44	23.48
5	1	24		23.63	23.45	23.51
5	12	0		22.57	22.62	22.59
5	12	6		22.54	22.57	22.52
5	12	11		22.61	22.59	22.48
5	25	0		22.58	22.56	22.53
10	1	0	QPSK	24.57	23.99	24.17
10	1	24		24.41	24.07	24.16
10	1	49		24.27	24.06	24.1
10	25	0		23.55	23.49	23.62
10	25	12		23.65	23.52	23.58
10	25	24		23.55	23.43	23.37
10	50	0		23.56	23.49	23.54
10	1	0	16-QAM	23.5	23.22	23.05
10	1	24		23.48	23.27	23
10	1	49		23.6	23.33	23
10	25	0		22.57	22.49	22.63
10	25	12		22.65	22.56	22.58
10	25	24		22.58	22.46	22.39
10	50	0		22.58	22.5	22.53



LTE Band 2 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
15	1	0	QPSK	24.09	23.85	24.2
15	1	37		24.23	23.97	24.23
15	1	74		24.09	24	24.12
15	36	0		23.5	23.48	23.59
15	36	18		23.58	23.5	23.57
15	36	39		23.56	23.51	23.48
15	75	0		23.55	23.48	23.56
15	1	0	16-QAM	23.43	23.13	23.27
15	1	38		23.58	23.23	23.31
15	1	75		23.49	23.32	23.19
15	36	0		22.55	22.55	22.61
15	36	18		22.65	22.61	22.59
15	36	39		22.62	22.59	22.46
15	75	0		22.58	22.51	22.59
20	1	0	QPSK	24.08	24	24
20	1	49		24.21	24.12	24.04
20	1	99		24.1	24.19	23.93
20	50	0		23.56	23.54	23.7
20	50	24		23.63	23.54	23.71
20	50	49		23.56	23.51	23.47
20	100	0		23.56	23.47	23.57
20	1	0	16-QAM	23.48	23.2	23.4
20	1	49		23.59	23.31	23.46
20	1	99		23.52	23.36	23.35
20	50	0		22.61	22.49	22.7
20	50	24		22.69	22.49	22.72
20	50	49		22.61	22.46	22.48
20	100	0		22.55	22.45	22.58



LTE Band 4 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
1.4	1	0	QPSK	23.5	23.74	23.53
1.4	1	2		23.44	23.69	23.72
1.4	1	5		23.38	23.27	23.78
1.4	3	0		23.44	23.87	24
1.4	3	1		23.45	23.87	24.01
1.4	3	2		23.42	23.86	24.02
1.4	6	0		22.44	22.88	23.07
1.4	1	0	16-QAM	22.21	22.62	22.46
1.4	1	2		22.26	22.59	22.48
1.4	1	5		22.5	22.63	22.47
1.4	3	0		22.71	23.07	23.15
1.4	3	1		22.69	23.1	23.17
1.4	3	2		22.67	23.06	23.19
1.4	6	0		21.62	22.04	22.2
3	1	0	QPSK	23.26	23.38	23.51
3	1	7		22.96	23.46	23.54
3	1	14		22.79	23.42	23.55
3	8	0		22.4	22.91	22.94
3	8	4		22.39	22.92	22.97
3	8	7		22.36	22.89	23.01
3	15	0		22.41	22.92	23
3	1	0	16-QAM	22.2	22.32	22.88
3	1	7		22.15	22.31	22.95
3	1	14		22.08	22.33	22.93
3	8	0		21.47	21.92	22.03
3	8	4		21.46	21.89	22.03
3	8	7		21.41	21.91	22.03
3	15	0		21.38	21.98	22.04



LTE Band 4 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	23.07	22.33	23.89
5	1	12		22.94	22.38	24.03
5	1	24		23.04	22.36	24.17
5	12	0		22.42	21.33	23.03
5	12	6		22.38	21.32	23.04
5	12	11		22.4	21.32	23.03
5	25	0		22.42	21.35	23.07
5	1	0	16-QAM	22.45	21.68	23.45
5	1	12		22.3	21.70	23.46
5	1	24		22.39	21.76	23.55
5	12	0		21.39	20.35	21.99
5	12	6		21.34	20.32	22
5	12	11		21.34	20.32	22.02
5	25	0		21.41	20.30	22.01
10	1	0	QPSK	23.61	23.98	23.2
10	1	24		23.53	24.06	23.58
10	1	49		23.7	24.06	23.65
10	25	0		22.4	22.92	22.85
10	25	12		22.49	22.91	22.94
10	25	24		22.47	22.86	22.87
10	50	0		22.47	22.88	22.93
10	1	0	16-QAM	22.43	23.22	23.06
10	1	24		22.37	23.32	23.09
10	1	49		22.57	23.38	23.24
10	25	0		21.4	21.92	21.9
10	25	12		21.46	21.94	21.73
10	25	24		21.5	21.86	21.87
10	50	0		21.44	21.88	21.93



LTE Band 4 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
15	1	0	QPSK	23.45	23.61	23.69
15	1	37		23.61	23.77	23.4
15	1	74		23.81	23.69	23.61
15	36	0		22.34	22.79	22.8
15	36	18		22.46	22.89	22.89
15	36	39		22.54	22.87	22.94
15	75	0		22.47	22.85	22.87
15	1	0	16-QAM	22.7	22.86	22.59
15	1	38		22.67	22.85	22.62
15	1	75		22.82	22.93	22.69
15	36	0		21.41	21.89	21.81
15	36	18		21.52	21.99	21.84
15	36	39		21.63	21.97	21.91
15	75	0		21.48	21.86	21.91
20	1	0	QPSK	23.32	23.06	23.21
20	1	49		23.25	23.46	23.28
20	1	99		23.5	23.39	23.4
20	50	0		22.44	22.84	22.87
20	50	24		22.62	22.96	22.92
20	50	49		22.74	22.92	22.86
20	100	0		22.56	22.84	22.86
20	1	0	16-QAM	22.34	22.23	22.61
20	1	49		22.61	22.61	22.67
20	1	99		22.88	22.63	22.79
20	50	0		21.49	21.8	21.89
20	50	24		21.69	21.92	21.94
20	50	49		21.78	21.86	21.88
20	100	0		21.58	21.82	21.85



LTE Band 5 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
1.4	1	0	QPSK	23.76	23.68	23.64
1.4	1	2		23.82	23.69	23.75
1.4	1	5		23.8	23.68	23.6
1.4	3	0		23.74	23.67	23.67
1.4	3	1		23.77	23.65	23.7
1.4	3	2		23.75	23.61	23.78
1.4	6	0		22.83	22.84	22.89
1.4	1	0	16-QAM	22.68	22.74	22.55
1.4	1	2		22.71	22.69	22.62
1.4	1	5		22.67	22.63	22.76
1.4	3	0		22.91	22.94	23.04
1.4	3	1		22.93	22.93	23.1
1.4	3	2		22.91	22.93	23.14
1.4	6	0		21.92	21.95	22.09
3	1	0	QPSK	23.73	23.69	23.57
3	1	7		23.84	23.46	23.48
3	1	14		23.94	23.35	23.47
3	8	0		22.77	22.71	22.75
3	8	4		22.8	22.74	22.75
3	8	7		22.77	22.73	22.86
3	15	0		22.81	22.73	22.8
3	1	0	16-QAM	22.71	22.88	22.55
3	1	7		22.65	22.9	22.53
3	1	14		22.76	22.71	22.71
3	8	0		21.75	21.76	21.8
3	8	4		21.79	21.79	21.8
3	8	7		21.78	21.76	21.88
3	15	0		21.86	21.79	21.78



LTE Band 5 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	23.84	23.91	24.06
5	1	12		23.88	23.78	23.73
5	1	24		23.84	23.89	24.07
5	12	0		22.81	22.87	22.91
5	12	6		22.9	22.81	22.81
5	12	11		22.93	22.78	22.83
5	25	0		22.88	22.8	22.87
5	1	0	16-QAM	23.17	23.44	23.51
5	1	12		23.25	23.28	23.25
5	1	24		23.24	23.35	23.51
5	12	0		21.8	21.85	21.86
5	12	6		21.93	21.81	21.76
5	12	11		21.96	21.75	21.76
5	25	0		21.85	21.77	21.88
10	1	0	QPSK	23.89	23.75	23.82
10	1	24		23.82	23.68	24.02
10	1	49		23.84	23.79	24
10	25	0		22.76	22.78	22.88
10	25	12		22.82	22.79	22.94
10	25	24		22.71	22.78	22.66
10	50	0		22.76	22.79	22.83
10	1	0	16-QAM	23.23	23.05	22.79
10	1	24		23.22	23.02	22.99
10	1	49		23.34	23.1	22.91
10	25	0		21.79	21.82	21.9
10	25	12		21.84	21.82	21.95
10	25	24		21.72	21.79	21.69
10	50	0		21.74	21.81	21.81



LTE Band 12 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
1.4	1	0	QPSK	24.99	24.86	24.72
1.4	1	2		24.95	24.89	24.7
1.4	1	5		24.9	24.8	24.64
1.4	3	0		24.96	24.91	24.75
1.4	3	1		24.98	24.9	24.76
1.4	3	2		25.01	24.91	24.81
1.4	6	0		23.93	23.93	23.83
1.4	1	0	16-QAM	24.17	24.05	23.26
1.4	1	2		24.21	24.05	23.48
1.4	1	5		24.21	23.96	23.55
1.4	3	0		24.2	24.09	23.95
1.4	3	1		24.22	24.11	23.94
1.4	3	2		24.22	24.1	23.9
1.4	6	0		23.11	23.1	23.98
3	1	0	QPSK	24.88	24.99	24.83
3	1	7		24.89	25.06	24.87
3	1	14		24.91	24.93	24.89
3	8	0		23.88	23.91	23.71
3	8	4		23.95	23.93	23.73
3	8	7		23.96	23.92	23.74
3	15	0		23.95	23.93	23.75
3	1	0	16-QAM	24.17	23.95	24.13
3	1	7		24.19	23.88	24.18
3	1	14		24.22	23.82	24.19
3	8	0		22.92	22.93	22.78
3	8	4		23.01	22.94	22.75
3	8	7		22.99	22.9	22.78
3	15	0		22.9	22.97	22.78



LTE Band 12 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	25.01	25.08	24.6
5	1	12		24.99	24.88	24.46
5	1	24		25.02	24.67	24.58
5	12	0		23.95	23.97	23.75
5	12	6		23.98	23.92	23.75
5	12	11		23.94	23.93	23.67
5	25	0		23.94	23.95	23.73
5	1	0	16-QAM	24.28	23.96	23.81
5	1	12		24.29	23.82	23.71
5	1	24		24.37	23.84	23.86
5	12	0		22.99	22.91	22.74
5	12	6		23.06	22.88	22.71
5	12	11		22.99	22.87	22.64
5	25	0		22.92	22.91	22.76
10	1	0	QPSK	25.02	24.93	24.38
10	1	24		25.03	24.74	24.28
10	1	49		25.02	24.47	24.3
10	25	0		23.95	23.99	23.83
10	25	12		23.98	23.95	23.81
10	25	24		23.87	23.9	23.67
10	50	0		23.91	23.95	23.8
10	1	0	16-QAM	23.93	23.87	23.61
10	1	24		23.9	23.92	23.51
10	1	49		23.89	23.81	23.56
10	25	0		22.93	22.97	22.85
10	25	12		23	22.95	22.91
10	25	24		22.86	22.91	22.71
10	50	0		22.91	22.95	22.76



LTE Band 17 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	24.53	24.39	24.41
5	1	12		24.46	24.35	24.33
5	1	24		24.66	24.35	24.47
5	12	0		23.89	23.85	23.78
5	12	6		23.88	23.82	23.76
5	12	11		23.87	23.82	23.74
5	25	0		23.92	23.83	23.73
5	1	0	16-QAM	23.95	23.7	23.71
5	1	12		23.83	23.65	23.64
5	1	24		23.97	23.66	23.8
5	12	0		22.85	22.91	22.74
5	12	6		22.84	22.89	22.72
5	12	11		22.81	22.88	22.7
5	25	0		22.95	22.83	22.73
10	1	0	QPSK	24.91	24.95	25
10	1	24		24.91	24.95	24.95
10	1	49		24.79	24.87	24.9
10	25	0		23.89	23.83	23.83
10	25	12		23.91	23.89	23.86
10	25	24		23.84	23.76	23.73
10	50	0		23.9	23.81	23.78
10	1	0	16-QAM	24.14	23.85	24.14
10	1	24		24.13	23.82	24.02
10	1	49		24.02	23.73	24.01
10	25	0		22.92	22.87	22.85
10	25	12		22.93	22.89	22.87
10	25	24		22.87	22.77	22.72
10	50	0		22.88	22.8	22.81



LTE Band 30 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	23.31	22.79	22.59
5	1	12		23.33	22.65	22.73
5	1	24		23.25	22.67	22.7
5	12	0		22.18	22.13	22.07
5	12	6		22.19	22.05	22.08
5	12	11		22.09	22.02	21.98
5	25	0		22.2	22.07	22.07
5	1	0	16-QAM	22.64	22.07	21.95
5	1	12		22.52	22.14	22.09
5	1	24		22.35	22.01	22.06
5	12	0		21.14	21.12	21.05
5	12	6		21.12	21.1	21.06
5	12	11		21.05	21.06	20.98
5	25	0		21.22	21.07	21.03
10	1	0	QPSK	/	23.11	/
10	1	24		/	22.79	/
10	1	49		/	22.55	/
10	25	0		/	22.18	/
10	25	12		/	22.1	/
10	25	24		/	21.98	/
10	50	0		/	22.07	/
10	1	0	16-QAM	/	22.17	/
10	1	24		/	21.93	/
10	1	49		/	21.9	/
10	25	0		/	21.19	/
10	25	12		/	21.1	/
10	25	24		/	20.96	/
10	50	0		/	21.09	/



LTE Band 48 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	20.25	22.63	22.63
5	1	12		20.03	22.36	22.76
5	1	24		19.9	22.29	22.92
5	12	0		20.08	21.55	21.52
5	12	6		19.97	21.43	21.53
5	12	11		19.88	21.31	21.61
5	25	0		19.99	21.42	21.56
5	1	0	16-QAM	20.82	22	22.09
5	1	12		20.6	21.65	22.09
5	1	24		20.46	21.66	22.3
5	12	0		20.06	20.66	20.61
5	12	6		19.96	20.56	20.51
5	12	11		19.86	20.44	20.58
5	25	0		19.9	20.49	20.56
10	1	0	QPSK	20.11	22.91	22.25
10	1	24		20.1	22.5	22.44
10	1	49		19.43	22.16	22.64
10	25	0		20.3	21.62	21.32
10	25	12		20.09	21.47	21.4
10	25	24		19.81	21.16	21.43
10	50	0		20.02	21.42	21.38
10	1	0	16-QAM	20.59	22.08	21.35
10	1	24		20.58	21.59	21.46
10	1	49		19.9	21.3	21.75
10	25	0		20.2	20.76	20.41
10	25	12		20.11	20.57	20.57
10	25	24		19.84	20.31	20.43
10	50	0		20.01	20.6	20.49



LTE Band 48 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
15	1	0	QPSK	20.45	22.78	22.12
15	1	37		20.08	22.43	22.4
15	1	74		19.78	21.95	22.73
15	36	0		20.35	21.7	21.16
15	36	18		20.11	21.4	21.26
15	36	39		19.8	21.05	21.38
15	75	0		20.11	21.38	21.28
15	1	0	16-QAM	20.9	21.81	21.27
15	1	38		20.57	21.51	21.46
15	1	75		20.27	21.25	21.8
15	36	0		20.4	21.03	20.27
15	36	18		20.15	20.55	20.42
15	36	39		19.84	20.24	20.45
15	75	0		20.08	20.51	20.42
20	1	0	QPSK	20.34	22.65	21.9
20	1	49		20.17	22.5	22.18
20	1	99		20.43	21.92	22.54
20	50	0		20.43	21.73	21.1
20	50	24		20.17	21.5	21.21
20	50	49		20.18	20.98	21.24
20	100	0		20.31	21.37	21.17
20	1	0	16-QAM	20.57	21.78	21.21
20	1	49		20.41	21.62	21.39
20	1	99		20.79	21.05	21.82
20	50	0		20.46	20.83	20.22
20	50	24		20.21	20.56	20.31
20	50	49		20.18	20.08	20.35
20	100	0		20.29	20.45	20.3



LTE Band 66 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
1.4	1	0	QPSK	23.76	23.67	24.45
1.4	1	2		23.69	23.72	24.43
1.4	1	5		23.66	23.54	24.39
1.4	3	0		23.68	24	24.44
1.4	3	1		23.68	24.01	24.44
1.4	3	2		23.68	24.03	24.43
1.4	6	0		22.77	23.02	23.5
1.4	1	0	16-QAM	22.31	22.78	23.34
1.4	1	2		22.53	22.84	23.37
1.4	1	5		22.31	22.77	23.33
1.4	3	0		22.85	23.25	23.61
1.4	3	1		22.83	23.26	23.61
1.4	3	2		22.84	23.25	23.61
1.4	6	0		21.86	22.21	22.61
3	1	0	QPSK	23.75	24.01	24.43
3	1	7		23.77	24	24.49
3	1	14		23.79	23.98	24.4
3	8	0		22.62	22.98	23.48
3	8	4		22.64	23	23.44
3	8	7		22.62	22.99	23.42
3	15	0		22.63	23.01	23.48
3	1	0	16-QAM	23.08	23.29	23.41
3	1	7		23.07	23.29	23.26
3	1	14		23.07	23.27	23.26
3	8	0		21.7	22.06	22.49
3	8	4		21.66	22.02	22.43
3	8	7		21.71	22.04	22.43
3	15	0		21.72	21.99	22.54



LTE Band 66 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	23.77	24.21	24.58
5	1	12		23.75	23.91	24.49
5	1	24		23.85	24.14	24.46
5	12	0		22.67	23.07	23.5
5	12	6		22.67	23.02	23.48
5	12	11		22.72	22.99	23.41
5	25	0		22.68	23.08	23.48
5	1	0	16-QAM	23.07	23.44	23.33
5	1	12		22.87	23.38	23.28
5	1	24		23	23.35	23.31
5	12	0		21.64	22.04	22.63
5	12	6		21.64	21.98	22.57
5	12	11		21.69	21.96	22.45
5	25	0		21.66	22.1	22.49
10	1	0	QPSK	23.72	24.14	24.65
10	1	24		23.7	24.17	24.7
10	1	49		23.66	24.12	24.61
10	25	0		22.64	23.04	23.61
10	25	12		22.72	23.08	23.62
10	25	24		22.62	22.96	23.44
10	50	0		22.69	23.02	23.52
10	1	0	16-QAM	22.86	23.01	23.86
10	1	24		22.9	23.05	23.91
10	1	49		22.87	23	23.83
10	25	0		21.67	23.03	22.62
10	25	12		21.77	22.06	22.6
10	25	24		21.67	21.97	22.44
10	50	0		21.7	22.01	22.51



LTE Band 66 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
15	1	0	QPSK	23.72	23.98	24
15	1	37		23.75	23.94	23.98
15	1	74		23.95	23.79	23.89
15	36	0		22.62	22.99	23.43
15	36	18		22.63	23.02	23.52
15	36	39		22.7	22.95	23.43
15	75	0		22.67	23.05	23.46
15	1	0	16-QAM	22.81	23.17	23
15	1	38		22.86	23.08	23.19
15	1	75		23.06	23.25	23.1
15	36	0		21.62	22.09	22.55
15	36	18		21.67	22.09	22.62
15	36	39		21.75	22.04	22.52
15	75	0		21.74	22.03	22.49
20	1	0	QPSK	23.61	23.95	23.48
20	1	49		23.3	23.81	23.88
20	1	99		23.7	23.68	23.79
20	50	0		22.65	23.11	23.41
20	50	24		22.79	23.13	23.53
20	50	49		22.86	23	23.4
20	100	0		22.75	23.04	23.41
20	1	0	16-QAM	22.56	22.76	22.89
20	1	49		22.65	22.82	23.26
20	1	99		23.01	22.88	23.2
20	50	0		21.68	22.07	22.41
20	50	24		21.84	22.1	22.54
20	50	49		21.91	21.97	22.42
20	100	0		21.73	22.04	22.4



LTE Band 71 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
5	1	0	QPSK	23.51	23.6	23.61
5	1	12		23.5	23.49	22.68
5	1	24		23.63	23.63	23.62
5	12	0		22.39	22.66	22.46
5	12	6		22.44	22.62	22.44
5	12	11		22.31	22.52	22.45
5	25	0		22.4	22.6	22.49
5	1	0	16-QAM	22.92	22.96	23.01
5	1	12		22.88	22.95	22.88
5	1	24		22.98	22.98	23.01
5	12	0		21.34	21.71	21.42
5	12	6		21.34	21.54	21.38
5	12	11		21.25	21.51	21.42
5	25	0		21.41	21.58	21.47
10	1	0	QPSK	23.45	23.61	23.68
10	1	24		23.42	23.49	23.49
10	1	49		23.38	23.64	23.54
10	25	0		22.69	22.61	22.39
10	25	12		22.48	22.6	22.52
10	25	24		22.47	22.42	22.26
10	50	0		22.61	22.6	22.31
10	1	0	16-QAM	22.86	22.76	22.52
10	1	24		22.92	22.82	22.46
10	1	49		22.82	22.78	22.41
10	25	0		21.67	21.62	21.38
10	25	12		21.5	21.6	21.52
10	25	24		21.5	21.4	21.29
10	50	0		21.58	21.58	21.35



LTE Band 71 Maximum Average Power [dBm]						
BW [MHz]	RB Size	RB Offset	Mod	Lowest	Middle	Highest
15	1	0	QPSK	23.42	23.27	23.41
15	1	37		23.47	23.5	23.36
15	1	74		23.62	23.55	23.32
15	36	0		22.54	22.46	22.49
15	36	18		22.42	22.59	22.55
15	36	39		22.69	22.49	22.55
15	75	0		22.65	22.51	22.53
15	1	0	16-QAM	22.49	22.77	22.69
15	1	38		22.56	23.03	22.71
15	1	75		22.77	23.1	22.65
15	36	0		21.54	21.48	21.56
15	36	18		21.39	21.6	21.59
15	36	39		21.64	21.54	21.57
15	75	0		21.63	21.5	21.48
20	1	0	QPSK	23.23	23.27	23.52
20	1	49		23.33	23.52	23.62
20	1	99		23.6	23.63	23.02
20	50	0		22.53	22.3	22.75
20	50	24		22.49	22.58	22.63
20	50	49		22.43	22.47	22.66
20	100	0		22.5	22.41	22.7
20	1	0	16-QAM	22.6	22.65	22.69
20	1	49		22.6	23.05	22.93
20	1	99		22.88	22.97	22.68
20	50	0		21.55	21.31	21.74
20	50	24		21.52	21.69	21.57
20	50	49		21.45	21.55	21.62
20	100	0		21.49	21.4	21.7



SA Power

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	Verdict
N5	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	22.19	PASS
N5	15	5	DFT-PI2BPSK	L	Edge_1RB_Right	22.35	PASS
N5	15	5	DFT-PI2BPSK	L	Outer_Full	22.32	PASS
N5	15	5	DFT-PI2BPSK	L	Inner_Full	22.82	PASS
N5	15	5	DFT-QPSK	L	Edge_1RB_Left	21.69	PASS
N5	15	5	DFT-QPSK	L	Edge_1RB_Right	21.79	PASS
N5	15	5	DFT-QPSK	L	Outer_Full	21.89	PASS
N5	15	5	DFT-QPSK	L	Inner_Full	22.82	PASS
N5	15	5	DFT-16QAM	L	Edge_1RB_Left	20.79	PASS
N5	15	5	DFT-16QAM	L	Edge_1RB_Right	20.96	PASS
N5	15	5	DFT-16QAM	L	Outer_Full	20.86	PASS
N5	15	5	DFT-16QAM	L	Inner_Full	21.85	PASS
N5	15	5	DFT-64QAM	L	Edge_1RB_Left	20.16	PASS
N5	15	5	DFT-64QAM	L	Edge_1RB_Right	20.32	PASS
N5	15	5	DFT-64QAM	L	Outer_Full	20.31	PASS
N5	15	5	DFT-64QAM	L	Inner_Full	20.36	PASS
N5	15	5	DFT-256QAM	L	Edge_1RB_Left	18.37	PASS
N5	15	5	DFT-256QAM	L	Edge_1RB_Right	18.23	PASS
N5	15	5	DFT-256QAM	L	Outer_Full	18.33	PASS
N5	15	5	DFT-256QAM	L	Inner_Full	18.32	PASS
N5	15	5	CP-QPSK	L	Edge_1RB_Left	19.71	PASS
N5	15	5	CP-QPSK	L	Edge_1RB_Right	19.83	PASS
N5	15	5	CP-QPSK	L	Outer_Full	19.82	PASS
N5	15	5	CP-QPSK	L	Inner_Full	21.36	PASS
N5	15	5	CP-16QAM	L	Edge_1RB_Left	20.08	PASS
N5	15	5	CP-16QAM	L	Edge_1RB_Right	20.09	PASS
N5	15	5	CP-16QAM	L	Outer_Full	19.83	PASS
N5	15	5	CP-16QAM	L	Inner_Full	20.81	PASS
N5	15	5	CP-64QAM	L	Edge_1RB_Left	19.43	PASS
N5	15	5	CP-64QAM	L	Edge_1RB_Right	19.56	PASS
N5	15	5	CP-64QAM	L	Outer_Full	19.22	PASS
N5	15	5	CP-64QAM	L	Inner_Full	19.31	PASS
N5	15	5	CP-256QAM	L	Edge_1RB_Left	16.2	PASS
N5	15	5	CP-256QAM	L	Edge_1RB_Right	16.57	PASS
N5	15	5	CP-256QAM	L	Outer_Full	16.35	PASS
N5	15	5	CP-256QAM	L	Inner_Full	16.42	PASS
N5	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	22.37	PASS
N5	15	5	DFT-PI2BPSK	M	Edge_1RB_Right	22.26	PASS
N5	15	5	DFT-PI2BPSK	M	Outer_Full	22.44	PASS
N5	15	5	DFT-PI2BPSK	M	Inner_Full	22.91	PASS
N5	15	5	DFT-QPSK	M	Edge_1RB_Left	21.94	PASS



N5	15	5	DFT-QPSK	M	Edge_1RB_Right	21.8	PASS
N5	15	5	DFT-QPSK	M	Outer_Full	21.94	PASS
N5	15	5	DFT-QPSK	M	Inner_Full	22.89	PASS
N5	15	5	DFT-16QAM	M	Edge_1RB_Left	21.02	PASS
N5	15	5	DFT-16QAM	M	Edge_1RB_Right	21.04	PASS
N5	15	5	DFT-16QAM	M	Outer_Full	20.93	PASS
N5	15	5	DFT-16QAM	M	Inner_Full	21.88	PASS
N5	15	5	DFT-64QAM	M	Edge_1RB_Left	20.8	PASS
N5	15	5	DFT-64QAM	M	Edge_1RB_Right	20.69	PASS
N5	15	5	DFT-64QAM	M	Outer_Full	20.39	PASS
N5	15	5	DFT-64QAM	M	Inner_Full	20.47	PASS
N5	15	5	DFT-256QAM	M	Edge_1RB_Left	18.28	PASS
N5	15	5	DFT-256QAM	M	Edge_1RB_Right	18.52	PASS
N5	15	5	DFT-256QAM	M	Outer_Full	18.37	PASS
N5	15	5	DFT-256QAM	M	Inner_Full	18.18	PASS
N5	15	5	CP-QPSK	M	Edge_1RB_Left	19.99	PASS
N5	15	5	CP-QPSK	M	Edge_1RB_Right	19.91	PASS
N5	15	5	CP-QPSK	M	Outer_Full	19.85	PASS
N5	15	5	CP-QPSK	M	Inner_Full	21.4	PASS
N5	15	5	CP-16QAM	M	Edge_1RB_Left	20.29	PASS
N5	15	5	CP-16QAM	M	Edge_1RB_Right	20.03	PASS
N5	15	5	CP-16QAM	M	Outer_Full	19.83	PASS
N5	15	5	CP-16QAM	M	Inner_Full	20.88	PASS
N5	15	5	CP-64QAM	M	Edge_1RB_Left	19.57	PASS
N5	15	5	CP-64QAM	M	Edge_1RB_Right	19.5	PASS
N5	15	5	CP-64QAM	M	Outer_Full	19.33	PASS
N5	15	5	CP-64QAM	M	Inner_Full	19.2	PASS
N5	15	5	CP-256QAM	M	Edge_1RB_Left	16.32	PASS
N5	15	5	CP-256QAM	M	Edge_1RB_Right	16.57	PASS
N5	15	5	CP-256QAM	M	Outer_Full	16.43	PASS
N5	15	5	CP-256QAM	M	Inner_Full	16.45	PASS
N5	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	22.29	PASS
N5	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	22.09	PASS
N5	15	5	DFT-PI2BPSK	H	Outer_Full	22.32	PASS
N5	15	5	DFT-PI2BPSK	H	Inner_Full	22.84	PASS
N5	15	5	DFT-QPSK	H	Edge_1RB_Left	21.79	PASS
N5	15	5	DFT-QPSK	H	Edge_1RB_Right	21.63	PASS
N5	15	5	DFT-QPSK	H	Outer_Full	21.81	PASS
N5	15	5	DFT-QPSK	H	Inner_Full	22.86	PASS
N5	15	5	DFT-16QAM	H	Edge_1RB_Left	21.01	PASS
N5	15	5	DFT-16QAM	H	Edge_1RB_Right	20.83	PASS
N5	15	5	DFT-16QAM	H	Outer_Full	20.81	PASS
N5	15	5	DFT-16QAM	H	Inner_Full	21.82	PASS
N5	15	5	DFT-64QAM	H	Edge_1RB_Left	20.69	PASS



N5	15	5	DFT-64QAM	H	Edge_1RB_Right	20.47	PASS
N5	15	5	DFT-64QAM	H	Outer_Full	20.3	PASS
N5	15	5	DFT-64QAM	H	Inner_Full	20.27	PASS
N5	15	5	DFT-256QAM	H	Edge_1RB_Left	18.37	PASS
N5	15	5	DFT-256QAM	H	Edge_1RB_Right	18.22	PASS
N5	15	5	DFT-256QAM	H	Outer_Full	18.3	PASS
N5	15	5	DFT-256QAM	H	Inner_Full	18.15	PASS
N5	15	5	CP-QPSK	H	Edge_1RB_Left	19.84	PASS
N5	15	5	CP-QPSK	H	Edge_1RB_Right	19.69	PASS
N5	15	5	CP-QPSK	H	Outer_Full	19.74	PASS
N5	15	5	CP-QPSK	H	Inner_Full	21.29	PASS
N5	15	5	CP-16QAM	H	Edge_1RB_Left	19.96	PASS
N5	15	5	CP-16QAM	H	Edge_1RB_Right	19.44	PASS
N5	15	5	CP-16QAM	H	Outer_Full	19.78	PASS
N5	15	5	CP-16QAM	H	Inner_Full	20.72	PASS
N5	15	5	CP-64QAM	H	Edge_1RB_Left	19.59	PASS
N5	15	5	CP-64QAM	H	Edge_1RB_Right	18.91	PASS
N5	15	5	CP-64QAM	H	Outer_Full	19.28	PASS
N5	15	5	CP-64QAM	H	Inner_Full	19.33	PASS
N5	15	5	CP-256QAM	H	Edge_1RB_Left	16.17	PASS
N5	15	5	CP-256QAM	H	Edge_1RB_Right	16	PASS
N5	15	5	CP-256QAM	H	Outer_Full	16.25	PASS
N5	15	5	CP-256QAM	H	Inner_Full	16.3	PASS
N5	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.1	PASS
N5	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.13	PASS
N5	15	20	DFT-PI2BPSK	L	Outer_Full	22.4	PASS
N5	15	20	DFT-PI2BPSK	L	Inner_Full	22.93	PASS
N5	15	20	DFT-QPSK	L	Edge_1RB_Left	21.61	PASS
N5	15	20	DFT-QPSK	L	Edge_1RB_Right	21.61	PASS
N5	15	20	DFT-QPSK	L	Outer_Full	21.94	PASS
N5	15	20	DFT-QPSK	L	Inner_Full	23	PASS
N5	15	20	DFT-16QAM	L	Edge_1RB_Left	20.81	PASS
N5	15	20	DFT-16QAM	L	Edge_1RB_Right	20.82	PASS
N5	15	20	DFT-16QAM	L	Outer_Full	20.88	PASS
N5	15	20	DFT-16QAM	L	Inner_Full	21.97	PASS
N5	15	20	DFT-64QAM	L	Edge_1RB_Left	20.14	PASS
N5	15	20	DFT-64QAM	L	Edge_1RB_Right	20.13	PASS
N5	15	20	DFT-64QAM	L	Outer_Full	20.37	PASS
N5	15	20	DFT-64QAM	L	Inner_Full	20.44	PASS
N5	15	20	DFT-256QAM	L	Edge_1RB_Left	18.3	PASS
N5	15	20	DFT-256QAM	L	Edge_1RB_Right	18.22	PASS
N5	15	20	DFT-256QAM	L	Outer_Full	18.3	PASS
N5	15	20	DFT-256QAM	L	Inner_Full	18.44	PASS
N5	15	20	CP-QPSK	L	Edge_1RB_Left	19.58	PASS



N5	15	20	CP-QPSK	L	Edge_1RB_Right	19.69	PASS
N5	15	20	CP-QPSK	L	Outer_Full	19.8	PASS
N5	15	20	CP-QPSK	L	Inner_Full	21.4	PASS
N5	15	20	CP-16QAM	L	Edge_1RB_Left	19.84	PASS
N5	15	20	CP-16QAM	L	Edge_1RB_Right	19.73	PASS
N5	15	20	CP-16QAM	L	Outer_Full	19.79	PASS
N5	15	20	CP-16QAM	L	Inner_Full	21.01	PASS
N5	15	20	CP-64QAM	L	Edge_1RB_Left	19.23	PASS
N5	15	20	CP-64QAM	L	Edge_1RB_Right	19.39	PASS
N5	15	20	CP-64QAM	L	Outer_Full	19.32	PASS
N5	15	20	CP-64QAM	L	Inner_Full	19.32	PASS
N5	15	20	CP-256QAM	L	Edge_1RB_Left	16.08	PASS
N5	15	20	CP-256QAM	L	Edge_1RB_Right	16.08	PASS
N5	15	20	CP-256QAM	L	Outer_Full	16.33	PASS
N5	15	20	CP-256QAM	L	Inner_Full	16.4	PASS
N5	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.2	PASS
N5	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.03	PASS
N5	15	20	DFT-PI2BPSK	M	Outer_Full	22.39	PASS
N5	15	20	DFT-PI2BPSK	M	Inner_Full	22.93	PASS
N5	15	20	DFT-QPSK	M	Edge_1RB_Left	21.65	PASS
N5	15	20	DFT-QPSK	M	Edge_1RB_Right	21.58	PASS
N5	15	20	DFT-QPSK	M	Outer_Full	21.87	PASS
N5	15	20	DFT-QPSK	M	Inner_Full	22.95	PASS
N5	15	20	DFT-16QAM	M	Edge_1RB_Left	20.93	PASS
N5	15	20	DFT-16QAM	M	Edge_1RB_Right	20.78	PASS
N5	15	20	DFT-16QAM	M	Outer_Full	11.8	PASS
N5	15	20	DFT-16QAM	M	Inner_Full	21.93	PASS
N5	15	20	DFT-64QAM	M	Edge_1RB_Left	10.25	PASS
N5	15	20	DFT-64QAM	M	Edge_1RB_Right	19.7	PASS
N5	15	20	DFT-64QAM	M	Outer_Full	20.33	PASS
N5	15	20	DFT-64QAM	M	Inner_Full	20.46	PASS
N5	15	20	DFT-256QAM	M	Edge_1RB_Left	18.28	PASS
N5	15	20	DFT-256QAM	M	Edge_1RB_Right	18.15	PASS
N5	15	20	DFT-256QAM	M	Outer_Full	18.29	PASS
N5	15	20	DFT-256QAM	M	Inner_Full	18.39	PASS
N5	15	20	CP-QPSK	M	Edge_1RB_Left	19.73	PASS
N5	15	20	CP-QPSK	M	Edge_1RB_Right	19.58	PASS
N5	15	20	CP-QPSK	M	Outer_Full	19.77	PASS
N5	15	20	CP-QPSK	M	Inner_Full	21.37	PASS
N5	15	20	CP-16QAM	M	Edge_1RB_Left	19.98	PASS
N5	15	20	CP-16QAM	M	Edge_1RB_Right	19.84	PASS
N5	15	20	CP-16QAM	M	Outer_Full	19.8	PASS
N5	15	20	CP-16QAM	M	Inner_Full	20.96	PASS
N5	15	20	CP-64QAM	M	Edge_1RB_Left	19.42	PASS



N5	15	20	CP-64QAM	M	Edge_1RB_Right	19.16	PASS
N5	15	20	CP-64QAM	M	Outer_Full	19.27	PASS
N5	15	20	CP-64QAM	M	Inner_Full	19.35	PASS
N5	15	20	CP-256QAM	M	Edge_1RB_Left	16.28	PASS
N5	15	20	CP-256QAM	M	Edge_1RB_Right	16.06	PASS
N5	15	20	CP-256QAM	M	Outer_Full	16.35	PASS
N5	15	20	CP-256QAM	M	Inner_Full	16.43	PASS
N5	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.26	PASS
N5	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	21.9	PASS
N5	15	20	DFT-PI2BPSK	H	Outer_Full	22.32	PASS
N5	15	20	DFT-PI2BPSK	H	Inner_Full	22.9	PASS
N5	15	20	DFT-QPSK	H	Edge_1RB_Left	21.74	PASS
N5	15	20	DFT-QPSK	H	Edge_1RB_Right	21.37	PASS
N5	15	20	DFT-QPSK	H	Outer_Full	21.85	PASS
N5	15	20	DFT-QPSK	H	Inner_Full	22.99	PASS
N5	15	20	DFT-16QAM	H	Edge_1RB_Left	20.99	PASS
N5	15	20	DFT-16QAM	H	Edge_1RB_Right	20.57	PASS
N5	15	20	DFT-16QAM	H	Outer_Full	20.75	PASS
N5	15	20	DFT-16QAM	H	Inner_Full	21.97	PASS
N5	15	20	DFT-64QAM	H	Edge_1RB_Left	20.03	PASS
N5	15	20	DFT-64QAM	H	Edge_1RB_Right	19.69	PASS
N5	15	20	DFT-64QAM	H	Outer_Full	20.29	PASS
N5	15	20	DFT-64QAM	H	Inner_Full	20.42	PASS
N5	15	20	DFT-256QAM	H	Edge_1RB_Left	18.43	PASS
N5	15	20	DFT-256QAM	H	Edge_1RB_Right	18.03	PASS
N5	15	20	DFT-256QAM	H	Outer_Full	18.23	PASS
N5	15	20	DFT-256QAM	H	Inner_Full	18.34	PASS
N5	15	20	CP-QPSK	H	Edge_1RB_Left	19.72	PASS
N5	15	20	CP-QPSK	H	Edge_1RB_Right	19.38	PASS
N5	15	20	CP-QPSK	H	Outer_Full	19.75	PASS
N5	15	20	CP-QPSK	H	Inner_Full	21.35	PASS
N5	15	20	CP-16QAM	H	Edge_1RB_Left	19.96	PASS
N5	15	20	CP-16QAM	H	Edge_1RB_Right	19.66	PASS
N5	15	20	CP-16QAM	H	Outer_Full	19.72	PASS
N5	15	20	CP-16QAM	H	Inner_Full	20.98	PASS
N5	15	20	CP-64QAM	H	Edge_1RB_Left	19.39	PASS
N5	15	20	CP-64QAM	H	Edge_1RB_Right	19.12	PASS
N5	15	20	CP-64QAM	H	Outer_Full	19.16	PASS
N5	15	20	CP-64QAM	H	Inner_Full	19.32	PASS
N5	15	20	CP-256QAM	H	Edge_1RB_Left	16.23	PASS
N5	15	20	CP-256QAM	H	Edge_1RB_Right	15.9	PASS
N5	15	20	CP-256QAM	H	Outer_Full	16.27	PASS
N5	15	20	CP-256QAM	H	Inner_Full	16.35	PASS



Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	Verdict
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.77	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.69	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	22.64	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	23.25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	22.16	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	22.15	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	23.27	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	21.47	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	21.63	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	21.41	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	22.29	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Left	21	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Right	20.97	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	20.82	PASS
N41	30	20	DFT-64QAM	L	Inner_Full	20.99	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Left	19.1	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Right	19.19	PASS
N41	30	20	DFT-256QAM	L	Outer_Full	18.88	PASS
N41	30	20	DFT-256QAM	L	Inner_Full	18.89	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	20.42	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	20.44	PASS
N41	30	20	CP-QPSK	L	Outer_Full	20.36	PASS
N41	30	20	CP-QPSK	L	Inner_Full	21.72	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Left	20.62	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Right	20.95	PASS
N41	30	20	CP-16QAM	L	Outer_Full	20.41	PASS
N41	30	20	CP-16QAM	L	Inner_Full	21.33	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Left	20.06	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Right	20.12	PASS
N41	30	20	CP-64QAM	L	Outer_Full	19.81	PASS
N41	30	20	CP-64QAM	L	Inner_Full	19.92	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Left	16.7	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Right	17.19	PASS
N41	30	20	CP-256QAM	L	Outer_Full	16.83	PASS
N41	30	20	CP-256QAM	L	Inner_Full	16.94	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.21	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.59	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	23.24	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	21.84	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	22.03	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	22	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	23.16	PASS



N41	30	20	DFT-16QAM	M	Edge_1RB_Left	21.15	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	21.3	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	21.15	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	22.12	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Left	20.5	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Right	20.62	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	20.74	PASS
N41	30	20	DFT-64QAM	M	Inner_Full	20.84	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Left	19.03	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Right	18.85	PASS
N41	30	20	DFT-256QAM	M	Outer_Full	18.71	PASS
N41	30	20	DFT-256QAM	M	Inner_Full	18.82	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	20.01	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	20.33	PASS
N41	30	20	CP-QPSK	M	Outer_Full	20.16	PASS
N41	30	20	CP-QPSK	M	Inner_Full	21.6	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Left	20.22	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Right	20.39	PASS
N41	30	20	CP-16QAM	M	Outer_Full	20.22	PASS
N41	30	20	CP-16QAM	M	Inner_Full	21.34	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Left	19.82	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Right	20.16	PASS
N41	30	20	CP-64QAM	M	Outer_Full	19.65	PASS
N41	30	20	CP-64QAM	M	Inner_Full	19.73	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Left	17.18	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Right	17.01	PASS
N41	30	20	CP-256QAM	M	Outer_Full	16.61	PASS
N41	30	20	CP-256QAM	M	Inner_Full	16.83	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.53	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.48	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	22.73	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	23.33	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	21.82	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	21.92	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	22.27	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	23.31	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	20.9	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	20.96	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	21.17	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	22.29	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Left	20.39	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Right	20.62	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	20.84	PASS
N41	30	20	DFT-64QAM	H	Inner_Full	20.78	PASS



N41	30	20	DFT-256QAM	H	Edge_1RB_Left	18.73	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Right	19.57	PASS
N41	30	20	DFT-256QAM	H	Outer_Full	18.73	PASS
N41	30	20	DFT-256QAM	H	Inner_Full	19.04	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	20.13	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	20.23	PASS
N41	30	20	CP-QPSK	H	Outer_Full	20.35	PASS
N41	30	20	CP-QPSK	H	Inner_Full	21.78	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Left	20.46	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Right	20.62	PASS
N41	30	20	CP-16QAM	H	Outer_Full	20.29	PASS
N41	30	20	CP-16QAM	H	Inner_Full	21.38	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Left	19.98	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Right	20.18	PASS
N41	30	20	CP-64QAM	H	Outer_Full	19.89	PASS
N41	30	20	CP-64QAM	H	Inner_Full	19.9	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Left	18.48	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Right	17.78	PASS
N41	30	20	CP-256QAM	H	Outer_Full	16.72	PASS
N41	30	20	CP-256QAM	H	Inner_Full	16.87	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	21.69	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	21.67	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	22.6	PASS
N41	30	100	DFT-PI2BPSK	L	Inner_Full	23.23	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	21.2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	21.17	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	22.13	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	23.2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Left	20.9	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Right	20.81	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	21.22	PASS
N41	30	100	DFT-16QAM	L	Inner_Full	22.23	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Left	20.17	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Right	20.09	PASS
N41	30	100	DFT-64QAM	L	Outer_Full	20.78	PASS
N41	30	100	DFT-64QAM	L	Inner_Full	20.87	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Left	18.26	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Right	18.19	PASS
N41	30	100	DFT-256QAM	L	Outer_Full	18.78	PASS
N41	30	100	DFT-256QAM	L	Inner_Full	18.83	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	19.7	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	19.78	PASS
N41	30	100	CP-QPSK	L	Outer_Full	20.25	PASS
N41	30	100	CP-QPSK	L	Inner_Full	21.68	PASS



N41	30	100	CP-16QAM	L	Edge_1RB_Left	19.65	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Right	19.67	PASS
N41	30	100	CP-16QAM	L	Outer_Full	20.28	PASS
N41	30	100	CP-16QAM	L	Inner_Full	21.34	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Left	19.52	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Right	19.29	PASS
N41	30	100	CP-64QAM	L	Outer_Full	19.73	PASS
N41	30	100	CP-64QAM	L	Inner_Full	19.88	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Left	16.29	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Right	16.22	PASS
N41	30	100	CP-256QAM	L	Outer_Full	16.76	PASS
N41	30	100	CP-256QAM	L	Inner_Full	16.83	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	21.5	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	21.79	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	22.51	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	23.2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	20.98	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	21.15	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	23.2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Left	20.43	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Right	20.64	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	21.18	PASS
N41	30	100	DFT-16QAM	M	Inner_Full	22.19	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Left	19.69	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Right	19.82	PASS
N41	30	100	DFT-64QAM	M	Outer_Full	20.68	PASS
N41	30	100	DFT-64QAM	M	Inner_Full	20.83	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Left	17.89	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Right	18.08	PASS
N41	30	100	DFT-256QAM	M	Outer_Full	18.62	PASS
N41	30	100	DFT-256QAM	M	Inner_Full	18.81	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	19.46	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	19.47	PASS
N41	30	100	CP-QPSK	M	Outer_Full	20.11	PASS
N41	30	100	CP-QPSK	M	Inner_Full	21.69	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Left	19.38	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Right	19.68	PASS
N41	30	100	CP-16QAM	M	Outer_Full	20.14	PASS
N41	30	100	CP-16QAM	M	Inner_Full	21.31	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Left	19.19	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Right	19.13	PASS
N41	30	100	CP-64QAM	M	Outer_Full	19.64	PASS
N41	30	100	CP-64QAM	M	Inner_Full	19.79	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Left	16.11	PASS



N41	30	100	CP-256QAM	M	Edge_1RB_Right	16.28	PASS
N41	30	100	CP-256QAM	M	Outer_Full	16.59	PASS
N41	30	100	CP-256QAM	M	Inner_Full	16.8	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	21.58	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	21.67	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	22.5	PASS
N41	30	100	DFT-PI2BPSK	H	Inner_Full	23.12	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	21.1	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	21.16	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	22.05	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	23.19	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Left	20.85	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Right	20.63	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	21.23	PASS
N41	30	100	DFT-16QAM	H	Inner_Full	22.17	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Left	20.22	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Right	20.37	PASS
N41	30	100	DFT-64QAM	H	Outer_Full	20.73	PASS
N41	30	100	DFT-64QAM	H	Inner_Full	20.87	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Left	18.14	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Right	18.2	PASS
N41	30	100	DFT-256QAM	H	Outer_Full	18.79	PASS
N41	30	100	DFT-256QAM	H	Inner_Full	18.82	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	19.6	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	19.42	PASS
N41	30	100	CP-QPSK	H	Outer_Full	20.24	PASS
N41	30	100	CP-QPSK	H	Inner_Full	21.66	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Left	19.63	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Right	19.61	PASS
N41	30	100	CP-16QAM	H	Outer_Full	20.2	PASS
N41	30	100	CP-16QAM	H	Inner_Full	21.32	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Left	19.09	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Right	19.07	PASS
N41	30	100	CP-64QAM	H	Outer_Full	19.73	PASS
N41	30	100	CP-64QAM	H	Inner_Full	19.8	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Left	16.19	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Right	16.32	PASS
N41	30	100	CP-256QAM	H	Outer_Full	16.71	PASS
N41	30	100	CP-256QAM	H	Inner_Full	16.79	PASS



Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	Verdict
N48	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.16	PASS
N48	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	21.8	PASS
N48	30	20	DFT-PI2BPSK	L	Outer_Full	22.2	PASS
N48	30	20	DFT-PI2BPSK	L	Inner_Full	22.84	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Left	21.59	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Right	21.28	PASS
N48	30	20	DFT-QPSK	L	Outer_Full	21.74	PASS
N48	30	20	DFT-QPSK	L	Inner_Full	22.89	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Left	20.92	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Right	20.61	PASS
N48	30	20	DFT-16QAM	L	Outer_Full	20.78	PASS
N48	30	20	DFT-16QAM	L	Inner_Full	21.89	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Left	20.44	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Right	19.99	PASS
N48	30	20	DFT-64QAM	L	Outer_Full	20.38	PASS
N48	30	20	DFT-64QAM	L	Inner_Full	20.48	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Left	18.34	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Right	18.1	PASS
N48	30	20	DFT-256QAM	L	Outer_Full	18.37	PASS
N48	30	20	DFT-256QAM	L	Inner_Full	18.38	PASS
N48	30	20	CP-QPSK	L	Edge_1RB_Left	20.05	PASS
N48	30	20	CP-QPSK	L	Edge_1RB_Right	19.52	PASS
N48	30	20	CP-QPSK	L	Outer_Full	19.83	PASS
N48	30	20	CP-QPSK	L	Inner_Full	21.29	PASS
N48	30	20	CP-16QAM	L	Edge_1RB_Left	19.79	PASS
N48	30	20	CP-16QAM	L	Edge_1RB_Right	19.2	PASS
N48	30	20	CP-16QAM	L	Outer_Full	19.85	PASS
N48	30	20	CP-16QAM	L	Inner_Full	20.79	PASS
N48	30	20	CP-64QAM	L	Edge_1RB_Left	19.67	PASS
N48	30	20	CP-64QAM	L	Edge_1RB_Right	19.19	PASS
N48	30	20	CP-64QAM	L	Outer_Full	19.31	PASS
N48	30	20	CP-64QAM	L	Inner_Full	19.41	PASS
N48	30	20	CP-256QAM	L	Edge_1RB_Left	16.48	PASS
N48	30	20	CP-256QAM	L	Edge_1RB_Right	16.16	PASS
N48	30	20	CP-256QAM	L	Outer_Full	16.26	PASS
N48	30	20	CP-256QAM	L	Inner_Full	16.42	PASS
N48	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	21.51	PASS
N48	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	21.87	PASS
N48	30	20	DFT-PI2BPSK	M	Outer_Full	21.73	PASS
N48	30	20	DFT-PI2BPSK	M	Inner_Full	22.37	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Left	20.8	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Right	21.19	PASS
N48	30	20	DFT-QPSK	M	Outer_Full	21.27	PASS



N48	30	20	DFT-QPSK	M	Inner_Full	22.3	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Left	20.09	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Right	20.63	PASS
N48	30	20	DFT-16QAM	M	Outer_Full	20.24	PASS
N48	30	20	DFT-16QAM	M	Inner_Full	21.31	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Left	19.6	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Right	20.14	PASS
N48	30	20	DFT-64QAM	M	Outer_Full	19.89	PASS
N48	30	20	DFT-64QAM	M	Inner_Full	19.98	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Left	17.73	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Right	18.29	PASS
N48	30	20	DFT-256QAM	M	Outer_Full	17.85	PASS
N48	30	20	DFT-256QAM	M	Inner_Full	17.85	PASS
N48	30	20	CP-QPSK	M	Edge_1RB_Left	18.82	PASS
N48	30	20	CP-QPSK	M	Edge_1RB_Right	19.44	PASS
N48	30	20	CP-QPSK	M	Outer_Full	19.24	PASS
N48	30	20	CP-QPSK	M	Inner_Full	20.79	PASS
N48	30	20	CP-16QAM	M	Edge_1RB_Left	18.75	PASS
N48	30	20	CP-16QAM	M	Edge_1RB_Right	19.12	PASS
N48	30	20	CP-16QAM	M	Outer_Full	19.31	PASS
N48	30	20	CP-16QAM	M	Inner_Full	20.27	PASS
N48	30	20	CP-64QAM	M	Edge_1RB_Left	18.51	PASS
N48	30	20	CP-64QAM	M	Edge_1RB_Right	19.02	PASS
N48	30	20	CP-64QAM	M	Outer_Full	18.81	PASS
N48	30	20	CP-64QAM	M	Inner_Full	18.77	PASS
N48	30	20	CP-256QAM	M	Edge_1RB_Left	15.31	PASS
N48	30	20	CP-256QAM	M	Edge_1RB_Right	15.9	PASS
N48	30	20	CP-256QAM	M	Outer_Full	15.75	PASS
N48	30	20	CP-256QAM	M	Inner_Full	15.85	PASS
N48	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	21.94	PASS
N48	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	21.77	PASS
N48	30	20	DFT-PI2BPSK	H	Outer_Full	21.78	PASS
N48	30	20	DFT-PI2BPSK	H	Inner_Full	22.42	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Left	21.32	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Right	21.21	PASS
N48	30	20	DFT-QPSK	H	Outer_Full	21.43	PASS
N48	30	20	DFT-QPSK	H	Inner_Full	22.43	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Left	20.58	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Right	20.56	PASS
N48	30	20	DFT-16QAM	H	Outer_Full	20.39	PASS
N48	30	20	DFT-16QAM	H	Inner_Full	21.5	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Left	20.16	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Right	20.02	PASS
N48	30	20	DFT-64QAM	H	Outer_Full	19.93	PASS
N48	30	20	DFT-64QAM	H	Inner_Full	19.86	PASS
N48	30	20	DFT-256QAM	H	Edge_1RB_Left	18.3	PASS



N48	30	20	DFT-256QAM	H	Edge_1RB_Right	18.19	PASS
N48	30	20	DFT-256QAM	H	Outer_Full	17.96	PASS
N48	30	20	DFT-256QAM	H	Inner_Full	17.91	PASS
N48	30	20	CP-QPSK	H	Edge_1RB_Left	19.66	PASS
N48	30	20	CP-QPSK	H	Edge_1RB_Right	19.43	PASS
N48	30	20	CP-QPSK	H	Outer_Full	19.45	PASS
N48	30	20	CP-QPSK	H	Inner_Full	20.88	PASS
N48	30	20	CP-16QAM	H	Edge_1RB_Left	19.6	PASS
N48	30	20	CP-16QAM	H	Edge_1RB_Right	19.11	PASS
N48	30	20	CP-16QAM	H	Outer_Full	19.48	PASS
N48	30	20	CP-16QAM	H	Inner_Full	20.36	PASS
N48	30	20	CP-64QAM	H	Edge_1RB_Left	19.47	PASS
N48	30	20	CP-64QAM	H	Edge_1RB_Right	19.33	PASS
N48	30	20	CP-64QAM	H	Outer_Full	18.95	PASS
N48	30	20	CP-64QAM	H	Inner_Full	18.94	PASS
N48	30	20	CP-256QAM	H	Edge_1RB_Left	15.96	PASS
N48	30	20	CP-256QAM	H	Edge_1RB_Right	16.13	PASS
N48	30	20	CP-256QAM	H	Outer_Full	15.85	PASS
N48	30	20	CP-256QAM	H	Inner_Full	15.94	PASS
N48	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	21.28	PASS
N48	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	20.59	PASS
N48	30	100	DFT-PI2BPSK	L	Outer_Full	21.63	PASS
N48	30	100	DFT-PI2BPSK	L	Inner_Full	22.19	PASS
N48	30	100	DFT-QPSK	L	Edge_1RB_Left	20.67	PASS
N48	30	100	DFT-QPSK	L	Edge_1RB_Right	19.99	PASS
N48	30	100	DFT-QPSK	L	Outer_Full	21.15	PASS
N48	30	100	DFT-QPSK	L	Inner_Full	22.16	PASS
N48	30	100	DFT-16QAM	L	Edge_1RB_Left	19.93	PASS
N48	30	100	DFT-16QAM	L	Edge_1RB_Right	19.37	PASS
N48	30	100	DFT-16QAM	L	Outer_Full	20.17	PASS
N48	30	100	DFT-16QAM	L	Inner_Full	21.18	PASS
N48	30	100	DFT-64QAM	L	Edge_1RB_Left	19.67	PASS
N48	30	100	DFT-64QAM	L	Edge_1RB_Right	19.07	PASS
N48	30	100	DFT-64QAM	L	Outer_Full	19.83	PASS
N48	30	100	DFT-64QAM	L	Inner_Full	19.9	PASS
N48	30	100	DFT-256QAM	L	Edge_1RB_Left	17.9	PASS
N48	30	100	DFT-256QAM	L	Edge_1RB_Right	17.25	PASS
N48	30	100	DFT-256QAM	L	Outer_Full	17.86	PASS
N48	30	100	DFT-256QAM	L	Inner_Full	17.83	PASS
N48	30	100	CP-QPSK	L	Edge_1RB_Left	19.19	PASS
N48	30	100	CP-QPSK	L	Edge_1RB_Right	18.45	PASS
N48	30	100	CP-QPSK	L	Outer_Full	19.33	PASS
N48	30	100	CP-QPSK	L	Inner_Full	20.69	PASS
N48	30	100	CP-16QAM	L	Edge_1RB_Left	19.42	PASS
N48	30	100	CP-16QAM	L	Edge_1RB_Right	18.55	PASS



N48	30	100	CP-16QAM	L	Outer_Full	19.33	PASS
N48	30	100	CP-16QAM	L	Inner_Full	20.2	PASS
N48	30	100	CP-64QAM	L	Edge_1RB_Left	18.51	PASS
N48	30	100	CP-64QAM	L	Edge_1RB_Right	18.06	PASS
N48	30	100	CP-64QAM	L	Outer_Full	18.7	PASS
N48	30	100	CP-64QAM	L	Inner_Full	18.82	PASS
N48	30	100	CP-256QAM	L	Edge_1RB_Left	15.45	PASS
N48	30	100	CP-256QAM	L	Edge_1RB_Right	14.88	PASS
N48	30	100	CP-256QAM	L	Outer_Full	15.77	PASS
N48	30	100	CP-256QAM	L	Inner_Full	15.77	PASS
N48	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	20.65	PASS
N48	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	20.98	PASS
N48	30	100	DFT-PI2BPSK	M	Outer_Full	21.55	PASS
N48	30	100	DFT-PI2BPSK	M	Inner_Full	22.16	PASS
N48	30	100	DFT-QPSK	M	Edge_1RB_Left	20.07	PASS
N48	30	100	DFT-QPSK	M	Edge_1RB_Right	20.55	PASS
N48	30	100	DFT-QPSK	M	Outer_Full	21.15	PASS
N48	30	100	DFT-QPSK	M	Inner_Full	22.23	PASS
N48	30	100	DFT-16QAM	M	Edge_1RB_Left	19.14	PASS
N48	30	100	DFT-16QAM	M	Edge_1RB_Right	19.59	PASS
N48	30	100	DFT-16QAM	M	Outer_Full	20.07	PASS
N48	30	100	DFT-16QAM	M	Inner_Full	21.22	PASS
N48	30	100	DFT-64QAM	M	Edge_1RB_Left	19.01	PASS
N48	30	100	DFT-64QAM	M	Edge_1RB_Right	19.11	PASS
N48	30	100	DFT-64QAM	M	Outer_Full	19.8	PASS
N48	30	100	DFT-64QAM	M	Inner_Full	19.81	PASS
N48	30	100	DFT-256QAM	M	Edge_1RB_Left	17.25	PASS
N48	30	100	DFT-256QAM	M	Edge_1RB_Right	17.42	PASS
N48	30	100	DFT-256QAM	M	Outer_Full	17.81	PASS
N48	30	100	DFT-256QAM	M	Inner_Full	17.78	PASS
N48	30	100	CP-QPSK	M	Edge_1RB_Left	18.52	PASS
N48	30	100	CP-QPSK	M	Edge_1RB_Right	18.79	PASS
N48	30	100	CP-QPSK	M	Outer_Full	19.26	PASS
N48	30	100	CP-QPSK	M	Inner_Full	20.67	PASS
N48	30	100	CP-16QAM	M	Edge_1RB_Left	18.45	PASS
N48	30	100	CP-16QAM	M	Edge_1RB_Right	18.84	PASS
N48	30	100	CP-16QAM	M	Outer_Full	19.29	PASS
N48	30	100	CP-16QAM	M	Inner_Full	20.18	PASS
N48	30	100	CP-64QAM	M	Edge_1RB_Left	18.25	PASS
N48	30	100	CP-64QAM	M	Edge_1RB_Right	18.57	PASS
N48	30	100	CP-64QAM	M	Outer_Full	18.78	PASS
N48	30	100	CP-64QAM	M	Inner_Full	18.81	PASS
N48	30	100	CP-256QAM	M	Edge_1RB_Left	15.11	PASS
N48	30	100	CP-256QAM	M	Edge_1RB_Right	15.16	PASS
N48	30	100	CP-256QAM	M	Outer_Full	15.72	PASS
N48	30	100	CP-256QAM	M	Inner_Full	15.78	PASS



N48	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	20.83	PASS
N48	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	20.78	PASS
N48	30	100	DFT-PI2BPSK	H	Outer_Full	21.59	PASS
N48	30	100	DFT-PI2BPSK	H	Inner_Full	22.33	PASS
N48	30	100	DFT-QPSK	H	Edge_1RB_Left	20.25	PASS
N48	30	100	DFT-QPSK	H	Edge_1RB_Right	20.18	PASS
N48	30	100	DFT-QPSK	H	Outer_Full	21.12	PASS
N48	30	100	DFT-QPSK	H	Inner_Full	22.3	PASS
N48	30	100	DFT-16QAM	H	Edge_1RB_Left	19.72	PASS
N48	30	100	DFT-16QAM	H	Edge_1RB_Right	19.7	PASS
N48	30	100	DFT-16QAM	H	Outer_Full	20.1	PASS
N48	30	100	DFT-16QAM	H	Inner_Full	21.31	PASS
N48	30	100	DFT-64QAM	H	Edge_1RB_Left	19.19	PASS
N48	30	100	DFT-64QAM	H	Edge_1RB_Right	18.82	PASS
N48	30	100	DFT-64QAM	H	Outer_Full	19.82	PASS
N48	30	100	DFT-64QAM	H	Inner_Full	19.94	PASS
N48	30	100	DFT-256QAM	H	Edge_1RB_Left	17.33	PASS
N48	30	100	DFT-256QAM	H	Edge_1RB_Right	17.3	PASS
N48	30	100	DFT-256QAM	H	Outer_Full	17.73	PASS
N48	30	100	DFT-256QAM	H	Inner_Full	17.92	PASS
N48	30	100	CP-QPSK	H	Edge_1RB_Left	18.74	PASS
N48	30	100	CP-QPSK	H	Edge_1RB_Right	18.56	PASS
N48	30	100	CP-QPSK	H	Outer_Full	19.2	PASS
N48	30	100	CP-QPSK	H	Inner_Full	20.83	PASS
N48	30	100	CP-16QAM	H	Edge_1RB_Left	18.74	PASS
N48	30	100	CP-16QAM	H	Edge_1RB_Right	18.51	PASS
N48	30	100	CP-16QAM	H	Outer_Full	19.33	PASS
N48	30	100	CP-16QAM	H	Inner_Full	20.41	PASS
N48	30	100	CP-64QAM	H	Edge_1RB_Left	18.17	PASS
N48	30	100	CP-64QAM	H	Edge_1RB_Right	18.2	PASS
N48	30	100	CP-64QAM	H	Outer_Full	18.71	PASS
N48	30	100	CP-64QAM	H	Inner_Full	18.93	PASS
N48	30	100	CP-256QAM	H	Edge_1RB_Left	15.33	PASS
N48	30	100	CP-256QAM	H	Edge_1RB_Right	15.07	PASS
N48	30	100	CP-256QAM	H	Outer_Full	15.73	PASS
N48	30	100	CP-256QAM	H	Inner_Full	15.94	PASS



Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	Verdict
N71	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	22.99	PASS
N71	15	5	DFT-PI2BPSK	L	Edge_1RB_Right	23.14	PASS
N71	15	5	DFT-PI2BPSK	L	Outer_Full	22.99	PASS
N71	15	5	DFT-PI2BPSK	L	Inner_Full	23.64	PASS
N71	15	5	DFT-QPSK	L	Edge_1RB_Left	22.51	PASS
N71	15	5	DFT-QPSK	L	Edge_1RB_Right	22.61	PASS
N71	15	5	DFT-QPSK	L	Outer_Full	22.57	PASS
N71	15	5	DFT-QPSK	L	Inner_Full	23.71	PASS
N71	15	5	DFT-16QAM	L	Edge_1RB_Left	21.67	PASS
N71	15	5	DFT-16QAM	L	Edge_1RB_Right	21.75	PASS
N71	15	5	DFT-16QAM	L	Outer_Full	21.56	PASS
N71	15	5	DFT-16QAM	L	Inner_Full	22.7	PASS
N71	15	5	DFT-64QAM	L	Edge_1RB_Left	20.86	PASS
N71	15	5	DFT-64QAM	L	Edge_1RB_Right	21.41	PASS
N71	15	5	DFT-64QAM	L	Outer_Full	20.99	PASS
N71	15	5	DFT-64QAM	L	Inner_Full	21.29	PASS
N71	15	5	DFT-256QAM	L	Edge_1RB_Left	18.93	PASS
N71	15	5	DFT-256QAM	L	Edge_1RB_Right	19.04	PASS
N71	15	5	DFT-256QAM	L	Outer_Full	19.05	PASS
N71	15	5	DFT-256QAM	L	Inner_Full	19.14	PASS
N71	15	5	CP-QPSK	L	Edge_1RB_Left	20.65	PASS
N71	15	5	CP-QPSK	L	Edge_1RB_Right	20.74	PASS
N71	15	5	CP-QPSK	L	Outer_Full	20.57	PASS
N71	15	5	CP-QPSK	L	Inner_Full	22.16	PASS
N71	15	5	CP-16QAM	L	Edge_1RB_Left	20.69	PASS
N71	15	5	CP-16QAM	L	Edge_1RB_Right	20.79	PASS
N71	15	5	CP-16QAM	L	Outer_Full	20.54	PASS
N71	15	5	CP-16QAM	L	Inner_Full	21.6	PASS
N71	15	5	CP-64QAM	L	Edge_1RB_Left	20.37	PASS
N71	15	5	CP-64QAM	L	Edge_1RB_Right	20.28	PASS
N71	15	5	CP-64QAM	L	Outer_Full	20.02	PASS
N71	15	5	CP-64QAM	L	Inner_Full	20.2	PASS
N71	15	5	CP-256QAM	L	Edge_1RB_Left	16.81	PASS
N71	15	5	CP-256QAM	L	Edge_1RB_Right	17.28	PASS
N71	15	5	CP-256QAM	L	Outer_Full	17.04	PASS
N71	15	5	CP-256QAM	L	Inner_Full	17.16	PASS
N71	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	23.14	PASS
N71	15	5	DFT-PI2BPSK	M	Edge_1RB_Right	23.06	PASS
N71	15	5	DFT-PI2BPSK	M	Outer_Full	23.11	PASS
N71	15	5	DFT-PI2BPSK	M	Inner_Full	23.62	PASS
N71	15	5	DFT-QPSK	M	Edge_1RB_Left	22.6	PASS
N71	15	5	DFT-QPSK	M	Edge_1RB_Right	22.6	PASS
N71	15	5	DFT-QPSK	M	Outer_Full	22.68	PASS



N71	15	5	DFT-QPSK	M	Inner_Full	23.64	PASS
N71	15	5	DFT-16QAM	M	Edge_1RB_Left	21.7	PASS
N71	15	5	DFT-16QAM	M	Edge_1RB_Right	22	PASS
N71	15	5	DFT-16QAM	M	Outer_Full	21.66	PASS
N71	15	5	DFT-16QAM	M	Inner_Full	22.71	PASS
N71	15	5	DFT-64QAM	M	Edge_1RB_Left	21.42	PASS
N71	15	5	DFT-64QAM	M	Edge_1RB_Right	21.36	PASS
N71	15	5	DFT-64QAM	M	Outer_Full	21.23	PASS
N71	15	5	DFT-64QAM	M	Inner_Full	21.27	PASS
N71	15	5	DFT-256QAM	M	Edge_1RB_Left	19.01	PASS
N71	15	5	DFT-256QAM	M	Edge_1RB_Right	18.98	PASS
N71	15	5	DFT-256QAM	M	Outer_Full	19.04	PASS
N71	15	5	DFT-256QAM	M	Inner_Full	19.01	PASS
N71	15	5	CP-QPSK	M	Edge_1RB_Left	20.8	PASS
N71	15	5	CP-QPSK	M	Edge_1RB_Right	20.68	PASS
N71	15	5	CP-QPSK	M	Outer_Full	20.6	PASS
N71	15	5	CP-QPSK	M	Inner_Full	22.09	PASS
N71	15	5	CP-16QAM	M	Edge_1RB_Left	20.86	PASS
N71	15	5	CP-16QAM	M	Edge_1RB_Right	20.81	PASS
N71	15	5	CP-16QAM	M	Outer_Full	20.63	PASS
N71	15	5	CP-16QAM	M	Inner_Full	21.6	PASS
N71	15	5	CP-64QAM	M	Edge_1RB_Left	20.49	PASS
N71	15	5	CP-64QAM	M	Edge_1RB_Right	20.2	PASS
N71	15	5	CP-64QAM	M	Outer_Full	20.07	PASS
N71	15	5	CP-64QAM	M	Inner_Full	20.07	PASS
N71	15	5	CP-256QAM	M	Edge_1RB_Left	17.27	PASS
N71	15	5	CP-256QAM	M	Edge_1RB_Right	16.9	PASS
N71	15	5	CP-256QAM	M	Outer_Full	17.16	PASS
N71	15	5	CP-256QAM	M	Inner_Full	17.16	PASS
N71	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	23.02	PASS
N71	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	23.17	PASS
N71	15	5	DFT-PI2BPSK	H	Inner_Full	23.45	PASS
N71	15	5	DFT-QPSK	H	Edge_1RB_Left	22.57	PASS
N71	15	5	DFT-QPSK	H	Edge_1RB_Right	22.62	PASS
N71	15	5	DFT-QPSK	H	Outer_Full	22.67	PASS
N71	15	5	DFT-QPSK	H	Inner_Full	23.53	PASS
N71	15	5	DFT-16QAM	H	Edge_1RB_Left	22.01	PASS
N71	15	5	DFT-16QAM	H	Edge_1RB_Right	22.03	PASS
N71	15	5	DFT-16QAM	H	Outer_Full	21.63	PASS
N71	15	5	DFT-16QAM	H	Inner_Full	22.71	PASS
N71	15	5	DFT-64QAM	H	Edge_1RB_Left	21.35	PASS
N71	15	5	DFT-64QAM	H	Edge_1RB_Right	21.22	PASS
N71	15	5	DFT-64QAM	H	Outer_Full	21.19	PASS
N71	15	5	DFT-64QAM	H	Inner_Full	21.28	PASS
N71	15	5	DFT-256QAM	H	Edge_1RB_Left	19.26	PASS
N71	15	5	DFT-256QAM	H	Edge_1RB_Right	19.06	PASS



N71	15	5	DFT-256QAM	H	Outer_Full	19.06	PASS
N71	15	5	DFT-256QAM	H	Inner_Full	19.05	PASS
N71	15	5	CP-QPSK	H	Edge_1RB_Left	20.73	PASS
N71	15	5	CP-QPSK	H	Edge_1RB_Right	20.65	PASS
N71	15	5	CP-QPSK	H	Outer_Full	20.58	PASS
N71	15	5	CP-QPSK	H	Inner_Full	22.16	PASS
N71	15	5	CP-16QAM	H	Edge_1RB_Left	20.69	PASS
N71	15	5	CP-16QAM	H	Edge_1RB_Right	20.76	PASS
N71	15	5	CP-16QAM	H	Outer_Full	20.57	PASS
N71	15	5	CP-16QAM	H	Inner_Full	21.67	PASS
N71	15	5	CP-64QAM	H	Edge_1RB_Left	20.22	PASS
N71	15	5	CP-64QAM	H	Edge_1RB_Right	20.2	PASS
N71	15	5	CP-64QAM	H	Outer_Full	20.08	PASS
N71	15	5	CP-64QAM	H	Inner_Full	20.2	PASS
N71	15	5	CP-256QAM	H	Edge_1RB_Left	17.26	PASS
N71	15	5	CP-256QAM	H	Edge_1RB_Right	17.41	PASS
N71	15	5	CP-256QAM	H	Outer_Full	17.1	PASS
N71	15	5	CP-256QAM	H	Inner_Full	17.12	PASS
N71	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.73	PASS
N71	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.89	PASS
N71	15	20	DFT-QPSK	L	Edge_1RB_Left	22.41	PASS
N71	15	20	DFT-QPSK	L	Edge_1RB_Right	22.55	PASS
N71	15	20	DFT-QPSK	L	Outer_Full	21.63	PASS
N71	15	20	DFT-16QAM	L	Edge_1RB_Left	21.45	PASS
N71	15	20	DFT-16QAM	L	Edge_1RB_Right	21.83	PASS
N71	15	20	DFT-16QAM	L	Outer_Full	21.78	PASS
N71	15	20	DFT-16QAM	L	Inner_Full	22.71	PASS
N71	15	20	DFT-64QAM	L	Edge_1RB_Left	20.9	PASS
N71	15	20	DFT-64QAM	L	Edge_1RB_Right	21.03	PASS
N71	15	20	DFT-64QAM	L	Outer_Full	21.3	PASS
N71	15	20	DFT-64QAM	L	Inner_Full	21.13	PASS
N71	15	20	DFT-256QAM	L	Edge_1RB_Left	19.21	PASS
N71	15	20	DFT-256QAM	L	Edge_1RB_Right	19.34	PASS
N71	15	20	DFT-256QAM	L	Outer_Full	19.28	PASS
N71	15	20	DFT-256QAM	L	Inner_Full	19.16	PASS
N71	15	20	CP-QPSK	L	Edge_1RB_Left	20.51	PASS
N71	15	20	CP-QPSK	L	Edge_1RB_Right	20.58	PASS
N71	15	20	CP-QPSK	L	Outer_Full	20.69	PASS
N71	15	20	CP-QPSK	L	Inner_Full	22.14	PASS
N71	15	20	CP-16QAM	L	Edge_1RB_Left	20.42	PASS
N71	15	20	CP-16QAM	L	Edge_1RB_Right	21.04	PASS
N71	15	20	CP-16QAM	L	Outer_Full	20.76	PASS
N71	15	20	CP-16QAM	L	Inner_Full	21.6	PASS
N71	15	20	CP-64QAM	L	Edge_1RB_Left	20.02	PASS
N71	15	20	CP-64QAM	L	Edge_1RB_Right	20.07	PASS



N71	15	20	CP-64QAM	L	Outer_Full	20.23	PASS
N71	15	20	CP-64QAM	L	Inner_Full	20.07	PASS
N71	15	20	CP-256QAM	L	Edge_1RB_Left	16.8	PASS
N71	15	20	CP-256QAM	L	Edge_1RB_Right	16.64	PASS
N71	15	20	CP-256QAM	L	Outer_Full	17.23	PASS
N71	15	20	CP-256QAM	L	Inner_Full	17.12	PASS
N71	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.91	PASS
N71	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.92	PASS
N71	15	20	DFT-PI2BPSK	M	Outer_Full	22.76	PASS
N71	15	20	DFT-PI2BPSK	M	Inner_Full	23.54	PASS
N71	15	20	DFT-QPSK	M	Edge_1RB_Left	22.39	PASS
N71	15	20	DFT-QPSK	M	Edge_1RB_Right	22.39	PASS
N71	15	20	DFT-QPSK	M	Outer_Full	22.25	PASS
N71	15	20	DFT-QPSK	M	Inner_Full	23.53	PASS
N71	15	20	DFT-16QAM	M	Edge_1RB_Left	21.9	PASS
N71	15	20	DFT-16QAM	M	Edge_1RB_Right	21.35	PASS
N71	15	20	DFT-16QAM	M	Outer_Full	21.3	PASS
N71	15	20	DFT-16QAM	M	Inner_Full	22.51	PASS
N71	15	20	DFT-64QAM	M	Edge_1RB_Left	21.04	PASS
N71	15	20	DFT-64QAM	M	Edge_1RB_Right	20.9	PASS
N71	15	20	DFT-64QAM	M	Outer_Full	20.66	PASS
N71	15	20	DFT-64QAM	M	Inner_Full	21.07	PASS
N71	15	20	DFT-256QAM	M	Edge_1RB_Left	18.93	PASS
N71	15	20	DFT-256QAM	M	Edge_1RB_Right	18.94	PASS
N71	15	20	DFT-256QAM	M	Outer_Full	18.69	PASS
N71	15	20	DFT-256QAM	M	Inner_Full	18.94	PASS
N71	15	20	CP-QPSK	M	Edge_1RB_Left	20.36	PASS
N71	15	20	CP-QPSK	M	Edge_1RB_Right	20.31	PASS
N71	15	20	CP-QPSK	M	Outer_Full	20.2	PASS
N71	15	20	CP-QPSK	M	Inner_Full	21.99	PASS
N71	15	20	CP-16QAM	M	Edge_1RB_Left	20.67	PASS
N71	15	20	CP-16QAM	M	Edge_1RB_Right	20.24	PASS
N71	15	20	CP-16QAM	M	Outer_Full	20.19	PASS
N71	15	20	CP-16QAM	M	Inner_Full	21.46	PASS
N71	15	20	CP-64QAM	M	Edge_1RB_Left	19.58	PASS
N71	15	20	CP-64QAM	M	Edge_1RB_Right	19.61	PASS
N71	15	20	CP-64QAM	M	Outer_Full	19.73	PASS
N71	15	20	CP-64QAM	M	Inner_Full	20.01	PASS
N71	15	20	CP-256QAM	M	Edge_1RB_Left	17.23	PASS
N71	15	20	CP-256QAM	M	Edge_1RB_Right	16.63	PASS
N71	15	20	CP-256QAM	M	Outer_Full	16.72	PASS
N71	15	20	CP-256QAM	M	Inner_Full	16.93	PASS
N71	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.87	PASS
N71	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.84	PASS
N71	15	20	DFT-PI2BPSK	H	Inner_Full	23.69	PASS
N71	15	20	DFT-QPSK	H	Edge_1RB_Left	22.38	PASS



N71	15	20	DFT-QPSK	H	Edge_1RB_Right	22.42	PASS
N71	15	20	DFT-QPSK	H	Outer_Full	22.81	PASS
N71	15	20	DFT-QPSK	H	Inner_Full	23.67	PASS
N71	15	20	DFT-16QAM	H	Edge_1RB_Left	21.92	PASS
N71	15	20	DFT-16QAM	H	Edge_1RB_Right	21.3	PASS
N71	15	20	DFT-16QAM	H	Outer_Full	21.76	PASS
N71	15	20	DFT-16QAM	H	Inner_Full	22.64	PASS
N71	15	20	DFT-64QAM	H	Edge_1RB_Left	20.61	PASS
N71	15	20	DFT-64QAM	H	Edge_1RB_Right	20.71	PASS
N71	15	20	DFT-64QAM	H	Outer_Full	21.28	PASS
N71	15	20	DFT-64QAM	H	Inner_Full	21.15	PASS
N71	15	20	DFT-256QAM	H	Edge_1RB_Left	19.07	PASS
N71	15	20	DFT-256QAM	H	Edge_1RB_Right	19.04	PASS
N71	15	20	DFT-256QAM	H	Outer_Full	19.35	PASS
N71	15	20	DFT-256QAM	H	Inner_Full	19.05	PASS
N71	15	20	CP-QPSK	H	Edge_1RB_Left	22.36	PASS
N71	15	20	CP-QPSK	H	Edge_1RB_Right	22.41	PASS
N71	15	20	CP-QPSK	H	Outer_Full	22.38	PASS
N71	15	20	CP-QPSK	H	Inner_Full	22.45	PASS
N71	15	20	CP-16QAM	H	Edge_1RB_Left	22.45	PASS
N71	15	20	CP-16QAM	H	Edge_1RB_Right	22.45	PASS
N71	15	20	CP-16QAM	H	Outer_Full	22.38	PASS
N71	15	20	CP-16QAM	H	Inner_Full	22.38	PASS
N71	15	20	CP-64QAM	H	Edge_1RB_Left	22.37	PASS
N71	15	20	CP-64QAM	H	Edge_1RB_Right	22.38	PASS
N71	15	20	CP-64QAM	H	Outer_Full	22.38	PASS
N71	15	20	CP-64QAM	H	Inner_Full	22.36	PASS
N71	15	20	CP-256QAM	H	Edge_1RB_Left	17.3	PASS
N71	15	20	CP-256QAM	H	Edge_1RB_Right	17.3	PASS
N71	15	20	CP-256QAM	H	Outer_Full	17.27	PASS
N71	15	20	CP-256QAM	H	Inner_Full	17.17	PASS



Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Verdict
N77-3450-3550	30	10	DFT-PI2BPSK	L	Outer_Full	23.05	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Inner_Full	23.54	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	22.93	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	22.93	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Outer_Full	22.51	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Inner_Full	23.54	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	22.43	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Right	22.36	PASS
N77-3450-3550	30	10	DFT-16QAM	L	Outer_Full	21.56	PASS
N77-3450-3550	30	10	DFT-16QAM	L	Inner_Full	22.64	PASS
N77-3450-3550	30	10	DFT-16QAM	L	Edge_1RB_Left	21.61	PASS
N77-3450-3550	30	10	DFT-16QAM	L	Edge_1RB_Right	21.95	PASS
N77-3450-3550	30	10	DFT-64QAM	L	Outer_Full	21.01	PASS
N77-3450-3550	30	10	DFT-64QAM	L	Inner_Full	21.01	PASS
N77-3450-3550	30	10	DFT-64QAM	L	Edge_1RB_Left	21.12	PASS
N77-3450-3550	30	10	DFT-64QAM	L	Edge_1RB_Right	21	PASS
N77-3450-3550	30	10	DFT-256QAM	L	Outer_Full	19.18	PASS
N77-3450-3550	30	10	DFT-256QAM	L	Inner_Full	19.24	PASS
N77-3450-3550	30	10	DFT-256QAM	L	Edge_1RB_Left	19.48	PASS
N77-3450-3550	30	10	DFT-256QAM	L	Edge_1RB_Right	19.59	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Outer_Full	23.3	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Inner_Full	23.81	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	23.25	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	23.2	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Outer_Full	22.82	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Inner_Full	23.83	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	22.71	PASS



N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Right	22.68	PASS
N77-3450-3550	30	10	DFT-16QAM	M	Outer_Full	21.85	PASS
N77-3450-3550	30	10	DFT-16QAM	M	Inner_Full	22.96	PASS
N77-3450-3550	30	10	DFT-16QAM	M	Edge_1RB_Left	22.04	PASS
N77-3450-3550	30	10	DFT-16QAM	M	Edge_1RB_Right	22.04	PASS
N77-3450-3550	30	10	DFT-64QAM	M	Outer_Full	21.35	PASS
N77-3450-3550	30	10	DFT-64QAM	M	Inner_Full	21.36	PASS
N77-3450-3550	30	10	DFT-64QAM	M	Edge_1RB_Left	21.41	PASS
N77-3450-3550	30	10	DFT-64QAM	M	Edge_1RB_Right	21.33	PASS
N77-3450-3550	30	10	DFT-256QAM	M	Outer_Full	19.31	PASS
N77-3450-3550	30	10	DFT-256QAM	M	Inner_Full	19.3	PASS
N77-3450-3550	30	10	DFT-256QAM	M	Edge_1RB_Left	19.23	PASS
N77-3450-3550	30	10	DFT-256QAM	M	Edge_1RB_Right	19.22	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Outer_Full	22.9	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Inner_Full	23.41	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	22.76	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	23	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Outer_Full	22.45	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Inner_Full	23.43	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	22.28	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Right	22.46	PASS
N77-3450-3550	30	10	DFT-16QAM	H	Outer_Full	21.44	PASS
N77-3450-3550	30	10	DFT-16QAM	H	Inner_Full	22.46	PASS
N77-3450-3550	30	10	DFT-16QAM	H	Edge_1RB_Left	21.7	PASS
N77-3450-3550	30	10	DFT-16QAM	H	Edge_1RB_Right	21.92	PASS
N77-3450-3550	30	10	DFT-64QAM	H	Outer_Full	20.95	PASS
N77-3450-3550	30	10	DFT-64QAM	H	Inner_Full	20.88	PASS
N77-3450-3550	30	10	DFT-64QAM	H	Edge_1RB_Left	21.02	PASS



N77-3450-3550	30	10	DFT-64QAM	H	Edge_1RB_Right	21.2	PASS
N77-3450-3550	30	10	DFT-256QAM	H	Outer_Full	18.97	PASS
N77-3450-3550	30	10	DFT-256QAM	H	Inner_Full	18.95	PASS
N77-3450-3550	30	10	DFT-256QAM	H	Edge_1RB_Left	18.78	PASS
N77-3450-3550	30	10	DFT-256QAM	H	Edge_1RB_Right	19.22	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	L	Outer_Full	23.07	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	L	Inner_Full	23.54	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	22.91	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	22.92	PASS
N77-3450-3550	30	15	DFT-QPSK	L	Outer_Full	22.53	PASS
N77-3450-3550	30	15	DFT-QPSK	L	Inner_Full	23.49	PASS
N77-3450-3550	30	15	DFT-QPSK	L	Edge_1RB_Left	22.42	PASS
N77-3450-3550	30	15	DFT-QPSK	L	Edge_1RB_Right	22.46	PASS
N77-3450-3550	30	15	DFT-16QAM	L	Outer_Full	21.56	PASS
N77-3450-3550	30	15	DFT-16QAM	L	Inner_Full	22.56	PASS
N77-3450-3550	30	15	DFT-16QAM	L	Edge_1RB_Left	21.73	PASS
N77-3450-3550	30	15	DFT-16QAM	L	Edge_1RB_Right	21.37	PASS
N77-3450-3550	30	15	DFT-64QAM	L	Outer_Full	21.08	PASS
N77-3450-3550	30	15	DFT-64QAM	L	Inner_Full	20.96	PASS
N77-3450-3550	30	15	DFT-64QAM	L	Edge_1RB_Left	20.95	PASS
N77-3450-3550	30	15	DFT-64QAM	L	Edge_1RB_Right	20.97	PASS
N77-3450-3550	30	15	DFT-256QAM	L	Outer_Full	19.24	PASS
N77-3450-3550	30	15	DFT-256QAM	L	Inner_Full	19.13	PASS
N77-3450-3550	30	15	DFT-256QAM	L	Edge_1RB_Left	19.28	PASS
N77-3450-3550	30	15	DFT-256QAM	L	Edge_1RB_Right	19.33	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	M	Outer_Full	23.29	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	M	Inner_Full	23.73	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	23.27	PASS



N77-3450-3550	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	23.22	PASS
N77-3450-3550	30	15	DFT-QPSK	M	Outer_Full	22.71	PASS
N77-3450-3550	30	15	DFT-QPSK	M	Inner_Full	23.77	PASS
N77-3450-3550	30	15	DFT-QPSK	M	Edge_1RB_Left	22.7	PASS
N77-3450-3550	30	15	DFT-QPSK	M	Edge_1RB_Right	22.68	PASS
N77-3450-3550	30	15	DFT-16QAM	M	Outer_Full	21.75	PASS
N77-3450-3550	30	15	DFT-16QAM	M	Inner_Full	22.76	PASS
N77-3450-3550	30	15	DFT-16QAM	M	Edge_1RB_Left	21.57	PASS
N77-3450-3550	30	15	DFT-16QAM	M	Edge_1RB_Right	21.71	PASS
N77-3450-3550	30	15	DFT-64QAM	M	Outer_Full	21.28	PASS
N77-3450-3550	30	15	DFT-64QAM	M	Inner_Full	21.25	PASS
N77-3450-3550	30	15	DFT-64QAM	M	Edge_1RB_Left	21.36	PASS
N77-3450-3550	30	15	DFT-64QAM	M	Edge_1RB_Right	21.79	PASS
N77-3450-3550	30	15	DFT-256QAM	M	Outer_Full	19.22	PASS
N77-3450-3550	30	15	DFT-256QAM	M	Inner_Full	19.3	PASS
N77-3450-3550	30	15	DFT-256QAM	M	Edge_1RB_Left	19.17	PASS
N77-3450-3550	30	15	DFT-256QAM	M	Edge_1RB_Right	19.29	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	H	Outer_Full	22.91	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	H	Inner_Full	23.35	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	H	Edge_1RB_Left	22.75	PASS
N77-3450-3550	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	22.86	PASS
N77-3450-3550	30	15	DFT-QPSK	H	Outer_Full	22.42	PASS
N77-3450-3550	30	15	DFT-QPSK	H	Inner_Full	23.34	PASS
N77-3450-3550	30	15	DFT-QPSK	H	Edge_1RB_Left	22.2	PASS
N77-3450-3550	30	15	DFT-QPSK	H	Edge_1RB_Right	22.33	PASS
N77-3450-3550	30	15	DFT-16QAM	H	Outer_Full	21.44	PASS
N77-3450-3550	30	15	DFT-16QAM	H	Inner_Full	22.36	PASS
N77-3450-3550	30	15	DFT-16QAM	H	Edge_1RB_Left	21.27	PASS



N77-3450-3550	30	15	DFT-16QAM	H	Edge_1RB_Right	21.38	PASS
N77-3450-3550	30	15	DFT-64QAM	H	Outer_Full	20.9	PASS
N77-3450-3550	30	15	DFT-64QAM	H	Inner_Full	20.91	PASS
N77-3450-3550	30	15	DFT-64QAM	H	Edge_1RB_Left	20.8	PASS
N77-3450-3550	30	15	DFT-64QAM	H	Edge_1RB_Right	20.95	PASS
N77-3450-3550	30	15	DFT-256QAM	H	Outer_Full	18.81	PASS
N77-3450-3550	30	15	DFT-256QAM	H	Inner_Full	18.84	PASS
N77-3450-3550	30	15	DFT-256QAM	H	Edge_1RB_Left	18.76	PASS
N77-3450-3550	30	15	DFT-256QAM	H	Edge_1RB_Right	18.98	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	L	Outer_Full	22.96	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	L	Inner_Full	23.53	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.85	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.93	PASS
N77-3450-3550	30	20	DFT-QPSK	L	Outer_Full	22.51	PASS
N77-3450-3550	30	20	DFT-QPSK	L	Inner_Full	23.54	PASS
N77-3450-3550	30	20	DFT-QPSK	L	Edge_1RB_Left	22.33	PASS
N77-3450-3550	30	20	DFT-QPSK	L	Edge_1RB_Right	22.44	PASS
N77-3450-3550	30	20	DFT-16QAM	L	Outer_Full	21.52	PASS
N77-3450-3550	30	20	DFT-16QAM	L	Inner_Full	22.57	PASS
N77-3450-3550	30	20	DFT-16QAM	L	Edge_1RB_Left	21.41	PASS
N77-3450-3550	30	20	DFT-16QAM	L	Edge_1RB_Right	21.51	PASS
N77-3450-3550	30	20	DFT-64QAM	L	Outer_Full	21	PASS
N77-3450-3550	30	20	DFT-64QAM	L	Inner_Full	21.07	PASS
N77-3450-3550	30	20	DFT-64QAM	L	Edge_1RB_Left	20.7	PASS
N77-3450-3550	30	20	DFT-64QAM	L	Edge_1RB_Right	21.13	PASS
N77-3450-3550	30	20	DFT-256QAM	L	Outer_Full	19.09	PASS
N77-3450-3550	30	20	DFT-256QAM	L	Inner_Full	19.16	PASS
N77-3450-3550	30	20	DFT-256QAM	L	Edge_1RB_Left	19.02	PASS



N77-3450-3550	30	20	DFT-256QAM	L	Edge_1RB_Right	19.14	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	M	Outer_Full	23.27	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	M	Inner_Full	23.73	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	23.21	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	23.17	PASS
N77-3450-3550	30	20	DFT-QPSK	M	Outer_Full	22.72	PASS
N77-3450-3550	30	20	DFT-QPSK	M	Inner_Full	23.76	PASS
N77-3450-3550	30	20	DFT-QPSK	M	Edge_1RB_Left	22.66	PASS
N77-3450-3550	30	20	DFT-QPSK	M	Edge_1RB_Right	22.58	PASS
N77-3450-3550	30	20	DFT-16QAM	M	Outer_Full	21.79	PASS
N77-3450-3550	30	20	DFT-16QAM	M	Inner_Full	22.74	PASS
N77-3450-3550	30	20	DFT-16QAM	M	Edge_1RB_Left	21.86	PASS
N77-3450-3550	30	20	DFT-16QAM	M	Edge_1RB_Right	21.73	PASS
N77-3450-3550	30	20	DFT-64QAM	M	Outer_Full	21.27	PASS
N77-3450-3550	30	20	DFT-64QAM	M	Inner_Full	21.28	PASS
N77-3450-3550	30	20	DFT-64QAM	M	Edge_1RB_Left	21.44	PASS
N77-3450-3550	30	20	DFT-64QAM	M	Edge_1RB_Right	21.45	PASS
N77-3450-3550	30	20	DFT-256QAM	M	Outer_Full	19.27	PASS
N77-3450-3550	30	20	DFT-256QAM	M	Inner_Full	19.2	PASS
N77-3450-3550	30	20	DFT-256QAM	M	Edge_1RB_Left	19.23	PASS
N77-3450-3550	30	20	DFT-256QAM	M	Edge_1RB_Right	19.24	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	H	Outer_Full	22.78	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	H	Inner_Full	23.27	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.69	PASS
N77-3450-3550	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.73	PASS
N77-3450-3550	30	20	DFT-QPSK	H	Outer_Full	22.34	PASS
N77-3450-3550	30	20	DFT-QPSK	H	Inner_Full	23.32	PASS
N77-3450-3550	30	20	DFT-QPSK	H	Edge_1RB_Left	22.12	PASS



N77-3450-3550	30	20	DFT-QPSK	H	Edge_1RB_Right	22.25	PASS
N77-3450-3550	30	20	DFT-16QAM	H	Outer_Full	21.33	PASS
N77-3450-3550	30	20	DFT-16QAM	H	Inner_Full	22.35	PASS
N77-3450-3550	30	20	DFT-16QAM	H	Edge_1RB_Left	21.36	PASS
N77-3450-3550	30	20	DFT-16QAM	H	Edge_1RB_Right	21.16	PASS
N77-3450-3550	30	20	DFT-64QAM	H	Outer_Full	20.78	PASS
N77-3450-3550	30	20	DFT-64QAM	H	Inner_Full	20.75	PASS
N77-3450-3550	30	20	DFT-64QAM	H	Edge_1RB_Left	20.94	PASS
N77-3450-3550	30	20	DFT-64QAM	H	Edge_1RB_Right	20.93	PASS
N77-3450-3550	30	20	DFT-256QAM	H	Outer_Full	18.8	PASS
N77-3450-3550	30	20	DFT-256QAM	H	Inner_Full	18.76	PASS
N77-3450-3550	30	20	DFT-256QAM	H	Edge_1RB_Left	18.84	PASS
N77-3450-3550	30	20	DFT-256QAM	H	Edge_1RB_Right	19.09	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	L	Outer_Full	23.09	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	L	Inner_Full	23.62	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	L	Edge_1RB_Left	22.82	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	L	Edge_1RB_Right	23.16	PASS
N77-3450-3550	30	25	DFT-QPSK	L	Outer_Full	22.63	PASS
N77-3450-3550	30	25	DFT-QPSK	L	Inner_Full	23.59	PASS
N77-3450-3550	30	25	DFT-QPSK	L	Edge_1RB_Left	22.37	PASS
N77-3450-3550	30	25	DFT-QPSK	L	Edge_1RB_Right	22.66	PASS
N77-3450-3550	30	25	DFT-16QAM	L	Outer_Full	21.6	PASS
N77-3450-3550	30	25	DFT-16QAM	L	Inner_Full	22.54	PASS
N77-3450-3550	30	25	DFT-16QAM	L	Edge_1RB_Left	21.3	PASS
N77-3450-3550	30	25	DFT-16QAM	L	Edge_1RB_Right	21.72	PASS
N77-3450-3550	30	25	DFT-64QAM	L	Outer_Full	21.1	PASS
N77-3450-3550	30	25	DFT-64QAM	L	Inner_Full	21.08	PASS
N77-3450-3550	30	25	DFT-64QAM	L	Edge_1RB_Left	21.27	PASS



N77-3450-3550	30	25	DFT-64QAM	L	Edge_1RB_Right	21.13	PASS
N77-3450-3550	30	25	DFT-256QAM	L	Outer_Full	19.14	PASS
N77-3450-3550	30	25	DFT-256QAM	L	Inner_Full	19.25	PASS
N77-3450-3550	30	25	DFT-256QAM	L	Edge_1RB_Left	19.49	PASS
N77-3450-3550	30	25	DFT-256QAM	L	Edge_1RB_Right	19.79	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	M	Outer_Full	23.28	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	M	Inner_Full	23.8	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	M	Edge_1RB_Left	23.16	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	M	Edge_1RB_Right	23.17	PASS
N77-3450-3550	30	25	DFT-QPSK	M	Outer_Full	22.8	PASS
N77-3450-3550	30	25	DFT-QPSK	M	Inner_Full	23.79	PASS
N77-3450-3550	30	25	DFT-QPSK	M	Edge_1RB_Left	22.68	PASS
N77-3450-3550	30	25	DFT-QPSK	M	Edge_1RB_Right	22.63	PASS
N77-3450-3550	30	25	DFT-16QAM	M	Outer_Full	21.81	PASS
N77-3450-3550	30	25	DFT-16QAM	M	Inner_Full	22.83	PASS
N77-3450-3550	30	25	DFT-16QAM	M	Edge_1RB_Left	22	PASS
N77-3450-3550	30	25	DFT-16QAM	M	Edge_1RB_Right	21.9	PASS
N77-3450-3550	30	25	DFT-64QAM	M	Outer_Full	21.31	PASS
N77-3450-3550	30	25	DFT-64QAM	M	Inner_Full	21.33	PASS
N77-3450-3550	30	25	DFT-64QAM	M	Edge_1RB_Left	21.47	PASS
N77-3450-3550	30	25	DFT-64QAM	M	Edge_1RB_Right	21.42	PASS
N77-3450-3550	30	25	DFT-256QAM	M	Outer_Full	19.28	PASS
N77-3450-3550	30	25	DFT-256QAM	M	Inner_Full	19.32	PASS
N77-3450-3550	30	25	DFT-256QAM	M	Edge_1RB_Left	19.42	PASS
N77-3450-3550	30	25	DFT-256QAM	M	Edge_1RB_Right	19.37	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	H	Outer_Full	22.88	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	H	Inner_Full	23.32	PASS
N77-3450-3550	30	25	DFT-PI2BPSK	H	Edge_1RB_Left	22.79	PASS



N77-3450-3550	30	25	DFT-PI2BPSK	H	Edge_1RB_Right	22.77	PASS
N77-3450-3550	30	25	DFT-QPSK	H	Outer_Full	22.4	PASS
N77-3450-3550	30	25	DFT-QPSK	H	Inner_Full	23.34	PASS
N77-3450-3550	30	25	DFT-QPSK	H	Edge_1RB_Left	22.42	PASS
N77-3450-3550	30	25	DFT-QPSK	H	Edge_1RB_Right	22.21	PASS
N77-3450-3550	30	25	DFT-16QAM	H	Outer_Full	21.31	PASS
N77-3450-3550	30	25	DFT-16QAM	H	Inner_Full	22.38	PASS
N77-3450-3550	30	25	DFT-16QAM	H	Edge_1RB_Left	21.48	PASS
N77-3450-3550	30	25	DFT-16QAM	H	Edge_1RB_Right	21.41	PASS
N77-3450-3550	30	25	DFT-64QAM	H	Outer_Full	20.9	PASS
N77-3450-3550	30	25	DFT-64QAM	H	Inner_Full	20.91	PASS
N77-3450-3550	30	25	DFT-64QAM	H	Edge_1RB_Left	20.61	PASS
N77-3450-3550	30	25	DFT-64QAM	H	Edge_1RB_Right	20.68	PASS
N77-3450-3550	30	25	DFT-256QAM	H	Outer_Full	18.9	PASS
N77-3450-3550	30	25	DFT-256QAM	H	Inner_Full	18.89	PASS
N77-3450-3550	30	25	DFT-256QAM	H	Edge_1RB_Left	19.02	PASS
N77-3450-3550	30	25	DFT-256QAM	H	Edge_1RB_Right	18.82	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	L	Outer_Full	23.14	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	L	Inner_Full	23.62	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	22.77	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	23.16	PASS
N77-3450-3550	30	30	DFT-QPSK	L	Outer_Full	22.62	PASS
N77-3450-3550	30	30	DFT-QPSK	L	Inner_Full	23.59	PASS
N77-3450-3550	30	30	DFT-QPSK	L	Edge_1RB_Left	22.25	PASS
N77-3450-3550	30	30	DFT-QPSK	L	Edge_1RB_Right	22.59	PASS
N77-3450-3550	30	30	DFT-16QAM	L	Outer_Full	21.63	PASS
N77-3450-3550	30	30	DFT-16QAM	L	Inner_Full	22.6	PASS
N77-3450-3550	30	30	DFT-16QAM	L	Edge_1RB_Left	21.27	PASS



N77-3450-3550	30	30	DFT-16QAM	L	Edge_1RB_Right	21.98	PASS
N77-3450-3550	30	30	DFT-64QAM	L	Outer_Full	21.13	PASS
N77-3450-3550	30	30	DFT-64QAM	L	Inner_Full	21.2	PASS
N77-3450-3550	30	30	DFT-64QAM	L	Edge_1RB_Left	20.84	PASS
N77-3450-3550	30	30	DFT-64QAM	L	Edge_1RB_Right	21.21	PASS
N77-3450-3550	30	30	DFT-256QAM	L	Outer_Full	19.24	PASS
N77-3450-3550	30	30	DFT-256QAM	L	Inner_Full	19.36	PASS
N77-3450-3550	30	30	DFT-256QAM	L	Edge_1RB_Left	19.08	PASS
N77-3450-3550	30	30	DFT-256QAM	L	Edge_1RB_Right	19.45	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	M	Outer_Full	23.3	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	M	Inner_Full	23.79	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	23.16	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	23.03	PASS
N77-3450-3550	30	30	DFT-QPSK	M	Outer_Full	22.82	PASS
N77-3450-3550	30	30	DFT-QPSK	M	Inner_Full	23.73	PASS
N77-3450-3550	30	30	DFT-QPSK	M	Edge_1RB_Left	22.66	PASS
N77-3450-3550	30	30	DFT-QPSK	M	Edge_1RB_Right	22.55	PASS
N77-3450-3550	30	30	DFT-16QAM	M	Outer_Full	21.8	PASS
N77-3450-3550	30	30	DFT-16QAM	M	Inner_Full	22.77	PASS
N77-3450-3550	30	30	DFT-16QAM	M	Edge_1RB_Left	21.46	PASS
N77-3450-3550	30	30	DFT-16QAM	M	Edge_1RB_Right	21.36	PASS
N77-3450-3550	30	30	DFT-64QAM	M	Outer_Full	21.38	PASS
N77-3450-3550	30	30	DFT-64QAM	M	Inner_Full	21.28	PASS
N77-3450-3550	30	30	DFT-64QAM	M	Edge_1RB_Left	21.05	PASS
N77-3450-3550	30	30	DFT-64QAM	M	Edge_1RB_Right	20.83	PASS
N77-3450-3550	30	30	DFT-256QAM	M	Outer_Full	19.3	PASS
N77-3450-3550	30	30	DFT-256QAM	M	Inner_Full	19.24	PASS
N77-3450-3550	30	30	DFT-256QAM	M	Edge_1RB_Left	19.3	PASS



N77-3450-3550	30	30	DFT-256QAM	M	Edge_1RB_Right	19.27	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	H	Outer_Full	22.86	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	H	Inner_Full	23.41	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	22.91	PASS
N77-3450-3550	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	22.63	PASS
N77-3450-3550	30	30	DFT-QPSK	H	Outer_Full	22.39	PASS
N77-3450-3550	30	30	DFT-QPSK	H	Inner_Full	23.35	PASS
N77-3450-3550	30	30	DFT-QPSK	H	Edge_1RB_Left	22.35	PASS
N77-3450-3550	30	30	DFT-QPSK	H	Edge_1RB_Right	22.14	PASS
N77-3450-3550	30	30	DFT-16QAM	H	Outer_Full	21.36	PASS
N77-3450-3550	30	30	DFT-16QAM	H	Inner_Full	22.43	PASS
N77-3450-3550	30	30	DFT-16QAM	H	Edge_1RB_Left	21.75	PASS
N77-3450-3550	30	30	DFT-16QAM	H	Edge_1RB_Right	21.46	PASS
N77-3450-3550	30	30	DFT-64QAM	H	Outer_Full	20.91	PASS
N77-3450-3550	30	30	DFT-64QAM	H	Inner_Full	20.93	PASS
N77-3450-3550	30	30	DFT-64QAM	H	Edge_1RB_Left	21.13	PASS
N77-3450-3550	30	30	DFT-64QAM	H	Edge_1RB_Right	20.84	PASS
N77-3450-3550	30	30	DFT-256QAM	H	Outer_Full	18.88	PASS
N77-3450-3550	30	30	DFT-256QAM	H	Inner_Full	18.87	PASS
N77-3450-3550	30	30	DFT-256QAM	H	Edge_1RB_Left	19.08	PASS
N77-3450-3550	30	30	DFT-256QAM	H	Edge_1RB_Right	18.69	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	L	Outer_Full	23.14	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	L	Inner_Full	23.71	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	22.48	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	22.71	PASS
N77-3450-3550	30	40	DFT-QPSK	L	Outer_Full	22.62	PASS
N77-3450-3550	30	40	DFT-QPSK	L	Inner_Full	23.65	PASS
N77-3450-3550	30	40	DFT-QPSK	L	Edge_1RB_Left	21.89	PASS



N77-3450-3550	30	40	DFT-QPSK	L	Edge_1RB_Right	22.2	PASS
N77-3450-3550	30	40	DFT-16QAM	L	Outer_Full	21.64	PASS
N77-3450-3550	30	40	DFT-16QAM	L	Inner_Full	22.7	PASS
N77-3450-3550	30	40	DFT-16QAM	L	Edge_1RB_Left	21.05	PASS
N77-3450-3550	30	40	DFT-16QAM	L	Edge_1RB_Right	21.36	PASS
N77-3450-3550	30	40	DFT-64QAM	L	Outer_Full	21.09	PASS
N77-3450-3550	30	40	DFT-64QAM	L	Inner_Full	21.17	PASS
N77-3450-3550	30	40	DFT-64QAM	L	Edge_1RB_Left	20.33	PASS
N77-3450-3550	30	40	DFT-64QAM	L	Edge_1RB_Right	20.59	PASS
N77-3450-3550	30	40	DFT-256QAM	L	Outer_Full	19.22	PASS
N77-3450-3550	30	40	DFT-256QAM	L	Inner_Full	19.28	PASS
N77-3450-3550	30	40	DFT-256QAM	L	Edge_1RB_Left	18.4	PASS
N77-3450-3550	30	40	DFT-256QAM	L	Edge_1RB_Right	18.59	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	M	Outer_Full	23.27	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	M	Inner_Full	23.8	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	22.86	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	22.71	PASS
N77-3450-3550	30	40	DFT-QPSK	M	Outer_Full	22.76	PASS
N77-3450-3550	30	40	DFT-QPSK	M	Inner_Full	23.82	PASS
N77-3450-3550	30	40	DFT-QPSK	M	Edge_1RB_Left	22.38	PASS
N77-3450-3550	30	40	DFT-QPSK	M	Edge_1RB_Right	22.25	PASS
N77-3450-3550	30	40	DFT-16QAM	M	Outer_Full	21.78	PASS
N77-3450-3550	30	40	DFT-16QAM	M	Inner_Full	22.83	PASS
N77-3450-3550	30	40	DFT-16QAM	M	Edge_1RB_Left	21.34	PASS
N77-3450-3550	30	40	DFT-16QAM	M	Edge_1RB_Right	21.16	PASS
N77-3450-3550	30	40	DFT-64QAM	M	Outer_Full	21.24	PASS
N77-3450-3550	30	40	DFT-64QAM	M	Inner_Full	21.36	PASS
N77-3450-3550	30	40	DFT-64QAM	M	Edge_1RB_Left	21.12	PASS



N77-3450-3550	30	40	DFT-64QAM	M	Edge_1RB_Right	20.9	PASS
N77-3450-3550	30	40	DFT-256QAM	M	Outer_Full	19.23	PASS
N77-3450-3550	30	40	DFT-256QAM	M	Inner_Full	19.35	PASS
N77-3450-3550	30	40	DFT-256QAM	M	Edge_1RB_Left	19	PASS
N77-3450-3550	30	40	DFT-256QAM	M	Edge_1RB_Right	18.92	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	H	Outer_Full	23	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	H	Inner_Full	23.51	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	22.67	PASS
N77-3450-3550	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	22.35	PASS
N77-3450-3550	30	40	DFT-QPSK	H	Outer_Full	22.48	PASS
N77-3450-3550	30	40	DFT-QPSK	H	Inner_Full	23.54	PASS
N77-3450-3550	30	40	DFT-QPSK	H	Edge_1RB_Left	22.21	PASS
N77-3450-3550	30	40	DFT-QPSK	H	Edge_1RB_Right	21.84	PASS
N77-3450-3550	30	40	DFT-16QAM	H	Outer_Full	21.42	PASS
N77-3450-3550	30	40	DFT-16QAM	H	Inner_Full	22.53	PASS
N77-3450-3550	30	40	DFT-16QAM	H	Edge_1RB_Left	21.27	PASS
N77-3450-3550	30	40	DFT-16QAM	H	Edge_1RB_Right	20.96	PASS
N77-3450-3550	30	40	DFT-64QAM	H	Outer_Full	21.03	PASS
N77-3450-3550	30	40	DFT-64QAM	H	Inner_Full	21.02	PASS
N77-3450-3550	30	40	DFT-64QAM	H	Edge_1RB_Left	20.59	PASS
N77-3450-3550	30	40	DFT-64QAM	H	Edge_1RB_Right	20.62	PASS
N77-3450-3550	30	40	DFT-256QAM	H	Outer_Full	19	PASS
N77-3450-3550	30	40	DFT-256QAM	H	Inner_Full	19.01	PASS
N77-3450-3550	30	40	DFT-256QAM	H	Edge_1RB_Left	18.8	PASS
N77-3450-3550	30	40	DFT-256QAM	H	Edge_1RB_Right	18.46	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Outer_Full	23.23	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Inner_Full	23.85	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	22.72	PASS



N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	22.92	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Outer_Full	22.71	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Inner_Full	23.82	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	22.27	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Right	22.46	PASS
N77-3450-3550	30	50	DFT-16QAM	L	Outer_Full	21.76	PASS
N77-3450-3550	30	50	DFT-16QAM	L	Inner_Full	22.87	PASS
N77-3450-3550	30	50	DFT-16QAM	L	Edge_1RB_Left	21.49	PASS
N77-3450-3550	30	50	DFT-16QAM	L	Edge_1RB_Right	21.66	PASS
N77-3450-3550	30	50	DFT-64QAM	L	Outer_Full	21.19	PASS
N77-3450-3550	30	50	DFT-64QAM	L	Inner_Full	21.33	PASS
N77-3450-3550	30	50	DFT-64QAM	L	Edge_1RB_Left	20.53	PASS
N77-3450-3550	30	50	DFT-64QAM	L	Edge_1RB_Right	21.11	PASS
N77-3450-3550	30	50	DFT-256QAM	L	Outer_Full	19.35	PASS
N77-3450-3550	30	50	DFT-256QAM	L	Inner_Full	19.46	PASS
N77-3450-3550	30	50	DFT-256QAM	L	Edge_1RB_Left	19.38	PASS
N77-3450-3550	30	50	DFT-256QAM	L	Edge_1RB_Right	19.35	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Outer_Full	23.3	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Inner_Full	23.84	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	23.06	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	22.87	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Outer_Full	22.8	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Inner_Full	23.9	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	22.56	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Right	22.34	PASS
N77-3450-3550	30	50	DFT-16QAM	M	Outer_Full	21.77	PASS
N77-3450-3550	30	50	DFT-16QAM	M	Inner_Full	22.85	PASS
N77-3450-3550	30	50	DFT-16QAM	M	Edge_1RB_Left	21.8	PASS



N77-3450-3550	30	50	DFT-16QAM	M	Edge_1RB_Right	21.54	PASS
N77-3450-3550	30	50	DFT-64QAM	M	Outer_Full	21.38	PASS
N77-3450-3550	30	50	DFT-64QAM	M	Inner_Full	21.4	PASS
N77-3450-3550	30	50	DFT-64QAM	M	Edge_1RB_Left	21.31	PASS
N77-3450-3550	30	50	DFT-64QAM	M	Edge_1RB_Right	21.13	PASS
N77-3450-3550	30	50	DFT-256QAM	M	Outer_Full	19.33	PASS
N77-3450-3550	30	50	DFT-256QAM	M	Inner_Full	19.4	PASS
N77-3450-3550	30	50	DFT-256QAM	M	Edge_1RB_Left	19.3	PASS
N77-3450-3550	30	50	DFT-256QAM	M	Edge_1RB_Right	19.01	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Outer_Full	23.02	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Inner_Full	23.6	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	22.83	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	22.63	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Outer_Full	22.57	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Inner_Full	23.63	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	22.42	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Right	22.14	PASS
N77-3450-3550	30	50	DFT-16QAM	H	Outer_Full	21.51	PASS
N77-3450-3550	30	50	DFT-16QAM	H	Inner_Full	22.63	PASS
N77-3450-3550	30	50	DFT-16QAM	H	Edge_1RB_Left	21.66	PASS
N77-3450-3550	30	50	DFT-16QAM	H	Edge_1RB_Right	21.42	PASS
N77-3450-3550	30	50	DFT-64QAM	H	Outer_Full	20.99	PASS
N77-3450-3550	30	50	DFT-64QAM	H	Inner_Full	21.18	PASS
N77-3450-3550	30	50	DFT-64QAM	H	Edge_1RB_Left	20.82	PASS
N77-3450-3550	30	50	DFT-64QAM	H	Edge_1RB_Right	20.62	PASS
N77-3450-3550	30	50	DFT-256QAM	H	Outer_Full	18.99	PASS
N77-3450-3550	30	50	DFT-256QAM	H	Inner_Full	19.11	PASS
N77-3450-3550	30	50	DFT-256QAM	H	Edge_1RB_Left	18.75	PASS



N77-3450-3550	30	50	DFT-256QAM	H	Edge_1RB_Right	18.76	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	L	Outer_Full	23.12	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	L	Inner_Full	23.85	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	22.5	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	22.79	PASS
N77-3450-3550	30	60	DFT-QPSK	L	Outer_Full	22.67	PASS
N77-3450-3550	30	60	DFT-QPSK	L	Inner_Full	23.84	PASS
N77-3450-3550	30	60	DFT-QPSK	L	Edge_1RB_Left	21.94	PASS
N77-3450-3550	30	60	DFT-QPSK	L	Edge_1RB_Right	22.32	PASS
N77-3450-3550	30	60	DFT-16QAM	L	Outer_Full	21.61	PASS
N77-3450-3550	30	60	DFT-16QAM	L	Inner_Full	22.82	PASS
N77-3450-3550	30	60	DFT-16QAM	L	Edge_1RB_Left	21.27	PASS
N77-3450-3550	30	60	DFT-16QAM	L	Edge_1RB_Right	21.56	PASS
N77-3450-3550	30	60	DFT-64QAM	L	Outer_Full	21.14	PASS
N77-3450-3550	30	60	DFT-64QAM	L	Inner_Full	21.34	PASS
N77-3450-3550	30	60	DFT-64QAM	L	Edge_1RB_Left	20.8	PASS
N77-3450-3550	30	60	DFT-64QAM	L	Edge_1RB_Right	21.03	PASS
N77-3450-3550	30	60	DFT-256QAM	L	Outer_Full	19.29	PASS
N77-3450-3550	30	60	DFT-256QAM	L	Inner_Full	19.45	PASS
N77-3450-3550	30	60	DFT-256QAM	L	Edge_1RB_Left	19.24	PASS
N77-3450-3550	30	60	DFT-256QAM	L	Edge_1RB_Right	19.29	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	M	Outer_Full	23.26	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	M	Inner_Full	23.81	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	22.72	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	22.41	PASS
N77-3450-3550	30	60	DFT-QPSK	M	Outer_Full	22.76	PASS
N77-3450-3550	30	60	DFT-QPSK	M	Inner_Full	23.8	PASS
N77-3450-3550	30	60	DFT-QPSK	M	Edge_1RB_Left	22.35	PASS



N77-3450-3550	30	60	DFT-QPSK	M	Edge_1RB_Right	21.94	PASS
N77-3450-3550	30	60	DFT-16QAM	M	Outer_Full	21.8	PASS
N77-3450-3550	30	60	DFT-16QAM	M	Inner_Full	22.91	PASS
N77-3450-3550	30	60	DFT-16QAM	M	Edge_1RB_Left	21.33	PASS
N77-3450-3550	30	60	DFT-16QAM	M	Edge_1RB_Right	21.02	PASS
N77-3450-3550	30	60	DFT-64QAM	M	Outer_Full	21.26	PASS
N77-3450-3550	30	60	DFT-64QAM	M	Inner_Full	21.33	PASS
N77-3450-3550	30	60	DFT-64QAM	M	Edge_1RB_Left	20.77	PASS
N77-3450-3550	30	60	DFT-64QAM	M	Edge_1RB_Right	20.46	PASS
N77-3450-3550	30	60	DFT-256QAM	M	Outer_Full	19.24	PASS
N77-3450-3550	30	60	DFT-256QAM	M	Inner_Full	19.35	PASS
N77-3450-3550	30	60	DFT-256QAM	M	Edge_1RB_Left	19.15	PASS
N77-3450-3550	30	60	DFT-256QAM	M	Edge_1RB_Right	18.58	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	H	Outer_Full	23.12	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	H	Inner_Full	23.73	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	22.85	PASS
N77-3450-3550	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	22.48	PASS
N77-3450-3550	30	60	DFT-QPSK	H	Outer_Full	22.56	PASS
N77-3450-3550	30	60	DFT-QPSK	H	Inner_Full	23.77	PASS
N77-3450-3550	30	60	DFT-QPSK	H	Edge_1RB_Left	22.37	PASS
N77-3450-3550	30	60	DFT-QPSK	H	Edge_1RB_Right	22.03	PASS
N77-3450-3550	30	60	DFT-16QAM	H	Outer_Full	21.6	PASS
N77-3450-3550	30	60	DFT-16QAM	H	Inner_Full	22.78	PASS
N77-3450-3550	30	60	DFT-16QAM	H	Edge_1RB_Left	21.25	PASS
N77-3450-3550	30	60	DFT-16QAM	H	Edge_1RB_Right	20.83	PASS
N77-3450-3550	30	60	DFT-64QAM	H	Outer_Full	21.08	PASS
N77-3450-3550	30	60	DFT-64QAM	H	Inner_Full	21.26	PASS
N77-3450-3550	30	60	DFT-64QAM	H	Edge_1RB_Left	21.14	PASS



N77-3450-3550	30	60	DFT-64QAM	H	Edge_1RB_Right	20.86	PASS
N77-3450-3550	30	60	DFT-256QAM	H	Outer_Full	19.05	PASS
N77-3450-3550	30	60	DFT-256QAM	H	Inner_Full	19.26	PASS
N77-3450-3550	30	60	DFT-256QAM	H	Edge_1RB_Left	19.12	PASS
N77-3450-3550	30	60	DFT-256QAM	H	Edge_1RB_Right	18.78	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	L	Outer_Full	22.89	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	L	Inner_Full	23.9	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	L	Edge_1RB_Left	22.48	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	L	Edge_1RB_Right	22.74	PASS
N77-3450-3550	30	70	DFT-QPSK	L	Outer_Full	22.38	PASS
N77-3450-3550	30	70	DFT-QPSK	L	Inner_Full	23.95	PASS
N77-3450-3550	30	70	DFT-QPSK	L	Edge_1RB_Left	22.17	PASS
N77-3450-3550	30	70	DFT-QPSK	L	Edge_1RB_Right	23.17	PASS
N77-3450-3550	30	70	DFT-16QAM	L	Outer_Full	21.43	PASS
N77-3450-3550	30	70	DFT-16QAM	L	Inner_Full	22.95	PASS
N77-3450-3550	30	70	DFT-16QAM	L	Edge_1RB_Left	21.27	PASS
N77-3450-3550	30	70	DFT-16QAM	L	Edge_1RB_Right	22.8	PASS
N77-3450-3550	30	70	DFT-64QAM	L	Outer_Full	20.9	PASS
N77-3450-3550	30	70	DFT-64QAM	L	Inner_Full	21.45	PASS
N77-3450-3550	30	70	DFT-64QAM	L	Edge_1RB_Left	20.71	PASS
N77-3450-3550	30	70	DFT-64QAM	L	Edge_1RB_Right	21.31	PASS
N77-3450-3550	30	70	DFT-256QAM	L	Outer_Full	18.9	PASS
N77-3450-3550	30	70	DFT-256QAM	L	Inner_Full	19.34	PASS
N77-3450-3550	30	70	DFT-256QAM	L	Edge_1RB_Right	19.03	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	M	Outer_Full	22.9	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	M	Inner_Full	23.9	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	M	Edge_1RB_Left	22.83	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	M	Edge_1RB_Right	22.8	PASS



N77-3450-3550	30	70	DFT-QPSK	M	Outer_Full	22.39	PASS
N77-3450-3550	30	70	DFT-QPSK	M	Inner_Full	23.88	PASS
N77-3450-3550	30	70	DFT-QPSK	M	Edge_1RB_Right	22.3	PASS
N77-3450-3550	30	70	DFT-16QAM	M	Outer_Full	21.39	PASS
N77-3450-3550	30	70	DFT-16QAM	M	Inner_Full	22.85	PASS
N77-3450-3550	30	70	DFT-16QAM	M	Edge_1RB_Left	21.45	PASS
N77-3450-3550	30	70	DFT-16QAM	M	Edge_1RB_Right	22.73	PASS
N77-3450-3550	30	70	DFT-64QAM	M	Outer_Full	20.9	PASS
N77-3450-3550	30	70	DFT-64QAM	M	Inner_Full	21.38	PASS
N77-3450-3550	30	70	DFT-64QAM	M	Edge_1RB_Left	20.6	PASS
N77-3450-3550	30	70	DFT-64QAM	M	Edge_1RB_Right	20.82	PASS
N77-3450-3550	30	70	DFT-256QAM	M	Outer_Full	18.9	PASS
N77-3450-3550	30	70	DFT-256QAM	M	Inner_Full	19.34	PASS
N77-3450-3550	30	70	DFT-256QAM	M	Edge_1RB_Right	19.15	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	H	Outer_Full	22.81	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	H	Inner_Full	23.82	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	H	Edge_1RB_Left	22.79	PASS
N77-3450-3550	30	70	DFT-PI2BPSK	H	Edge_1RB_Right	23.45	PASS
N77-3450-3550	30	70	DFT-QPSK	H	Outer_Full	22.26	PASS
N77-3450-3550	30	70	DFT-QPSK	H	Inner_Full	23.79	PASS
N77-3450-3550	30	70	DFT-QPSK	H	Edge_1RB_Left	22.16	PASS
N77-3450-3550	30	70	DFT-QPSK	H	Edge_1RB_Right	23.69	PASS
N77-3450-3550	30	70	DFT-16QAM	H	Outer_Full	21.32	PASS
N77-3450-3550	30	70	DFT-16QAM	H	Inner_Full	22.81	PASS
N77-3450-3550	30	70	DFT-16QAM	H	Edge_1RB_Left	21.25	PASS
N77-3450-3550	30	70	DFT-16QAM	H	Edge_1RB_Right	22.04	PASS
N77-3450-3550	30	70	DFT-64QAM	H	Outer_Full	20.78	PASS
N77-3450-3550	30	70	DFT-64QAM	H	Inner_Full	21.26	PASS



N77-3450-3550	30	70	DFT-64QAM	H	Edge_1RB_Left	20.63	PASS
N77-3450-3550	30	70	DFT-64QAM	H	Edge_1RB_Right	21.1	PASS
N77-3450-3550	30	70	DFT-256QAM	H	Outer_Full	18.78	PASS
N77-3450-3550	30	70	DFT-256QAM	H	Inner_Full	19.28	PASS
N77-3450-3550	30	70	DFT-256QAM	H	Edge_1RB_Left	18.29	PASS
N77-3450-3550	30	70	DFT-256QAM	H	Edge_1RB_Right	18.98	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	L	Outer_Full	23.18	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	L	Inner_Full	23.9	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	22.43	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	22.31	PASS
N77-3450-3550	30	80	DFT-QPSK	L	Outer_Full	22.71	PASS
N77-3450-3550	30	80	DFT-QPSK	L	Inner_Full	23.95	PASS
N77-3450-3550	30	80	DFT-QPSK	L	Edge_1RB_Left	21.99	PASS
N77-3450-3550	30	80	DFT-QPSK	L	Edge_1RB_Right	21.85	PASS
N77-3450-3550	30	80	DFT-16QAM	L	Outer_Full	21.72	PASS
N77-3450-3550	30	80	DFT-16QAM	L	Inner_Full	22.92	PASS
N77-3450-3550	30	80	DFT-16QAM	L	Edge_1RB_Left	21	PASS
N77-3450-3550	30	80	DFT-16QAM	L	Edge_1RB_Right	20.94	PASS
N77-3450-3550	30	80	DFT-64QAM	L	Outer_Full	21.18	PASS
N77-3450-3550	30	80	DFT-64QAM	L	Inner_Full	21.33	PASS
N77-3450-3550	30	80	DFT-64QAM	L	Edge_1RB_Left	20.81	PASS
N77-3450-3550	30	80	DFT-64QAM	L	Edge_1RB_Right	20.7	PASS
N77-3450-3550	30	80	DFT-256QAM	L	Outer_Full	19.14	PASS
N77-3450-3550	30	80	DFT-256QAM	L	Inner_Full	19.41	PASS
N77-3450-3550	30	80	DFT-256QAM	L	Edge_1RB_Left	19.12	PASS
N77-3450-3550	30	80	DFT-256QAM	L	Edge_1RB_Right	18.67	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	M	Outer_Full	23.13	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	M	Inner_Full	23.91	PASS



N77-3450-3550	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	22.45	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	22.12	PASS
N77-3450-3550	30	80	DFT-QPSK	M	Outer_Full	22.62	PASS
N77-3450-3550	30	80	DFT-QPSK	M	Inner_Full	23.86	PASS
N77-3450-3550	30	80	DFT-QPSK	M	Edge_1RB_Left	21.82	PASS
N77-3450-3550	30	80	DFT-QPSK	M	Edge_1RB_Right	21.58	PASS
N77-3450-3550	30	80	DFT-16QAM	M	Outer_Full	21.68	PASS
N77-3450-3550	30	80	DFT-16QAM	M	Inner_Full	22.86	PASS
N77-3450-3550	30	80	DFT-16QAM	M	Edge_1RB_Left	20.83	PASS
N77-3450-3550	30	80	DFT-16QAM	M	Edge_1RB_Right	20.63	PASS
N77-3450-3550	30	80	DFT-64QAM	M	Outer_Full	21.16	PASS
N77-3450-3550	30	80	DFT-64QAM	M	Inner_Full	21.32	PASS
N77-3450-3550	30	80	DFT-64QAM	M	Edge_1RB_Left	20.85	PASS
N77-3450-3550	30	80	DFT-64QAM	M	Edge_1RB_Right	20.56	PASS
N77-3450-3550	30	80	DFT-256QAM	M	Outer_Full	19.14	PASS
N77-3450-3550	30	80	DFT-256QAM	M	Inner_Full	19.39	PASS
N77-3450-3550	30	80	DFT-256QAM	M	Edge_1RB_Left	18.97	PASS
N77-3450-3550	30	80	DFT-256QAM	M	Edge_1RB_Right	18.3	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	H	Outer_Full	23.14	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	H	Inner_Full	23.79	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	22.68	PASS
N77-3450-3550	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	22.33	PASS
N77-3450-3550	30	80	DFT-QPSK	H	Outer_Full	22.61	PASS
N77-3450-3550	30	80	DFT-QPSK	H	Inner_Full	23.82	PASS
N77-3450-3550	30	80	DFT-QPSK	H	Edge_1RB_Left	22.1	PASS
N77-3450-3550	30	80	DFT-QPSK	H	Edge_1RB_Right	21.73	PASS
N77-3450-3550	30	80	DFT-16QAM	H	Outer_Full	21.62	PASS
N77-3450-3550	30	80	DFT-16QAM	H	Inner_Full	22.8	PASS



N77-3450-3550	30	80	DFT-16QAM	H	Edge_1RB_Left	21.5	PASS
N77-3450-3550	30	80	DFT-16QAM	H	Edge_1RB_Right	21.18	PASS
N77-3450-3550	30	80	DFT-64QAM	H	Outer_Full	21.14	PASS
N77-3450-3550	30	80	DFT-64QAM	H	Inner_Full	21.37	PASS
N77-3450-3550	30	80	DFT-64QAM	H	Edge_1RB_Left	20.37	PASS
N77-3450-3550	30	80	DFT-64QAM	H	Edge_1RB_Right	20.15	PASS
N77-3450-3550	30	80	DFT-256QAM	H	Outer_Full	19.08	PASS
N77-3450-3550	30	80	DFT-256QAM	H	Inner_Full	19.31	PASS
N77-3450-3550	30	80	DFT-256QAM	H	Edge_1RB_Left	18.9	PASS
N77-3450-3550	30	80	DFT-256QAM	H	Edge_1RB_Right	18.48	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	L	Outer_Full	23.12	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	L	Inner_Full	23.9	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	22.27	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	22.03	PASS
N77-3450-3550	30	90	DFT-QPSK	L	Outer_Full	22.66	PASS
N77-3450-3550	30	90	DFT-QPSK	L	Inner_Full	23.94	PASS
N77-3450-3550	30	90	DFT-QPSK	L	Edge_1RB_Left	21.77	PASS
N77-3450-3550	30	90	DFT-QPSK	L	Edge_1RB_Right	21.49	PASS
N77-3450-3550	30	90	DFT-16QAM	L	Outer_Full	21.62	PASS
N77-3450-3550	30	90	DFT-16QAM	L	Inner_Full	22.84	PASS
N77-3450-3550	30	90	DFT-16QAM	L	Edge_1RB_Left	20.71	PASS
N77-3450-3550	30	90	DFT-16QAM	L	Edge_1RB_Right	20.32	PASS
N77-3450-3550	30	90	DFT-64QAM	L	Outer_Full	21.13	PASS
N77-3450-3550	30	90	DFT-64QAM	L	Inner_Full	21.44	PASS
N77-3450-3550	30	90	DFT-64QAM	L	Edge_1RB_Right	19.6	PASS
N77-3450-3550	30	90	DFT-256QAM	L	Inner_Full	19.36	PASS
N77-3450-3550	30	90	DFT-256QAM	L	Edge_1RB_Left	18.74	PASS
N77-3450-3550	30	90	DFT-256QAM	L	Edge_1RB_Right	18.08	PASS



N77-3450-3550	30	90	DFT-PI2BPSK	M	Outer_Full	23.11	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	M	Inner_Full	23.9	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	22.16	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	22.04	PASS
N77-3450-3550	30	90	DFT-QPSK	M	Outer_Full	22.62	PASS
N77-3450-3550	30	90	DFT-QPSK	M	Inner_Full	23.89	PASS
N77-3450-3550	30	90	DFT-QPSK	M	Edge_1RB_Left	21.69	PASS
N77-3450-3550	30	90	DFT-QPSK	M	Edge_1RB_Right	21.5	PASS
N77-3450-3550	30	90	DFT-16QAM	M	Outer_Full	21.62	PASS
N77-3450-3550	30	90	DFT-16QAM	M	Inner_Full	22.84	PASS
N77-3450-3550	30	90	DFT-16QAM	M	Edge_1RB_Left	20.72	PASS
N77-3450-3550	30	90	DFT-16QAM	M	Edge_1RB_Right	20.53	PASS
N77-3450-3550	30	90	DFT-64QAM	M	Outer_Full	21.12	PASS
N77-3450-3550	30	90	DFT-64QAM	M	Inner_Full	21.34	PASS
N77-3450-3550	30	90	DFT-64QAM	M	Edge_1RB_Left	20.35	PASS
N77-3450-3550	30	90	DFT-64QAM	M	Edge_1RB_Right	20.39	PASS
N77-3450-3550	30	90	DFT-256QAM	M	Outer_Full	19.05	PASS
N77-3450-3550	30	90	DFT-256QAM	M	Inner_Full	19.34	PASS
N77-3450-3550	30	90	DFT-256QAM	M	Edge_1RB_Left	18.57	PASS
N77-3450-3550	30	90	DFT-256QAM	M	Edge_1RB_Right	18.19	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	H	Outer_Full	23.06	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	H	Inner_Full	23.85	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	H	Edge_1RB_Left	22.26	PASS
N77-3450-3550	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	22.17	PASS
N77-3450-3550	30	90	DFT-QPSK	H	Outer_Full	22.59	PASS
N77-3450-3550	30	90	DFT-QPSK	H	Inner_Full	23.83	PASS
N77-3450-3550	30	90	DFT-QPSK	H	Edge_1RB_Left	21.78	PASS
N77-3450-3550	30	90	DFT-QPSK	H	Edge_1RB_Right	21.63	PASS



N77-3450-3550	30	90	DFT-16QAM	H	Outer_Full	21.54	PASS
N77-3450-3550	30	90	DFT-16QAM	H	Inner_Full	22.9	PASS
N77-3450-3550	30	90	DFT-16QAM	H	Edge_1RB_Left	20.89	PASS
N77-3450-3550	30	90	DFT-16QAM	H	Edge_1RB_Right	20.83	PASS
N77-3450-3550	30	90	DFT-64QAM	H	Outer_Full	21.09	PASS
N77-3450-3550	30	90	DFT-64QAM	H	Inner_Full	21.41	PASS
N77-3450-3550	30	90	DFT-64QAM	H	Edge_1RB_Left	20.48	PASS
N77-3450-3550	30	90	DFT-64QAM	H	Edge_1RB_Right	20.39	PASS
N77-3450-3550	30	90	DFT-256QAM	H	Outer_Full	19.05	PASS
N77-3450-3550	30	90	DFT-256QAM	H	Inner_Full	19.34	PASS
N77-3450-3550	30	90	DFT-256QAM	H	Edge_1RB_Left	18.62	PASS
N77-3450-3550	30	90	DFT-256QAM	H	Edge_1RB_Right	18.19	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	22.94	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Inner_Full	23.77	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	21.8	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	21.74	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	22.46	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Inner_Full	23.97	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	21.32	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Right	21.27	PASS
N77-3450-3550	30	100	DFT-16QAM	M	Outer_Full	21.48	PASS
N77-3450-3550	30	100	DFT-16QAM	M	Inner_Full	22.84	PASS
N77-3450-3550	30	100	DFT-16QAM	M	Edge_1RB_Left	20.61	PASS
N77-3450-3550	30	100	DFT-16QAM	M	Edge_1RB_Right	20.33	PASS
N77-3450-3550	30	100	DFT-64QAM	M	Outer_Full	20.92	PASS
N77-3450-3550	30	100	DFT-64QAM	M	Inner_Full	21.32	PASS
N77-3450-3550	30	100	DFT-64QAM	M	Edge_1RB_Left	19.66	PASS
N77-3450-3550	30	100	DFT-64QAM	M	Edge_1RB_Right	19.58	PASS



N77-3450-3550	30	100	DFT-256QAM	M	Outer_Full	18.89	PASS
N77-3450-3550	30	100	DFT-256QAM	M	Inner_Full	19.31	PASS
N77-3450-3550	30	100	DFT-256QAM	M	Edge_1RB_Left	18.66	PASS
N77-3450-3550	30	100	DFT-256QAM	M	Edge_1RB_Right	18.19	PASS
N77-3450-3550	30	90	DFT-256QAM	L	Outer_Full	19.09	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Outer_Full	21.93	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Inner_Full	22.46	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	21.84	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	21.93	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Outer_Full	21.24	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Inner_Full	22.35	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	21.16	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Right	21.22	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Outer_Full	20.36	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Inner_Full	21.35	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Edge_1RB_Left	20.39	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Edge_1RB_Right	20.37	PASS
N77-3700-3980	30	10	DFT-64QAM	L	Outer_Full	19.79	PASS
N77-3700-3980	30	10	DFT-64QAM	L	Inner_Full	19.87	PASS
N77-3700-3980	30	10	DFT-64QAM	L	Edge_1RB_Left	19.88	PASS
N77-3700-3980	30	10	DFT-64QAM	L	Edge_1RB_Right	19.99	PASS
N77-3700-3980	30	10	DFT-256QAM	L	Outer_Full	17.72	PASS
N77-3700-3980	30	10	DFT-256QAM	L	Inner_Full	17.77	PASS
N77-3700-3980	30	10	DFT-256QAM	L	Edge_1RB_Left	17.52	PASS
N77-3700-3980	30	10	DFT-256QAM	L	Edge_1RB_Right	17.53	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Outer_Full	22.42	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Inner_Full	22.96	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	22.33	PASS



N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	22.34	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Outer_Full	21.97	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Inner_Full	22.9	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	21.78	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Right	21.81	PASS
N77-3700-3980	30	10	DFT-16QAM	M	Outer_Full	20.95	PASS
N77-3700-3980	30	10	DFT-16QAM	M	Inner_Full	21.96	PASS
N77-3700-3980	30	10	DFT-16QAM	M	Edge_1RB_Left	21.02	PASS
N77-3700-3980	30	10	DFT-16QAM	M	Edge_1RB_Right	21.06	PASS
N77-3700-3980	30	10	DFT-64QAM	M	Outer_Full	20.32	PASS
N77-3700-3980	30	10	DFT-64QAM	M	Inner_Full	20.35	PASS
N77-3700-3980	30	10	DFT-64QAM	M	Edge_1RB_Left	20.36	PASS
N77-3700-3980	30	10	DFT-64QAM	M	Edge_1RB_Right	20.44	PASS
N77-3700-3980	30	10	DFT-256QAM	M	Outer_Full	18.31	PASS
N77-3700-3980	30	10	DFT-256QAM	M	Inner_Full	18.23	PASS
N77-3700-3980	30	10	DFT-256QAM	M	Edge_1RB_Left	18.49	PASS
N77-3700-3980	30	10	DFT-256QAM	M	Edge_1RB_Right	18.73	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Outer_Full	22.89	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Inner_Full	23.41	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	22.72	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	22.89	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Outer_Full	22.44	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Inner_Full	23.37	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	22.21	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Right	22.39	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Outer_Full	21.46	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Inner_Full	22.45	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Edge_1RB_Left	21.4	PASS



N77-3700-3980	30	10	DFT-16QAM	H	Edge_1RB_Right	21.3	PASS
N77-3700-3980	30	10	DFT-64QAM	H	Outer_Full	20.88	PASS
N77-3700-3980	30	10	DFT-64QAM	H	Inner_Full	21	PASS
N77-3700-3980	30	10	DFT-64QAM	H	Edge_1RB_Left	20.83	PASS
N77-3700-3980	30	10	DFT-64QAM	H	Edge_1RB_Right	20.6	PASS
N77-3700-3980	30	10	DFT-256QAM	H	Outer_Full	18.93	PASS
N77-3700-3980	30	10	DFT-256QAM	H	Inner_Full	18.89	PASS
N77-3700-3980	30	10	DFT-256QAM	H	Edge_1RB_Left	18.71	PASS
N77-3700-3980	30	10	DFT-256QAM	H	Edge_1RB_Right	18.91	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	L	Outer_Full	21.82	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	L	Inner_Full	22.32	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	21.58	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	21.84	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Outer_Full	21.32	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Inner_Full	22.27	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Edge_1RB_Left	20.99	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Edge_1RB_Right	21.21	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Outer_Full	20.39	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Inner_Full	21.35	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Edge_1RB_Left	20.07	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Edge_1RB_Right	20.27	PASS
N77-3700-3980	30	15	DFT-64QAM	L	Outer_Full	19.78	PASS
N77-3700-3980	30	15	DFT-64QAM	L	Inner_Full	19.77	PASS
N77-3700-3980	30	15	DFT-64QAM	L	Edge_1RB_Left	19.53	PASS
N77-3700-3980	30	15	DFT-64QAM	L	Edge_1RB_Right	19.81	PASS
N77-3700-3980	30	15	DFT-256QAM	L	Outer_Full	17.81	PASS
N77-3700-3980	30	15	DFT-256QAM	L	Inner_Full	17.81	PASS
N77-3700-3980	30	15	DFT-256QAM	L	Edge_1RB_Left	17.67	PASS



N77-3700-3980	30	15	DFT-256QAM	L	Edge_1RB_Right	17.78	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	M	Outer_Full	22.28	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	M	Inner_Full	22.75	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	22.08	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	22.21	PASS
N77-3700-3980	30	15	DFT-QPSK	M	Outer_Full	21.74	PASS
N77-3700-3980	30	15	DFT-QPSK	M	Inner_Full	22.68	PASS
N77-3700-3980	30	15	DFT-QPSK	M	Edge_1RB_Left	21.57	PASS
N77-3700-3980	30	15	DFT-QPSK	M	Edge_1RB_Right	21.67	PASS
N77-3700-3980	30	15	DFT-16QAM	M	Outer_Full	20.78	PASS
N77-3700-3980	30	15	DFT-16QAM	M	Inner_Full	21.77	PASS
N77-3700-3980	30	15	DFT-16QAM	M	Edge_1RB_Left	20.76	PASS
N77-3700-3980	30	15	DFT-16QAM	M	Edge_1RB_Right	20.58	PASS
N77-3700-3980	30	15	DFT-64QAM	M	Outer_Full	20.27	PASS
N77-3700-3980	30	15	DFT-64QAM	M	Inner_Full	20.19	PASS
N77-3700-3980	30	15	DFT-64QAM	M	Edge_1RB_Left	20.01	PASS
N77-3700-3980	30	15	DFT-64QAM	M	Edge_1RB_Right	20.08	PASS
N77-3700-3980	30	15	DFT-256QAM	M	Outer_Full	18.2	PASS
N77-3700-3980	30	15	DFT-256QAM	M	Inner_Full	18.3	PASS
N77-3700-3980	30	15	DFT-256QAM	M	Edge_1RB_Left	18.34	PASS
N77-3700-3980	30	15	DFT-256QAM	M	Edge_1RB_Right	18.43	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	H	Outer_Full	22.69	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	H	Inner_Full	23.22	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	H	Edge_1RB_Left	22.43	PASS
N77-3700-3980	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	22.64	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Outer_Full	22.21	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Inner_Full	23.21	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Edge_1RB_Left	21.88	PASS



N77-3700-3980	30	15	DFT-QPSK	H	Edge_1RB_Right	22.18	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Outer_Full	21.27	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Inner_Full	22.24	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Edge_1RB_Left	21.26	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Edge_1RB_Right	21.58	PASS
N77-3700-3980	30	15	DFT-64QAM	H	Outer_Full	20.69	PASS
N77-3700-3980	30	15	DFT-64QAM	H	Inner_Full	20.82	PASS
N77-3700-3980	30	15	DFT-64QAM	H	Edge_1RB_Left	20.43	PASS
N77-3700-3980	30	15	DFT-64QAM	H	Edge_1RB_Right	20.66	PASS
N77-3700-3980	30	15	DFT-256QAM	H	Outer_Full	18.65	PASS
N77-3700-3980	30	15	DFT-256QAM	H	Inner_Full	18.71	PASS
N77-3700-3980	30	15	DFT-256QAM	H	Edge_1RB_Left	18.52	PASS
N77-3700-3980	30	15	DFT-256QAM	H	Edge_1RB_Right	18.8	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	L	Outer_Full	21.77	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	L	Inner_Full	22.31	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	21.42	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	21.73	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Outer_Full	21.26	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Inner_Full	22.29	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Edge_1RB_Left	21	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Edge_1RB_Right	21.25	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Outer_Full	20.34	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Inner_Full	21.22	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Edge_1RB_Left	20.21	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Edge_1RB_Right	20.48	PASS
N77-3700-3980	30	20	DFT-64QAM	L	Outer_Full	19.81	PASS
N77-3700-3980	30	20	DFT-64QAM	L	Inner_Full	19.81	PASS
N77-3700-3980	30	20	DFT-64QAM	L	Edge_1RB_Left	19.41	PASS



N77-3700-3980	30	20	DFT-64QAM	L	Edge_1RB_Right	19.64	PASS
N77-3700-3980	30	20	DFT-256QAM	L	Outer_Full	17.76	PASS
N77-3700-3980	30	20	DFT-256QAM	L	Inner_Full	17.87	PASS
N77-3700-3980	30	20	DFT-256QAM	L	Edge_1RB_Left	17.87	PASS
N77-3700-3980	30	20	DFT-256QAM	L	Edge_1RB_Right	17.76	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	M	Outer_Full	22.32	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	M	Inner_Full	22.84	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.16	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.21	PASS
N77-3700-3980	30	20	DFT-QPSK	M	Outer_Full	21.78	PASS
N77-3700-3980	30	20	DFT-QPSK	M	Inner_Full	22.86	PASS
N77-3700-3980	30	20	DFT-QPSK	M	Edge_1RB_Left	21.7	PASS
N77-3700-3980	30	20	DFT-QPSK	M	Edge_1RB_Right	21.78	PASS
N77-3700-3980	30	20	DFT-16QAM	M	Outer_Full	20.82	PASS
N77-3700-3980	30	20	DFT-16QAM	M	Inner_Full	21.84	PASS
N77-3700-3980	30	20	DFT-16QAM	M	Edge_1RB_Left	20.37	PASS
N77-3700-3980	30	20	DFT-16QAM	M	Edge_1RB_Right	20.45	PASS
N77-3700-3980	30	20	DFT-64QAM	M	Outer_Full	20.33	PASS
N77-3700-3980	30	20	DFT-64QAM	M	Inner_Full	20.4	PASS
N77-3700-3980	30	20	DFT-64QAM	M	Edge_1RB_Left	20.3	PASS
N77-3700-3980	30	20	DFT-64QAM	M	Edge_1RB_Right	20.59	PASS
N77-3700-3980	30	20	DFT-256QAM	M	Outer_Full	18.33	PASS
N77-3700-3980	30	20	DFT-256QAM	M	Inner_Full	18.27	PASS
N77-3700-3980	30	20	DFT-256QAM	M	Edge_1RB_Left	18.34	PASS
N77-3700-3980	30	20	DFT-256QAM	M	Edge_1RB_Right	18.33	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Outer_Full	22.66	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Inner_Full	23.15	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.24	PASS



N77-3700-3980	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.62	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Outer_Full	22.16	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Inner_Full	23.12	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Edge_1RB_Left	21.77	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Edge_1RB_Right	22.16	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Outer_Full	21.24	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Inner_Full	22.25	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Edge_1RB_Left	20.83	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Edge_1RB_Right	21.24	PASS
N77-3700-3980	30	20	DFT-64QAM	H	Outer_Full	20.68	PASS
N77-3700-3980	30	20	DFT-64QAM	H	Inner_Full	20.7	PASS
N77-3700-3980	30	20	DFT-64QAM	H	Edge_1RB_Left	20.19	PASS
N77-3700-3980	30	20	DFT-64QAM	H	Edge_1RB_Right	20.76	PASS
N77-3700-3980	30	20	DFT-256QAM	H	Outer_Full	18.69	PASS
N77-3700-3980	30	20	DFT-256QAM	H	Inner_Full	18.57	PASS
N77-3700-3980	30	20	DFT-256QAM	H	Edge_1RB_Left	18.41	PASS
N77-3700-3980	30	20	DFT-256QAM	H	Edge_1RB_Right	18.79	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	L	Outer_Full	21.78	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	L	Inner_Full	22.31	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	L	Edge_1RB_Left	21.31	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	L	Edge_1RB_Right	21.66	PASS
N77-3700-3980	30	25	DFT-QPSK	L	Outer_Full	21.28	PASS
N77-3700-3980	30	25	DFT-QPSK	L	Inner_Full	22.32	PASS
N77-3700-3980	30	25	DFT-QPSK	L	Edge_1RB_Left	20.85	PASS
N77-3700-3980	30	25	DFT-QPSK	L	Edge_1RB_Right	21.16	PASS
N77-3700-3980	30	25	DFT-16QAM	L	Outer_Full	20.26	PASS
N77-3700-3980	30	25	DFT-16QAM	L	Inner_Full	21.26	PASS
N77-3700-3980	30	25	DFT-16QAM	L	Edge_1RB_Left	20.12	PASS



N77-3700-3980	30	25	DFT-16QAM	L	Edge_1RB_Right	20.29	PASS
N77-3700-3980	30	25	DFT-64QAM	L	Outer_Full	19.77	PASS
N77-3700-3980	30	25	DFT-64QAM	L	Inner_Full	19.77	PASS
N77-3700-3980	30	25	DFT-64QAM	L	Edge_1RB_Left	19.71	PASS
N77-3700-3980	30	25	DFT-64QAM	L	Edge_1RB_Right	19.56	PASS
N77-3700-3980	30	25	DFT-256QAM	L	Outer_Full	17.75	PASS
N77-3700-3980	30	25	DFT-256QAM	L	Inner_Full	17.79	PASS
N77-3700-3980	30	25	DFT-256QAM	L	Edge_1RB_Left	17.56	PASS
N77-3700-3980	30	25	DFT-256QAM	L	Edge_1RB_Right	17.68	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	M	Outer_Full	22.35	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	M	Inner_Full	22.87	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	M	Edge_1RB_Left	22.09	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	M	Edge_1RB_Right	22.39	PASS
N77-3700-3980	30	25	DFT-QPSK	M	Outer_Full	21.97	PASS
N77-3700-3980	30	25	DFT-QPSK	M	Inner_Full	22.98	PASS
N77-3700-3980	30	25	DFT-QPSK	M	Edge_1RB_Left	21.8	PASS
N77-3700-3980	30	25	DFT-QPSK	M	Edge_1RB_Right	21.99	PASS
N77-3700-3980	30	25	DFT-16QAM	M	Outer_Full	20.98	PASS
N77-3700-3980	30	25	DFT-16QAM	M	Inner_Full	22.02	PASS
N77-3700-3980	30	25	DFT-16QAM	M	Edge_1RB_Left	20.81	PASS
N77-3700-3980	30	25	DFT-16QAM	M	Edge_1RB_Right	21.18	PASS
N77-3700-3980	30	25	DFT-64QAM	M	Outer_Full	20.49	PASS
N77-3700-3980	30	25	DFT-64QAM	M	Inner_Full	20.46	PASS
N77-3700-3980	30	25	DFT-64QAM	M	Edge_1RB_Left	20.1	PASS
N77-3700-3980	30	25	DFT-64QAM	M	Edge_1RB_Right	20.37	PASS
N77-3700-3980	30	25	DFT-256QAM	M	Outer_Full	18.45	PASS
N77-3700-3980	30	25	DFT-256QAM	M	Inner_Full	18.36	PASS
N77-3700-3980	30	25	DFT-256QAM	M	Edge_1RB_Left	18.25	PASS



N77-3700-3980	30	25	DFT-256QAM	M	Edge_1RB_Right	18.88	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	H	Outer_Full	22.64	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	H	Inner_Full	23.24	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	H	Edge_1RB_Left	22.27	PASS
N77-3700-3980	30	25	DFT-PI2BPSK	H	Edge_1RB_Right	22.64	PASS
N77-3700-3980	30	25	DFT-QPSK	H	Outer_Full	22.18	PASS
N77-3700-3980	30	25	DFT-QPSK	H	Inner_Full	23.25	PASS
N77-3700-3980	30	25	DFT-QPSK	H	Edge_1RB_Left	21.84	PASS
N77-3700-3980	30	25	DFT-QPSK	H	Edge_1RB_Right	22.09	PASS
N77-3700-3980	30	25	DFT-16QAM	H	Outer_Full	21.06	PASS
N77-3700-3980	30	25	DFT-16QAM	H	Inner_Full	22.18	PASS
N77-3700-3980	30	25	DFT-16QAM	H	Edge_1RB_Left	20.67	PASS
N77-3700-3980	30	25	DFT-16QAM	H	Edge_1RB_Right	21.1	PASS
N77-3700-3980	30	25	DFT-64QAM	H	Outer_Full	20.71	PASS
N77-3700-3980	30	25	DFT-64QAM	H	Inner_Full	20.65	PASS
N77-3700-3980	30	25	DFT-64QAM	H	Edge_1RB_Left	20.35	PASS
N77-3700-3980	30	25	DFT-64QAM	H	Edge_1RB_Right	20.54	PASS
N77-3700-3980	30	25	DFT-256QAM	H	Outer_Full	18.5	PASS
N77-3700-3980	30	25	DFT-256QAM	H	Inner_Full	18.55	PASS
N77-3700-3980	30	25	DFT-256QAM	H	Edge_1RB_Left	18.4	PASS
N77-3700-3980	30	25	DFT-256QAM	H	Edge_1RB_Right	18.84	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	L	Outer_Full	21.6	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	L	Inner_Full	22.18	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	21.07	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	21.48	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Outer_Full	21.11	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Inner_Full	22.23	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Edge_1RB_Left	20.58	PASS



N77-3700-3980	30	30	DFT-QPSK	L	Edge_1RB_Right	20.85	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Outer_Full	20.07	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Inner_Full	21.18	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Edge_1RB_Left	19.83	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Edge_1RB_Right	20.12	PASS
N77-3700-3980	30	30	DFT-64QAM	L	Outer_Full	19.51	PASS
N77-3700-3980	30	30	DFT-64QAM	L	Inner_Full	19.65	PASS
N77-3700-3980	30	30	DFT-64QAM	L	Edge_1RB_Left	19.12	PASS
N77-3700-3980	30	30	DFT-64QAM	L	Edge_1RB_Right	19.43	PASS
N77-3700-3980	30	30	DFT-256QAM	L	Outer_Full	17.6	PASS
N77-3700-3980	30	30	DFT-256QAM	L	Inner_Full	17.59	PASS
N77-3700-3980	30	30	DFT-256QAM	L	Edge_1RB_Left	17.09	PASS
N77-3700-3980	30	30	DFT-256QAM	L	Edge_1RB_Right	17.41	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	M	Outer_Full	22.2	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	M	Inner_Full	22.71	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	21.86	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	22.21	PASS
N77-3700-3980	30	30	DFT-QPSK	M	Outer_Full	21.67	PASS
N77-3700-3980	30	30	DFT-QPSK	M	Inner_Full	22.7	PASS
N77-3700-3980	30	30	DFT-QPSK	M	Edge_1RB_Left	21.38	PASS
N77-3700-3980	30	30	DFT-QPSK	M	Edge_1RB_Right	21.65	PASS
N77-3700-3980	30	30	DFT-16QAM	M	Outer_Full	20.72	PASS
N77-3700-3980	30	30	DFT-16QAM	M	Inner_Full	21.68	PASS
N77-3700-3980	30	30	DFT-16QAM	M	Edge_1RB_Left	20.58	PASS
N77-3700-3980	30	30	DFT-16QAM	M	Edge_1RB_Right	20.85	PASS
N77-3700-3980	30	30	DFT-64QAM	M	Outer_Full	20.21	PASS
N77-3700-3980	30	30	DFT-64QAM	M	Inner_Full	20.19	PASS
N77-3700-3980	30	30	DFT-64QAM	M	Edge_1RB_Left	20.28	PASS



N77-3700-3980	30	30	DFT-64QAM	M	Edge_1RB_Right	19.98	PASS
N77-3700-3980	30	30	DFT-256QAM	M	Outer_Full	18.21	PASS
N77-3700-3980	30	30	DFT-256QAM	M	Inner_Full	18.13	PASS
N77-3700-3980	30	30	DFT-256QAM	M	Edge_1RB_Left	17.86	PASS
N77-3700-3980	30	30	DFT-256QAM	M	Edge_1RB_Right	18.24	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	H	Outer_Full	22.37	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	H	Inner_Full	22.88	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	21.96	PASS
N77-3700-3980	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	22.31	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Outer_Full	21.83	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Inner_Full	22.86	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Edge_1RB_Left	21.3	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Edge_1RB_Right	21.69	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Outer_Full	21.16	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Inner_Full	22.2	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Edge_1RB_Left	21.06	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Edge_1RB_Right	21.54	PASS
N77-3700-3980	30	30	DFT-64QAM	H	Outer_Full	20.73	PASS
N77-3700-3980	30	30	DFT-64QAM	H	Inner_Full	20.74	PASS
N77-3700-3980	30	30	DFT-64QAM	H	Edge_1RB_Left	20.14	PASS
N77-3700-3980	30	30	DFT-64QAM	H	Edge_1RB_Right	20.46	PASS
N77-3700-3980	30	30	DFT-256QAM	H	Outer_Full	18.68	PASS
N77-3700-3980	30	30	DFT-256QAM	H	Inner_Full	18.69	PASS
N77-3700-3980	30	30	DFT-256QAM	H	Edge_1RB_Left	18.77	PASS
N77-3700-3980	30	30	DFT-256QAM	H	Edge_1RB_Right	19.12	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	L	Outer_Full	21.98	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	L	Inner_Full	22.63	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	21.32	PASS



N77-3700-3980	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	21.55	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Outer_Full	21.49	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Inner_Full	22.59	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Edge_1RB_Left	20.8	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Edge_1RB_Right	21.12	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Outer_Full	20.54	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Inner_Full	21.57	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Edge_1RB_Left	20.13	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Edge_1RB_Right	20.46	PASS
N77-3700-3980	30	40	DFT-64QAM	L	Outer_Full	19.87	PASS
N77-3700-3980	30	40	DFT-64QAM	L	Inner_Full	20.16	PASS
N77-3700-3980	30	40	DFT-64QAM	L	Edge_1RB_Left	19.52	PASS
N77-3700-3980	30	40	DFT-64QAM	L	Edge_1RB_Right	19.92	PASS
N77-3700-3980	30	40	DFT-256QAM	L	Outer_Full	17.96	PASS
N77-3700-3980	30	40	DFT-256QAM	L	Inner_Full	18.12	PASS
N77-3700-3980	30	40	DFT-256QAM	L	Edge_1RB_Left	17.06	PASS
N77-3700-3980	30	40	DFT-256QAM	L	Edge_1RB_Right	17.5	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	M	Outer_Full	22.51	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	M	Inner_Full	23.01	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	22.06	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	22.36	PASS
N77-3700-3980	30	40	DFT-QPSK	M	Outer_Full	22.02	PASS
N77-3700-3980	30	40	DFT-QPSK	M	Inner_Full	23.05	PASS
N77-3700-3980	30	40	DFT-QPSK	M	Edge_1RB_Left	21.63	PASS
N77-3700-3980	30	40	DFT-QPSK	M	Edge_1RB_Right	21.8	PASS
N77-3700-3980	30	40	DFT-16QAM	M	Outer_Full	21.01	PASS
N77-3700-3980	30	40	DFT-16QAM	M	Inner_Full	22.08	PASS
N77-3700-3980	30	40	DFT-16QAM	M	Edge_1RB_Left	20.71	PASS



N77-3700-3980	30	40	DFT-16QAM	M	Edge_1RB_Right	20.98	PASS
N77-3700-3980	30	40	DFT-64QAM	M	Outer_Full	20.5	PASS
N77-3700-3980	30	40	DFT-64QAM	M	Inner_Full	20.47	PASS
N77-3700-3980	30	40	DFT-64QAM	M	Edge_1RB_Left	20.29	PASS
N77-3700-3980	30	40	DFT-64QAM	M	Edge_1RB_Right	20.36	PASS
N77-3700-3980	30	40	DFT-256QAM	M	Outer_Full	18.51	PASS
N77-3700-3980	30	40	DFT-256QAM	M	Inner_Full	18.56	PASS
N77-3700-3980	30	40	DFT-256QAM	M	Edge_1RB_Left	18.27	PASS
N77-3700-3980	30	40	DFT-256QAM	M	Edge_1RB_Right	18.78	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	H	Outer_Full	22.67	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	H	Inner_Full	23.23	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	22.18	PASS
N77-3700-3980	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	22.54	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Outer_Full	22.2	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Inner_Full	23.25	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Edge_1RB_Left	21.76	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Edge_1RB_Right	22	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Outer_Full	21.26	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Inner_Full	22.27	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Edge_1RB_Left	20.63	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Edge_1RB_Right	20.9	PASS
N77-3700-3980	30	40	DFT-64QAM	H	Outer_Full	20.69	PASS
N77-3700-3980	30	40	DFT-64QAM	H	Inner_Full	20.78	PASS
N77-3700-3980	30	40	DFT-64QAM	H	Edge_1RB_Left	20.46	PASS
N77-3700-3980	30	40	DFT-64QAM	H	Edge_1RB_Right	20.72	PASS
N77-3700-3980	30	40	DFT-256QAM	H	Outer_Full	18.67	PASS
N77-3700-3980	30	40	DFT-256QAM	H	Inner_Full	18.73	PASS
N77-3700-3980	30	40	DFT-256QAM	H	Edge_1RB_Left	18.24	PASS



N77-3700-3980	30	40	DFT-256QAM	H	Edge_1RB_Right	18.52	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Outer_Full	22.17	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Inner_Full	22.79	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	21.62	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	22.1	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Outer_Full	21.64	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Inner_Full	22.71	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	21.15	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Right	21.68	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Outer_Full	20.67	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Inner_Full	21.82	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Edge_1RB_Left	20.42	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Edge_1RB_Right	20.69	PASS
N77-3700-3980	30	50	DFT-64QAM	L	Outer_Full	20.1	PASS
N77-3700-3980	30	50	DFT-64QAM	L	Inner_Full	20.25	PASS
N77-3700-3980	30	50	DFT-64QAM	L	Edge_1RB_Left	19.63	PASS
N77-3700-3980	30	50	DFT-64QAM	L	Edge_1RB_Right	20.19	PASS
N77-3700-3980	30	50	DFT-256QAM	L	Outer_Full	18.12	PASS
N77-3700-3980	30	50	DFT-256QAM	L	Inner_Full	18.19	PASS
N77-3700-3980	30	50	DFT-256QAM	L	Edge_1RB_Left	17.76	PASS
N77-3700-3980	30	50	DFT-256QAM	L	Edge_1RB_Right	18.03	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	M	Outer_Full	22.65	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	M	Inner_Full	23.12	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	22.43	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	22.76	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Outer_Full	22.17	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Inner_Full	23.15	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	22.01	PASS



N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Right	22.3	PASS
N77-3700-3980	30	50	DFT-16QAM	M	Outer_Full	21.16	PASS
N77-3700-3980	30	50	DFT-16QAM	M	Inner_Full	22.14	PASS
N77-3700-3980	30	50	DFT-16QAM	M	Edge_1RB_Left	21.13	PASS
N77-3700-3980	30	50	DFT-16QAM	M	Edge_1RB_Right	21.52	PASS
N77-3700-3980	30	50	DFT-64QAM	M	Outer_Full	20.69	PASS
N77-3700-3980	30	50	DFT-64QAM	M	Inner_Full	20.67	PASS
N77-3700-3980	30	50	DFT-64QAM	M	Edge_1RB_Left	20.61	PASS
N77-3700-3980	30	50	DFT-64QAM	M	Edge_1RB_Right	20.98	PASS
N77-3700-3980	30	50	DFT-256QAM	M	Outer_Full	18.65	PASS
N77-3700-3980	30	50	DFT-256QAM	M	Inner_Full	18.64	PASS
N77-3700-3980	30	50	DFT-256QAM	M	Edge_1RB_Left	18.56	PASS
N77-3700-3980	30	50	DFT-256QAM	M	Edge_1RB_Right	18.79	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Outer_Full	22.73	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Inner_Full	23.26	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	22.45	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	22.78	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Outer_Full	22.24	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Inner_Full	23.21	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	21.92	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Right	22.2	PASS
N77-3700-3980	30	50	DFT-16QAM	H	Outer_Full	21.25	PASS
N77-3700-3980	30	50	DFT-16QAM	H	Inner_Full	22.31	PASS
N77-3700-3980	30	50	DFT-16QAM	H	Edge_1RB_Left	21.22	PASS
N77-3700-3980	30	50	DFT-16QAM	H	Edge_1RB_Right	21.51	PASS
N77-3700-3980	30	50	DFT-64QAM	H	Outer_Full	20.75	PASS
N77-3700-3980	30	50	DFT-64QAM	H	Inner_Full	20.78	PASS
N77-3700-3980	30	50	DFT-64QAM	H	Edge_1RB_Left	20.34	PASS



N77-3700-3980	30	50	DFT-64QAM	H	Edge_1RB_Right	20.92	PASS
N77-3700-3980	30	50	DFT-256QAM	H	Outer_Full	18.78	PASS
N77-3700-3980	30	50	DFT-256QAM	H	Inner_Full	18.75	PASS
N77-3700-3980	30	50	DFT-256QAM	H	Edge_1RB_Left	18.84	PASS
N77-3700-3980	30	50	DFT-256QAM	H	Edge_1RB_Right	19.17	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	L	Outer_Full	22.12	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	L	Inner_Full	22.76	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	21.43	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	22.13	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Edge_1RB_Right	21.52	PASS
N77-3700-3980	30	60	DFT-16QAM	L	Outer_Full	20.6	PASS
N77-3700-3980	30	60	DFT-16QAM	L	Inner_Full	21.7	PASS
N77-3700-3980	30	60	DFT-16QAM	L	Edge_1RB_Left	20.37	PASS
N77-3700-3980	30	60	DFT-64QAM	L	Outer_Full	20.11	PASS
N77-3700-3980	30	60	DFT-64QAM	L	Inner_Full	20.21	PASS
N77-3700-3980	30	60	DFT-64QAM	L	Edge_1RB_Left	19.78	PASS
N77-3700-3980	30	60	DFT-256QAM	L	Outer_Full	18.07	PASS
N77-3700-3980	30	60	DFT-256QAM	L	Inner_Full	18.17	PASS
N77-3700-3980	30	60	DFT-256QAM	L	Edge_1RB_Left	17.49	PASS
N77-3700-3980	30	60	DFT-256QAM	L	Edge_1RB_Right	18.17	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	M	Outer_Full	22.63	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	M	Inner_Full	23.13	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	22.24	PASS
N77-3700-3980	30	60	DFT-QPSK	M	Inner_Full	23.14	PASS
N77-3700-3980	30	60	DFT-QPSK	M	Edge_1RB_Left	21.77	PASS
N77-3700-3980	30	60	DFT-QPSK	M	Edge_1RB_Right	22.1	PASS
N77-3700-3980	30	60	DFT-16QAM	M	Outer_Full	21.15	PASS
N77-3700-3980	30	60	DFT-16QAM	M	Inner_Full	22.11	PASS



N77-3700-3980	30	60	DFT-16QAM	M	Edge_1RB_Left	20.69	PASS
N77-3700-3980	30	60	DFT-16QAM	M	Edge_1RB_Right	21.07	PASS
N77-3700-3980	30	60	DFT-64QAM	M	Outer_Full	20.6	PASS
N77-3700-3980	30	60	DFT-64QAM	M	Inner_Full	20.6	PASS
N77-3700-3980	30	60	DFT-64QAM	M	Edge_1RB_Left	20.41	PASS
N77-3700-3980	30	60	DFT-64QAM	M	Edge_1RB_Right	20.79	PASS
N77-3700-3980	30	60	DFT-256QAM	M	Outer_Full	18.62	PASS
N77-3700-3980	30	60	DFT-256QAM	M	Inner_Full	18.62	PASS
N77-3700-3980	30	60	DFT-256QAM	M	Edge_1RB_Left	18.38	PASS
N77-3700-3980	30	60	DFT-256QAM	M	Edge_1RB_Right	18.71	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	H	Outer_Full	22.73	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	H	Inner_Full	23.28	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	22.15	PASS
N77-3700-3980	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	22.59	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Outer_Full	22.26	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Inner_Full	23.3	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Edge_1RB_Left	21.65	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Edge_1RB_Right	22.1	PASS
N77-3700-3980	30	60	DFT-16QAM	H	Outer_Full	21.2	PASS
N77-3700-3980	30	60	DFT-16QAM	H	Inner_Full	22.28	PASS
N77-3700-3980	30	60	DFT-16QAM	H	Edge_1RB_Left	20.83	PASS
N77-3700-3980	30	60	DFT-16QAM	H	Edge_1RB_Right	21.21	PASS
N77-3700-3980	30	60	DFT-64QAM	H	Outer_Full	20.66	PASS
N77-3700-3980	30	60	DFT-64QAM	H	Inner_Full	20.77	PASS
N77-3700-3980	30	60	DFT-64QAM	H	Edge_1RB_Left	19.97	PASS
N77-3700-3980	30	60	DFT-64QAM	H	Edge_1RB_Right	20.51	PASS
N77-3700-3980	30	60	DFT-256QAM	H	Outer_Full	18.74	PASS
N77-3700-3980	30	60	DFT-256QAM	H	Inner_Full	18.73	PASS



N77-3700-3980	30	60	DFT-256QAM	H	Edge_1RB_Left	18.23	PASS
N77-3700-3980	30	60	DFT-256QAM	H	Edge_1RB_Right	18.72	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	L	Outer_Full	21.92	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	L	Inner_Full	22.8	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	L	Edge_1RB_Right	22.01	PASS
N77-3700-3980	30	70	DFT-QPSK	L	Outer_Full	21.41	PASS
N77-3700-3980	30	70	DFT-QPSK	L	Inner_Full	22.79	PASS
N77-3700-3980	30	70	DFT-QPSK	L	Edge_1RB_Left	21.82	PASS
N77-3700-3980	30	70	DFT-16QAM	L	Outer_Full	20.43	PASS
N77-3700-3980	30	70	DFT-16QAM	L	Inner_Full	21.79	PASS
N77-3700-3980	30	70	DFT-16QAM	L	Edge_1RB_Left	20.32	PASS
N77-3700-3980	30	70	DFT-16QAM	L	Edge_1RB_Right	21.55	PASS
N77-3700-3980	30	70	DFT-64QAM	L	Edge_1RB_Left	20.76	PASS
N77-3700-3980	30	70	DFT-256QAM	L	Inner_Full	18.26	PASS
N77-3700-3980	30	70	DFT-256QAM	L	Edge_1RB_Left	17.84	PASS
N77-3700-3980	30	70	DFT-256QAM	L	Edge_1RB_Right	18.65	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	M	Inner_Full	23.28	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	M	Edge_1RB_Left	22.29	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	M	Edge_1RB_Right	22.58	PASS
N77-3700-3980	30	70	DFT-QPSK	M	Outer_Full	22.3	PASS
N77-3700-3980	30	70	DFT-QPSK	M	Inner_Full	23.29	PASS
N77-3700-3980	30	70	DFT-QPSK	M	Edge_1RB_Left	21.76	PASS
N77-3700-3980	30	70	DFT-QPSK	M	Edge_1RB_Right	22.07	PASS
N77-3700-3980	30	70	DFT-16QAM	M	Outer_Full	21.33	PASS
N77-3700-3980	30	70	DFT-16QAM	M	Inner_Full	22.24	PASS
N77-3700-3980	30	70	DFT-16QAM	M	Edge_1RB_Left	21.03	PASS
N77-3700-3980	30	70	DFT-16QAM	M	Edge_1RB_Right	21.34	PASS
N77-3700-3980	30	70	DFT-64QAM	M	Outer_Full	20.75	PASS



N77-3700-3980	30	70	DFT-64QAM	M	Inner_Full	20.76	PASS
N77-3700-3980	30	70	DFT-64QAM	M	Edge_1RB_Left	20.59	PASS
N77-3700-3980	30	70	DFT-64QAM	M	Edge_1RB_Right	20.97	PASS
N77-3700-3980	30	70	DFT-256QAM	M	Inner_Full	18.79	PASS
N77-3700-3980	30	70	DFT-256QAM	M	Edge_1RB_Left	17.75	PASS
N77-3700-3980	30	70	DFT-256QAM	M	Edge_1RB_Right	18.13	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	H	Edge_1RB_Left	22.28	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	H	Edge_1RB_Right	22.67	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Inner_Full	23.28	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Edge_1RB_Left	21.79	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Edge_1RB_Right	22.12	PASS
N77-3700-3980	30	70	DFT-16QAM	H	Outer_Full	21.3	PASS
N77-3700-3980	30	70	DFT-16QAM	H	Inner_Full	22.34	PASS
N77-3700-3980	30	70	DFT-16QAM	H	Edge_1RB_Right	21.4	PASS
N77-3700-3980	30	70	DFT-64QAM	H	Outer_Full	20.72	PASS
N77-3700-3980	30	70	DFT-64QAM	H	Inner_Full	20.8	PASS
N77-3700-3980	30	70	DFT-64QAM	H	Edge_1RB_Left	20.34	PASS
N77-3700-3980	30	70	DFT-64QAM	H	Edge_1RB_Right	21	PASS
N77-3700-3980	30	70	DFT-256QAM	H	Outer_Full	18.71	PASS
N77-3700-3980	30	70	DFT-256QAM	H	Inner_Full	18.77	PASS
N77-3700-3980	30	70	DFT-256QAM	H	Edge_1RB_Left	18.35	PASS
N77-3700-3980	30	70	DFT-256QAM	H	Edge_1RB_Right	18.7	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	L	Inner_Full	23.19	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	21.74	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	22.51	PASS
N77-3700-3980	30	80	DFT-QPSK	L	Inner_Full	23.1	PASS
N77-3700-3980	30	80	DFT-QPSK	L	Edge_1RB_Left	21.19	PASS
N77-3700-3980	30	80	DFT-QPSK	L	Edge_1RB_Right	21.96	PASS



N77-3700-3980	30	80	DFT-16QAM	L	Outer_Full	21.11	PASS
N77-3700-3980	30	80	DFT-16QAM	L	Inner_Full	22.19	PASS
N77-3700-3980	30	80	DFT-16QAM	L	Edge_1RB_Left	20.41	PASS
N77-3700-3980	30	80	DFT-16QAM	L	Edge_1RB_Right	21.14	PASS
N77-3700-3980	30	80	DFT-64QAM	L	Outer_Full	20.57	PASS
N77-3700-3980	30	80	DFT-64QAM	L	Inner_Full	20.63	PASS
N77-3700-3980	30	80	DFT-64QAM	L	Edge_1RB_Left	19.81	PASS
N77-3700-3980	30	80	DFT-64QAM	L	Edge_1RB_Right	20.57	PASS
N77-3700-3980	30	80	DFT-256QAM	L	Outer_Full	18.51	PASS
N77-3700-3980	30	80	DFT-256QAM	L	Inner_Full	18.61	PASS
N77-3700-3980	30	80	DFT-256QAM	L	Edge_1RB_Left	17.93	PASS
N77-3700-3980	30	80	DFT-256QAM	L	Edge_1RB_Right	18.62	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	M	Outer_Full	22.72	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	M	Inner_Full	23.33	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	22.13	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	22.34	PASS
N77-3700-3980	30	80	DFT-QPSK	M	Outer_Full	22.25	PASS
N77-3700-3980	30	80	DFT-QPSK	M	Inner_Full	23.35	PASS
N77-3700-3980	30	80	DFT-QPSK	M	Edge_1RB_Left	21.64	PASS
N77-3700-3980	30	80	DFT-QPSK	M	Edge_1RB_Right	21.97	PASS
N77-3700-3980	30	80	DFT-16QAM	M	Outer_Full	21.23	PASS
N77-3700-3980	30	80	DFT-16QAM	M	Inner_Full	22.36	PASS
N77-3700-3980	30	80	DFT-16QAM	M	Edge_1RB_Left	20.78	PASS
N77-3700-3980	30	80	DFT-16QAM	M	Edge_1RB_Right	21.11	PASS
N77-3700-3980	30	80	DFT-64QAM	M	Outer_Full	20.74	PASS
N77-3700-3980	30	80	DFT-64QAM	M	Inner_Full	20.83	PASS
N77-3700-3980	30	80	DFT-64QAM	M	Edge_1RB_Left	20.03	PASS
N77-3700-3980	30	80	DFT-64QAM	M	Edge_1RB_Right	20.17	PASS



N77-3700-3980	30	80	DFT-256QAM	M	Outer_Full	18.71	PASS
N77-3700-3980	30	80	DFT-256QAM	M	Inner_Full	18.78	PASS
N77-3700-3980	30	80	DFT-256QAM	M	Edge_1RB_Left	18.35	PASS
N77-3700-3980	30	80	DFT-256QAM	M	Edge_1RB_Right	18.66	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	H	Inner_Full	23.34	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	22.36	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	22.56	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Outer_Full	22.25	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Inner_Full	23.37	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Edge_1RB_Left	21.88	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Edge_1RB_Right	22.05	PASS
N77-3700-3980	30	80	DFT-16QAM	H	Outer_Full	21.24	PASS
N77-3700-3980	30	80	DFT-16QAM	H	Inner_Full	22.28	PASS
N77-3700-3980	30	80	DFT-16QAM	H	Edge_1RB_Left	20.87	PASS
N77-3700-3980	30	80	DFT-16QAM	H	Edge_1RB_Right	21.04	PASS
N77-3700-3980	30	80	DFT-64QAM	H	Outer_Full	20.77	PASS
N77-3700-3980	30	80	DFT-64QAM	H	Inner_Full	20.77	PASS
N77-3700-3980	30	80	DFT-64QAM	H	Edge_1RB_Left	20.54	PASS
N77-3700-3980	30	80	DFT-64QAM	H	Edge_1RB_Right	20.71	PASS
N77-3700-3980	30	80	DFT-256QAM	H	Outer_Full	18.74	PASS
N77-3700-3980	30	80	DFT-256QAM	H	Inner_Full	18.8	PASS
N77-3700-3980	30	80	DFT-256QAM	H	Edge_1RB_Left	18.61	PASS
N77-3700-3980	30	80	DFT-256QAM	H	Edge_1RB_Right	18.77	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	L	Outer_Full	22.57	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	L	Inner_Full	23.16	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	21.5	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	22.35	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Outer_Full	22.08	PASS



N77-3700-3980	30	90	DFT-QPSK	L	Inner_Full	23.18	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Edge_1RB_Left	21.04	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Edge_1RB_Right	21.77	PASS
N77-3700-3980	30	90	DFT-16QAM	L	Outer_Full	21.02	PASS
N77-3700-3980	30	90	DFT-16QAM	L	Inner_Full	22.15	PASS
N77-3700-3980	30	90	DFT-16QAM	L	Edge_1RB_Left	20.21	PASS
N77-3700-3980	30	90	DFT-16QAM	L	Edge_1RB_Right	20.93	PASS
N77-3700-3980	30	90	DFT-64QAM	L	Outer_Full	20.6	PASS
N77-3700-3980	30	90	DFT-64QAM	L	Inner_Full	20.72	PASS
N77-3700-3980	30	90	DFT-64QAM	L	Edge_1RB_Left	19.72	PASS
N77-3700-3980	30	90	DFT-64QAM	L	Edge_1RB_Right	20.08	PASS
N77-3700-3980	30	90	DFT-256QAM	L	Outer_Full	18.53	PASS
N77-3700-3980	30	90	DFT-256QAM	L	Inner_Full	18.74	PASS
N77-3700-3980	30	90	DFT-256QAM	L	Edge_1RB_Left	17.47	PASS
N77-3700-3980	30	90	DFT-256QAM	L	Edge_1RB_Right	18.51	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	M	Outer_Full	22.82	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	M	Inner_Full	23.48	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	21.96	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	22.32	PASS
N77-3700-3980	30	90	DFT-QPSK	M	Outer_Full	22.32	PASS
N77-3700-3980	30	90	DFT-QPSK	M	Inner_Full	23.5	PASS
N77-3700-3980	30	90	DFT-QPSK	M	Edge_1RB_Left	21.38	PASS
N77-3700-3980	30	90	DFT-QPSK	M	Edge_1RB_Right	21.76	PASS
N77-3700-3980	30	90	DFT-16QAM	M	Outer_Full	21.23	PASS
N77-3700-3980	30	90	DFT-16QAM	M	Inner_Full	22.48	PASS
N77-3700-3980	30	90	DFT-16QAM	M	Edge_1RB_Left	20.68	PASS
N77-3700-3980	30	90	DFT-16QAM	M	Edge_1RB_Right	21.06	PASS
N77-3700-3980	30	90	DFT-64QAM	M	Outer_Full	20.82	PASS



N77-3700-3980	30	90	DFT-64QAM	M	Inner_Full	21.01	PASS
N77-3700-3980	30	90	DFT-64QAM	M	Edge_1RB_Left	20.04	PASS
N77-3700-3980	30	90	DFT-64QAM	M	Edge_1RB_Right	20.42	PASS
N77-3700-3980	30	90	DFT-256QAM	M	Outer_Full	18.79	PASS
N77-3700-3980	30	90	DFT-256QAM	M	Inner_Full	18.97	PASS
N77-3700-3980	30	90	DFT-256QAM	M	Edge_1RB_Left	17.92	PASS
N77-3700-3980	30	90	DFT-256QAM	M	Edge_1RB_Right	18.37	PASS
N77-3700-3980	30	60	DFT-QPSK	M	Outer_Full	22.29	PASS
N77-3700-3980	30	70	DFT-64QAM	L	Outer_Full	19.9	PASS
N77-3700-3980	30	70	DFT-64QAM	L	Inner_Full	20.27	PASS
N77-3700-3980	30	70	DFT-64QAM	L	Edge_1RB_Right	20.09	PASS
N77-3700-3980	30	70	DFT-256QAM	L	Outer_Full	17.89	PASS
N77-3700-3980	30	70	DFT-256QAM	M	Outer_Full	18.54	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	H	Outer_Full	22.47	PASS
N77-3700-3980	30	70	DFT-PI2BPSK	H	Inner_Full	23.42	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Outer_Full	22.06	PASS
N77-3700-3980	30	80	DFT-PI2BPSK	H	Outer_Full	22.83	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	H	Outer_Full	22.78	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	H	Inner_Full	23.38	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	H	Edge_1RB_Left	22.12	PASS
N77-3700-3980	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	22.38	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Outer_Full	22.3	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Inner_Full	23.38	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Edge_1RB_Left	21.65	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Edge_1RB_Right	21.81	PASS
N77-3700-3980	30	90	DFT-16QAM	H	Outer_Full	21.23	PASS
N77-3700-3980	30	90	DFT-16QAM	H	Inner_Full	22.31	PASS
N77-3700-3980	30	90	DFT-16QAM	H	Edge_1RB_Left	20.78	PASS



N77-3700-3980	30	90	DFT-16QAM	H	Edge_1RB_Right	21.04	PASS
N77-3700-3980	30	90	DFT-64QAM	H	Outer_Full	20.71	PASS
N77-3700-3980	30	90	DFT-64QAM	H	Inner_Full	20.82	PASS
N77-3700-3980	30	90	DFT-64QAM	H	Edge_1RB_Left	20.23	PASS
N77-3700-3980	30	90	DFT-64QAM	H	Edge_1RB_Right	20.41	PASS
N77-3700-3980	30	90	DFT-256QAM	H	Outer_Full	18.76	PASS
N77-3700-3980	30	90	DFT-256QAM	H	Inner_Full	18.86	PASS
N77-3700-3980	30	90	DFT-256QAM	H	Edge_1RB_Left	18.1	PASS
N77-3700-3980	30	90	DFT-256QAM	H	Edge_1RB_Right	18.34	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	22.52	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Inner_Full	23.17	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	21.22	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	21.81	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	22.04	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Inner_Full	23.21	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	20.64	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	21.3	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Outer_Full	21	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Inner_Full	22.19	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Edge_1RB_Left	19.88	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Edge_1RB_Right	20.48	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Outer_Full	20.52	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Inner_Full	20.66	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Edge_1RB_Left	18.91	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Edge_1RB_Right	19.55	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Outer_Full	18.5	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Inner_Full	18.68	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Edge_1RB_Left	17.38	PASS



N77-3700-3980	30	100	DFT-256QAM	L	Edge_1RB_Right	17.99	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Inner_Full	23.26	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	21.59	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	21.81	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	22.12	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Inner_Full	23.29	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	21.09	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	21.34	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Outer_Full	21.07	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Inner_Full	22.29	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Edge_1RB_Left	20.22	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Edge_1RB_Right	20.47	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Outer_Full	20.57	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Inner_Full	20.83	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Edge_1RB_Left	19.45	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Edge_1RB_Right	19.78	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Outer_Full	18.54	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Inner_Full	18.82	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Edge_1RB_Left	17.62	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Edge_1RB_Right	18.19	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	22.63	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	22.63	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Inner_Full	23.31	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	21.78	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	21.98	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	22.19	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Inner_Full	23.31	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	21.15	PASS



N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	21.44	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Outer_Full	21.09	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Inner_Full	22.28	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Edge_1RB_Left	20.55	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Edge_1RB_Right	20.83	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Outer_Full	20.61	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Inner_Full	20.75	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Edge_1RB_Left	20.2	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Edge_1RB_Right	20.46	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Outer_Full	18.63	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Inner_Full	18.79	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Edge_1RB_Left	17.92	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Edge_1RB_Right	18.19	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Outer_Full	21.02	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Inner_Full	22.14	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Edge_1RB_Left	20.43	PASS



NSA Power

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Verdict
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.54	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.72	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	22.69	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	23.25	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.04	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.23	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+L	Outer_Full	22.20	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+L	Inner_Full	23.33	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.25	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.49	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+L	Outer_Full	21.34	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+L	Inner_Full	22.27	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.79	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.96	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+L	Outer_Full	20.89	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+L	Inner_Full	20.92	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	19.23	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	19.54	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+L	Outer_Full	18.85	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+L	Inner_Full	18.93	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.35	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.47	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+L	Outer_Full	20.33	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+L	Inner_Full	21.76	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	20.20	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.55	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+L	Outer_Full	20.28	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+L	Inner_Full	21.31	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.72	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	20.00	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+L	Outer_Full	19.83	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+L	Inner_Full	19.95	PASS



DC_2A_n41_A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	16.96	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	17.34	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+L	Outer_Full	16.73	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+L	Inner_Full	16.90	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.38	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.55	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	22.57	PASS
DC_2A_n41_A	30	5+20	DFT-PI2BPSK	M+M	Inner_Full	23.16	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.83	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.08	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+M	Outer_Full	22.14	PASS
DC_2A_n41_A	30	5+20	DFT-QPSK	M+M	Inner_Full	23.21	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.05	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.10	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+M	Outer_Full	21.24	PASS
DC_2A_n41_A	30	5+20	DFT-16QAM	M+M	Inner_Full	22.16	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.51	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.72	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+M	Outer_Full	20.86	PASS
DC_2A_n41_A	30	5+20	DFT-64QAM	M+M	Inner_Full	20.81	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.82	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	19.05	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+M	Outer_Full	18.74	PASS
DC_2A_n41_A	30	5+20	DFT-256QAM	M+M	Inner_Full	18.83	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.09	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.40	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+M	Outer_Full	20.25	PASS
DC_2A_n41_A	30	5+20	CP-QPSK	M+M	Inner_Full	21.67	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	19.93	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	20.23	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+M	Outer_Full	20.20	PASS
DC_2A_n41_A	30	5+20	CP-16QAM	M+M	Inner_Full	21.23	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.54	PASS
DC_2A_n41	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.84	PASS



A							
DC_2A_n41 A	30	5+20	CP-64QAM	M+M	Outer_Full	19.82	PASS
DC_2A_n41 A	30	5+20	CP-64QAM	M+M	Inner_Full	19.77	PASS
DC_2A_n41 A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	16.63	PASS
DC_2A_n41 A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	17.04	PASS
DC_2A_n41 A	30	5+20	CP-256QAM	M+M	Outer_Full	16.74	PASS
DC_2A_n41 A	30	5+20	CP-256QAM	M+M	Inner_Full	16.77	PASS
DC_2A_n41 A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.41	PASS
DC_2A_n41 A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.53	PASS
DC_2A_n41 A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	22.76	PASS
DC_2A_n41 A	30	5+20	DFT-PI2BPSK	M+H	Inner_Full	23.28	PASS
DC_2A_n41 A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.90	PASS
DC_2A_n41 A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.04	PASS
DC_2A_n41 A	30	5+20	DFT-QPSK	M+H	Outer_Full	22.24	PASS
DC_2A_n41 A	30	5+20	DFT-QPSK	M+H	Inner_Full	23.28	PASS
DC_2A_n41 A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.14	PASS
DC_2A_n41 A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.30	PASS
DC_2A_n41 A	30	5+20	DFT-16QAM	M+H	Outer_Full	21.37	PASS
DC_2A_n41 A	30	5+20	DFT-16QAM	M+H	Inner_Full	22.29	PASS
DC_2A_n41 A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.63	PASS
DC_2A_n41 A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.95	PASS
DC_2A_n41 A	30	5+20	DFT-64QAM	M+H	Outer_Full	20.94	PASS
DC_2A_n41 A	30	5+20	DFT-64QAM	M+H	Inner_Full	20.96	PASS
DC_2A_n41 A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.67	PASS
DC_2A_n41 A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	18.77	PASS
DC_2A_n41 A	30	5+20	DFT-256QAM	M+H	Outer_Full	18.89	PASS
DC_2A_n41 A	30	5+20	DFT-256QAM	M+H	Inner_Full	19.02	PASS
DC_2A_n41 A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.27	PASS
DC_2A_n41 A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.42	PASS
DC_2A_n41 A	30	5+20	CP-QPSK	M+H	Outer_Full	20.41	PASS
DC_2A_n41 A	30	5+20	CP-QPSK	M+H	Inner_Full	21.79	PASS
DC_2A_n41 A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.37	PASS
DC_2A_n41 A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.17	PASS
DC_2A_n41 A	30	5+20	CP-16QAM	M+H	Outer_Full	20.32	PASS



DC_2A_n41_A	30	5+20	CP-16QAM	M+H	Inner_Full	21.38	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.79	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.86	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+H	Outer_Full	19.90	PASS
DC_2A_n41_A	30	5+20	CP-64QAM	M+H	Inner_Full	19.93	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	16.76	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	16.99	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+H	Outer_Full	16.79	PASS
DC_2A_n41_A	30	5+20	CP-256QAM	M+H	Inner_Full	16.91	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.41	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.42	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+L	Outer_Full	22.70	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+L	Inner_Full	23.30	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.92	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.91	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+L	Outer_Full	22.18	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+L	Inner_Full	23.30	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	21.06	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.08	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+L	Outer_Full	21.39	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+L	Inner_Full	22.30	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	20.77	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	20.57	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+L	Outer_Full	20.80	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+L	Inner_Full	20.94	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	18.77	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	18.58	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+L	Outer_Full	18.78	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+L	Inner_Full	18.92	PASS
DC_2A_n41_A	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	20.11	PASS
DC_2A_n41_A	30	5+30	CP-QPSK	M+L	Edge_1RB_Right	20.23	PASS
DC_2A_n41_A	30	5+30	CP-QPSK	M+L	Outer_Full	20.32	PASS
DC_2A_n41_A	30	5+30	CP-QPSK	M+L	Inner_Full	21.84	PASS
DC_2A_n41	30	5+30	CP-16QAM	M+L	Edge_1RB_Left	20.05	PASS



A							
DC_2A_n41 A	30	5+30	CP-16QAM	M+L	Edge_1RB_Right	20.02	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+L	Outer_Full	20.27	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+L	Inner_Full	21.43	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+L	Edge_1RB_Left	19.77	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+L	Edge_1RB_Right	19.67	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+L	Outer_Full	19.79	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+L	Inner_Full	19.86	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+L	Edge_1RB_Left	16.69	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+L	Edge_1RB_Right	16.66	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+L	Outer_Full	16.83	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+L	Inner_Full	16.94	PASS
DC_2A_n41 A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.17	PASS
DC_2A_n41 A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.28	PASS
DC_2A_n41 A	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	22.58	PASS
DC_2A_n41 A	30	5+30	DFT-PI2BPSK	M+M	Inner_Full	23.14	PASS
DC_2A_n41 A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.67	PASS
DC_2A_n41 A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.74	PASS
DC_2A_n41 A	30	5+30	DFT-QPSK	M+M	Outer_Full	22.12	PASS
DC_2A_n41 A	30	5+30	DFT-QPSK	M+M	Inner_Full	23.12	PASS
DC_2A_n41 A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	20.72	PASS
DC_2A_n41 A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	21.01	PASS
DC_2A_n41 A	30	5+30	DFT-16QAM	M+M	Outer_Full	21.34	PASS
DC_2A_n41 A	30	5+30	DFT-16QAM	M+M	Inner_Full	22.13	PASS
DC_2A_n41 A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.32	PASS
DC_2A_n41 A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Right	20.51	PASS
DC_2A_n41 A	30	5+30	DFT-64QAM	M+M	Outer_Full	20.66	PASS
DC_2A_n41 A	30	5+30	DFT-64QAM	M+M	Inner_Full	20.76	PASS
DC_2A_n41 A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	18.59	PASS
DC_2A_n41 A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Right	18.68	PASS
DC_2A_n41 A	30	5+30	DFT-256QAM	M+M	Outer_Full	18.66	PASS
DC_2A_n41 A	30	5+30	DFT-256QAM	M+M	Inner_Full	18.80	PASS
DC_2A_n41 A	30	5+30	CP-QPSK	M+M	Edge_1RB_Left	19.94	PASS
DC_2A_n41 A	30	5+30	CP-QPSK	M+M	Edge_1RB_Right	20.06	PASS



DC_2A_n41_A	30	5+30	CP-QPSK	M+M	Outer_Full	20.18	PASS
DC_2A_n41_A	30	5+30	CP-QPSK	M+M	Inner_Full	21.70	PASS
DC_2A_n41_A	30	5+30	CP-16QAM	M+M	Edge_1RB_Left	19.77	PASS
DC_2A_n41_A	30	5+30	CP-16QAM	M+M	Edge_1RB_Right	20.03	PASS
DC_2A_n41_A	30	5+30	CP-16QAM	M+M	Outer_Full	20.13	PASS
DC_2A_n41_A	30	5+30	CP-16QAM	M+M	Inner_Full	21.27	PASS
DC_2A_n41_A	30	5+30	CP-64QAM	M+M	Edge_1RB_Left	19.28	PASS
DC_2A_n41_A	30	5+30	CP-64QAM	M+M	Edge_1RB_Right	19.40	PASS
DC_2A_n41_A	30	5+30	CP-64QAM	M+M	Outer_Full	19.65	PASS
DC_2A_n41_A	30	5+30	CP-64QAM	M+M	Inner_Full	19.75	PASS
DC_2A_n41_A	30	5+30	CP-256QAM	M+M	Edge_1RB_Left	16.66	PASS
DC_2A_n41_A	30	5+30	CP-256QAM	M+M	Edge_1RB_Right	16.79	PASS
DC_2A_n41_A	30	5+30	CP-256QAM	M+M	Outer_Full	16.66	PASS
DC_2A_n41_A	30	5+30	CP-256QAM	M+M	Inner_Full	16.78	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.14	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.42	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+H	Outer_Full	22.66	PASS
DC_2A_n41_A	30	5+30	DFT-PI2BPSK	M+H	Inner_Full	23.16	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.66	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.86	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+H	Outer_Full	22.11	PASS
DC_2A_n41_A	30	5+30	DFT-QPSK	M+H	Inner_Full	23.20	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.82	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.07	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+H	Outer_Full	21.26	PASS
DC_2A_n41_A	30	5+30	DFT-16QAM	M+H	Inner_Full	22.15	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.47	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	20.62	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+H	Outer_Full	20.77	PASS
DC_2A_n41_A	30	5+30	DFT-64QAM	M+H	Inner_Full	20.81	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	18.30	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	18.96	PASS
DC_2A_n41_A	30	5+30	DFT-256QAM	M+H	Outer_Full	18.79	PASS
DC_2A_n41	30	5+30	DFT-256QAM	M+H	Inner_Full	18.77	PASS



A							
DC_2A_n41 A	30	5+30	CP-QPSK	M+H	Edge_1RB_Left	19.91	PASS
DC_2A_n41 A	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	20.10	PASS
DC_2A_n41 A	30	5+30	CP-QPSK	M+H	Outer_Full	20.17	PASS
DC_2A_n41 A	30	5+30	CP-QPSK	M+H	Inner_Full	21.65	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+H	Edge_1RB_Left	19.87	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+H	Edge_1RB_Right	20.00	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+H	Outer_Full	20.18	PASS
DC_2A_n41 A	30	5+30	CP-16QAM	M+H	Inner_Full	21.29	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+H	Edge_1RB_Left	19.50	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+H	Edge_1RB_Right	19.66	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+H	Outer_Full	19.76	PASS
DC_2A_n41 A	30	5+30	CP-64QAM	M+H	Inner_Full	19.83	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+H	Edge_1RB_Left	16.53	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+H	Edge_1RB_Right	16.67	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+H	Outer_Full	16.78	PASS
DC_2A_n41 A	30	5+30	CP-256QAM	M+H	Inner_Full	16.76	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.29	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.11	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	22.53	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+L	Inner_Full	23.29	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.82	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.63	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+L	Outer_Full	22.19	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+L	Inner_Full	23.25	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.99	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.66	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+L	Outer_Full	21.26	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+L	Inner_Full	22.36	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.67	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.34	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+L	Outer_Full	20.68	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+L	Inner_Full	20.89	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.48	PASS



DC_2A_n41_A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.57	PASS
DC_2A_n41_A	30	5+40	DFT-256QAM	M+L	Outer_Full	18.78	PASS
DC_2A_n41_A	30	5+40	DFT-256QAM	M+L	Inner_Full	18.89	PASS
DC_2A_n41_A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	20.04	PASS
DC_2A_n41_A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	19.85	PASS
DC_2A_n41_A	30	5+40	CP-QPSK	M+L	Outer_Full	20.17	PASS
DC_2A_n41_A	30	5+40	CP-QPSK	M+L	Inner_Full	21.71	PASS
DC_2A_n41_A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	20.10	PASS
DC_2A_n41_A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	19.70	PASS
DC_2A_n41_A	30	5+40	CP-16QAM	M+L	Outer_Full	20.15	PASS
DC_2A_n41_A	30	5+40	CP-16QAM	M+L	Inner_Full	21.43	PASS
DC_2A_n41_A	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	19.45	PASS
DC_2A_n41_A	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	19.34	PASS
DC_2A_n41_A	30	5+40	CP-64QAM	M+L	Outer_Full	19.69	PASS
DC_2A_n41_A	30	5+40	CP-64QAM	M+L	Inner_Full	19.83	PASS
DC_2A_n41_A	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	17.11	PASS
DC_2A_n41_A	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	16.31	PASS
DC_2A_n41_A	30	5+40	CP-256QAM	M+L	Outer_Full	16.72	PASS
DC_2A_n41_A	30	5+40	CP-256QAM	M+L	Inner_Full	16.89	PASS
DC_2A_n41_A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.97	PASS
DC_2A_n41_A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.04	PASS
DC_2A_n41_A	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	22.51	PASS
DC_2A_n41_A	30	5+40	DFT-PI2BPSK	M+M	Inner_Full	23.16	PASS
DC_2A_n41_A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.55	PASS
DC_2A_n41_A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.52	PASS
DC_2A_n41_A	30	5+40	DFT-QPSK	M+M	Outer_Full	22.09	PASS
DC_2A_n41_A	30	5+40	DFT-QPSK	M+M	Inner_Full	23.13	PASS
DC_2A_n41_A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.57	PASS
DC_2A_n41_A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.67	PASS
DC_2A_n41_A	30	5+40	DFT-16QAM	M+M	Outer_Full	21.19	PASS
DC_2A_n41_A	30	5+40	DFT-16QAM	M+M	Inner_Full	22.21	PASS
DC_2A_n41_A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.42	PASS
DC_2A_n41_A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.29	PASS
DC_2A_n41	30	5+40	DFT-64QAM	M+M	Outer_Full	20.68	PASS



A							
DC_2A_n41 A	30	5+40	DFT-64QAM	M+M	Inner_Full	20.74	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.16	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.16	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+M	Outer_Full	18.72	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+M	Inner_Full	18.78	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	19.68	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	19.82	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+M	Outer_Full	20.10	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+M	Inner_Full	21.63	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	19.53	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	19.96	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+M	Outer_Full	20.05	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+M	Inner_Full	21.27	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+M	Edge_1RB_Left	19.22	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+M	Edge_1RB_Right	19.16	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+M	Outer_Full	19.66	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+M	Inner_Full	19.75	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+M	Edge_1RB_Left	16.57	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+M	Edge_1RB_Right	16.22	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+M	Outer_Full	16.69	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+M	Inner_Full	16.72	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.01	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.22	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	22.53	PASS
DC_2A_n41 A	30	5+40	DFT-PI2BPSK	M+H	Inner_Full	23.12	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.58	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.62	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+H	Outer_Full	22.02	PASS
DC_2A_n41 A	30	5+40	DFT-QPSK	M+H	Inner_Full	23.13	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.64	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.76	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+H	Outer_Full	21.16	PASS
DC_2A_n41 A	30	5+40	DFT-16QAM	M+H	Inner_Full	22.26	PASS



DC_2A_n41 A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.24	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.37	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+H	Outer_Full	20.66	PASS
DC_2A_n41 A	30	5+40	DFT-64QAM	M+H	Inner_Full	20.73	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.55	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	18.69	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+H	Outer_Full	18.70	PASS
DC_2A_n41 A	30	5+40	DFT-256QAM	M+H	Inner_Full	18.81	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	19.78	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	19.83	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+H	Outer_Full	20.11	PASS
DC_2A_n41 A	30	5+40	CP-QPSK	M+H	Inner_Full	21.61	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	19.70	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	19.88	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+H	Outer_Full	20.08	PASS
DC_2A_n41 A	30	5+40	CP-16QAM	M+H	Inner_Full	21.20	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	19.29	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	19.40	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+H	Outer_Full	19.61	PASS
DC_2A_n41 A	30	5+40	CP-64QAM	M+H	Inner_Full	19.65	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	16.39	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	16.92	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+H	Outer_Full	16.57	PASS
DC_2A_n41 A	30	5+40	CP-256QAM	M+H	Inner_Full	16.64	PASS
DC_2A_n41 A	30	5+50	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.38	PASS
DC_2A_n41 A	30	5+50	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.38	PASS
DC_2A_n41 A	30	5+50	DFT-PI2BPSK	M+L	Outer_Full	22.61	PASS
DC_2A_n41 A	30	5+50	DFT-PI2BPSK	M+L	Inner_Full	23.23	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	21.96	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	21.78	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+L	Outer_Full	22.14	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+L	Inner_Full	23.31	PASS
DC_2A_n41 A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	21.20	PASS
DC_2A_n41	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	21.04	PASS



A							
DC_2A_n41_A	30	5+50	DFT-16QAM	M+L	Outer_Full	21.29	PASS
DC_2A_n41_A	30	5+50	DFT-16QAM	M+L	Inner_Full	22.31	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+L	Edge_1RB_Left	20.61	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+L	Edge_1RB_Right	20.51	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+L	Outer_Full	20.76	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+L	Inner_Full	20.87	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Left	18.64	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Right	18.96	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+L	Outer_Full	18.74	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+L	Inner_Full	18.84	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	20.14	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	20.14	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+L	Outer_Full	20.22	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+L	Inner_Full	21.77	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	20.10	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	20.01	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+L	Outer_Full	20.25	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+L	Inner_Full	21.29	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+L	Edge_1RB_Left	19.69	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+L	Edge_1RB_Right	19.59	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+L	Outer_Full	19.79	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+L	Inner_Full	19.74	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+L	Edge_1RB_Left	16.68	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+L	Edge_1RB_Right	16.92	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+L	Outer_Full	16.71	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+L	Inner_Full	16.82	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.23	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.20	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+M	Outer_Full	22.52	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+M	Inner_Full	23.16	PASS
DC_2A_n41_A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	21.74	PASS
DC_2A_n41_A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	21.71	PASS
DC_2A_n41_A	30	5+50	DFT-QPSK	M+M	Outer_Full	22.14	PASS



DC_2A_n41_A	30	5+50	DFT-QPSK	M+M	Inner_Full	23.18	PASS
DC_2A_n41_A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.87	PASS
DC_2A_n41_A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.72	PASS
DC_2A_n41_A	30	5+50	DFT-16QAM	M+M	Outer_Full	21.07	PASS
DC_2A_n41_A	30	5+50	DFT-16QAM	M+M	Inner_Full	22.13	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Left	20.47	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Right	20.55	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+M	Outer_Full	20.69	PASS
DC_2A_n41_A	30	5+50	DFT-64QAM	M+M	Inner_Full	20.84	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Left	18.51	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Right	18.76	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+M	Outer_Full	18.64	PASS
DC_2A_n41_A	30	5+50	DFT-256QAM	M+M	Inner_Full	18.75	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	20.00	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	20.01	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+M	Outer_Full	20.14	PASS
DC_2A_n41_A	30	5+50	CP-QPSK	M+M	Inner_Full	21.63	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	19.92	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	19.84	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+M	Outer_Full	20.19	PASS
DC_2A_n41_A	30	5+50	CP-16QAM	M+M	Inner_Full	21.26	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+M	Edge_1RB_Left	19.43	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+M	Edge_1RB_Right	19.39	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+M	Outer_Full	19.71	PASS
DC_2A_n41_A	30	5+50	CP-64QAM	M+M	Inner_Full	19.75	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+M	Edge_1RB_Left	16.91	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+M	Edge_1RB_Right	16.81	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+M	Outer_Full	16.66	PASS
DC_2A_n41_A	30	5+50	CP-256QAM	M+M	Inner_Full	16.72	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.39	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.42	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+H	Outer_Full	22.60	PASS
DC_2A_n41_A	30	5+50	DFT-PI2BPSK	M+H	Inner_Full	23.13	PASS
DC_2A_n41	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	21.97	PASS



A							
DC_2A_n41 A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	21.93	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+H	Outer_Full	22.06	PASS
DC_2A_n41 A	30	5+50	DFT-QPSK	M+H	Inner_Full	23.04	PASS
DC_2A_n41 A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.13	PASS
DC_2A_n41 A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	21.09	PASS
DC_2A_n41 A	30	5+50	DFT-16QAM	M+H	Outer_Full	21.21	PASS
DC_2A_n41 A	30	5+50	DFT-16QAM	M+H	Inner_Full	22.11	PASS
DC_2A_n41 A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Left	20.98	PASS
DC_2A_n41 A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Right	20.71	PASS
DC_2A_n41 A	30	5+50	DFT-64QAM	M+H	Outer_Full	20.73	PASS
DC_2A_n41 A	30	5+50	DFT-64QAM	M+H	Inner_Full	20.70	PASS
DC_2A_n41 A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Left	18.89	PASS
DC_2A_n41 A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Right	18.69	PASS
DC_2A_n41 A	30	5+50	DFT-256QAM	M+H	Outer_Full	18.73	PASS
DC_2A_n41 A	30	5+50	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_2A_n41 A	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	20.23	PASS
DC_2A_n41 A	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	20.15	PASS
DC_2A_n41 A	30	5+50	CP-QPSK	M+H	Outer_Full	20.17	PASS
DC_2A_n41 A	30	5+50	CP-QPSK	M+H	Inner_Full	21.60	PASS
DC_2A_n41 A	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	20.01	PASS
DC_2A_n41 A	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	20.30	PASS
DC_2A_n41 A	30	5+50	CP-16QAM	M+H	Outer_Full	20.16	PASS
DC_2A_n41 A	30	5+50	CP-16QAM	M+H	Inner_Full	21.19	PASS
DC_2A_n41 A	30	5+50	CP-64QAM	M+H	Edge_1RB_Left	19.66	PASS
DC_2A_n41 A	30	5+50	CP-64QAM	M+H	Edge_1RB_Right	19.63	PASS
DC_2A_n41 A	30	5+50	CP-64QAM	M+H	Outer_Full	19.72	PASS
DC_2A_n41 A	30	5+50	CP-64QAM	M+H	Inner_Full	19.67	PASS
DC_2A_n41 A	30	5+50	CP-256QAM	M+H	Edge_1RB_Left	17.06	PASS
DC_2A_n41 A	30	5+50	CP-256QAM	M+H	Edge_1RB_Right	16.57	PASS
DC_2A_n41 A	30	5+50	CP-256QAM	M+H	Outer_Full	16.71	PASS
DC_2A_n41 A	30	5+50	CP-256QAM	M+H	Inner_Full	16.60	PASS
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.26	PASS
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.19	PASS



DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	22.63	PASS
DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+L	Inner_Full	23.16	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	21.86	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.71	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+L	Outer_Full	22.07	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+L	Inner_Full	23.19	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.00	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	20.91	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+L	Outer_Full	21.28	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+L	Inner_Full	22.17	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	20.43	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	20.59	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+L	Outer_Full	20.75	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+L	Inner_Full	20.78	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	18.74	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	18.74	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+L	Outer_Full	18.68	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+L	Inner_Full	18.82	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	20.00	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	20.00	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+L	Outer_Full	20.27	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+L	Inner_Full	21.66	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	19.83	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	19.84	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+L	Outer_Full	20.25	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+L	Inner_Full	21.28	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+L	Edge_1RB_Left	19.57	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+L	Edge_1RB_Right	19.47	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+L	Outer_Full	19.71	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+L	Inner_Full	19.75	PASS
DC_2A_n41_A	30	5+60	CP-256QAM	M+L	Edge_1RB_Left	16.92	PASS
DC_2A_n41_A	30	5+60	CP-256QAM	M+L	Edge_1RB_Right	16.70	PASS
DC_2A_n41_A	30	5+60	CP-256QAM	M+L	Outer_Full	16.71	PASS
DC_2A_n41	30	5+60	CP-256QAM	M+L	Inner_Full	16.77	PASS



A							
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.15	PASS
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.03	PASS
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	22.46	PASS
DC_2A_n41 A	30	5+60	DFT-PI2BPSK	M+M	Inner_Full	23.20	PASS
DC_2A_n41 A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.65	PASS
DC_2A_n41 A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.54	PASS
DC_2A_n41 A	30	5+60	DFT-QPSK	M+M	Outer_Full	22.05	PASS
DC_2A_n41 A	30	5+60	DFT-QPSK	M+M	Inner_Full	23.15	PASS
DC_2A_n41 A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.00	PASS
DC_2A_n41 A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.77	PASS
DC_2A_n41 A	30	5+60	DFT-16QAM	M+M	Outer_Full	21.14	PASS
DC_2A_n41 A	30	5+60	DFT-16QAM	M+M	Inner_Full	22.19	PASS
DC_2A_n41 A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	20.60	PASS
DC_2A_n41 A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Right	20.34	PASS
DC_2A_n41 A	30	5+60	DFT-64QAM	M+M	Outer_Full	20.62	PASS
DC_2A_n41 A	30	5+60	DFT-64QAM	M+M	Inner_Full	20.72	PASS
DC_2A_n41 A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	18.60	PASS
DC_2A_n41 A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Right	18.37	PASS
DC_2A_n41 A	30	5+60	DFT-256QAM	M+M	Outer_Full	18.60	PASS
DC_2A_n41 A	30	5+60	DFT-256QAM	M+M	Inner_Full	18.72	PASS
DC_2A_n41 A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	19.95	PASS
DC_2A_n41 A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	20.00	PASS
DC_2A_n41 A	30	5+60	CP-QPSK	M+M	Outer_Full	20.11	PASS
DC_2A_n41 A	30	5+60	CP-QPSK	M+M	Inner_Full	21.64	PASS
DC_2A_n41 A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	19.97	PASS
DC_2A_n41 A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	19.71	PASS
DC_2A_n41 A	30	5+60	CP-16QAM	M+M	Outer_Full	20.09	PASS
DC_2A_n41 A	30	5+60	CP-16QAM	M+M	Inner_Full	21.26	PASS
DC_2A_n41 A	30	5+60	CP-64QAM	M+M	Edge_1RB_Left	19.52	PASS
DC_2A_n41 A	30	5+60	CP-64QAM	M+M	Edge_1RB_Right	19.44	PASS
DC_2A_n41 A	30	5+60	CP-64QAM	M+M	Outer_Full	19.58	PASS
DC_2A_n41 A	30	5+60	CP-64QAM	M+M	Inner_Full	19.75	PASS
DC_2A_n41 A	30	5+60	CP-256QAM	M+M	Edge_1RB_Left	16.31	PASS



DC_2A_n41_A	30	5+60	CP-256QAM	M+M	Edge_1RB_Right	16.88	PASS
DC_2A_n41_A	30	5+60	CP-256QAM	M+M	Outer_Full	16.65	PASS
DC_2A_n41_A	30	5+60	CP-256QAM	M+M	Inner_Full	16.75	PASS
DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.19	PASS
DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.26	PASS
DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	22.52	PASS
DC_2A_n41_A	30	5+60	DFT-PI2BPSK	M+H	Inner_Full	23.04	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	21.71	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.82	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+H	Outer_Full	22.04	PASS
DC_2A_n41_A	30	5+60	DFT-QPSK	M+H	Inner_Full	23.07	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	20.92	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	20.98	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+H	Outer_Full	21.22	PASS
DC_2A_n41_A	30	5+60	DFT-16QAM	M+H	Inner_Full	22.07	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	20.39	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	20.52	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+H	Outer_Full	20.69	PASS
DC_2A_n41_A	30	5+60	DFT-64QAM	M+H	Inner_Full	20.65	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	18.91	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	18.88	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+H	Outer_Full	18.74	PASS
DC_2A_n41_A	30	5+60	DFT-256QAM	M+H	Inner_Full	18.73	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	20.10	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	19.95	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+H	Outer_Full	20.16	PASS
DC_2A_n41_A	30	5+60	CP-QPSK	M+H	Inner_Full	21.55	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	20.00	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	19.99	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+H	Outer_Full	20.17	PASS
DC_2A_n41_A	30	5+60	CP-16QAM	M+H	Inner_Full	21.19	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	19.57	PASS
DC_2A_n41_A	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	19.41	PASS
DC_2A_n41	30	5+60	CP-64QAM	M+H	Outer_Full	19.61	PASS



A							
DC_2A_n41 A	30	5+60	CP-64QAM	M+H	Inner_Full	19.63	PASS
DC_2A_n41 A	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	17.14	PASS
DC_2A_n41 A	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	16.63	PASS
DC_2A_n41 A	30	5+60	CP-256QAM	M+H	Outer_Full	16.58	PASS
DC_2A_n41 A	30	5+60	CP-256QAM	M+H	Inner_Full	16.66	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.35	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.34	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+L	Outer_Full	22.38	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+L	Inner_Full	22.38	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	21.85	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	21.90	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+L	Outer_Full	21.86	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+L	Inner_Full	21.87	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	21.05	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	20.99	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+L	Outer_Full	21.06	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+L	Inner_Full	21.09	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Left	20.56	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Right	20.57	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+L	Outer_Full	20.54	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+L	Inner_Full	20.52	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Left	18.51	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Right	18.52	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+L	Outer_Full	18.54	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+L	Inner_Full	18.58	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+L	Edge_1RB_Left	20.06	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+L	Edge_1RB_Right	20.04	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+L	Outer_Full	20.00	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+L	Inner_Full	20.06	PASS
DC_2A_n41 A	30	5+70	CP-16QAM	M+L	Edge_1RB_Left	19.98	PASS
DC_2A_n41 A	30	5+70	CP-16QAM	M+L	Edge_1RB_Right	20.01	PASS
DC_2A_n41 A	30	5+70	CP-16QAM	M+L	Outer_Full	20.04	PASS
DC_2A_n41 A	30	5+70	CP-16QAM	M+L	Inner_Full	20.06	PASS



DC_2A_n41_A	30	5+70	CP-64QAM	M+L	Edge_1RB_Left	19.61	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+L	Edge_1RB_Right	19.61	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+L	Outer_Full	19.53	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+L	Inner_Full	19.53	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+L	Edge_1RB_Left	16.56	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+L	Edge_1RB_Right	16.46	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+L	Outer_Full	16.48	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+L	Inner_Full	16.45	PASS
DC_2A_n41_A	30	5+70	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.32	PASS
DC_2A_n41_A	30	5+70	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.34	PASS
DC_2A_n41_A	30	5+70	DFT-PI2BPSK	M+M	Outer_Full	22.31	PASS
DC_2A_n41_A	30	5+70	DFT-PI2BPSK	M+M	Inner_Full	22.33	PASS
DC_2A_n41_A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	21.88	PASS
DC_2A_n41_A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	21.85	PASS
DC_2A_n41_A	30	5+70	DFT-QPSK	M+M	Outer_Full	21.89	PASS
DC_2A_n41_A	30	5+70	DFT-QPSK	M+M	Inner_Full	21.89	PASS
DC_2A_n41_A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	21.01	PASS
DC_2A_n41_A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	21.02	PASS
DC_2A_n41_A	30	5+70	DFT-16QAM	M+M	Outer_Full	20.95	PASS
DC_2A_n41_A	30	5+70	DFT-16QAM	M+M	Inner_Full	21.01	PASS
DC_2A_n41_A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Left	20.57	PASS
DC_2A_n41_A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Right	20.56	PASS
DC_2A_n41_A	30	5+70	DFT-64QAM	M+M	Outer_Full	20.52	PASS
DC_2A_n41_A	30	5+70	DFT-64QAM	M+M	Inner_Full	20.56	PASS
DC_2A_n41_A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Left	18.46	PASS
DC_2A_n41_A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Right	18.51	PASS
DC_2A_n41_A	30	5+70	DFT-256QAM	M+M	Outer_Full	18.50	PASS
DC_2A_n41_A	30	5+70	DFT-256QAM	M+M	Inner_Full	18.48	PASS
DC_2A_n41_A	30	5+70	CP-QPSK	M+M	Edge_1RB_Left	20.01	PASS
DC_2A_n41_A	30	5+70	CP-QPSK	M+M	Edge_1RB_Right	20.00	PASS
DC_2A_n41_A	30	5+70	CP-QPSK	M+M	Outer_Full	20.01	PASS
DC_2A_n41_A	30	5+70	CP-QPSK	M+M	Inner_Full	20.00	PASS
DC_2A_n41_A	30	5+70	CP-16QAM	M+M	Edge_1RB_Left	20.01	PASS
DC_2A_n41	30	5+70	CP-16QAM	M+M	Edge_1RB_Right	20.00	PASS



A							
DC_2A_n41 A	30	5+70	CP-16QAM	M+M	Outer_Full	20.03	PASS
DC_2A_n41 A	30	5+70	CP-16QAM	M+M	Inner_Full	19.98	PASS
DC_2A_n41 A	30	5+70	CP-64QAM	M+M	Edge_1RB_Left	19.49	PASS
DC_2A_n41 A	30	5+70	CP-64QAM	M+M	Edge_1RB_Right	19.56	PASS
DC_2A_n41 A	30	5+70	CP-64QAM	M+M	Outer_Full	19.48	PASS
DC_2A_n41 A	30	5+70	CP-64QAM	M+M	Inner_Full	19.54	PASS
DC_2A_n41 A	30	5+70	CP-256QAM	M+M	Edge_1RB_Left	16.52	PASS
DC_2A_n41 A	30	5+70	CP-256QAM	M+M	Edge_1RB_Right	16.45	PASS
DC_2A_n41 A	30	5+70	CP-256QAM	M+M	Outer_Full	16.43	PASS
DC_2A_n41 A	30	5+70	CP-256QAM	M+M	Inner_Full	16.48	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.37	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.34	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+H	Outer_Full	22.40	PASS
DC_2A_n41 A	30	5+70	DFT-PI2BPSK	M+H	Inner_Full	22.35	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	21.89	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	21.88	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+H	Outer_Full	21.92	PASS
DC_2A_n41 A	30	5+70	DFT-QPSK	M+H	Inner_Full	21.89	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.05	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	21.03	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+H	Outer_Full	21.02	PASS
DC_2A_n41 A	30	5+70	DFT-16QAM	M+H	Inner_Full	21.01	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Left	20.62	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Right	20.54	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+H	Outer_Full	20.51	PASS
DC_2A_n41 A	30	5+70	DFT-64QAM	M+H	Inner_Full	20.47	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Left	18.52	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Right	18.61	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+H	Outer_Full	18.53	PASS
DC_2A_n41 A	30	5+70	DFT-256QAM	M+H	Inner_Full	18.48	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+H	Edge_1RB_Left	20.03	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+H	Edge_1RB_Right	20.00	PASS
DC_2A_n41 A	30	5+70	CP-QPSK	M+H	Outer_Full	20.01	PASS



DC_2A_n41_A	30	5+70	CP-QPSK	M+H	Inner_Full	20.00	PASS
DC_2A_n41_A	30	5+70	CP-16QAM	M+H	Edge_1RB_Left	20.00	PASS
DC_2A_n41_A	30	5+70	CP-16QAM	M+H	Edge_1RB_Right	19.98	PASS
DC_2A_n41_A	30	5+70	CP-16QAM	M+H	Outer_Full	20.01	PASS
DC_2A_n41_A	30	5+70	CP-16QAM	M+H	Inner_Full	20.00	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+H	Edge_1RB_Left	19.50	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+H	Edge_1RB_Right	19.52	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+H	Outer_Full	19.57	PASS
DC_2A_n41_A	30	5+70	CP-64QAM	M+H	Inner_Full	19.60	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+H	Edge_1RB_Left	16.52	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+H	Edge_1RB_Right	16.45	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+H	Outer_Full	16.45	PASS
DC_2A_n41_A	30	5+70	CP-256QAM	M+H	Inner_Full	16.54	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.02	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.86	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	22.51	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+L	Inner_Full	23.09	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	21.61	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.25	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+L	Outer_Full	22.09	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+L	Inner_Full	23.07	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.81	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.61	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+L	Outer_Full	21.23	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+L	Inner_Full	22.10	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	20.70	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Right	20.16	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+L	Outer_Full	20.72	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+L	Inner_Full	20.74	PASS
DC_2A_n41_A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	18.74	PASS
DC_2A_n41_A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Right	18.39	PASS
DC_2A_n41_A	30	5+80	DFT-256QAM	M+L	Outer_Full	18.67	PASS
DC_2A_n41_A	30	5+80	DFT-256QAM	M+L	Inner_Full	18.79	PASS
DC_2A_n41	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	19.90	PASS



A							
DC_2A_n41 A	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	19.71	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+L	Outer_Full	20.22	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+L	Inner_Full	21.57	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	19.80	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	19.70	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+L	Outer_Full	20.11	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+L	Inner_Full	21.24	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+L	Edge_1RB_Left	19.41	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+L	Edge_1RB_Right	19.17	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+L	Outer_Full	19.65	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+L	Inner_Full	19.74	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+L	Edge_1RB_Left	16.70	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+L	Edge_1RB_Right	16.45	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+L	Outer_Full	16.60	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+L	Inner_Full	16.71	PASS
DC_2A_n41 A	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.03	PASS
DC_2A_n41 A	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.90	PASS
DC_2A_n41 A	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	22.38	PASS
DC_2A_n41 A	30	5+80	DFT-PI2BPSK	M+M	Inner_Full	23.14	PASS
DC_2A_n41 A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.56	PASS
DC_2A_n41 A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.44	PASS
DC_2A_n41 A	30	5+80	DFT-QPSK	M+M	Outer_Full	21.94	PASS
DC_2A_n41 A	30	5+80	DFT-QPSK	M+M	Inner_Full	23.18	PASS
DC_2A_n41 A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.81	PASS
DC_2A_n41 A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.80	PASS
DC_2A_n41 A	30	5+80	DFT-16QAM	M+M	Outer_Full	21.15	PASS
DC_2A_n41 A	30	5+80	DFT-16QAM	M+M	Inner_Full	22.20	PASS
DC_2A_n41 A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	20.27	PASS
DC_2A_n41 A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	20.45	PASS
DC_2A_n41 A	30	5+80	DFT-64QAM	M+M	Outer_Full	20.57	PASS
DC_2A_n41 A	30	5+80	DFT-64QAM	M+M	Inner_Full	20.77	PASS
DC_2A_n41 A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	18.48	PASS
DC_2A_n41 A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Right	18.46	PASS



DC_2A_n41_A	30	5+80	DFT-256QAM	M+M	Outer_Full	18.60	PASS
DC_2A_n41_A	30	5+80	DFT-256QAM	M+M	Inner_Full	18.79	PASS
DC_2A_n41_A	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	19.86	PASS
DC_2A_n41_A	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	19.85	PASS
DC_2A_n41_A	30	5+80	CP-QPSK	M+M	Outer_Full	20.15	PASS
DC_2A_n41_A	30	5+80	CP-QPSK	M+M	Inner_Full	21.55	PASS
DC_2A_n41_A	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	19.84	PASS
DC_2A_n41_A	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	19.87	PASS
DC_2A_n41_A	30	5+80	CP-16QAM	M+M	Outer_Full	20.05	PASS
DC_2A_n41_A	30	5+80	CP-16QAM	M+M	Inner_Full	21.26	PASS
DC_2A_n41_A	30	5+80	CP-64QAM	M+M	Edge_1RB_Left	19.29	PASS
DC_2A_n41_A	30	5+80	CP-64QAM	M+M	Edge_1RB_Right	19.29	PASS
DC_2A_n41_A	30	5+80	CP-64QAM	M+M	Outer_Full	19.58	PASS
DC_2A_n41_A	30	5+80	CP-64QAM	M+M	Inner_Full	19.72	PASS
DC_2A_n41_A	30	5+80	CP-256QAM	M+M	Edge_1RB_Left	16.66	PASS
DC_2A_n41_A	30	5+80	CP-256QAM	M+M	Edge_1RB_Right	16.67	PASS
DC_2A_n41_A	30	5+80	CP-256QAM	M+M	Outer_Full	16.57	PASS
DC_2A_n41_A	30	5+80	CP-256QAM	M+M	Inner_Full	16.73	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.89	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.07	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	22.50	PASS
DC_2A_n41_A	30	5+80	DFT-PI2BPSK	M+H	Inner_Full	23.05	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	21.34	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.65	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+H	Outer_Full	21.93	PASS
DC_2A_n41_A	30	5+80	DFT-QPSK	M+H	Inner_Full	23.11	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.62	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.84	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+H	Outer_Full	21.16	PASS
DC_2A_n41_A	30	5+80	DFT-16QAM	M+H	Inner_Full	22.16	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	20.78	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Right	20.77	PASS
DC_2A_n41_A	30	5+80	DFT-64QAM	M+H	Outer_Full	20.70	PASS
DC_2A_n41	30	5+80	DFT-64QAM	M+H	Inner_Full	20.74	PASS



A							
DC_2A_n41 A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	18.28	PASS
DC_2A_n41 A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	18.48	PASS
DC_2A_n41 A	30	5+80	DFT-256QAM	M+H	Outer_Full	18.64	PASS
DC_2A_n41 A	30	5+80	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	19.76	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	20.01	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+H	Outer_Full	20.17	PASS
DC_2A_n41 A	30	5+80	CP-QPSK	M+H	Inner_Full	21.57	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	19.82	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	20.01	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+H	Outer_Full	20.16	PASS
DC_2A_n41 A	30	5+80	CP-16QAM	M+H	Inner_Full	21.22	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+H	Edge_1RB_Left	19.16	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+H	Edge_1RB_Right	19.35	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+H	Outer_Full	19.64	PASS
DC_2A_n41 A	30	5+80	CP-64QAM	M+H	Inner_Full	19.72	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+H	Edge_1RB_Left	16.44	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+H	Edge_1RB_Right	16.67	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+H	Outer_Full	16.62	PASS
DC_2A_n41 A	30	5+80	CP-256QAM	M+H	Inner_Full	16.68	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.91	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.76	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+L	Outer_Full	22.53	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+L	Inner_Full	23.13	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	21.38	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.18	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+L	Outer_Full	22.06	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+L	Inner_Full	23.13	PASS
DC_2A_n41 A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	20.60	PASS
DC_2A_n41 A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.51	PASS
DC_2A_n41 A	30	5+90	DFT-16QAM	M+L	Outer_Full	21.25	PASS
DC_2A_n41 A	30	5+90	DFT-16QAM	M+L	Inner_Full	22.16	PASS
DC_2A_n41 A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Left	20.38	PASS



DC_2A_n41_A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Right	20.22	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+L	Outer_Full	20.69	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+L	Inner_Full	20.72	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Left	18.48	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Right	18.44	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+L	Outer_Full	18.71	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+L	Inner_Full	18.76	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	19.78	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	19.71	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+L	Outer_Full	20.18	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+L	Inner_Full	21.58	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	19.64	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	19.66	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+L	Outer_Full	20.19	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+L	Inner_Full	21.25	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+L	Edge_1RB_Left	19.20	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+L	Edge_1RB_Right	19.17	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+L	Outer_Full	19.67	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+L	Inner_Full	19.73	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+L	Edge_1RB_Left	16.44	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+L	Edge_1RB_Right	16.21	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+L	Outer_Full	16.63	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+L	Inner_Full	16.75	PASS
DC_2A_n41_A	30	5+90	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.82	PASS
DC_2A_n41_A	30	5+90	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.83	PASS
DC_2A_n41_A	30	5+90	DFT-PI2BPSK	M+M	Outer_Full	22.44	PASS
DC_2A_n41_A	30	5+90	DFT-PI2BPSK	M+M	Inner_Full	23.13	PASS
DC_2A_n41_A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.28	PASS
DC_2A_n41_A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	21.34	PASS
DC_2A_n41_A	30	5+90	DFT-QPSK	M+M	Outer_Full	21.98	PASS
DC_2A_n41_A	30	5+90	DFT-QPSK	M+M	Inner_Full	23.21	PASS
DC_2A_n41_A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.61	PASS
DC_2A_n41_A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	20.62	PASS
DC_2A_n41	30	5+90	DFT-16QAM	M+M	Outer_Full	21.16	PASS



A							
DC_2A_n41 A	30	5+90	DFT-16QAM	M+M	Inner_Full	22.11	PASS
DC_2A_n41 A	30	5+90	DFT-64QAM	M+M	Edge_1RB_Left	20.31	PASS
DC_2A_n41 A	30	5+90	DFT-64QAM	M+M	Edge_1RB_Right	20.29	PASS
DC_2A_n41 A	30	5+90	DFT-64QAM	M+M	Outer_Full	20.69	PASS
DC_2A_n41 A	30	5+90	DFT-64QAM	M+M	Inner_Full	20.76	PASS
DC_2A_n41 A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Left	18.08	PASS
DC_2A_n41 A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Right	18.23	PASS
DC_2A_n41 A	30	5+90	DFT-256QAM	M+M	Outer_Full	18.66	PASS
DC_2A_n41 A	30	5+90	DFT-256QAM	M+M	Inner_Full	18.74	PASS
DC_2A_n41 A	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	19.69	PASS
DC_2A_n41 A	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	19.78	PASS
DC_2A_n41 A	30	5+90	CP-QPSK	M+M	Outer_Full	20.15	PASS
DC_2A_n41 A	30	5+90	CP-QPSK	M+M	Inner_Full	21.61	PASS
DC_2A_n41 A	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	19.58	PASS
DC_2A_n41 A	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	19.70	PASS
DC_2A_n41 A	30	5+90	CP-16QAM	M+M	Outer_Full	20.14	PASS
DC_2A_n41 A	30	5+90	CP-16QAM	M+M	Inner_Full	21.27	PASS
DC_2A_n41 A	30	5+90	CP-64QAM	M+M	Edge_1RB_Left	19.19	PASS
DC_2A_n41 A	30	5+90	CP-64QAM	M+M	Edge_1RB_Right	19.30	PASS
DC_2A_n41 A	30	5+90	CP-64QAM	M+M	Outer_Full	19.57	PASS
DC_2A_n41 A	30	5+90	CP-64QAM	M+M	Inner_Full	19.74	PASS
DC_2A_n41 A	30	5+90	CP-256QAM	M+M	Edge_1RB_Left	16.42	PASS
DC_2A_n41 A	30	5+90	CP-256QAM	M+M	Edge_1RB_Right	16.63	PASS
DC_2A_n41 A	30	5+90	CP-256QAM	M+M	Outer_Full	16.55	PASS
DC_2A_n41 A	30	5+90	CP-256QAM	M+M	Inner_Full	16.64	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.88	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.93	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+H	Outer_Full	22.50	PASS
DC_2A_n41 A	30	5+90	DFT-PI2BPSK	M+H	Inner_Full	23.15	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	21.36	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	21.39	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+H	Outer_Full	22.05	PASS
DC_2A_n41 A	30	5+90	DFT-QPSK	M+H	Inner_Full	23.11	PASS



DC_2A_n41_A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	20.71	PASS
DC_2A_n41_A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	20.73	PASS
DC_2A_n41_A	30	5+90	DFT-16QAM	M+H	Outer_Full	21.23	PASS
DC_2A_n41_A	30	5+90	DFT-16QAM	M+H	Inner_Full	22.08	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Left	20.18	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Right	20.34	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+H	Outer_Full	20.69	PASS
DC_2A_n41_A	30	5+90	DFT-64QAM	M+H	Inner_Full	20.74	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Left	18.13	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Right	18.25	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+H	Outer_Full	18.67	PASS
DC_2A_n41_A	30	5+90	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	19.84	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	19.83	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+H	Outer_Full	20.12	PASS
DC_2A_n41_A	30	5+90	CP-QPSK	M+H	Inner_Full	21.52	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	19.62	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	19.72	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+H	Outer_Full	20.15	PASS
DC_2A_n41_A	30	5+90	CP-16QAM	M+H	Inner_Full	21.20	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+H	Edge_1RB_Left	19.29	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+H	Edge_1RB_Right	19.27	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+H	Outer_Full	19.66	PASS
DC_2A_n41_A	30	5+90	CP-64QAM	M+H	Inner_Full	19.70	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+H	Edge_1RB_Left	16.35	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+H	Edge_1RB_Right	16.58	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+H	Outer_Full	16.60	PASS
DC_2A_n41_A	30	5+90	CP-256QAM	M+H	Inner_Full	16.70	PASS
DC_2A_n41_A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.66	PASS
DC_2A_n41_A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.64	PASS
DC_2A_n41_A	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	22.47	PASS
DC_2A_n41_A	30	5+100	DFT-PI2BPSK	M+L	Inner_Full	23.11	PASS
DC_2A_n41_A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.08	PASS
DC_2A_n41	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.15	PASS



A							
DC_2A_n41 A	30	5+100	DFT-QPSK	M+L	Outer_Full	21.95	PASS
DC_2A_n41 A	30	5+100	DFT-QPSK	M+L	Inner_Full	23.09	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	20.65	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.40	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+L	Outer_Full	21.18	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+L	Inner_Full	22.07	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	19.98	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	20.23	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+L	Outer_Full	20.70	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+L	Inner_Full	20.77	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	18.34	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	18.03	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+L	Outer_Full	18.67	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+L	Inner_Full	18.73	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	19.59	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	19.59	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+L	Outer_Full	20.21	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+L	Inner_Full	21.59	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	19.43	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	19.65	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+L	Outer_Full	20.18	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+L	Inner_Full	21.27	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	18.97	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+L	Edge_1RB_Right	19.05	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+L	Outer_Full	19.67	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+L	Inner_Full	19.76	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	16.26	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+L	Edge_1RB_Right	16.28	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+L	Outer_Full	16.65	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+L	Inner_Full	16.69	PASS
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.45	PASS
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.58	PASS
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	22.43	PASS



DC_2A_n41_A	30	5+100	DFT-PI2BPSK	M+M	Inner_Full	23.10	PASS
DC_2A_n41_A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	20.91	PASS
DC_2A_n41_A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.14	PASS
DC_2A_n41_A	30	5+100	DFT-QPSK	M+M	Outer_Full	21.90	PASS
DC_2A_n41_A	30	5+100	DFT-QPSK	M+M	Inner_Full	23.14	PASS
DC_2A_n41_A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	20.34	PASS
DC_2A_n41_A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	20.62	PASS
DC_2A_n41_A	30	5+100	DFT-16QAM	M+M	Outer_Full	21.09	PASS
DC_2A_n41_A	30	5+100	DFT-16QAM	M+M	Inner_Full	22.13	PASS
DC_2A_n41_A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	19.96	PASS
DC_2A_n41_A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	20.00	PASS
DC_2A_n41_A	30	5+100	DFT-64QAM	M+M	Outer_Full	20.59	PASS
DC_2A_n41_A	30	5+100	DFT-64QAM	M+M	Inner_Full	20.76	PASS
DC_2A_n41_A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	17.90	PASS
DC_2A_n41_A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	18.40	PASS
DC_2A_n41_A	30	5+100	DFT-256QAM	M+M	Outer_Full	18.58	PASS
DC_2A_n41_A	30	5+100	DFT-256QAM	M+M	Inner_Full	18.77	PASS
DC_2A_n41_A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	19.33	PASS
DC_2A_n41_A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	19.69	PASS
DC_2A_n41_A	30	5+100	CP-QPSK	M+M	Outer_Full	20.06	PASS
DC_2A_n41_A	30	5+100	CP-QPSK	M+M	Inner_Full	21.58	PASS
DC_2A_n41_A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	19.32	PASS
DC_2A_n41_A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	19.59	PASS
DC_2A_n41_A	30	5+100	CP-16QAM	M+M	Outer_Full	20.08	PASS
DC_2A_n41_A	30	5+100	CP-16QAM	M+M	Inner_Full	21.26	PASS
DC_2A_n41_A	30	5+100	CP-64QAM	M+M	Edge_1RB_Left	18.90	PASS
DC_2A_n41_A	30	5+100	CP-64QAM	M+M	Edge_1RB_Right	19.13	PASS
DC_2A_n41_A	30	5+100	CP-64QAM	M+M	Outer_Full	19.58	PASS
DC_2A_n41_A	30	5+100	CP-64QAM	M+M	Inner_Full	19.74	PASS
DC_2A_n41_A	30	5+100	CP-256QAM	M+M	Edge_1RB_Left	16.10	PASS
DC_2A_n41_A	30	5+100	CP-256QAM	M+M	Edge_1RB_Right	16.44	PASS
DC_2A_n41_A	30	5+100	CP-256QAM	M+M	Outer_Full	16.56	PASS
DC_2A_n41_A	30	5+100	CP-256QAM	M+M	Inner_Full	16.70	PASS
DC_2A_n41	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.54	PASS



A							
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.61	PASS
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	22.49	PASS
DC_2A_n41 A	30	5+100	DFT-PI2BPSK	M+H	Inner_Full	23.06	PASS
DC_2A_n41 A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.01	PASS
DC_2A_n41 A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.12	PASS
DC_2A_n41 A	30	5+100	DFT-QPSK	M+H	Outer_Full	21.99	PASS
DC_2A_n41 A	30	5+100	DFT-QPSK	M+H	Inner_Full	23.10	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	20.30	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	20.44	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+H	Outer_Full	21.19	PASS
DC_2A_n41 A	30	5+100	DFT-16QAM	M+H	Inner_Full	22.10	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	19.97	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	19.95	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+H	Outer_Full	20.70	PASS
DC_2A_n41 A	30	5+100	DFT-64QAM	M+H	Inner_Full	20.67	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	17.98	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	18.59	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+H	Outer_Full	18.65	PASS
DC_2A_n41 A	30	5+100	DFT-256QAM	M+H	Inner_Full	18.74	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	19.53	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	19.58	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+H	Outer_Full	20.12	PASS
DC_2A_n41 A	30	5+100	CP-QPSK	M+H	Inner_Full	21.55	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	19.52	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	19.41	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+H	Outer_Full	20.14	PASS
DC_2A_n41 A	30	5+100	CP-16QAM	M+H	Inner_Full	21.23	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+H	Edge_1RB_Left	18.96	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	18.94	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+H	Outer_Full	19.61	PASS
DC_2A_n41 A	30	5+100	CP-64QAM	M+H	Inner_Full	19.67	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+H	Edge_1RB_Left	16.19	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	16.21	PASS



DC_2A_n41 A	30	5+100	CP-256QAM	M+H	Outer_Full	16.66	PASS
DC_2A_n41 A	30	5+100	CP-256QAM	M+H	Inner_Full	16.67	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.96	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	23.03	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	22.92	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Inner_Full	23.57	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.52	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.63	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.53	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Inner_Full	23.65	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	21.26	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	21.48	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	21.54	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Inner_Full	22.54	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	21.01	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	21.11	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	20.96	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Inner_Full	21.04	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	19.09	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	19.05	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.93	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Inner_Full	19.08	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	20.62	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	20.86	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	20.45	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Inner_Full	22.15	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	20.41	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	20.60	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	20.46	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Inner_Full	21.64	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	19.91	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	20.47	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	19.88	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Inner_Full	20.17	PASS



A							
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	17.10	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	17.24	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	17.01	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Inner_Full	17.06	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.01	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	23.05	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	23.08	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	23.57	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	22.51	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	22.56	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.61	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Inner_Full	23.61	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	21.26	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	21.27	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	21.52	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Inner_Full	22.61	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	21.13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	21.09	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	21.04	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Inner_Full	21.06	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	19.09	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	19.25	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	19.06	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Inner_Full	19.14	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	20.79	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	20.79	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	20.51	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Inner_Full	22.07	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	20.61	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	20.50	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	20.49	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Inner_Full	21.56	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	19.96	PASS



DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	20.08	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	19.87	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Inner_Full	19.95	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	17.36	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	17.27	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	17.00	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Inner_Full	17.05	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.92	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.96	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	23.01	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	23.41	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	22.54	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	22.60	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	22.57	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Inner_Full	23.42	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	21.24	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	21.34	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	21.52	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.61	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	21.02	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	21.12	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	21.05	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Inner_Full	21.02	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	19.04	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	19.16	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.99	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Inner_Full	19.11	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	20.74	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	20.82	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Outer_Full	20.52	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Inner_Full	22.05	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	20.43	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	20.45	PASS
DC_2A_n71	15	5+5	CP-16QAM	M+H	Outer_Full	20.56	PASS



A							
DC_2A_n71 A	15	5+5	CP-16QAM	M+H	Inner_Full	21.59	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	19.89	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	20.13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	19.91	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Inner_Full	20.15	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	17.12	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	17.29	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	16.98	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Inner_Full	17.17	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.79	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.82	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	22.92	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	23.48	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.26	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.45	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+L	Outer_Full	3.47	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+L	Inner_Full	23.52	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	21.00	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	21.10	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.38	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.41	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	21.10	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.90	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.85	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.96	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.86	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.97	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.77	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.90	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	20.57	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	20.69	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+L	Outer_Full	20.32	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+L	Inner_Full	21.95	PASS



DC_2A_n71_A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	20.23	PASS
DC_2A_n71_A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	20.24	PASS
DC_2A_n71_A	15	5+10	CP-16QAM	M+L	Outer_Full	20.30	PASS
DC_2A_n71_A	15	5+10	CP-16QAM	M+L	Inner_Full	21.48	PASS
DC_2A_n71_A	15	5+10	CP-64QAM	M+L	Edge_1RB_Left	19.72	PASS
DC_2A_n71_A	15	5+10	CP-64QAM	M+L	Edge_1RB_Right	19.81	PASS
DC_2A_n71_A	15	5+10	CP-64QAM	M+L	Outer_Full	19.74	PASS
DC_2A_n71_A	15	5+10	CP-64QAM	M+L	Inner_Full	20.06	PASS
DC_2A_n71_A	15	5+10	CP-256QAM	M+L	Edge_1RB_Left	16.93	PASS
DC_2A_n71_A	15	5+10	CP-256QAM	M+L	Edge_1RB_Right	17.02	PASS
DC_2A_n71_A	15	5+10	CP-256QAM	M+L	Outer_Full	16.87	PASS
DC_2A_n71_A	15	5+10	CP-256QAM	M+L	Inner_Full	17.01	PASS
DC_2A_n71_A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.84	PASS
DC_2A_n71_A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.81	PASS
DC_2A_n71_A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	22.76	PASS
DC_2A_n71_A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	23.42	PASS
DC_2A_n71_A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	22.44	PASS
DC_2A_n71_A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.33	PASS
DC_2A_n71_A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.29	PASS
DC_2A_n71_A	15	5+10	DFT-QPSK	M+M	Inner_Full	23.44	PASS
DC_2A_n71_A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.36	PASS
DC_2A_n71_A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21.10	PASS
DC_2A_n71_A	15	5+10	DFT-16QAM	M+M	Outer_Full	21.33	PASS
DC_2A_n71_A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.42	PASS
DC_2A_n71_A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	21.12	PASS
DC_2A_n71_A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	21.09	PASS
DC_2A_n71_A	15	5+10	DFT-64QAM	M+M	Outer_Full	20.72	PASS
DC_2A_n71_A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.89	PASS
DC_2A_n71_A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	19.01	PASS
DC_2A_n71_A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.90	PASS
DC_2A_n71_A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.81	PASS
DC_2A_n71_A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.84	PASS
DC_2A_n71_A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	20.65	PASS
DC_2A_n71	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.71	PASS



A							
DC_2A_n71 A	15	5+10	CP-QPSK	M+M	Outer_Full	20.19	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+M	Inner_Full	21.85	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	20.24	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	20.26	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+M	Outer_Full	20.16	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+M	Inner_Full	21.39	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+M	Edge_1RB_Left	19.81	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+M	Edge_1RB_Right	19.79	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+M	Outer_Full	19.72	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+M	Inner_Full	19.87	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+M	Edge_1RB_Left	17.00	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+M	Edge_1RB_Right	16.96	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+M	Outer_Full	16.70	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+M	Inner_Full	16.93	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.82	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.81	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	22.81	PASS
DC_2A_n71 A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	23.42	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.34	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.44	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+H	Outer_Full	22.33	PASS
DC_2A_n71 A	15	5+10	DFT-QPSK	M+H	Inner_Full	23.42	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	21.18	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	21.19	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+H	Outer_Full	21.32	PASS
DC_2A_n71 A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.35	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	21.00	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.85	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+H	Outer_Full	20.73	PASS
DC_2A_n71 A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.86	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	0.57	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	19.07	PASS
DC_2A_n71 A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.84	PASS



DC_2A_n71 A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.84	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	20.61	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	20.59	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+H	Outer_Full	20.24	PASS
DC_2A_n71 A	15	5+10	CP-QPSK	M+H	Inner_Full	21.90	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	20.16	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	20.26	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+H	Outer_Full	20.29	PASS
DC_2A_n71 A	15	5+10	CP-16QAM	M+H	Inner_Full	21.46	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+H	Edge_1RB_Left	19.64	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+H	Edge_1RB_Right	20.08	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+H	Outer_Full	19.77	PASS
DC_2A_n71 A	15	5+10	CP-64QAM	M+H	Inner_Full	19.89	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+H	Edge_1RB_Left	16.93	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+H	Edge_1RB_Right	17.03	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+H	Outer_Full	16.79	PASS
DC_2A_n71 A	15	5+10	CP-256QAM	M+H	Inner_Full	16.86	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.84	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.96	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	23.25	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	23.66	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.47	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.54	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.72	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+L	Inner_Full	23.66	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	21.22	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.29	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.77	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.66	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.99	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	21.31	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+L	Outer_Full	21.25	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+L	Inner_Full	21.12	PASS
DC_2A_n71	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	19.04	PASS



A							
DC_2A_n71 A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	19.12	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+L	Outer_Full	19.25	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+L	Inner_Full	19.05	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	20.62	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.68	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+L	Outer_Full	20.70	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+L	Inner_Full	22.14	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	20.25	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	20.40	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+L	Outer_Full	20.69	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+L	Inner_Full	21.63	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+L	Edge_1RB_Left	19.64	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+L	Edge_1RB_Right	19.86	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+L	Outer_Full	20.23	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+L	Inner_Full	20.12	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+L	Edge_1RB_Left	17.05	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+L	Edge_1RB_Right	16.98	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+L	Outer_Full	17.27	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+L	Inner_Full	17.16	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.93	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.92	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	22.99	PASS
DC_2A_n71 A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	23.61	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	22.54	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.50	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.53	PASS
DC_2A_n71 A	15	5+15	DFT-QPSK	M+M	Inner_Full	23.62	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.20	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.10	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+M	Outer_Full	21.52	PASS
DC_2A_n71 A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.60	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	20.97	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	20.98	PASS



DC_2A_n71_A	15	5+15	DFT-64QAM	M+M	Outer_Full	21.04	PASS
DC_2A_n71_A	15	5+15	DFT-64QAM	M+M	Inner_Full	21.12	PASS
DC_2A_n71_A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	19.01	PASS
DC_2A_n71_A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	19.05	PASS
DC_2A_n71_A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.97	PASS
DC_2A_n71_A	15	5+15	DFT-256QAM	M+M	Inner_Full	19.03	PASS
DC_2A_n71_A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	20.69	PASS
DC_2A_n71_A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	20.76	PASS
DC_2A_n71_A	15	5+15	CP-QPSK	M+M	Outer_Full	20.46	PASS
DC_2A_n71_A	15	5+15	CP-QPSK	M+M	Inner_Full	22.10	PASS
DC_2A_n71_A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	20.34	PASS
DC_2A_n71_A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	20.42	PASS
DC_2A_n71_A	15	5+15	CP-16QAM	M+M	Outer_Full	20.44	PASS
DC_2A_n71_A	15	5+15	CP-16QAM	M+M	Inner_Full	21.60	PASS
DC_2A_n71_A	15	5+15	CP-64QAM	M+M	Edge_1RB_Left	19.89	PASS
DC_2A_n71_A	15	5+15	CP-64QAM	M+M	Edge_1RB_Right	19.91	PASS
DC_2A_n71_A	15	5+15	CP-64QAM	M+M	Outer_Full	19.93	PASS
DC_2A_n71_A	15	5+15	CP-64QAM	M+M	Inner_Full	20.05	PASS
DC_2A_n71_A	15	5+15	CP-256QAM	M+M	Edge_1RB_Left	17.10	PASS
DC_2A_n71_A	15	5+15	CP-256QAM	M+M	Edge_1RB_Right	17.11	PASS
DC_2A_n71_A	15	5+15	CP-256QAM	M+M	Outer_Full	16.94	PASS
DC_2A_n71_A	15	5+15	CP-256QAM	M+M	Inner_Full	17.17	PASS
DC_2A_n71_A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.91	PASS
DC_2A_n71_A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.93	PASS
DC_2A_n71_A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	23.10	PASS
DC_2A_n71_A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	23.60	PASS
DC_2A_n71_A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.45	PASS
DC_2A_n71_A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	22.53	PASS
DC_2A_n71_A	15	5+15	DFT-QPSK	M+H	Outer_Full	22.64	PASS
DC_2A_n71_A	15	5+15	DFT-QPSK	M+H	Inner_Full	23.57	PASS
DC_2A_n71_A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	21.15	PASS
DC_2A_n71_A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	21.22	PASS
DC_2A_n71_A	15	5+15	DFT-16QAM	M+H	Outer_Full	21.64	PASS
DC_2A_n71	15	5+15	DFT-16QAM	M+H	Inner_Full	22.63	PASS



A							
DC_2A_n71 A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	21.18	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	20.92	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+H	Outer_Full	21.11	PASS
DC_2A_n71 A	15	5+15	DFT-64QAM	M+H	Inner_Full	21.13	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	19.01	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	19.08	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+H	Outer_Full	19.06	PASS
DC_2A_n71 A	15	5+15	DFT-256QAM	M+H	Inner_Full	18.95	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	20.70	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	20.74	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+H	Outer_Full	20.63	PASS
DC_2A_n71 A	15	5+15	CP-QPSK	M+H	Inner_Full	22.09	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	20.70	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	20.35	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+H	Outer_Full	20.69	PASS
DC_2A_n71 A	15	5+15	CP-16QAM	M+H	Inner_Full	21.59	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+H	Edge_1RB_Left	19.85	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+H	Edge_1RB_Right	19.89	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+H	Outer_Full	20.06	PASS
DC_2A_n71 A	15	5+15	CP-64QAM	M+H	Inner_Full	20.13	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+H	Edge_1RB_Left	16.99	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+H	Edge_1RB_Right	17.08	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+H	Outer_Full	17.12	PASS
DC_2A_n71 A	15	5+15	CP-256QAM	M+H	Inner_Full	17.14	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.87	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.85	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	23.23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	23.64	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.42	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.44	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.69	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Inner_Full	23.64	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.14	PASS



DC_2A_n71_A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.14	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+L	Outer_Full	21.71	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+L	Inner_Full	22.63	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	21.01	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	21.06	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+L	Outer_Full	21.17	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+L	Inner_Full	21.10	PASS
DC_2A_n71_A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	19.04	PASS
DC_2A_n71_A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	19.12	PASS
DC_2A_n71_A	15	5+20	DFT-256QAM	M+L	Outer_Full	19.16	PASS
DC_2A_n71_A	15	5+20	DFT-256QAM	M+L	Inner_Full	19.11	PASS
DC_2A_n71_A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.57	PASS
DC_2A_n71_A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.74	PASS
DC_2A_n71_A	15	5+20	CP-QPSK	M+L	Outer_Full	20.64	PASS
DC_2A_n71_A	15	5+20	CP-QPSK	M+L	Inner_Full	22.13	PASS
DC_2A_n71_A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	20.54	PASS
DC_2A_n71_A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.62	PASS
DC_2A_n71_A	15	5+20	CP-16QAM	M+L	Outer_Full	20.62	PASS
DC_2A_n71_A	15	5+20	CP-16QAM	M+L	Inner_Full	21.64	PASS
DC_2A_n71_A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.62	PASS
DC_2A_n71_A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.77	PASS
DC_2A_n71_A	15	5+20	CP-64QAM	M+L	Outer_Full	20.11	PASS
DC_2A_n71_A	15	5+20	CP-64QAM	M+L	Inner_Full	20.09	PASS
DC_2A_n71_A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	17.00	PASS
DC_2A_n71_A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	17.07	PASS
DC_2A_n71_A	15	5+20	CP-256QAM	M+L	Outer_Full	17.16	PASS
DC_2A_n71_A	15	5+20	CP-256QAM	M+L	Inner_Full	17.18	PASS
DC_2A_n71_A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.85	PASS
DC_2A_n71_A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.86	PASS
DC_2A_n71_A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	22.79	PASS
DC_2A_n71_A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	23.65	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.42	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.44	PASS
DC_2A_n71	15	5+20	DFT-QPSK	M+M	Outer_Full	22.39	PASS



A							
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Inner_Full	23.65	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.19	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.15	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+M	Outer_Full	21.29	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+M	Inner_Full	22.61	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.97	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	21.01	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.86	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Inner_Full	21.02	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	19.09	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	19.10	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.86	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Inner_Full	19.10	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.70	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.77	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	20.23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Inner_Full	22.08	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	20.20	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	20.38	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	20.21	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Inner_Full	21.55	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.89	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.84	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	19.81	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Inner_Full	20.05	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	17.03	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	17.06	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	16.80	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Inner_Full	17.01	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.93	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.84	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	23.26	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	23.60	PASS



DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.45	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.48	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	22.77	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Inner_Full	23.68	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.43	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.55	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	21.77	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Inner_Full	13.21	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	21.20	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.91	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	21.27	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Inner_Full	21.13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	19.06	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	19.12	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	19.28	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Inner_Full	19.11	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.61	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.68	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	20.74	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Inner_Full	22.14	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.29	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.35	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	20.74	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Inner_Full	21.63	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.86	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.85	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	20.20	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Inner_Full	20.07	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	17.11	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	17.20	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	17.29	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Inner_Full	17.12	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.58	PASS
DC_66A_n4	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.81	PASS



1A							
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	22.81	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	23.32	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.17	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.27	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	22.26	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Inner_Full	23.50	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.26	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.26	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	21.47	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Inner_Full	22.41	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.84	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	21.27	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	20.98	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Inner_Full	21.00	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.92	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.87	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	19.03	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Inner_Full	18.98	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.33	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.44	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	20.34	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Inner_Full	21.85	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	20.28	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.37	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	20.41	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Inner_Full	21.44	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.72	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.91	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	19.84	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Inner_Full	20.05	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	17.00	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	17.29	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	16.86	PASS



DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Inner_Full	16.99	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.41	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.56	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	22.66	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Inner_Full	23.27	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.97	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.16	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	22.18	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Inner_Full	23.33	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.06	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.07	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	21.32	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Inner_Full	22.23	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.67	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.75	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	20.85	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Inner_Full	20.88	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.63	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.77	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	18.93	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Inner_Full	18.85	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.18	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.41	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	20.28	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Inner_Full	21.73	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	20.05	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	19.99	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	20.30	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Inner_Full	21.33	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.62	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.88	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	19.75	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Inner_Full	19.78	PASS
DC_66A_n4	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	16.97	PASS



1A							
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	17.20	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	16.72	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Inner_Full	16.91	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.47	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.63	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	22.81	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Inner_Full	23.33	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.09	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.12	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	22.30	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Inner_Full	23.42	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.08	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.03	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	21.47	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Inner_Full	22.31	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.91	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.98	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	20.96	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Inner_Full	20.96	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.82	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	19.05	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Outer_Full	19.01	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Inner_Full	19.03	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.33	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.41	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+H	Outer_Full	20.39	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+H	Inner_Full	21.99	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.11	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.30	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+H	Outer_Full	20.45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+H	Inner_Full	21.49	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.64	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.85	PASS



DC_66A_n4 1A	30	5+20	CP-64QAM	M+H	Outer_Full	19.92	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+H	Inner_Full	19.92	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	16.87	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	17.06	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+H	Outer_Full	16.92	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+H	Inner_Full	16.98	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.38	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.46	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+L	Outer_Full	22.78	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+L	Inner_Full	23.38	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	22.08	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.98	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+L	Outer_Full	22.29	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+L	Inner_Full	23.42	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.89	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.97	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+L	Outer_Full	21.40	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+L	Inner_Full	22.43	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	20.60	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	20.68	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+L	Outer_Full	20.88	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+L	Inner_Full	20.97	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	18.56	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	18.60	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+L	Outer_Full	18.89	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+L	Inner_Full	18.93	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	20.15	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+L	Edge_1RB_Right	20.19	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+L	Outer_Full	20.34	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+L	Inner_Full	21.88	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+L	Edge_1RB_Left	20.07	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+L	Edge_1RB_Right	20.00	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+L	Outer_Full	20.42	PASS
DC_66A_n4	30	5+30	CP-16QAM	M+L	Inner_Full	21.47	PASS



1A							
DC_66A_n4 1A	30	5+30	CP-64QAM	M+L	Edge_1RB_Left	19.50	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+L	Edge_1RB_Right	19.59	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+L	Outer_Full	19.84	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+L	Inner_Full	20.10	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+L	Edge_1RB_Left	16.67	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+L	Edge_1RB_Right	16.76	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+L	Outer_Full	16.88	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+L	Inner_Full	16.97	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.22	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.31	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	22.64	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+M	Inner_Full	23.22	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.81	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.82	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+M	Outer_Full	22.17	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+M	Inner_Full	23.23	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	20.82	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	20.67	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+M	Outer_Full	21.26	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+M	Inner_Full	22.22	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.23	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Right	20.41	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+M	Outer_Full	20.82	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+M	Inner_Full	20.81	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	18.21	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Right	18.52	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+M	Outer_Full	18.75	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+M	Inner_Full	18.81	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+M	Edge_1RB_Left	19.96	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+M	Edge_1RB_Right	20.11	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+M	Outer_Full	20.21	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+M	Inner_Full	21.77	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+M	Edge_1RB_Left	19.69	PASS



DC_66A_n4 1A	30	5+30	CP-16QAM	M+M	Edge_1RB_Right	19.94	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+M	Outer_Full	20.20	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+M	Inner_Full	21.35	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+M	Edge_1RB_Left	19.37	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+M	Edge_1RB_Right	19.46	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+M	Outer_Full	19.76	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+M	Inner_Full	19.90	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+M	Edge_1RB_Left	16.58	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+M	Edge_1RB_Right	16.63	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+M	Outer_Full	16.75	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+M	Inner_Full	16.88	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.14	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.43	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+H	Outer_Full	22.60	PASS
DC_66A_n4 1A	30	5+30	DFT-PI2BPSK	M+H	Inner_Full	23.30	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.76	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.02	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+H	Outer_Full	22.16	PASS
DC_66A_n4 1A	30	5+30	DFT-QPSK	M+H	Inner_Full	23.24	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.60	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	20.91	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+H	Outer_Full	21.35	PASS
DC_66A_n4 1A	30	5+30	DFT-16QAM	M+H	Inner_Full	22.25	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.43	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	20.48	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+H	Outer_Full	20.82	PASS
DC_66A_n4 1A	30	5+30	DFT-64QAM	M+H	Inner_Full	20.86	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	18.36	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	18.48	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+H	Outer_Full	18.76	PASS
DC_66A_n4 1A	30	5+30	DFT-256QAM	M+H	Inner_Full	18.81	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+H	Edge_1RB_Left	19.98	PASS
DC_66A_n4 1A	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	20.19	PASS
DC_66A_n4	30	5+30	CP-QPSK	M+H	Outer_Full	20.29	PASS



1A							
DC_66A_n4 1A	30	5+30	CP-QPSK	M+H	Inner_Full	21.76	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+H	Edge_1RB_Left	19.73	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+H	Edge_1RB_Right	20.03	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+H	Outer_Full	20.25	PASS
DC_66A_n4 1A	30	5+30	CP-16QAM	M+H	Inner_Full	21.31	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+H	Edge_1RB_Left	19.33	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+H	Edge_1RB_Right	19.51	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+H	Outer_Full	19.83	PASS
DC_66A_n4 1A	30	5+30	CP-64QAM	M+H	Inner_Full	19.89	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+H	Edge_1RB_Left	16.38	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+H	Edge_1RB_Right	16.60	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+H	Outer_Full	16.79	PASS
DC_66A_n4 1A	30	5+30	CP-256QAM	M+H	Inner_Full	16.88	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.21	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.14	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	22.77	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+L	Inner_Full	23.33	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.88	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.66	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	22.21	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+L	Inner_Full	23.35	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.68	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.55	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+L	Outer_Full	21.27	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+L	Inner_Full	22.44	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.58	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.51	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+L	Outer_Full	20.79	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+L	Inner_Full	20.96	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.56	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.40	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+L	Outer_Full	18.82	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+L	Inner_Full	19.00	PASS



DC_66A_n4 1A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	20.03	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	19.88	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+L	Outer_Full	20.20	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+L	Inner_Full	21.79	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	19.92	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	19.80	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+L	Outer_Full	20.22	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+L	Inner_Full	21.41	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	19.35	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	19.23	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+L	Outer_Full	19.80	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+L	Inner_Full	19.75	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	16.68	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	16.34	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+L	Outer_Full	16.72	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+L	Inner_Full	16.91	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.95	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.04	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	22.70	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+M	Inner_Full	23.18	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.62	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.69	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	22.15	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+M	Inner_Full	23.24	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.41	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.67	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+M	Outer_Full	21.27	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+M	Inner_Full	22.31	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.12	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.13	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+M	Outer_Full	20.73	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+M	Inner_Full	20.80	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.31	PASS
DC_66A_n4	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.27	PASS



1A							
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+M	Outer_Full	18.75	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+M	Inner_Full	18.75	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	19.86	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	19.90	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+M	Outer_Full	20.15	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+M	Inner_Full	21.67	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	19.42	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	19.61	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+M	Outer_Full	20.13	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+M	Inner_Full	21.30	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+M	Edge_1RB_Left	19.17	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+M	Edge_1RB_Right	19.24	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+M	Outer_Full	19.67	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+M	Inner_Full	19.82	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+M	Edge_1RB_Left	16.16	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+M	Edge_1RB_Right	16.53	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+M	Outer_Full	16.70	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+M	Inner_Full	16.87	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.10	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.22	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	22.62	PASS
DC_66A_n4 1A	30	5+40	DFT-PI2BPSK	M+H	Inner_Full	23.19	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.60	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.82	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+H	Outer_Full	22.12	PASS
DC_66A_n4 1A	30	5+40	DFT-QPSK	M+H	Inner_Full	23.20	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.60	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.66	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+H	Outer_Full	21.20	PASS
DC_66A_n4 1A	30	5+40	DFT-16QAM	M+H	Inner_Full	22.19	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.17	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.31	PASS
DC_66A_n4 1A	30	5+40	DFT-64QAM	M+H	Outer_Full	20.76	PASS



DC_66A_n4 1A	30	5+40	DFT-64QAM	M+H	Inner_Full	20.76	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.37	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	18.24	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+H	Outer_Full	18.72	PASS
DC_66A_n4 1A	30	5+40	DFT-256QAM	M+H	Inner_Full	18.78	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	19.95	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	19.95	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+H	Outer_Full	20.16	PASS
DC_66A_n4 1A	30	5+40	CP-QPSK	M+H	Inner_Full	21.70	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	19.65	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	19.84	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+H	Outer_Full	20.16	PASS
DC_66A_n4 1A	30	5+40	CP-16QAM	M+H	Inner_Full	21.28	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	19.17	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	19.38	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+H	Outer_Full	19.67	PASS
DC_66A_n4 1A	30	5+40	CP-64QAM	M+H	Inner_Full	19.75	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	19.24	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	16.34	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+H	Outer_Full	16.76	PASS
DC_66A_n4 1A	30	5+40	CP-256QAM	M+H	Inner_Full	16.75	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.26	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.22	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+L	Outer_Full	22.52	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+L	Inner_Full	23.18	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	21.93	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	21.71	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+L	Outer_Full	22.06	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+L	Inner_Full	23.18	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	21.07	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	20.98	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+L	Outer_Full	21.24	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+L	Inner_Full	22.22	PASS
DC_66A_n4	30	5+50	DFT-64QAM	M+L	Edge_1RB_Left	20.69	PASS



1A							
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+L	Edge_1RB_Right	20.67	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+L	Outer_Full	20.67	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+L	Inner_Full	20.83	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Left	18.58	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Right	18.41	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+L	Outer_Full	18.77	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+L	Inner_Full	18.76	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	19.99	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	19.90	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+L	Outer_Full	20.15	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+L	Inner_Full	21.65	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	19.94	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	19.89	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+L	Outer_Full	20.19	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+L	Inner_Full	21.27	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+L	Edge_1RB_Left	19.83	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+L	Edge_1RB_Right	19.59	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+L	Outer_Full	19.71	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+L	Inner_Full	19.76	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+L	Edge_1RB_Left	16.76	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+L	Edge_1RB_Right	16.68	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+L	Outer_Full	16.71	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+L	Inner_Full	16.78	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.22	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.21	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+M	Outer_Full	22.51	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+M	Inner_Full	23.09	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	21.78	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	21.73	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+M	Outer_Full	22.05	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+M	Inner_Full	23.11	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.96	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.91	PASS



DC_66A_n4 1A	30	5+50	DFT-16QAM	M+M	Outer_Full	21.17	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+M	Inner_Full	22.11	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Left	20.61	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Right	20.57	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+M	Outer_Full	20.67	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+M	Inner_Full	20.75	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Left	18.39	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Right	18.29	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+M	Outer_Full	18.66	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+M	Inner_Full	18.72	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	19.84	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	19.88	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+M	Outer_Full	20.16	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+M	Inner_Full	21.66	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	19.93	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	19.93	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+M	Outer_Full	20.16	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+M	Inner_Full	21.25	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+M	Edge_1RB_Left	19.74	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+M	Edge_1RB_Right	19.55	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+M	Outer_Full	19.62	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+M	Inner_Full	19.73	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+M	Edge_1RB_Left	16.58	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+M	Edge_1RB_Right	16.47	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+M	Outer_Full	16.64	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+M	Inner_Full	16.77	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.45	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.42	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+H	Outer_Full	22.56	PASS
DC_66A_n4 1A	30	5+50	DFT-PI2BPSK	M+H	Inner_Full	23.08	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	21.96	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	21.95	PASS
DC_66A_n4 1A	30	5+50	DFT-QPSK	M+H	Outer_Full	22.07	PASS
DC_66A_n4	30	5+50	DFT-QPSK	M+H	Inner_Full	23.11	PASS



1A							
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.14	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	21.16	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+H	Outer_Full	21.22	PASS
DC_66A_n4 1A	30	5+50	DFT-16QAM	M+H	Inner_Full	22.07	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Left	20.75	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Right	20.73	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+H	Outer_Full	20.75	PASS
DC_66A_n4 1A	30	5+50	DFT-64QAM	M+H	Inner_Full	20.66	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Left	18.59	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Right	18.65	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+H	Outer_Full	18.75	PASS
DC_66A_n4 1A	30	5+50	DFT-256QAM	M+H	Inner_Full	18.63	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	20.12	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	20.06	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+H	Outer_Full	20.28	PASS
DC_66A_n4 1A	30	5+50	CP-QPSK	M+H	Inner_Full	21.55	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	20.20	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	20.17	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+H	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+50	CP-16QAM	M+H	Inner_Full	21.16	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+H	Edge_1RB_Left	19.64	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+H	Edge_1RB_Right	19.93	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+H	Outer_Full	19.70	PASS
DC_66A_n4 1A	30	5+50	CP-64QAM	M+H	Inner_Full	19.62	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+H	Edge_1RB_Left	16.65	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+H	Edge_1RB_Right	16.64	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+H	Outer_Full	16.73	PASS
DC_66A_n4 1A	30	5+50	CP-256QAM	M+H	Inner_Full	16.69	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.30	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.32	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	22.65	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+L	Inner_Full	23.21	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	21.78	PASS



DC_66A_n4 1A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.84	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	22.11	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+L	Inner_Full	23.18	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.02	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	20.91	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+L	Outer_Full	21.24	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+L	Inner_Full	22.21	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	20.40	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	20.65	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+L	Outer_Full	20.76	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+L	Inner_Full	20.82	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	18.43	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	18.59	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+L	Outer_Full	18.75	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+L	Inner_Full	18.75	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	19.92	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	19.87	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+L	Outer_Full	20.23	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+L	Inner_Full	21.66	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	20.08	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	20.01	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+L	Outer_Full	20.23	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+L	Inner_Full	21.31	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+L	Edge_1RB_Left	19.67	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+L	Edge_1RB_Right	19.49	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+L	Outer_Full	19.71	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+L	Inner_Full	19.77	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+L	Edge_1RB_Left	16.62	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+L	Edge_1RB_Right	16.63	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+L	Outer_Full	16.68	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+L	Inner_Full	16.79	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.09	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.07	PASS
DC_66A_n4	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	22.50	PASS



1A							
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+M	Inner_Full	23.14	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.78	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.70	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	22.06	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+M	Inner_Full	23.13	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.94	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.94	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+M	Outer_Full	21.16	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+M	Inner_Full	22.17	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	20.55	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Right	20.49	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+M	Outer_Full	20.64	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+M	Inner_Full	20.76	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	18.41	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Right	18.55	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+M	Outer_Full	18.65	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+M	Inner_Full	18.76	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	19.83	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	19.80	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+M	Outer_Full	20.07	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+M	Inner_Full	21.67	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	20.10	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	19.88	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+M	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+M	Inner_Full	21.28	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+M	Edge_1RB_Left	19.25	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+M	Edge_1RB_Right	18.98	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+M	Outer_Full	19.65	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+M	Inner_Full	19.76	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+M	Edge_1RB_Left	16.52	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+M	Edge_1RB_Right	16.59	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+M	Outer_Full	16.60	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+M	Inner_Full	16.73	PASS



DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.30	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.19	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	22.56	PASS
DC_66A_n4 1A	30	5+60	DFT-PI2BPSK	M+H	Inner_Full	23.04	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	21.72	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.79	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	22.07	PASS
DC_66A_n4 1A	30	5+60	DFT-QPSK	M+H	Inner_Full	23.06	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.01	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.10	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+H	Outer_Full	21.16	PASS
DC_66A_n4 1A	30	5+60	DFT-16QAM	M+H	Inner_Full	22.09	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	20.68	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	20.78	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+H	Outer_Full	20.71	PASS
DC_66A_n4 1A	30	5+60	DFT-64QAM	M+H	Inner_Full	20.69	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	18.51	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	18.54	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+H	Outer_Full	18.71	PASS
DC_66A_n4 1A	30	5+60	DFT-256QAM	M+H	Inner_Full	18.66	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	19.93	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	19.93	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+H	Outer_Full	20.20	PASS
DC_66A_n4 1A	30	5+60	CP-QPSK	M+H	Inner_Full	21.60	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	20.03	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	20.08	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+H	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+60	CP-16QAM	M+H	Inner_Full	21.18	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	19.85	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	19.22	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+H	Outer_Full	19.67	PASS
DC_66A_n4 1A	30	5+60	CP-64QAM	M+H	Inner_Full	19.67	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	16.57	PASS
DC_66A_n4	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	16.76	PASS



1A							
DC_66A_n4 1A	30	5+60	CP-256QAM	M+H	Outer_Full	16.67	PASS
DC_66A_n4 1A	30	5+60	CP-256QAM	M+H	Inner_Full	16.63	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.36	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.38	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+L	Outer_Full	22.32	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+L	Inner_Full	22.40	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	21.79	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	21.94	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+L	Outer_Full	21.89	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+L	Inner_Full	21.89	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	21.04	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	21.15	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+L	Outer_Full	21.03	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+L	Inner_Full	21.06	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Left	20.58	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Right	20.60	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+L	Outer_Full	20.59	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+L	Inner_Full	20.58	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Left	18.56	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Right	18.57	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+L	Outer_Full	18.54	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+L	Inner_Full	18.54	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+L	Edge_1RB_Left	20.02	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+L	Edge_1RB_Right	20.07	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+L	Outer_Full	20.05	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+L	Inner_Full	20.03	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+L	Edge_1RB_Left	20.05	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+L	Edge_1RB_Right	20.04	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+L	Outer_Full	20.05	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+L	Inner_Full	20.06	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+L	Edge_1RB_Left	19.56	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+L	Edge_1RB_Right	19.51	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+L	Outer_Full	19.54	PASS



DC_66A_n4 1A	30	5+70	CP-64QAM	M+L	Inner_Full	19.52	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+L	Edge_1RB_Left	16.47	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+L	Edge_1RB_Right	16.58	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+L	Outer_Full	16.59	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+L	Inner_Full	16.54	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.33	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.30	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+M	Outer_Full	22.30	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+M	Inner_Full	22.33	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	21.83	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	21.81	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+M	Outer_Full	21.82	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+M	Inner_Full	21.81	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	21.03	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	21.05	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+M	Outer_Full	20.99	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+M	Inner_Full	21.09	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Left	20.55	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Right	20.44	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+M	Outer_Full	20.48	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+M	Inner_Full	20.54	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Left	18.47	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Right	18.50	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+M	Outer_Full	18.51	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+M	Inner_Full	18.46	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+M	Edge_1RB_Left	20.00	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+M	Edge_1RB_Right	19.93	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+M	Outer_Full	20.05	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+M	Inner_Full	19.97	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+M	Edge_1RB_Left	20.04	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+M	Edge_1RB_Right	20.02	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+M	Outer_Full	20.00	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+M	Inner_Full	19.94	PASS
DC_66A_n4	30	5+70	CP-64QAM	M+M	Edge_1RB_Left	19.45	PASS



1A							
DC_66A_n4 1A	30	5+70	CP-64QAM	M+M	Edge_1RB_Right	19.46	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+M	Outer_Full	19.48	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+M	Inner_Full	19.47	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+M	Edge_1RB_Left	16.45	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+M	Edge_1RB_Right	16.50	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+M	Outer_Full	16.45	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+M	Inner_Full	16.47	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.30	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.30	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+H	Outer_Full	22.32	PASS
DC_66A_n4 1A	30	5+70	DFT-PI2BPSK	M+H	Inner_Full	22.29	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	21.87	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	21.85	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+H	Outer_Full	21.85	PASS
DC_66A_n4 1A	30	5+70	DFT-QPSK	M+H	Inner_Full	21.85	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.05	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	21.07	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+H	Outer_Full	21.05	PASS
DC_66A_n4 1A	30	5+70	DFT-16QAM	M+H	Inner_Full	21.09	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Left	20.55	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Right	20.49	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+H	Outer_Full	20.56	PASS
DC_66A_n4 1A	30	5+70	DFT-64QAM	M+H	Inner_Full	20.49	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Left	18.52	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Right	18.57	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+H	Outer_Full	18.52	PASS
DC_66A_n4 1A	30	5+70	DFT-256QAM	M+H	Inner_Full	18.51	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+H	Edge_1RB_Left	20.02	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+H	Edge_1RB_Right	20.12	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+H	Outer_Full	19.97	PASS
DC_66A_n4 1A	30	5+70	CP-QPSK	M+H	Inner_Full	19.99	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+H	Edge_1RB_Left	19.99	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+H	Edge_1RB_Right	20.01	PASS



DC_66A_n4 1A	30	5+70	CP-16QAM	M+H	Outer_Full	20.07	PASS
DC_66A_n4 1A	30	5+70	CP-16QAM	M+H	Inner_Full	20.02	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+H	Edge_1RB_Left	19.47	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+H	Edge_1RB_Right	19.48	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+H	Outer_Full	19.50	PASS
DC_66A_n4 1A	30	5+70	CP-64QAM	M+H	Inner_Full	19.49	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+H	Edge_1RB_Left	16.54	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+H	Edge_1RB_Right	16.53	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+H	Outer_Full	16.52	PASS
DC_66A_n4 1A	30	5+70	CP-256QAM	M+H	Inner_Full	16.50	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.06	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.87	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	22.49	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+L	Inner_Full	23.08	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	21.59	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.30	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+L	Outer_Full	21.93	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+L	Inner_Full	23.11	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.83	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.72	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+L	Outer_Full	21.25	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+L	Inner_Full	22.13	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	20.43	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Right	20.28	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+L	Outer_Full	20.70	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+L	Inner_Full	20.80	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	18.33	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Right	18.02	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+L	Outer_Full	18.71	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+L	Inner_Full	18.76	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	19.77	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	19.62	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+L	Outer_Full	20.17	PASS
DC_66A_n4	30	5+80	CP-QPSK	M+L	Inner_Full	21.59	PASS



1A							
DC_66A_n4 1A	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	19.91	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	19.68	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+L	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+L	Inner_Full	21.22	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+L	Edge_1RB_Left	19.38	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+L	Edge_1RB_Right	19.03	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+L	Outer_Full	19.63	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+L	Inner_Full	19.74	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+L	Edge_1RB_Left	16.67	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+L	Edge_1RB_Right	16.05	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+L	Outer_Full	16.63	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+L	Inner_Full	16.70	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.99	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.94	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	22.40	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+M	Inner_Full	23.13	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.55	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.53	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+M	Outer_Full	21.96	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+M	Inner_Full	23.17	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.89	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.83	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+M	Outer_Full	21.19	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+M	Inner_Full	22.16	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	20.47	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	20.15	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+M	Outer_Full	20.61	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+M	Inner_Full	20.75	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	18.29	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Right	18.40	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+M	Outer_Full	18.59	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+M	Inner_Full	18.79	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	19.80	PASS



DC_66A_n4 1A	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	19.73	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+M	Outer_Full	20.13	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+M	Inner_Full	21.56	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	19.66	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	19.71	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+M	Outer_Full	20.06	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+M	Inner_Full	21.21	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+M	Edge_1RB_Left	19.60	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+M	Edge_1RB_Right	19.23	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+M	Outer_Full	19.64	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+M	Inner_Full	19.79	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+M	Edge_1RB_Left	16.58	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+M	Edge_1RB_Right	16.23	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+M	Outer_Full	16.52	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+M	Inner_Full	16.81	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.90	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.05	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	22.47	PASS
DC_66A_n4 1A	30	5+80	DFT-PI2BPSK	M+H	Inner_Full	23.13	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	21.49	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.60	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+H	Outer_Full	22.01	PASS
DC_66A_n4 1A	30	5+80	DFT-QPSK	M+H	Inner_Full	23.08	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.68	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.93	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+H	Outer_Full	21.18	PASS
DC_66A_n4 1A	30	5+80	DFT-16QAM	M+H	Inner_Full	22.08	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	20.27	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Right	20.48	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+H	Outer_Full	20.64	PASS
DC_66A_n4 1A	30	5+80	DFT-64QAM	M+H	Inner_Full	20.72	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	18.28	PASS
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	18.43	PASS
DC_66A_n4	30	5+80	DFT-256QAM	M+H	Outer_Full	18.65	PASS



1A							
DC_66A_n4 1A	30	5+80	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	19.73	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	19.91	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+H	Outer_Full	20.14	PASS
DC_66A_n4 1A	30	5+80	CP-QPSK	M+H	Inner_Full	21.55	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	19.71	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	20.19	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+H	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+80	CP-16QAM	M+H	Inner_Full	21.19	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+H	Edge_1RB_Left	19.22	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+H	Edge_1RB_Right	19.84	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+H	Outer_Full	19.61	PASS
DC_66A_n4 1A	30	5+80	CP-64QAM	M+H	Inner_Full	19.66	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+H	Edge_1RB_Left	16.44	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+H	Edge_1RB_Right	16.51	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+H	Outer_Full	16.59	PASS
DC_66A_n4 1A	30	5+80	CP-256QAM	M+H	Inner_Full	16.66	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.87	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.81	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+L	Outer_Full	22.56	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+L	Inner_Full	23.15	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	21.41	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.26	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+L	Outer_Full	22.11	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+L	Inner_Full	23.12	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	20.78	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.59	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+L	Outer_Full	21.21	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+L	Inner_Full	22.12	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Left	20.23	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Right	20.08	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+L	Outer_Full	20.73	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+L	Inner_Full	20.77	PASS



DC_66A_n4 1A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Left	18.21	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Right	18.08	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+L	Outer_Full	18.74	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+L	Inner_Full	18.78	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	19.69	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	19.60	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+L	Outer_Full	20.20	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+L	Inner_Full	21.62	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	19.85	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	19.59	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+L	Outer_Full	20.21	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+L	Inner_Full	21.26	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+L	Edge_1RB_Left	19.63	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+L	Edge_1RB_Right	19.41	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+L	Outer_Full	19.65	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+L	Inner_Full	19.76	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+L	Edge_1RB_Left	16.25	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+L	Edge_1RB_Right	16.17	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+L	Outer_Full	16.66	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+L	Inner_Full	16.74	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.82	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.90	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+M	Outer_Full	22.46	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+M	Inner_Full	23.14	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.38	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	21.39	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+M	Outer_Full	21.98	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+M	Inner_Full	23.12	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.68	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	20.71	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+M	Outer_Full	21.19	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+M	Inner_Full	22.14	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+M	Edge_1RB_Left	20.30	PASS
DC_66A_n4	30	5+90	DFT-64QAM	M+M	Edge_1RB_Right	20.45	PASS



1A							
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+M	Outer_Full	20.62	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+M	Inner_Full	20.75	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Left	18.26	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Right	18.11	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+M	Outer_Full	18.65	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+M	Inner_Full	18.74	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	19.61	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	19.72	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+M	Outer_Full	20.11	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+M	Inner_Full	21.59	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	19.63	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	19.73	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+M	Outer_Full	20.13	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+M	Inner_Full	21.27	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+M	Edge_1RB_Left	19.19	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+M	Edge_1RB_Right	19.24	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+M	Outer_Full	19.56	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+M	Inner_Full	19.70	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+M	Edge_1RB_Left	16.07	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+M	Edge_1RB_Right	16.08	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+M	Outer_Full	16.54	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+M	Inner_Full	16.67	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.88	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.86	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+H	Outer_Full	22.50	PASS
DC_66A_n4 1A	30	5+90	DFT-PI2BPSK	M+H	Inner_Full	23.11	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	21.42	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	21.45	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+H	Outer_Full	22.03	PASS
DC_66A_n4 1A	30	5+90	DFT-QPSK	M+H	Inner_Full	23.14	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	20.79	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	20.76	PASS
DC_66A_n4 1A	30	5+90	DFT-16QAM	M+H	Outer_Full	21.19	PASS



DC_66A_n4 1A	30	5+90	DFT-16QAM	M+H	Inner_Full	22.12	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Left	20.42	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Right	20.50	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+H	Outer_Full	20.71	PASS
DC_66A_n4 1A	30	5+90	DFT-64QAM	M+H	Inner_Full	20.75	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Left	18.16	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Right	18.22	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+H	Outer_Full	18.73	PASS
DC_66A_n4 1A	30	5+90	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	19.72	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	19.74	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+H	Outer_Full	20.20	PASS
DC_66A_n4 1A	30	5+90	CP-QPSK	M+H	Inner_Full	21.56	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	19.74	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	19.75	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+H	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+90	CP-16QAM	M+H	Inner_Full	21.26	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+H	Edge_1RB_Left	19.27	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+H	Edge_1RB_Right	19.29	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+H	Outer_Full	19.63	PASS
DC_66A_n4 1A	30	5+90	CP-64QAM	M+H	Inner_Full	19.72	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+H	Edge_1RB_Left	16.25	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+H	Edge_1RB_Right	16.25	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+H	Outer_Full	16.60	PASS
DC_66A_n4 1A	30	5+90	CP-256QAM	M+H	Inner_Full	16.66	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.61	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.64	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	22.46	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+L	Inner_Full	23.02	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.19	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.16	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	21.98	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+L	Inner_Full	23.12	PASS
DC_66A_n4	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	20.46	PASS



1A							
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.50	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+L	Outer_Full	21.19	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+L	Inner_Full	22.08	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	19.93	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	20.12	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+L	Outer_Full	20.65	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+L	Inner_Full	20.80	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	17.90	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	18.17	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+L	Outer_Full	18.66	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+L	Inner_Full	18.74	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	19.45	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	19.48	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+L	Outer_Full	20.15	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+L	Inner_Full	21.58	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	19.50	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	19.53	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+L	Outer_Full	20.17	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+L	Inner_Full	21.23	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	19.22	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+L	Edge_1RB_Right	19.13	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+L	Outer_Full	19.65	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+L	Inner_Full	19.70	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	16.23	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+L	Edge_1RB_Right	16.01	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+L	Outer_Full	16.65	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+L	Inner_Full	16.68	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.40	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.60	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	22.38	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+M	Inner_Full	23.09	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.02	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.12	PASS



DC_66A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	21.94	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+M	Inner_Full	23.11	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	20.34	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	20.55	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+M	Outer_Full	21.09	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+M	Inner_Full	22.11	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	19.99	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	20.19	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+M	Outer_Full	20.61	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+M	Inner_Full	20.82	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	17.79	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	17.91	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+M	Outer_Full	18.57	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+M	Inner_Full	18.75	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	19.22	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	19.54	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+M	Outer_Full	20.08	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+M	Inner_Full	21.63	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	19.31	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	19.64	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+M	Outer_Full	20.06	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+M	Inner_Full	21.26	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+M	Edge_1RB_Left	18.67	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+M	Edge_1RB_Right	18.71	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+M	Outer_Full	19.60	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+M	Inner_Full	19.74	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+M	Edge_1RB_Left	15.87	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+M	Edge_1RB_Right	16.07	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+M	Outer_Full	16.54	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+M	Inner_Full	16.73	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.58	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.63	PASS
DC_66A_n4 1A	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	22.45	PASS
DC_66A_n4	30	5+100	DFT-PI2BPSK	M+H	Inner_Full	23.01	PASS



1A							
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.08	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.12	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	21.94	PASS
DC_66A_n4 1A	30	5+100	DFT-QPSK	M+H	Inner_Full	23.08	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	20.50	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	20.54	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+H	Outer_Full	21.20	PASS
DC_66A_n4 1A	30	5+100	DFT-16QAM	M+H	Inner_Full	22.11	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	20.21	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	20.24	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+H	Outer_Full	20.66	PASS
DC_66A_n4 1A	30	5+100	DFT-64QAM	M+H	Inner_Full	20.79	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	17.90	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	18.03	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+H	Outer_Full	18.65	PASS
DC_66A_n4 1A	30	5+100	DFT-256QAM	M+H	Inner_Full	18.72	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	19.46	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	19.45	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+H	Outer_Full	20.14	PASS
DC_66A_n4 1A	30	5+100	CP-QPSK	M+H	Inner_Full	21.60	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	19.65	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	19.41	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+H	Outer_Full	20.12	PASS
DC_66A_n4 1A	30	5+100	CP-16QAM	M+H	Inner_Full	21.21	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+H	Edge_1RB_Left	19.45	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	19.02	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+H	Outer_Full	19.69	PASS
DC_66A_n4 1A	30	5+100	CP-64QAM	M+H	Inner_Full	19.76	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+H	Edge_1RB_Left	16.15	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	16.24	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+H	Outer_Full	16.63	PASS
DC_66A_n4 1A	30	5+100	CP-256QAM	M+H	Inner_Full	16.65	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.99	PASS



DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	23.17	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	22.97	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+L	Inner_Full	23.62	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.61	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.68	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.52	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+L	Inner_Full	23.65	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	21.56	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	21.60	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+L	Outer_Full	21.50	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+L	Inner_Full	22.50	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	21.10	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	21.23	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+L	Outer_Full	20.97	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+L	Inner_Full	21.04	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	19.09	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	19.18	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.87	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+L	Inner_Full	19.11	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	20.48	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	20.63	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+L	Outer_Full	20.41	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+L	Inner_Full	22.08	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	20.39	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	20.84	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+L	Outer_Full	20.47	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+L	Inner_Full	21.53	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	19.69	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	19.91	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+L	Outer_Full	19.92	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+L	Inner_Full	19.99	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	17.28	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	17.12	PASS
DC_66A_n7	15	5+5	CP-256QAM	M+L	Outer_Full	16.89	PASS



1A							
DC_66A_n7 1A	15	5+5	CP-256QAM	M+L	Inner_Full	17.14	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.04	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	23.05	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	23.06	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	23.55	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	22.65	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	22.66	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.63	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+M	Inner_Full	23.62	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	21.54	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	21.39	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+M	Outer_Full	21.55	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+M	Inner_Full	22.49	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	21.15	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	21.09	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+M	Outer_Full	21.09	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+M	Inner_Full	21.07	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	19.22	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	18.94	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.96	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+M	Inner_Full	19.04	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	20.60	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	20.62	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+M	Outer_Full	20.52	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+M	Inner_Full	22.05	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	20.42	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	20.46	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+M	Outer_Full	20.56	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+M	Inner_Full	21.51	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	19.77	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	19.74	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+M	Outer_Full	20.05	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+M	Inner_Full	19.94	PASS



DC_66A_n7 1A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	17.10	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	17.18	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+M	Outer_Full	17.02	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+M	Inner_Full	17.16	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	23.05	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	23.06	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	23.04	PASS
DC_66A_n7 1A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	23.45	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	22.61	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	22.67	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+H	Outer_Full	22.60	PASS
DC_66A_n7 1A	15	5+5	DFT-QPSK	M+H	Inner_Full	23.47	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	21.36	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	21.57	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+H	Outer_Full	21.55	PASS
DC_66A_n7 1A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.51	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	21.00	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	21.07	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+H	Outer_Full	20.96	PASS
DC_66A_n7 1A	15	5+5	DFT-64QAM	M+H	Inner_Full	21.06	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	18.84	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	19.15	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.94	PASS
DC_66A_n7 1A	15	5+5	DFT-256QAM	M+H	Inner_Full	19.10	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	20.48	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	20.62	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+H	Outer_Full	20.48	PASS
DC_66A_n7 1A	15	5+5	CP-QPSK	M+H	Inner_Full	22.04	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	20.38	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	20.48	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+H	Outer_Full	20.50	PASS
DC_66A_n7 1A	15	5+5	CP-16QAM	M+H	Inner_Full	21.55	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	19.68	PASS
DC_66A_n7	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	19.70	PASS



1A							
DC_66A_n7 1A	15	5+5	CP-64QAM	M+H	Outer_Full	19.88	PASS
DC_66A_n7 1A	15	5+5	CP-64QAM	M+H	Inner_Full	20.04	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	17.07	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	17.14	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+H	Outer_Full	16.94	PASS
DC_66A_n7 1A	15	5+5	CP-256QAM	M+H	Inner_Full	17.11	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.73	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.86	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	22.77	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	23.46	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.39	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.43	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.34	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+L	Inner_Full	23.48	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	21.56	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	21.67	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.44	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.49	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.89	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	21.16	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.90	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.91	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.83	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.80	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.85	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.91	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	20.19	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	20.37	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+L	Outer_Full	20.31	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+L	Inner_Full	21.97	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	20.22	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	20.25	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+L	Outer_Full	20.32	PASS



DC_66A_n7 1A	15	5+10	CP-16QAM	M+L	Inner_Full	21.44	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+L	Edge_1RB_Left	19.36	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+L	Edge_1RB_Right	19.62	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+L	Outer_Full	19.72	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+L	Inner_Full	19.95	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+L	Edge_1RB_Left	17.17	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+L	Edge_1RB_Right	17.34	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+L	Outer_Full	16.86	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+L	Inner_Full	16.94	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.87	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.86	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	22.75	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	23.41	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	22.46	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.46	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.23	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+M	Inner_Full	23.47	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.45	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21.32	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+M	Outer_Full	21.34	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.33	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	20.93	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	21.15	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+M	Outer_Full	20.75	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.92	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.82	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.74	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.73	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.78	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	20.39	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.41	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+M	Outer_Full	20.16	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+M	Inner_Full	21.88	PASS
DC_66A_n7	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	20.36	PASS



1A							
DC_66A_n7 1A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	20.32	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+M	Outer_Full	20.23	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+M	Inner_Full	21.40	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+M	Edge_1RB_Left	19.50	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+M	Edge_1RB_Right	19.60	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+M	Outer_Full	19.67	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+M	Inner_Full	19.89	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+M	Edge_1RB_Left	16.93	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+M	Edge_1RB_Right	17.04	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+M	Outer_Full	16.73	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+M	Inner_Full	16.85	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.87	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.89	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	22.77	PASS
DC_66A_n7 1A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	23.44	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.35	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.51	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+H	Outer_Full	22.27	PASS
DC_66A_n7 1A	15	5+10	DFT-QPSK	M+H	Inner_Full	23.51	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	21.34	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	21.54	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+H	Outer_Full	21.37	PASS
DC_66A_n7 1A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.40	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	21.54	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	21.60	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+H	Outer_Full	20.83	PASS
DC_66A_n7 1A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.83	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	18.78	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	18.82	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.79	PASS
DC_66A_n7 1A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.78	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	20.27	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	20.35	PASS



DC_66A_n7 1A	15	5+10	CP-QPSK	M+H	Outer_Full	20.30	PASS
DC_66A_n7 1A	15	5+10	CP-QPSK	M+H	Inner_Full	21.95	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	20.37	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	20.25	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+H	Outer_Full	20.34	PASS
DC_66A_n7 1A	15	5+10	CP-16QAM	M+H	Inner_Full	21.45	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+H	Edge_1RB_Left	19.48	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+H	Edge_1RB_Right	19.57	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+H	Outer_Full	19.74	PASS
DC_66A_n7 1A	15	5+10	CP-64QAM	M+H	Inner_Full	19.95	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+H	Edge_1RB_Left	16.89	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+H	Edge_1RB_Right	16.89	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+H	Outer_Full	16.80	PASS
DC_66A_n7 1A	15	5+10	CP-256QAM	M+H	Inner_Full	16.97	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.89	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.96	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	23.18	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	23.63	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.49	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.62	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.70	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+L	Inner_Full	23.65	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	21.36	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.50	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.73	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.67	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.98	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	21.07	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+L	Outer_Full	21.14	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+L	Inner_Full	21.23	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.84	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.87	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+L	Outer_Full	19.08	PASS
DC_66A_n7	15	5+15	DFT-256QAM	M+L	Inner_Full	19.06	PASS



1A							
DC_66A_n7 1A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	20.29	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.49	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+L	Outer_Full	20.67	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+L	Inner_Full	22.07	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	20.24	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	20.31	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+L	Outer_Full	20.70	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+L	Inner_Full	21.62	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+L	Edge_1RB_Left	19.53	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+L	Edge_1RB_Right	19.53	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+L	Outer_Full	20.13	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+L	Inner_Full	20.14	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+L	Edge_1RB_Left	16.89	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+L	Edge_1RB_Right	17.29	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+L	Outer_Full	17.23	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+L	Inner_Full	17.16	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.94	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.98	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	22.93	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	23.57	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	22.50	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.51	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.51	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+M	Inner_Full	23.58	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.43	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.74	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+M	Outer_Full	21.50	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.64	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	21.00	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	21.24	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+M	Outer_Full	20.95	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+M	Inner_Full	21.12	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	19.19	PASS



DC_66A_n7 1A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	19.15	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.95	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+M	Inner_Full	19.07	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	20.39	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	20.39	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+M	Outer_Full	20.41	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+M	Inner_Full	22.06	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	20.25	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	20.41	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+M	Outer_Full	20.46	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+M	Inner_Full	21.56	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+M	Edge_1RB_Left	19.59	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+M	Edge_1RB_Right	19.57	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+M	Outer_Full	19.92	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+M	Inner_Full	20.11	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+M	Edge_1RB_Left	16.98	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+M	Edge_1RB_Right	16.94	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+M	Outer_Full	16.92	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+M	Inner_Full	17.10	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.96	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.95	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	23.15	PASS
DC_66A_n7 1A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	23.53	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.45	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	22.51	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+H	Outer_Full	22.58	PASS
DC_66A_n7 1A	15	5+15	DFT-QPSK	M+H	Inner_Full	23.54	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	21.58	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	21.54	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+H	Outer_Full	21.61	PASS
DC_66A_n7 1A	15	5+15	DFT-16QAM	M+H	Inner_Full	22.60	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	21.02	PASS
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	21.46	PASS
DC_66A_n7	15	5+15	DFT-64QAM	M+H	Outer_Full	21.12	PASS



1A							
DC_66A_n7 1A	15	5+15	DFT-64QAM	M+H	Inner_Full	20.99	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	18.81	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	18.82	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+H	Outer_Full	18.99	PASS
DC_66A_n7 1A	15	5+15	DFT-256QAM	M+H	Inner_Full	18.96	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	20.41	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	20.47	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+H	Outer_Full	20.66	PASS
DC_66A_n7 1A	15	5+15	CP-QPSK	M+H	Inner_Full	22.03	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	20.31	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	20.34	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+H	Outer_Full	20.60	PASS
DC_66A_n7 1A	15	5+15	CP-16QAM	M+H	Inner_Full	21.48	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+H	Edge_1RB_Left	20.20	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+H	Edge_1RB_Right	20.23	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+H	Outer_Full	20.02	PASS
DC_66A_n7 1A	15	5+15	CP-64QAM	M+H	Inner_Full	20.01	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+H	Edge_1RB_Left	17.29	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+H	Edge_1RB_Right	17.35	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+H	Outer_Full	17.11	PASS
DC_66A_n7 1A	15	5+15	CP-256QAM	M+H	Inner_Full	17.06	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.76	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.88	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	23.20	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	23.61	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.51	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.44	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.71	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+L	Inner_Full	23.64	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.56	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.40	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+L	Outer_Full	21.57	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+L	Inner_Full	22.61	PASS



DC_66A_n7 1A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.95	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	21.13	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+L	Outer_Full	21.19	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+L	Inner_Full	21.08	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.84	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.88	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+L	Outer_Full	19.11	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+L	Inner_Full	19.04	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.32	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.43	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+L	Outer_Full	20.63	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+L	Inner_Full	22.10	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	20.21	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.41	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+L	Outer_Full	20.64	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+L	Inner_Full	21.63	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.49	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.80	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+L	Outer_Full	20.13	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+L	Inner_Full	20.12	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	16.87	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	16.99	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+L	Outer_Full	17.15	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+L	Inner_Full	17.06	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.88	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.92	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	22.73	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	23.53	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.47	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.49	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+M	Outer_Full	22.32	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+M	Inner_Full	23.59	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.34	PASS
DC_66A_n7	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.40	PASS



1A							
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+M	Outer_Full	21.28	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+M	Inner_Full	22.64	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	21.11	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	21.05	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.78	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+M	Inner_Full	21.08	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.78	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.86	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.71	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+M	Inner_Full	19.02	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.36	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.36	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+M	Outer_Full	20.25	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+M	Inner_Full	22.04	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	20.27	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	20.32	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+M	Outer_Full	20.22	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+M	Inner_Full	21.50	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.54	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.63	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+M	Outer_Full	19.74	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+M	Inner_Full	19.97	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	17.05	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	17.32	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+M	Outer_Full	16.75	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+M	Inner_Full	16.95	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.84	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.91	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	23.24	PASS
DC_66A_n7 1A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	23.59	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.46	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.47	PASS
DC_66A_n7 1A	15	5+20	DFT-QPSK	M+H	Outer_Full	22.77	PASS



DC_66A_n7 1A	15	5+20	DFT-QPSK	M+H	Inner_Full	23.60	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.31	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.34	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+H	Outer_Full	21.74	PASS
DC_66A_n7 1A	15	5+20	DFT-16QAM	M+H	Inner_Full	22.65	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	21.01	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	21.05	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+H	Outer_Full	21.25	PASS
DC_66A_n7 1A	15	5+20	DFT-64QAM	M+H	Inner_Full	21.09	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.84	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	18.87	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+H	Outer_Full	19.19	PASS
DC_66A_n7 1A	15	5+20	DFT-256QAM	M+H	Inner_Full	19.02	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.34	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.35	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+H	Outer_Full	20.70	PASS
DC_66A_n7 1A	15	5+20	CP-QPSK	M+H	Inner_Full	22.13	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.24	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.34	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+H	Outer_Full	20.74	PASS
DC_66A_n7 1A	15	5+20	CP-16QAM	M+H	Inner_Full	21.58	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	20.02	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.57	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+H	Outer_Full	20.16	PASS
DC_66A_n7 1A	15	5+20	CP-64QAM	M+H	Inner_Full	20.03	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	16.96	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	16.97	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+H	Outer_Full	17.23	PASS
DC_66A_n7 1A	15	5+20	CP-256QAM	M+H	Inner_Full	17.04	PASS



11. EUT and Test Setup Photo

11.1 EUT Photos

Front side



Back side





Right Edge



Left Edge





Top Edge

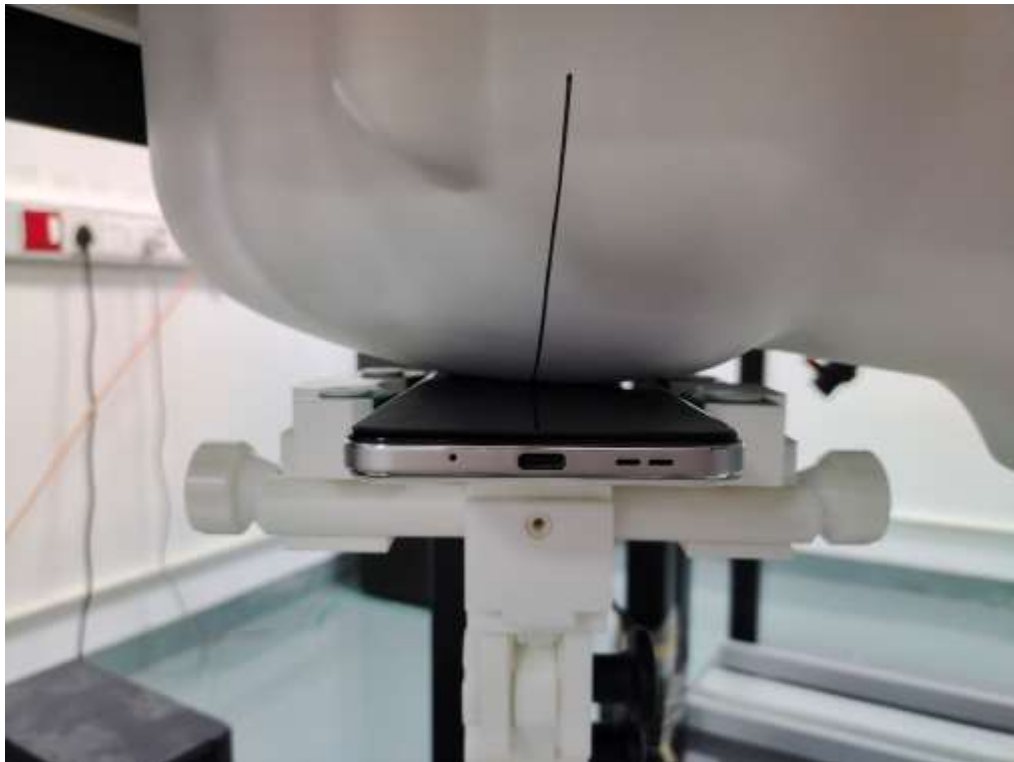


Bottom Edge

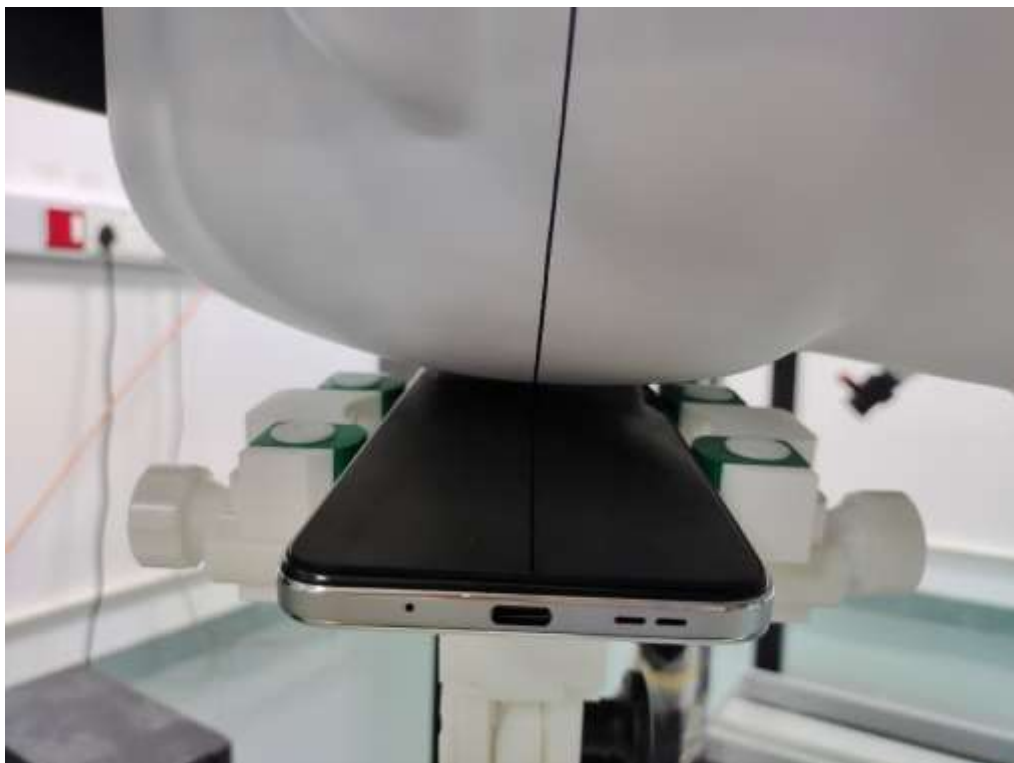


11.2 Setup Photos

Right Touch

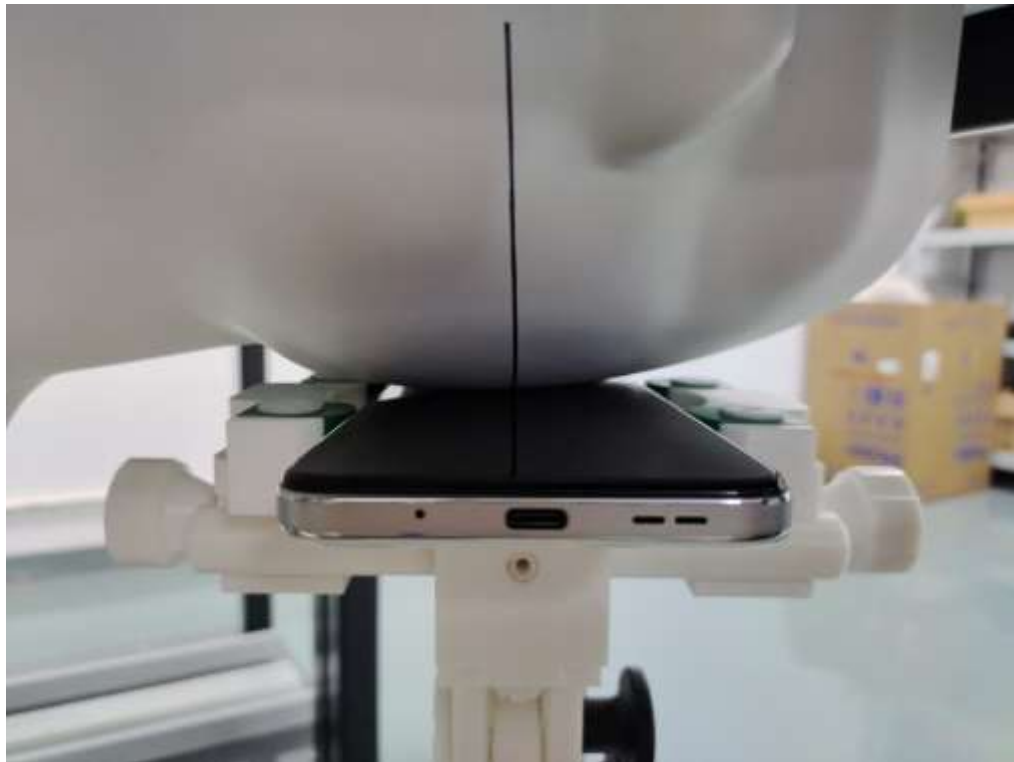


Right Tilt

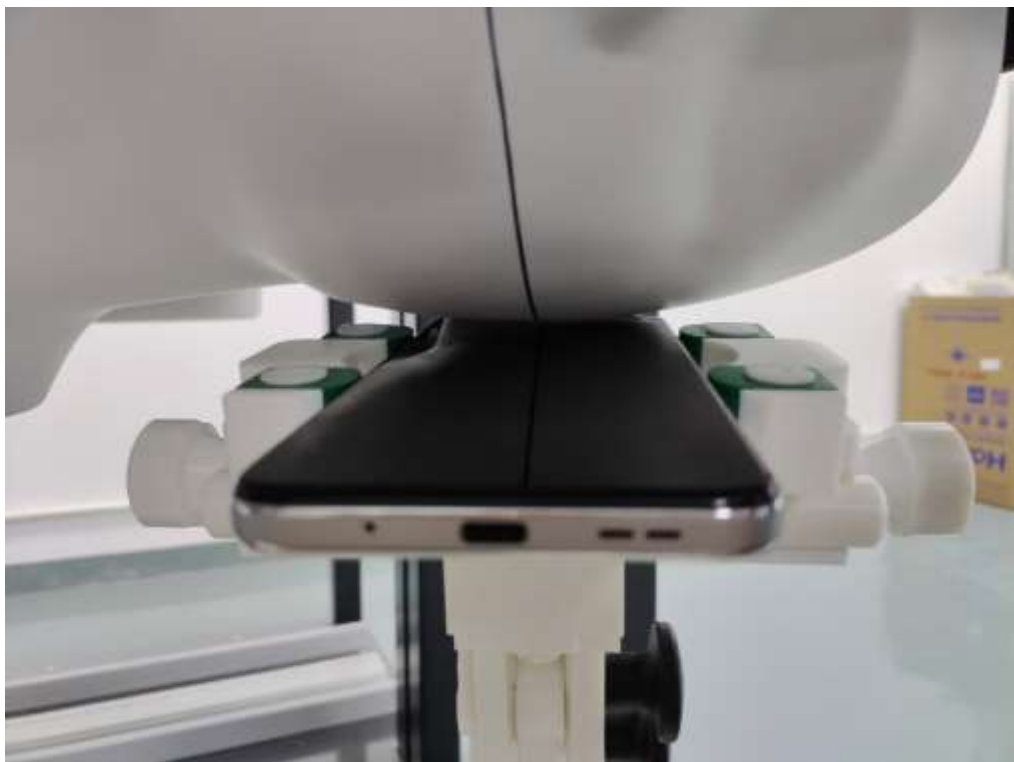




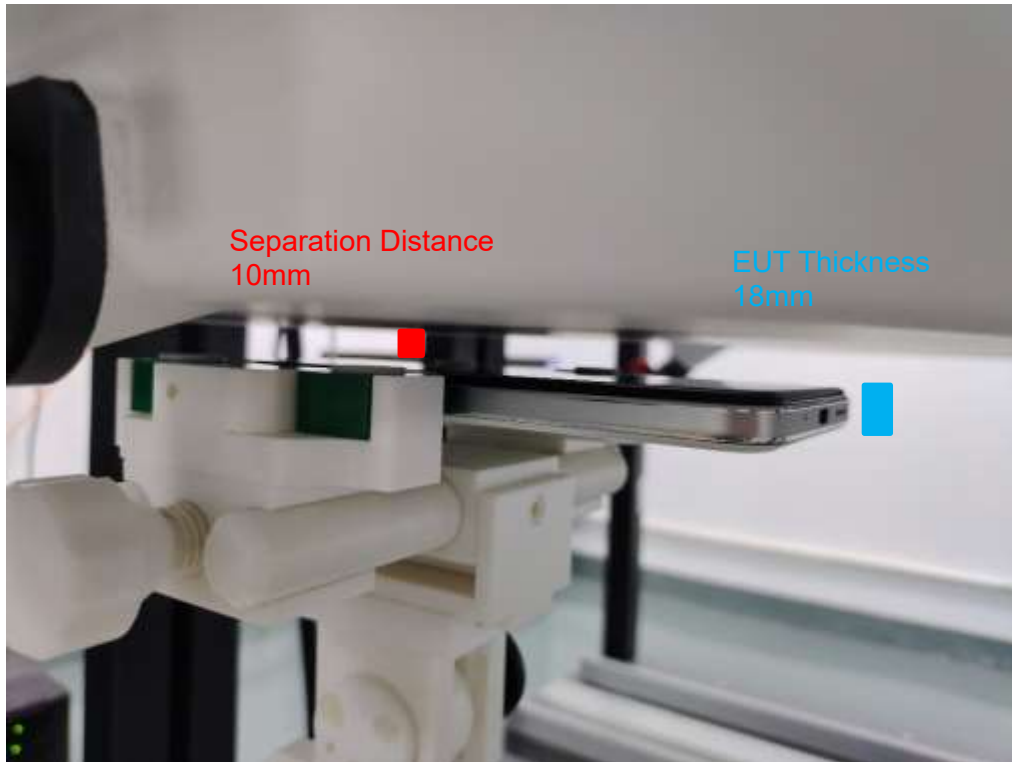
Left Touch



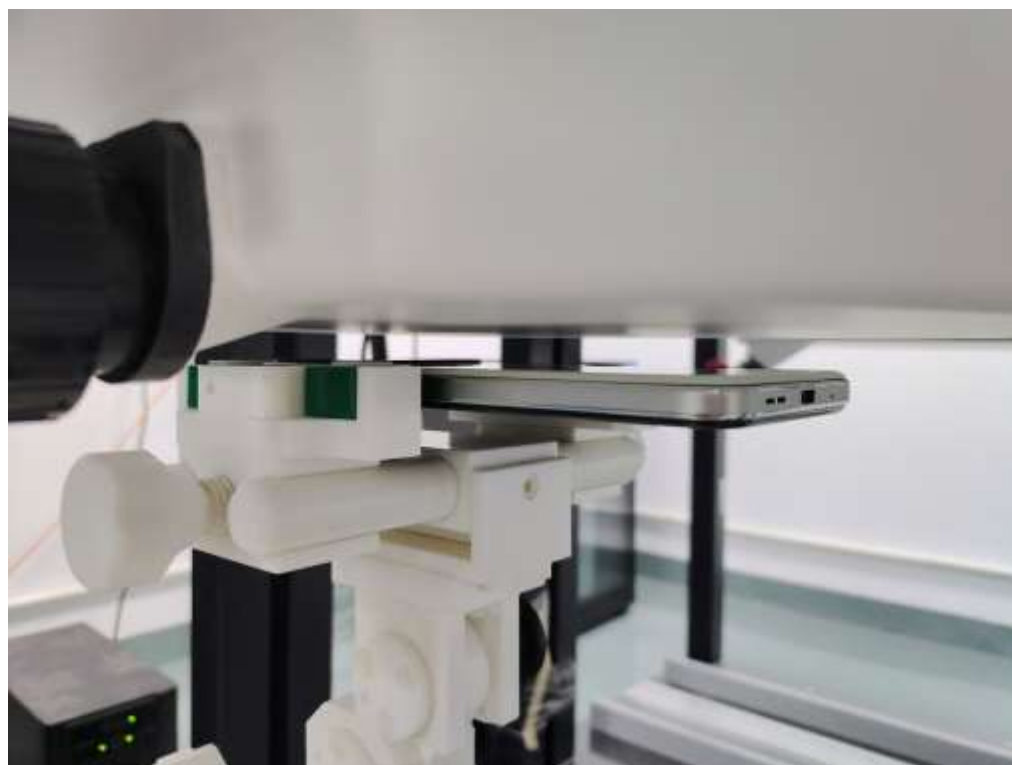
Left Tilt



Body Front side (separation distance is 10mm)

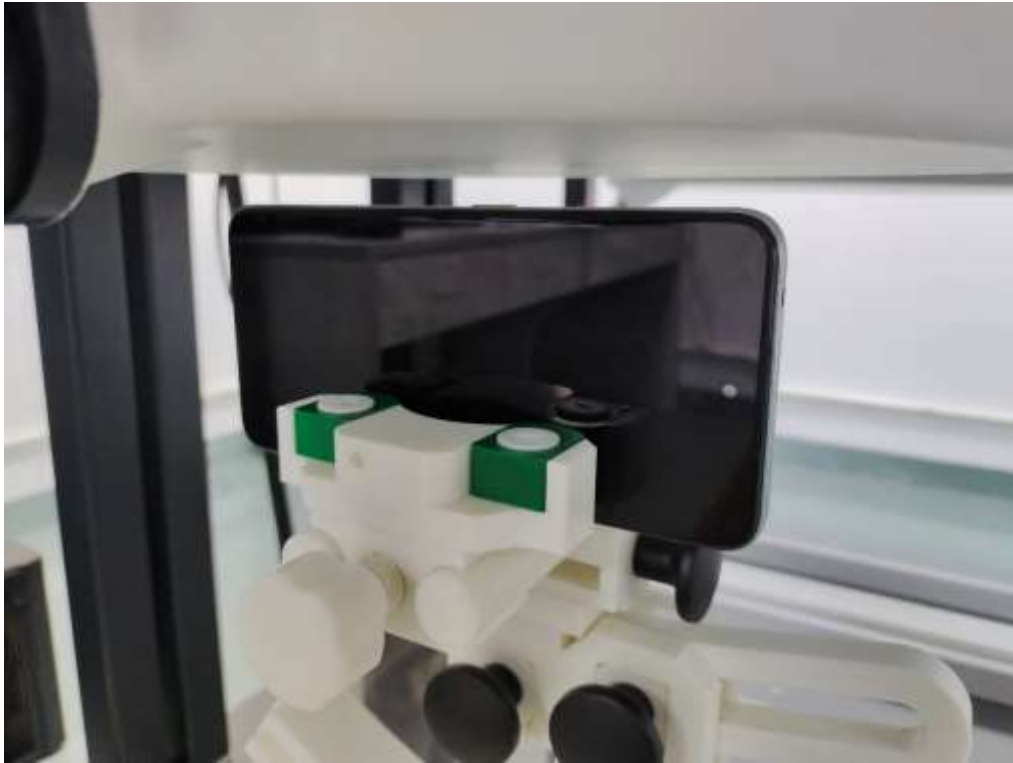


Body Back side (separation distance 10mm)

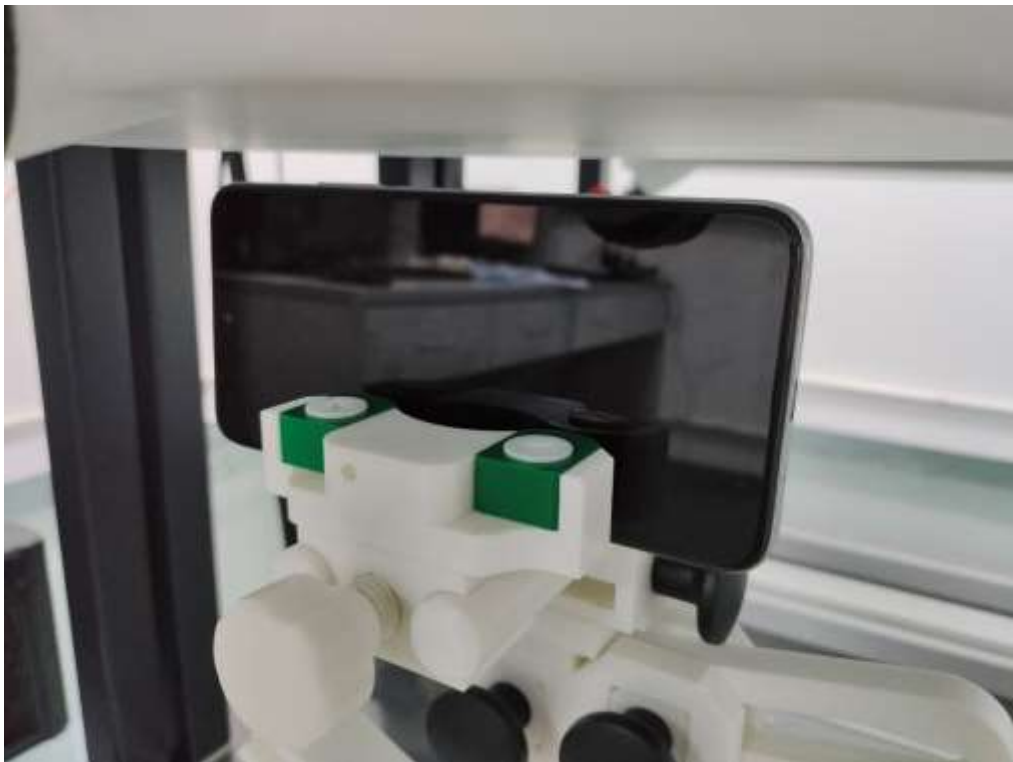




Body Left side (separation distance is 10mm)



Body Right side (separation distance is 10mm)



Body Top side (separation distance is 10mm)



Body Bottom side (separation distance is 10mm)





12. SAR Result Summary

12.1 Head SAR

Band	Model	Test Position	Freq.	SAR (1g) (W/kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
GSM850	Voice	Right Cheek	848.8	0.202	-1.78	33.00	32.69	0.217	1
		Right Tilt	848.8	0.107	-3.15	33.00	32.69	0.115	/
		Left Cheek	848.8	0.182	3.04	33.00	32.69	0.195	/
		Left Tilt	848.8	0.108	-2.04	33.00	32.69	0.116	/
GSM1900	Voice	Right Cheek	1909.8	0.165	-0.04	31.00	30.64	0.179	/
		Right Tilt	1909.8	0.102	3.10	31.00	30.64	0.111	/
		Left Cheek	1909.8	0.179	-1.91	31.00	30.64	0.194	3
		Left Tilt	1909.8	0.100	1.19	31.00	30.64	0.109	/
WCDMA Band II	RMC	Right Cheek	1880	0.271	1.08	24.00	23.61	0.296	/
		Right Tilt	1880	0.159	3.49	24.00	23.61	0.174	/
		Left Cheek	1880	0.319	-0.23	24.00	23.61	0.349	5
		Left Tilt	1880	0.176	2.44	24.00	23.61	0.193	/
WCDMA Band IV	RMC	Right Cheek	1740	0.223	-0.80	23.50	23.19	0.239	7
		Right Tilt	1740	0.145	0.54	23.50	23.19	0.156	/
		Left Cheek	1740	0.195	1.78	23.50	23.19	0.209	/
		Left Tilt	1740	0.093	0.01	23.50	23.19	0.100	/
WCDMA Band V	RMC	Right Cheek	826.4	0.184	-2.07	23.50	23.06	0.204	/
		Right Tilt	826.4	0.100	-0.78	23.50	23.06	0.111	/
		Left Cheek	826.4	0.208	1.11	23.50	23.06	0.230	9
		Left Tilt	826.4	0.121	-2.36	23.50	23.06	0.134	/
2.4G WLAN ANT A	802.11b	Right Cheek	2412	0.083	2.82	15.00	14.59	0.091	/
		Right Tilt	2412	0.141	-2.91	15.00	14.59	0.155	11
		Left Cheek	2412	0.053	3.78	15.00	14.59	0.058	/
		Left Tilt	2412	0.082	-2.54	15.00	14.59	0.090	/
2.4G WLAN ANT B	802.11b	Right Cheek	2412	0.050	-2.77	15.00	14.68	0.054	/
		Right Tilt	2412	0.073	-0.11	15.00	14.68	0.079	/
		Left Cheek	2412	0.069	-3.43	15.00	14.68	0.074	/
		Left Tilt	2412	0.109	0.24	15.00	14.68	0.117	13
2.4G WLAN ANT A MIMO	802.11 n-HT20	Right Cheek	2412	0.068	2.23	20.00	19.89	0.070	/
		Right Tilt	2412	0.110	-2.36	20.00	19.89	0.113	15
		Left Cheek	2412	0.048	-3.09	20.00	19.89	0.049	/
		Left Tilt	2412	0.087	-2.50	20.00	19.89	0.089	/
2.4G WLAN ANT B MIMO	802.11 n-HT20	Right Cheek	2412	0.048	0.82	20.00	19.89	0.049	/
		Right Tilt	2412	0.085	-3.94	20.00	19.89	0.087	/
		Left Cheek	2412	0.069	0.10	20.00	19.89	0.071	/
		Left Tilt	2412	0.120	0.94	20.00	19.89	0.123	17
5.2G WLAN ANT A	802.11n40	Right Cheek	5190.0	0.133	1.49	10.50	9.98	0.150	/
		Right Tilt	5190.0	0.241	-1.63	10.50	9.98	0.272	/
		Left Cheek	5190.0	0.142	-2.23	10.50	9.98	0.160	/
		Left Tilt	5190.0	0.280	0.49	10.50	9.98	0.316	19



5.2G WLAN ANT B	802.11n4 0	Right Cheek	5190.0	0.169	0.72	12.00	11.78	0.178	/
		Right Tilt	5190.0	0.337	-0.84	12.00	11.78	0.355	21
		Left Cheek	5190.0	0.177	3.94	12.00	11.78	0.186	/
		Left Tilt	5190.0	0.317	1.74	12.00	11.78	0.333	/
5.2G WLAN ANT A MIMO	802.11n- HT20	Right Cheek	5190.0	0.085	-2.82	14.50	13.98	0.096	/
		Right Tilt	5190.0	0.132	3.06	14.50	13.98	0.149	/
		Left Cheek	5190.0	0.096	-3.71	14.50	13.98	0.108	/
		Left Tilt	5190.0	0.161	-1.36	14.50	13.98	0.181	23
5.2G WLAN ANT B MIMO	802.11n- HT20	Right Cheek	5190.0	0.084	-0.40	14.50	13.98	0.095	/
		Right Tilt	5190.0	0.129	3.11	14.50	13.98	0.145	25
		Left Cheek	5190.0	0.067	-1.30	14.50	13.98	0.076	/
		Left Tilt	5190.0	0.105	-3.06	14.50	13.98	0.118	/
5.3G WLAN ANT A	802.11a	Right Cheek	5320.0	0.097	-2.32	5.50	5.15	0.105	/
		Right Tilt	5320.0	0.166	0.69	5.50	5.15	0.180	/
		Left Cheek	5320.0	0.134	2.85	5.50	5.15	0.145	/
		Left Tilt	5320.0	0.248	1.59	5.50	5.15	0.269	27
5.3G WLAN ANT B	802.11a	Right Cheek	5260.0	0.063	1.00	6.50	6.09	0.069	/
		Right Tilt	5260.0	0.112	1.77	6.50	6.09	0.123	29
		Left Cheek	5260.0	0.021	-2.74	6.50	6.09	0.023	/
		Left Tilt	5260.0	0.029	3.37	6.50	6.09	0.032	/
5.3G WLAN ANT A MIMO	802.11n- HT20	Right Cheek	5320.0	0.102	-0.21	9.00	8.41	0.117	/
		Right Tilt	5320.0	0.174	3.26	9.00	8.41	0.199	/
		Left Cheek	5320.0	0.123	3.30	9.00	8.41	0.141	/
		Left Tilt	5320.0	0.226	0.12	9.00	8.41	0.259	31
5.3G WLAN ANT B MIMO	802.11n- HT20	Right Cheek	5320.0	0.072	-0.66	9.00	8.41	0.082	/
		Right Tilt	5320.0	0.134	0.07	9.00	8.41	0.153	33
		Left Cheek	5320.0	0.053	1.86	9.00	8.41	0.061	/
		Left Tilt	5320.0	0.083	-1.00	9.00	8.41	0.095	/
5.6G WLAN ANT A	802.11n2 0	Right Cheek	5700.0	0.160	-3.18	5.00	4.55	0.177	/
		Right Tilt	5700.0	0.296	-0.29	5.00	4.55	0.328	35
		Left Cheek	5700.0	0.106	-3.37	5.00	4.55	0.118	/
		Left Tilt	5700.0	0.194	-2.46	5.00	4.55	0.215	/
5.6G WLAN ANT B	802.11n2 0	Right Cheek	5700.0	0.114	3.24	7.00	6.58	0.126	/
		Right Tilt	5700.0	0.211	3.42	7.00	6.58	0.232	37
		Left Cheek	5700.0	0.098	-0.31	7.00	6.58	0.108	/
		Left Tilt	5700.0	0.181	-2.40	7.00	6.58	0.199	/
5.6G WLAN ANT A MIMO	802.11n- HT20	Right Cheek	5700.0	0.091	-0.77	9.00	8.69	0.098	/
		Right Tilt	5700.0	0.157	-0.56	9.00	8.69	0.169	39
		Left Cheek	5700.0	0.059	2.36	9.00	8.69	0.063	/
		Left Tilt	5700.0	0.099	-3.19	9.00	8.69	0.106	/
5.6G WLAN ANT B MIMO	802.11n- HT20	Right Cheek	5700.0	0.145	1.85	9.00	8.69	0.156	/
		Right Tilt	5700.0	0.254	3.18	9.00	8.69	0.273	41
		Left Cheek	5700.0	0.078	-2.04	9.00	8.69	0.084	/



		Left Tilt	5700.0	0.151	0.87	9.00	8.69	0.162	/
5.8G WLAN ANT A	802.11a	Right Cheek	5745.0	0.167	-0.05	2.00	1.81	0.174	/
		Right Tilt	5745.0	0.298	-2.47	2.00	1.81	0.311	43
		Left Cheek	5745.0	0.173	-1.33	2.00	1.81	0.181	/
		Left Tilt	5745.0	0.273	-0.69	2.00	1.81	0.285	/
5.8G WLAN ANT B	802.11n20	Right Cheek	5745.0	0.154	-0.41	5.00	4.54	0.171	/
		Right Tilt	5745.0	0.305	-0.43	5.00	4.54	0.339	45
		Left Cheek	5745.0	0.085	-3.61	5.00	4.54	0.094	/
		Left Tilt	5745.0	0.169	1.93	5.00	4.54	0.188	/
5.8G WLAN ANT A MIMO	802.11n-HT20	Right Cheek	5785.0	0.115	3.03	6.50	6.15	0.125	/
		Right Tilt	5785.0	0.215	-1.68	6.50	6.15	0.233	47
		Left Cheek	5785.0	0.109	-0.47	6.50	6.15	0.118	/
		Left Tilt	5785.0	0.186	-1.36	6.50	6.15	0.202	/
5.8G WLAN ANT B MIMO	802.11n-HT20	Right Cheek	5785.0	0.062	-0.35	6.50	6.15	0.067	/
		Right Tilt	5785.0	0.109	-1.95	6.50	6.15	0.118	49
		Left Cheek	5785.0	0.043	-3.63	6.50	6.15	0.047	/
		Left Tilt	5785.0	0.076	-2.12	6.50	6.15	0.082	/
BT	GFSK	Right Cheek	2441.0	0.018	1.78	7.00	6.42	0.021	/
		Right Tilt	2441.0	0.023	2.01	7.00	6.42	0.026	/
		Left Cheek	2441.0	0.030	2.65	7.00	6.42	0.034	/
		Left Tilt	2441.0	0.044	3.40	7.00	6.42	0.050	51

Band	Mode	Max SAR	WIFI MIMO
		(W/Kg)	
2.4G WLAN	802.11 n-HT20 ANT A	0.113	0.236
	802.11 n-HT20 ANT B	0.123	
5.2G WLAN	802.11 n-HT20 ANT A	0.181	0.326
	802.11 n-HT20 ANT B	0.145	
5.3G WLAN	802.11 n-HT20 ANT A	0.259	0.412
	802.11 n-HT20 ANT B	0.153	
5.6G WLAN	802.11 n-HT20 ANT A	0.169	0.442
	802.11 n-HT20 ANT B	0.273	
5.8G WLAN	802.11 n-HT20 ANT A	0.233	0.351
	802.11 n-HT20 ANT B	0.118	



Band	BW (MHz)	Mod.	RB Size	RB offset	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
LTE Band 2	20M	QPSK	1	0	Right Cheek	1860	0.209	0.28	24.80	24.21	0.239	/
			50	0	Right Cheek	1900	0.190	-1.50	24.00	23.71	0.203	/
			1	0	Right Tilt	1860	0.108	2.51	24.80	24.21	0.124	/
			50	0	Right Tilt	1900	0.099	-3.59	24.00	23.71	0.106	/
			1	0	Left Cheek	1860	0.271	-0.88	24.80	24.21	0.310	53
			50	0	Left Cheek	1900	0.231	-3.11	24.00	23.71	0.247	/
			1	0	Left Tilt	1860	0.146	-2.19	24.80	24.21	0.167	/
			50	0	Left Tilt	1900	0.132	0.05	24.00	23.71	0.141	/
LTE Band 4	20M	QPSK	1	0	Right Cheek	1720	0.181	-0.30	24.50	23.50	0.228	/
			50	0	Right Cheek	1732.5	0.170	-1.02	23.00	22.96	0.172	/
			1	0	Right Tilt	1720	0.094	3.40	24.50	23.50	0.118	/
			50	0	Right Tilt	1732.5	0.086	-3.40	23.00	22.96	0.087	/
			1	0	Left Cheek	1720	0.237	1.58	24.50	23.50	0.298	55
			50	0	Left Cheek	1732.5	0.219	-3.49	23.00	22.96	0.221	/
			1	0	Left Tilt	1720	0.125	-2.98	24.50	23.50	0.157	/
			50	0	Left Tilt	1732.5	0.118	0.12	23.00	22.96	0.119	/
LTE Band 5	10M	QPSK	1	0	Right Cheek	844	0.236	2.79	24.50	24.02	0.264	57
			25	0	Right Cheek	844	0.211	0.61	23.00	22.94	0.214	/
			1	0	Right Tilt	844	0.130	-0.78	24.50	24.02	0.145	/
			25	0	Right Tilt	844	0.119	2.71	23.00	22.94	0.121	/
			1	0	Left Cheek	844	0.228	-2.64	24.50	24.02	0.255	/
			25	0	Left Cheek	844	0.207	-3.08	23.00	22.94	0.210	/
			1	0	Left Tilt	844	0.123	-1.09	24.50	24.02	0.137	/
			25	0	Left Tilt	844	0.116	3.11	23.00	22.94	0.118	/
LTE Band 12	10M	QPSK	1	0	Right Cheek	704	0.247	2.98	25.50	25.03	0.275	/
			25	0	Right Cheek	711	0.199	-3.51	24.50	23.99	0.224	/
			1	0	Right Tilt	704	0.130	-2.42	25.50	25.03	0.145	/
			25	0	Right Tilt	711	0.105	-4.00	24.50	23.99	0.118	/
			1	0	Left Cheek	704	0.274	3.52	25.50	25.03	0.305	59
			25	0	Left Cheek	711	0.244	-2.51	24.50	23.99	0.274	/
			1	0	Left Tilt	704	0.149	-0.14	25.50	25.03	0.166	/
			25	0	Left Tilt	711	0.127	0.93	24.50	23.99	0.143	/
LTE Band 17	10M	QPSK	1	0	Right Cheek	711	0.269	2.90	25.50	25.00	0.302	61
			25	0	Right Cheek	709	0.244	-1.24	24.00	23.91	0.249	/



			1	0	Right Tilt	711	0.136	-1.35	25.50	25.00	0.153	/
			25	0	Right Tilt	709	0.136	2.50	24.00	23.91	0.139	/
			1	0	Left Cheek	711	0.245	-1.55	25.50	25.00	0.275	/
			25	0	Left Cheek	709	0.207	-0.48	24.00	23.91	0.211	/
			1	0	Left Tilt	711	0.138	-1.96	25.50	25.00	0.155	/
			25	0	Left Tilt	709	0.110	2.06	24.00	23.91	0.112	/
LTE Band 30	10M	QPSK	1	0	Right Cheek	2310	0.126	-0.07	23.50	23.11	0.138	/
			25	0	Right Cheek	2310	0.103	0.77	22.50	22.18	0.111	/
			1	0	Right Tilt	2310	0.065	2.06	23.50	23.11	0.071	/
			25	0	Right Tilt	2310	0.070	0.50	22.50	22.18	0.075	/
			1	0	Left Cheek	2310	0.158	2.66	23.50	23.11	0.173	63
			25	0	Left Cheek	2310	0.143	-2.53	22.50	22.18	0.154	/
			1	0	Left Tilt	2310	0.080	-1.77	23.50	23.11	0.088	/
			25	0	Left Tilt	2310	0.074	1.96	22.50	22.18	0.080	/
LTE Band 48	20M	QPSK	1	0	Right Cheek	3625	0.714	2.30	23.00	22.65	0.774	/
			50	0	Right Cheek	3625	0.622	-2.80	22.00	21.73	0.662	/
			1	0	Right Tilt	3560	0.434	-0.28	23.00	20.43	0.784	/
			1	0	Right Tilt	3625	1.017	1.52	23.00	22.65	1.102	65
			1	0	Right Tilt	3690	0.816	2.70	23.00	22.54	0.907	/
			50	0	Right Tilt	3560	0.483	3.84	22.00	20.43	0.693	/
			50	0	Right Tilt	3625	0.848	0.53	22.00	21.73	0.902	/
			50	0	Right Tilt	3690	0.494	3.49	22.00	20.35	0.722	/
			100	0	Right Tilt	3560	0.687	0.52	22.00	21.37	0.794	/
			1	0	Left Cheek	3560	0.412	3.04	23.00	20.43	0.745	/
			1	0	Left Cheek	3625	0.967	-2.00	23.00	22.65	1.048	/
			1	0	Left Cheek	3690	0.787	-3.61	23.00	22.54	0.875	/
			50	0	Left Cheek	3560	0.478	1.11	22.00	20.43	0.686	/
			50	0	Left Cheek	3625	0.781	2.86	22.00	21.73	0.831	/
			50	0	Left Cheek	3690	0.461	2.28	22.00	20.35	0.674	/
			100	0	Left Cheek	3560	0.653	1.79	22.00	21.37	0.755	/
			1	0	Left Tilt	3560	0.425	1.64	23.00	20.43	0.768	/
			1	0	Left Tilt	3625	0.982	1.59	23.00	22.65	1.064	/
			1	0	Left Tilt	3690	0.752	0.58	23.00	22.54	0.836	/
			50	0	Left Tilt	3560	0.479	1.66	22.00	20.43	0.688	/
50	0	Left Tilt	3625	0.804	1.75	22.00	21.73	0.856	/			
50	0	Left Tilt	3690	0.462	0.63	22.00	20.35	0.676	/			
100	0	Left Tilt	3560	0.672	-2.29	22.00	21.37	0.777	/			



LTE Band 66	20M	QPSK	1	0	Right Cheek	1745	0.246	-0.57	25.00	23.95	0.313	/
			50	0	Right Cheek	1770	0.234	2.79	24.00	23.53	0.261	/
			1	0	Right Tilt	1745	0.128	2.03	25.00	23.95	0.163	/
			50	0	Right Tilt	1770	0.125	-3.78	24.00	23.53	0.139	/
			1	0	Left Cheek	1745	0.289	3.53	25.00	23.95	0.368	67
			50	0	Left Cheek	1770	0.243	-0.70	24.00	23.53	0.271	/
			1	0	Left Tilt	1745	0.158	3.99	25.00	23.95	0.201	/
			50	0	Left Tilt	1770	0.132	-0.81	24.00	23.53	0.147	/
LTE Band 71	20M	QPSK	1	0	Right Cheek	680.5	0.154	0.30	24.00	23.63	0.168	/
			50	0	Right Cheek	688	0.145	-3.82	23.00	22.75	0.154	/
			1	0	Right Tilt	680.5	0.084	-0.42	24.00	23.63	0.091	/
			50	0	Right Tilt	688	0.080	-2.74	23.00	22.75	0.085	/
			1	0	Left Cheek	680.5	0.192	-2.09	24.00	23.63	0.209	69
			50	0	Left Cheek	688	0.168	-0.89	23.00	22.75	0.178	/
			1	0	Left Tilt	680.5	0.097	2.44	24.00	23.63	0.106	/
			50	0	Left Tilt	688	0.089	3.17	23.00	22.75	0.094	/



Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N5	20M	DFT_QPSK	1 RB	Right Cheek	839	0.092	1.73	22.00	21.74	0.098	/
			Full RB	Right Cheek	844	0.116	-1.07	23.50	23.00	0.130	/
			1 RB	Right Tilt	839	0.051	-2.28	22.00	21.74	0.054	/
			Full RB	Right Tilt	844	0.063	3.33	23.50	23.00	0.071	/
			1 RB	Left Cheek	839	0.117	3.46	22.00	21.74	0.124	/
			Full RB	Left Cheek	844	0.138	3.07	23.50	23.00	0.155	71
			1 RB	Left Tilt	839	0.071	-2.95	22.00	21.74	0.075	/
			Full RB	Left Tilt	844	0.082	-1.07	23.50	23.00	0.092	/
SA N41	100M	DFT_QPSK	1 RB	Right Cheek	2546.01	0.053	0.49	22.00	21.20	0.064	/
			Full RB	Right Cheek	2592.99	0.060	0.33	23.50	23.20	0.064	/
			1 RB	Right Tilt	2546.01	0.043	-0.33	22.00	21.20	0.052	/
			Full RB	Right Tilt	2592.99	0.035	3.82	23.50	23.20	0.038	/
			1 RB	Left Cheek	2546.01	0.174	-2.30	22.00	21.20	0.209	/
			Full RB	Left Cheek	2592.99	0.196	-2.11	23.50	23.20	0.210	73
			1 RB	Left Tilt	2546.01	0.106	-2.11	22.00	21.20	0.127	/
			Full RB	Left Tilt	2592.99	0.115	-3.75	23.50	23.20	0.123	/
SA N48	100M	DFT_QPSK	1 RB	Right Cheek	3600	0.463	-1.63	21.00	20.67	0.500	/
			Full RB	Right Cheek	3649.98	0.530	0.56	23.00	22.30	0.623	/
			1 RB	Right Tilt	3600	0.681	-2.17	21.00	20.67	0.735	/
			Full RB	Right Tilt	3600	0.652	3.86	23.00	22.16	0.791	/
			Full RB	Right Tilt	3624.99	0.691	-1.67	23.00	22.23	0.825	/
			Full RB	Right Tilt	3649.98	0.790	0.25	23.00	22.30	0.928	/
			1 RB	Left Cheek	3600	0.482	-2.53	21.00	20.67	0.520	/
			Full RB	Left Cheek	3649.98	0.609	3.75	23.00	22.30	0.716	/
			1 RB	Left Tilt	3600	0.706	1.88	21.00	20.67	0.762	/
			Full RB	Left Tilt	3600	0.693	3.69	23.00	22.16	0.841	/
			Full RB	Left Tilt	3624.99	0.722	-2.36	23.00	22.23	0.862	/
			Full RB	Left Tilt	3649.98	0.844	-1.92	23.00	22.30	0.992	75



SA N71	100M	DFT_QPSK	1 RB	Right Cheek	688	0.107	1.53	23.00	22.42	0.122	/
			Full RB	Right Cheek	688	0.128	-2.25	24.00	23.67	0.138	/
			1 RB	Right Tilt	688	0.054	2.12	23.00	22.42	0.062	/
			Full RB	Right Tilt	688	0.072	1.66	24.00	23.67	0.078	/
			1 RB	Left Cheek	688	0.122	3.01	23.00	22.42	0.139	/
			Full RB	Left Cheek	688	0.143	1.39	24.00	23.67	0.154	77
			1 RB	Left Tilt	688	0.074	1.32	23.00	22.42	0.085	/
			Full RB	Left Tilt	688	0.084	-1.17	24.00	23.67	0.091	/
SA N77	100M	DFT_QPSK	1 RB	Right Cheek	3500	0.456	-0.77	21.50	21.32	0.475	/
			Full RB	Right Cheek	3500	0.572	1.95	24.00	23.97	0.576	/
			1 RB	Right Tilt	3500	0.696	1.54	21.50	21.32	0.725	/
			Full RB	Right Tilt	3500	0.788	-2.60	24.00	23.97	0.793	/
			1 RB	Left Cheek	3500	0.485	-1.60	21.50	21.32	0.506	/
			Full RB	Left Cheek	3500	0.626	0.26	24.00	23.97	0.630	/
			1 RB	Left Tilt	3500	0.736	-2.72	21.50	21.32	0.767	/
			Full RB	Left Tilt	3500	0.883	2.46	24.00	23.97	0.889	79



NSA

Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N41	100M	DFT_QPSK	1 RB	Right Cheek	2546.01	0.053	-2.26	22.00	21.20	0.064	/
			Full RB	Right Cheek	2592.99	0.060	-2.41	23.50	23.20	0.064	/
			1 RB	Right Tilt	2546.01	0.043	-3.90	22.00	21.20	0.052	/
			Full RB	Right Tilt	2592.99	0.035	-3.99	23.50	23.20	0.038	/
			1 RB	Left Cheek	2546.01	0.174	-2.20	22.00	21.20	0.209	/
			Full RB	Left Cheek	2592.99	0.196	0.54	23.50	23.20	0.210	73
			1 RB	Left Tilt	2546.01	0.106	-1.48	22.00	21.20	0.127	/
			Full RB	Left Tilt	2592.99	0.115	3.97	23.50	23.20	0.123	/
LTE Band 2	20M	QPSK	1	Right Cheek	1860	0.209	1.44	24.80	24.21	0.239	/
			50	Right Cheek	1900	0.190	-1.39	24.00	23.71	0.203	/
			1	Right Tilt	1860	0.108	1.06	24.80	24.21	0.124	/
			50	Right Tilt	1900	0.099	-3.20	24.00	23.71	0.106	/
			1	Left Cheek	1860	0.271	-3.72	24.80	24.21	0.310	63
			50	Left Cheek	1900	0.231	0.02	24.00	23.71	0.247	/
			1	Left Tilt	1860	0.146	-3.08	24.80	24.21	0.167	/
			50	Left Tilt	1900	0.132	2.41	24.00	23.71	0.141	/
LTE Band 66	20M	QPSK	1	Right Cheek	1745	0.246	0.65	25.00	23.95	0.313	/
			50	Right Cheek	1770	0.234	1.15	24.00	23.53	0.261	/
			1	Right Tilt	1745	0.128	3.10	25.00	23.95	0.163	/
			50	Right Tilt	1770	0.125	2.17	24.00	23.53	0.139	/
			1	Left Cheek	1745	0.289	1.59	25.00	23.95	0.368	67
			50	Left Cheek	1770	0.243	3.00	24.00	23.53	0.271	/
			1	Left Tilt	1745	0.158	-3.15	25.00	23.95	0.201	/
			50	Left Tilt	1770	0.132	-2.14	24.00	23.53	0.147	/

Band	Mode	Max SAR	NSA N41+ LTE B2
		(W/Kg)	
NSA N41+ LTE B2	SA N41	0.210	0.520
	LTE B2	0.310	
Band	Mode	Max SAR	NSA N41+ LTE B66
		(W/Kg)	
NSA N41+ LTE B66	SA N41	0.210	0.578
	LTE B66	0.368	



Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N71	100M	DFT_QPSK	1 RB	Right Cheek	688	0.107	-3.39	23.00	22.42	0.122	/
			Full RB	Right Cheek	688	0.128	2.13	24.00	23.67	0.138	/
			1 RB	Right Tilt	688	0.054	0.05	23.00	22.42	0.062	/
			Full RB	Right Tilt	688	0.072	-2.36	24.00	23.67	0.078	/
			1 RB	Left Cheek	688	0.122	-3.15	23.00	22.42	0.139	/
			Full RB	Left Cheek	688	0.143	-1.46	24.00	23.67	0.154	77
			1 RB	Left Tilt	688	0.074	2.79	23.00	22.42	0.085	/
			Full RB	Left Tilt	688	0.084	-3.61	24.00	23.67	0.091	/
LTE Band 2	20M	QPSK	1	Right Cheek	1860	0.209	1.89	24.80	24.21	0.239	/
			50	Right Cheek	1900	0.190	0.07	24.00	23.71	0.203	/
			1	Right Tilt	1860	0.108	0.98	24.80	24.21	0.124	/
			50	Right Tilt	1900	0.099	-1.82	24.00	23.71	0.106	/
			1	Left Cheek	1860	0.271	2.39	24.80	24.21	0.310	63
			50	Left Cheek	1900	0.231	2.62	24.00	23.71	0.247	/
			1	Left Tilt	1860	0.146	1.24	24.80	24.21	0.167	/
			50	Left Tilt	1900	0.132	-3.87	24.00	23.71	0.141	/
LTE Band 66	20M	QPSK	1	Right Cheek	1745	0.246	2.85	25.00	23.95	0.313	/
			50	Right Cheek	1770	0.234	2.74	24.00	23.53	0.261	/
			1	Right Tilt	1745	0.128	-0.95	25.00	23.95	0.163	/
			50	Right Tilt	1770	0.125	-0.77	24.00	23.53	0.139	/
			1	Left Cheek	1745	0.289	-0.50	25.00	23.95	0.368	67
			50	Left Cheek	1770	0.243	1.68	24.00	23.53	0.271	/
			1	Left Tilt	1745	0.158	-0.22	25.00	23.95	0.201	/
			50	Left Tilt	1770	0.132	-1.11	24.00	23.53	0.147	/

Band	Mode	Max SAR (W/Kg)	NSA N71+ LTE B2
		0.154	
NSA N71+ LTE B2	SA N71	0.154	0.464
	LTE B2	0.310	
Band	Mode	Max SAR (W/Kg)	NSA N71+ LTE B66
		0.154	
NSA N71+ LTE B66	SA N71	0.154	0.522
	LTE B66	0.368	

Note:

- Per KDB 447498 D01, the reported SAR is the measured SAR value adjusted for maximum tune-up tolerance.
 - Tune-up scaling Factor = tune-up limit power (mW) / EUT RF power (mW), where tune-up limit is the maximum rated power among all production units.
 - Scaled SAR(W/kg) = Measured SAR(W/kg) *Tune-up Scaling Factor
- Per KDB 865664 D01, Repeated measurement is not required when the original highest measured SAR is <0.80 W/kg.



12.2 Body-worn and Hotspot SAR

Band	Model	Test Position	Freq.	SAR (1g) (W/kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
GSM850	EGPRS (8PSK, 2-Slot)	Front Side	848.8	0.195	-3.30	27.50	27.39	0.200	/
		Back Side	848.8	0.200	0.78	27.50	27.39	0.205	2
		Left Side	848.8	0.117	0.57	27.50	27.39	0.120	/
		Right Side	848.8	0.197	-3.55	27.50	27.39	0.202	/
		Top Side	848.8	0.068	0.13	27.50	27.39	0.070	/
		Bottom Side	848.8	0.192	0.99	27.50	27.39	0.197	/
GSM1900	EGPRS (8PSK, 2-Slot)	Front Side	1880	0.339	0.65	27.50	27.36	0.350	/
		Back Side	1880	0.454	3.46	27.50	27.36	0.469	/
		Left Side	1880	0.203	0.84	27.50	27.36	0.210	/
		Right Side	1880	0.078	1.74	27.50	27.36	0.081	/
		Top Side	1880	0.035	-2.81	27.50	27.36	0.036	/
		Bottom Side	1880	0.499	-0.61	27.50	27.36	0.515	4
WCDMA Band II	RMC	Front Side	1880	0.303	0.90	24.00	23.61	0.331	/
		Back Side	1880	0.470	3.85	24.00	23.61	0.514	/
		Left Side	1880	0.242	3.20	24.00	23.61	0.265	/
		Right Side	1880	0.097	-3.23	24.00	23.61	0.106	/
		Bottom Side	1880	0.581	-0.83	24.00	23.61	0.636	6
WCDMA Band IV	RMC	Front Side	1740	0.452	-0.99	23.50	23.19	0.485	/
		Back Side	1740	0.459	-1.90	23.50	23.19	0.493	/
		Left Side	1740	0.226	3.40	23.50	23.19	0.243	/
		Right Side	1740	0.086	-1.87	23.50	23.19	0.092	/
		Bottom Side	1713	0.585	0.50	23.50	22.80	0.687	/
		Bottom Side	1740	0.785	-1.07	23.50	23.19	0.843	8
		Bottom Side	1752	0.651	-0.34	23.50	23.16	0.704	/
WCDMA Band V	RMC	Front Side	826.4	0.111	1.70	23.50	23.06	0.123	/
		Back Side	826.4	0.204	-2.95	23.50	23.06	0.226	10
		Left Side	826.4	0.115	-0.39	23.50	23.06	0.127	/
		Right Side	826.4	0.186	-3.03	23.50	23.06	0.206	/
		Bottom Side	826.4	0.195	3.19	23.50	23.06	0.216	/
2.4G WLAN ANT A	802.11b	Front Side	2412	0.073	-3.06	15.00	14.59	0.080	/
		Back Side	2412	0.118	1.17	15.00	14.59	0.130	12
		Right Side	2412	0.071	-0.83	15.00	14.59	0.078	/
		Top Side	2412	0.064	-3.27	15.00	14.59	0.070	/
2.4G WLAN ANT B	802.11b	Front Side	2412	0.043	-3.05	15.00	14.68	0.046	/
		Back Side	2412	0.049	-3.45	15.00	14.68	0.053	/
		Right Side	2412	0.157	-1.61	15.00	14.68	0.169	14
		Top Side	2412	0.047	-3.97	15.00	14.68	0.051	/
2.4G WLAN ANT A MIMO	802.11 n-HT20	Front Side	2412	0.069	-0.93	20.00	19.89	0.071	/
		Back Side	2412	0.113	0.84	20.00	19.89	0.116	16
		Left Side	2412	0.024	-1.08	20.00	19.89	0.025	/
		Right Side	2412	0.056	-2.52	20.00	19.89	0.057	/
		Top Side	2412	0.049	0.15	20.00	19.89	0.050	/
2.4G WLAN ANT B MIMO	802.11 n-HT20	Front Side	2412	0.084	1.64	20.00	19.89	0.086	18
		Back Side	2412	0.079	-0.55	20.00	19.89	0.081	/
		Left Side	2412	0.024	3.23	20.00	19.89	0.025	/



		Right Side	2412	0.056	2.48	20.00	19.89	0.057	/
		Top Side	2412	0.035	1.80	20.00	19.89	0.036	/
5.2G WLAN ANT A	802.11 n-HT40	Front Side	5190.0	0.184	0.11	10.50	9.98	0.207	20
		Back Side	5190.0	0.135	-0.73	10.50	9.98	0.152	/
		Right Side	5190.0	0.172	1.32	10.50	9.98	0.194	/
		Top Side	5190.0	0.114	-2.91	10.50	9.98	0.129	/
5.2G WLAN ANT B	802.11 n-HT40	Front Side	5190.0	0.055	-3.59	12.00	11.78	0.058	/
		Back Side	5190.0	0.064	2.74	12.00	11.78	0.067	/
		Right Side	5190.0	0.319	3.92	12.00	11.78	0.336	22
		Top Side	5190.0	0.137	1.04	12.00	11.78	0.144	/
5.2G WLAN ANT A MIMO	802.11 n-HT20	Front Side	5190.0	0.121	-0.21	14.50	13.98	0.136	24
		Back Side	5190.0	0.082	-0.43	14.50	13.98	0.092	/
		Left Side	5190.0	0.032	-1.53	14.50	13.98	0.036	/
		Right Side	5190.0	0.097	-0.17	14.50	13.98	0.109	/
		Top Side	5190.0	0.069	1.97	14.50	13.98	0.078	/
5.2G WLAN ANT B MIMO	802.11 n-HT20	Front Side	5190.0	0.082	-0.96	14.50	13.98	0.092	/
		Back Side	5190.0	0.092	1.76	14.50	13.98	0.104	/
		Left Side	5190.0	0.038	-1.38	14.50	13.98	0.043	/
		Right Side	5190.0	0.128	3.51	14.50	13.98	0.144	26
		Top Side	5190.0	0.099	2.88	14.50	13.98	0.112	/
5.3G WLAN ANT A	802.11a	Front Side	5320.0	0.143	3.66	5.50	5.15	0.155	/
		Back Side	5320.0	0.131	-1.05	5.50	5.15	0.142	/
		Right Side	5320.0	0.108	2.56	5.50	5.15	0.117	/
		Top Side	5320.0	0.166	-3.69	5.50	5.15	0.180	28
5.3G WLAN ANT B	802.11a	Front Side	5260.0	0.060	-2.25	6.50	6.09	0.066	/
		Back Side	5260.0	0.103	3.56	6.50	6.09	0.113	/
		Right Side	5260.0	0.351	2.59	6.50	6.09	0.386	30
		Top Side	5260.0	0.062	-0.42	6.50	6.09	0.068	/
5.3G WLAN ANT A MIMO	802.11 n-HT20	Front Side	5320.0	0.107	-3.68	9.00	8.41	0.123	/
		Back Side	5320.0	0.099	3.11	9.00	8.41	0.113	/
		Left Side	5320.0	0.033	0.66	9.00	8.41	0.038	/
		Right Side	5320.0	0.068	-0.59	9.00	8.41	0.078	/
		Top Side	5320.0	0.123	-3.85	9.00	8.41	0.141	32
5.3G WLAN ANT B MIMO	802.11 n-HT20	Front Side	5320.0	0.054	-1.58	9.00	8.41	0.062	/
		Back Side	5320.0	0.078	-1.18	9.00	8.41	0.089	/
		Left Side	5320.0	0.015	-3.62	9.00	8.41	0.017	/
		Right Side	5320.0	0.107	0.35	9.00	8.41	0.123	34
		Top Side	5320.0	0.052	2.61	9.00	8.41	0.060	/
5.6G WLAN ANT A	802.11 n-HT20	Front Side	5700.0	0.074	-2.63	5.00	4.55	0.082	/
		Back Side	5700.0	0.172	0.19	5.00	4.55	0.191	36
		Right Side	5700.0	0.143	-1.52	5.00	4.55	0.159	/
		Top Side	5700.0	0.119	-2.46	5.00	4.55	0.132	/
5.6G WLAN ANT B	802.11 n-HT20	Front Side	5700.0	0.074	-2.35	7.00	6.58	0.082	/
		Back Side	5700.0	0.097	-2.84	7.00	6.58	0.107	/
		Right Side	5700.0	0.284	-1.19	7.00	6.58	0.313	38
		Top Side	5700.0	0.131	0.24	7.00	6.58	0.144	/
5.6G WLAN ANT A MIMO	802.11 n-HT20	Front Side	5700.0	0.042	-0.23	9.00	8.69	0.045	/
		Back Side	5700.0	0.073	-2.51	9.00	8.69	0.078	40
		Right Side	5700.0	0.068	0.95	9.00	8.69	0.073	/
		Top Side	5700.0	0.067	-3.98	9.00	8.69	0.072	/



5.6G WLAN ANT B MIMO	802.11 n-HT20	Front Side	5700.0	0.062	-1.67	9.00	8.69	0.067	/
		Back Side	5700.0	0.074	0.78	9.00	8.69	0.079	/
		Right Side	5700.0	0.094	-0.12	9.00	8.69	0.101	42
		Top Side	5700.0	0.089	1.74	9.00	8.69	0.096	/
5.8G WLAN ANT A	802.11a	Front Side	5745.0	0.302	0.34	2.00	1.81	0.316	/
		Back Side	5745.0	0.318	-1.10	2.00	1.81	0.332	44
		Right Side	5745.0	0.239	-1.29	2.00	1.81	0.250	/
		Top Side	5745.0	0.275	-2.34	2.00	1.81	0.287	/
5.8G WLAN ANT B	802.11 n-HT20	Front Side	5745.0	0.130	1.25	5.00	4.54	0.145	/
		Back Side	5745.0	0.174	3.97	5.00	4.54	0.193	/
		Right Side	5745.0	0.296	-2.97	5.00	4.54	0.329	46
		Top Side	5745.0	0.135	0.88	5.00	4.54	0.150	/
5.8G WLAN ANT A MIMO	802.11 n-HT20	Front Side	5785.0	0.091	3.74	6.50	6.15	0.099	/
		Back Side	5785.0	0.113	2.23	6.50	6.15	0.122	48
		Right Side	5785.0	0.066	0.68	6.50	6.15	0.072	/
		Top Side	5785.0	0.085	1.16	6.50	6.15	0.092	/
5.8G WLAN ANT B MIMO	802.11 n-HT20	Front Side	5785.0	0.107	2.49	6.50	6.15	0.116	/
		Back Side	5785.0	0.135	0.65	6.50	6.15	0.146	/
		Right Side	5785.0	0.179	1.68	6.50	6.15	0.194	50
		Top Side	5785.0	0.143	2.49	6.50	6.15	0.155	/
BT	GFSK	Front Side	2441.0	0.030	3.82	7.00	6.42	0.034	/
		Back Side	2441.0	0.085	-3.51	7.00	6.42	0.097	52
		Right Side	2441.0	0.059	0.43	7.00	6.42	0.067	/
		Top Side	2441.0	0.029	-2.72	7.00	6.42	0.033	/

Band	Mode	Max SAR	WIFI MIMO
		(W/Kg)	
2.4G WLAN	802.11 n-HT20 ANT A	0.116	0.202
	802.11 n-HT20 ANT B	0.086	
5.2G WLAN	802.11 n-HT20 ANT A	0.136	0.28
	802.11 n-HT20 ANT B	0.144	
5.3G WLAN	802.11 n-HT20 ANT A	0.141	0.264
	802.11 n-HT20 ANT B	0.123	
5.6G WLAN	802.11 n-HT20 ANT A	0.078	0.179
	802.11 n-HT20 ANT B	0.101	
5.8G WLAN	802.11 n-HT20 ANT A	0.122	0.316
	802.11 n-HT20 ANT B	0.194	



Band	BW (MHz)	Mod.	RB Size	RB offset	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
LTE Band 2	20M	QPSK	1	0	Front side	1860	0.347	-3.99	24.00	23.71	0.371	/
			50	0	Front side	1900	0.313	-2.73	24.80	24.21	0.359	/
			1	0	Back Side	1860	0.448	-3.13	25.00	24.21	0.537	/
			50	0	Back Side	1900	0.406	-3.35	24.00	23.71	0.434	/
			1	0	Left Side	1860	0.256	-0.21	25.00	24.21	0.307	/
			50	0	Left Side	1900	0.231	0.83	24.00	23.71	0.247	/
			1	0	Right Side	1860	0.082	0.00	25.00	24.21	0.098	/
			50	0	Right Side	1900	0.061	-0.03	24.00	23.71	0.065	/
			1	0	Bottom Side	1860	0.582	3.60	24.80	24.21	0.667	54
			1	0	Bottom Side	1880	0.517	-0.62	24.80	24.19	0.595	/
			1	0	Bottom Side	1900	0.483	3.58	24.80	24.04	0.575	/
			50	0	Bottom Side	1900	0.531	0.77	24.00	23.71	0.568	/
LTE Band 4	20M	QPSK	1	0	Front side	1720	0.416	-1.06	24.50	23.50	0.524	/
			50	0	Front side	1732.5	0.357	3.68	23.00	22.96	0.360	/
			1	0	Back Side	1720	0.466	2.45	24.50	23.50	0.587	/
			50	0	Back Side	1732.5	0.423	-2.87	23.00	22.96	0.427	/
			1	0	Left Side	1720	0.248	1.38	24.50	23.50	0.312	/
			50	0	Left Side	1732.5	0.219	-3.73	23.00	22.96	0.221	/
			1	0	Right Side	1720	0.094	-1.69	24.50	23.50	0.118	/
			50	0	Right Side	1732.5	0.094	1.56	23.00	22.96	0.095	/
			1	0	Bottom Side	1720	0.868	3.78	24.50	23.50	1.093	5
			1	0	Bottom Side	1745	0.770	3.88	24.50	23.46	0.978	/
			1	0	Bottom Side	1745	0.611	-1.90	24.50	22.79	0.906	/
			50	0	Bottom Side	1732.5	0.786	-2.75	23.00	22.96	0.793	/
LTE Band 5	10M	QPSK	1	0	Front side	844	0.141	-0.57	24.50	24.02	0.157	/
			25	0	Front side	844	0.113	-3.10	23.00	22.94	0.115	/
			1	0	Back Side	844	0.201	-0.32	24.50	24.02	0.224	58
			25	0	Back Side	844	0.185	-1.18	23.00	22.94	0.188	/
			1	0	Left Side	844	0.045	-0.36	24.50	24.02	0.050	/
			25	0	Left Side	844	0.036	2.74	23.00	22.94	0.037	/
			1	0	Right Side	844	0.184	-2.61	24.50	24.02	0.206	/



			25	0	Right Side	844	0.163	-2.03	23.00	22.94	0.165	/
			1	0	Bottom Side	844	0.186	2.26	24.50	24.02	0.208	/
			25	0	Bottom Side	844	0.171	0.66	23.00	22.94	0.173	/
LTE Band 12	10M	QPSK	1	0	Front side	704	0.299	3.54	25.50	25.03	0.333	60
			25	0	Front side	711	0.265	-3.28	24.50	23.99	0.298	/
			1	0	Back Side	704	0.264	3.38	25.50	25.03	0.294	/
			25	0	Back Side	711	0.238	0.01	24.50	23.99	0.268	/
			1	0	Left Side	704	0.060	2.27	25.50	25.03	0.067	/
			25	0	Left Side	711	0.042	2.55	24.50	23.99	0.047	/
			1	0	Right Side	704	0.249	-0.81	25.50	25.03	0.277	/
			25	0	Right Side	711	0.237	2.55	24.50	23.99	0.267	/
			1	0	Bottom Side	704	0.136	-1.03	25.50	25.03	0.152	/
			25	0	Bottom Side	711	0.110	-2.35	24.50	23.99	0.124	/
LTE Band 17	10M	QPSK	1	0	Front side	711	0.302	-1.99	25.50	25.00	0.339	/
			25	0	Front side	709	0.256	1.58	24.00	23.91	0.261	/
			1	0	Back Side	711	0.310	2.57	25.50	25.00	0.348	62
			25	0	Back Side	709	0.279	1.91	24.00	23.91	0.285	/
			1	0	Left Side	711	0.067	-3.45	25.50	25.00	0.075	/
			25	0	Left Side	709	0.047	-3.11	24.00	23.91	0.048	/
			1	0	Right Side	711	0.275	3.32	25.50	25.00	0.309	/
			25	0	Right Side	709	0.247	3.93	24.00	23.91	0.252	/
			1	0	Bottom Side	711	0.143	2.89	25.50	25.00	0.160	/
			25	0	Bottom Side	709	0.118	-1.05	24.00	23.91	0.120	/
LTE Band 30	10M	QPSK	1	0	Front side	2310	0.271	-1.40	23.50	23.11	0.296	64
			25	0	Front side	2310	0.231	-2.28	22.50	22.18	0.249	/
			1	0	Back Side	2310	0.239	2.59	23.50	23.11	0.261	/
			25	0	Back Side	2310	0.203	2.48	22.50	22.18	0.219	/
			1	0	Left Side	2310	0.090	2.20	23.50	23.11	0.098	/
			25	0	Left Side	2310	0.092	-3.75	22.50	22.18	0.099	/
			1	0	Right Side	2310	0.138	1.01	23.50	23.11	0.151	/
			25	0	Right Side	2310	0.120	-1.48	22.50	22.18	0.129	/
			1	0	Bottom Side	2310	0.254	3.08	23.50	23.11	0.278	/
			25	0	Bottom Side	2310	0.214	-2.19	22.50	22.18	0.230	/



LTE Band 48	20M	QPSK	1	0	Front side	3625	0.359	-1.00	23.00	22.65	0.389	/
			50	0	Front side	3625	0.319	2.79	22.00	21.73	0.339	/
			1	0	Back Side	3625	0.266	0.27	23.00	22.65	0.288	/
			50	0	Back Side	3625	0.226	3.91	22.00	21.73	0.240	/
			1	0	Left Side	3625	0.087	-2.58	23.00	22.65	0.094	/
			50	0	Left Side	3625	0.083	3.72	22.00	21.73	0.088	/
			1	0	Right Side	3625	0.148	0.03	23.00	22.65	0.160	/
			50	0	Right Side	3625	0.122	-0.54	22.00	21.73	0.130	/
			1	0	Bottom Side	3625	0.607	-0.12	23.00	22.65	0.658	66
			50	0	Bottom Side	3625	0.529	-1.93	22.00	21.73	0.563	/
LTE Band 66	20M	QPSK	1	0	Front side	1745	0.382	-1.73	25.00	23.95	0.486	/
			50	0	Front side	1770	0.337	-1.28	24.00	23.53	0.376	/
			1	0	Back Side	1745	0.468	2.79	25.00	23.95	0.596	/
			50	0	Back Side	1770	0.425	-1.53	24.00	23.53	0.474	/
			1	0	Left Side	1745	0.129	-2.86	25.00	23.95	0.164	/
			50	0	Left Side	1770	0.100	0.19	24.00	23.53	0.111	/
			1	0	Right Side	1745	0.412	1.19	25.00	23.95	0.525	/
			50	0	Right Side	1770	0.354	2.94	24.00	23.53	0.394	/
			1	0	Bottom Side	1745	0.571	1.64	25.00	23.95	0.727	68
			50	0	Bottom Side	1770	0.507	-0.81	24.00	23.53	0.565	/
LTE Band 71	20M	QPSK	1	0	Front side	680.5	0.176	1.29	24.00	23.63	0.192	/
			50	0	Front side	688	0.156	3.06	23.00	22.75	0.165	/
			1	0	Back Side	680.5	0.288	0.64	24.00	23.63	0.314	70
			50	0	Back Side	688	0.258	0.42	23.00	22.75	0.273	/
			1	0	Left Side	680.5	0.210	3.43	24.00	23.63	0.229	/
			50	0	Left Side	688	0.143	2.14	23.00	22.75	0.151	/
			1	0	Right Side	680.5	0.261	-2.77	24.00	23.63	0.284	/
			50	0	Right Side	688	0.218	3.27	23.00	22.75	0.231	/
			1	0	Bottom Side	680.5	0.074	3.33	24.00	23.63	0.081	/
			50	0	Bottom Side	688	0.070	-2.57	23.00	22.75	0.074	/



Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N5	20M	DFT_QPSK	1 RB	Front Side	839	0.112	-2.07	22.00	21.74	0.119	/
			Full RB	Front Side	844	0.126	-1.85	23.50	23.00	0.141	/
			1 RB	Back Side	839	0.147	-1.68	22.00	21.74	0.156	/
			Full RB	Back Side	844	0.172	2.97	23.50	23.00	0.193	72
			1 RB	Left Edge	839	0.105	2.33	22.00	21.74	0.111	/
			Full RB	Left Edge	844	0.126	0.88	23.50	23.00	0.141	/
			1 RB	Right Edge	839	0.141	3.96	22.00	21.74	0.150	/
			Full RB	Right Edge	844	0.153	-3.58	23.50	23.00	0.172	/
			1 RB	Bottom Edge	839	0.119	2.42	22.00	21.74	0.126	/
			Full RB	Bottom Edge	844	0.137	-0.23	23.50	23.00	0.154	/
SA N41	100M	DFT_QPSK	1 RB	Front Side	2546.01	0.232	2.91	22.00	21.20	0.279	/
			Full RB	Front Side	2592.99	0.278	-2.01	23.50	23.20	0.298	/
			1 RB	Back Side	2546.01	0.353	0.25	22.00	21.20	0.424	/
			Full RB	Back Side	2592.99	0.430	-0.37	23.50	23.20	0.461	74
			1 RB	Left Edge	2546.01	0.253	0.04	22.00	21.20	0.304	/
			Full RB	Left Edge	2592.99	0.313	2.21	23.50	23.20	0.335	/
			1 RB	Right Edge	2546.01	0.075	-2.86	22.00	21.20	0.090	/
			Full RB	Right Edge	2592.99	0.097	-1.59	23.50	23.20	0.104	/
			1 RB	Bottom Edge	2546.01	0.254	0.71	22.00	21.20	0.305	/
			Full RB	Bottom Edge	2592.99	0.314	-1.75	23.50	23.20	0.336	/
SA N48	100M	DFT_QPSK	1 RB	Front Side	3600	0.209	-1.12	21.00	20.67	0.225	/
			Full RB	Front Side	3649.98	0.246	3.89	23.00	22.30	0.289	/
			1 RB	Back Side	3600	0.316	2.83	21.00	20.67	0.341	/
			Full RB	Back Side	3649.98	0.373	-3.78	23.00	22.30	0.438	/
			1 RB	Left Edge	3600	0.225	-2.49	21.00	20.67	0.243	/
			Full RB	Left Edge	3649.98	0.275	-1.94	23.00	22.30	0.323	/
			1 RB	Right Edge	3600	0.202	3.07	21.00	20.67	0.218	/
			Full RB	Right Edge	3649.98	0.248	-1.72	23.00	22.30	0.291	/
			1 RB	Top Side	3600	0.467	2.94	21.00	20.67	0.504	/
			Full RB	Top Side	3649.98	0.574	-2.93	23.00	22.30	0.674	76



SA N71	100M	DFT_QPSK	1 RB	Front Side	688	0.118	-3.09	23.00	22.42	0.135	/
			Full RB	Front Side	688	0.142	0.02	24.00	23.67	0.153	/
			1 RB	Back Side	688	0.124	-0.58	23.00	22.42	0.142	/
			Full RB	Back Side	688	0.155	3.87	24.00	23.67	0.167	78
			1 RB	Left Edge	688	0.094	-0.64	23.00	22.42	0.107	/
			Full RB	Left Edge	688	0.113	-2.40	24.00	23.67	0.122	/
			1 RB	Right Edge	688	0.129	-1.56	23.00	22.42	0.147	/
			Full RB	Right Edge	688	0.142	1.59	24.00	23.67	0.153	/
			1 RB	Bottom Edge	688	0.113	0.63	23.00	22.42	0.129	/
			Full RB	Bottom Edge	688	0.118	0.89	24.00	23.67	0.127	/
SA N77	100M	DFT_QPSK	1 RB	Front Side	3500	0.307	-0.70	21.50	21.32	0.320	/
			Full RB	Front Side	3500	0.362	-0.69	24.00	23.97	0.365	/
			1 RB	Back Side	3500	0.349	3.34	21.50	21.32	0.364	/
			Full RB	Back Side	3500	0.413	-0.63	24.00	23.97	0.416	/
			1 RB	Left Edge	3500	0.260	3.16	21.50	21.32	0.271	/
			Full RB	Left Edge	3500	0.309	-3.98	24.00	23.97	0.311	/
			1 RB	Right Edge	3500	0.077	-0.36	21.50	21.32	0.080	/
			Full RB	Right Edge	3500	0.084	1.99	24.00	23.97	0.085	/
			1 RB	Top Edge	3500	0.550	-0.88	21.50	21.32	0.573	/
			Full RB	Top Edge	3500	0.684	-1.14	24.00	23.97	0.689	80



NSA

Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N41	100M	DFT_QPSK	1 RB	Front Side	2546.01	0.232	-0.25	22.00	21.20	0.279	/
			Full RB	Front Side	2592.99	0.278	-1.43	23.50	23.20	0.298	/
			1 RB	Back Side	2546.01	0.353	2.04	22.00	21.20	0.424	/
			Full RB	Back Side	2592.99	0.430	3.12	23.50	23.20	0.461	74
			1 RB	Left Edge	2546.01	0.253	-1.85	22.00	21.20	0.304	/
			Full RB	Left Edge	2592.99	0.313	1.15	23.50	23.20	0.335	/
			1 RB	Right Edge	2546.01	0.075	-1.63	22.00	21.20	0.090	/
			Full RB	Right Edge	2592.99	0.097	1.02	23.50	23.20	0.104	/
			1 RB	Bottom Edge	2546.01	0.254	2.78	22.00	21.20	0.305	/
			Full RB	Bottom Edge	2592.99	0.314	-0.04	23.50	23.20	0.336	/
LTE Band 2	20M	QPSK	1	Front side	1860	0.347	-0.57	24.00	23.71	0.371	/
			50	Front side	1900	0.313	0.47	24.80	24.21	0.359	/
			1	Back Side	1860	0.448	-3.57	25.00	24.21	0.537	/
			50	Back Side	1900	0.406	1.97	24.00	23.71	0.434	/
			1	Left Side	1860	0.256	-2.06	25.00	24.21	0.307	/
			50	Left Side	1900	0.231	0.99	24.00	23.71	0.247	/
			1	Right Side	1860	0.082	-1.46	25.00	24.21	0.098	/
			50	Right Side	1900	0.061	-3.67	24.00	23.71	0.065	/
			1	Bottom Side	1860	0.582	0.99	24.80	24.21	0.667	54
			1	Bottom Side	1880	0.517	0.33	24.80	24.19	0.595	/
			1	Bottom Side	1900	0.483	3.98	24.80	24.04	0.575	/
50	Bottom Side	1900	0.531	-3.38	24.00	23.71	0.568	/			



LTE Band 66	20M	QPSK	1	Front side	1745	0.382	0.72	25.00	23.95	0.486	/
			50	Front side	1770	0.337	-2.94	24.00	23.53	0.376	/
			1	Back Side	1745	0.468	-2.52	25.00	23.95	0.596	/
			50	Back Side	1770	0.425	1.76	24.00	23.53	0.474	/
			1	Left Side	1745	0.129	1.02	25.00	23.95	0.164	/
			50	Left Side	1770	0.100	-2.54	24.00	23.53	0.111	/
			1	Right Side	1745	0.412	-3.98	25.00	23.95	0.525	/
			50	Right Side	1770	0.354	4.00	24.00	23.53	0.394	/
			1	Bottom Side	1745	0.571	-3.88	25.00	23.95	0.727	68
			50	Bottom Side	1770	0.507	-3.49	24.00	23.53	0.565	/

Band	Mode	Max SAR	NSA N41+ LTE B2
		(W/Kg)	
NSA N41+ LTE B2	SA N41	0.461	1.128
	LTE B2	0.667	
Band	Mode	Max SAR	NSA N41+LTE B66
		(W/Kg)	
NSA N41+ LTE B66	SA N41	0.461	1.188
	LTE B66	0.727	



Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)	Meas. No.
SA N71	100M	DFT_QPSK	1 RB	Front Side	688	0.118	-2.72	23.00	22.42	0.135	/
			Full RB	Front Side	688	0.142	2.29	24.00	23.67	0.153	/
			1 RB	Back Side	688	0.124	-3.17	23.00	22.42	0.142	/
			Full RB	Back Side	688	0.155	-0.75	24.00	23.67	0.167	78
			1 RB	Left Edge	688	0.094	3.58	23.00	22.42	0.107	/
			Full RB	Left Edge	688	0.113	3.92	24.00	23.67	0.122	/
			1 RB	Right Edge	688	0.129	1.17	23.00	22.42	0.147	/
			Full RB	Right Edge	688	0.142	-2.79	24.00	23.67	0.153	/
			1 RB	Bottom Edge	688	0.113	-2.93	23.00	22.42	0.129	/
			Full RB	Bottom Edge	688	0.118	-3.56	24.00	23.67	0.127	/
LTE Band 2	20M	QPSK	1	Front side	1860	0.347	-0.78	24.00	23.71	0.371	/
			50	Front side	1900	0.313	-1.42	24.80	24.21	0.359	/
			1	Back Side	1860	0.448	0.98	25.00	24.21	0.537	/
			50	Back Side	1900	0.406	-3.80	24.00	23.71	0.434	/
			1	Left Side	1860	0.256	-1.08	25.00	24.21	0.307	/
			50	Left Side	1900	0.231	-2.61	24.00	23.71	0.247	/
			1	Right Side	1860	0.082	-1.23	25.00	24.21	0.098	/
			50	Right Side	1900	0.061	2.87	24.00	23.71	0.065	/
			1	Bottom Side	1860	0.582	-1.62	24.80	24.21	0.667	54
			1	Bottom Side	1880	0.517	1.75	24.80	24.19	0.595	/
			1	Bottom Side	1900	0.483	3.73	24.80	24.04	0.575	/
			50	Bottom Side	1900	0.531	1.06	24.00	23.71	0.568	/



LTE Band 66	20M	QPSK	1	Front side	1745	0.382	-1.98	25.00	23.95	0.486	/
			50	Front side	1770	0.337	2.16	24.00	23.53	0.376	/
			1	Back Side	1745	0.468	2.67	25.00	23.95	0.596	/
			50	Back Side	1770	0.425	-2.85	24.00	23.53	0.474	/
			1	Left Side	1745	0.129	-2.37	25.00	23.95	0.164	/
			50	Left Side	1770	0.100	-3.57	24.00	23.53	0.111	/
			1	Right Side	1745	0.412	-0.49	25.00	23.95	0.525	/
			50	Right Side	1770	0.354	-2.56	24.00	23.53	0.394	/
			1	Bottom Side	1745	0.571	-0.50	25.00	23.95	0.727	68
			50	Bottom Side	1770	0.507	2.13	24.00	23.53	0.565	/

Band	Mode	Max SAR	NSA N71+ LTE B2
		(W/Kg)	
NSA N71+ LTE B2	SA N71	0.167	0.834
	LTE B2	0.667	
Band	Mode	Max SAR	NSA N71+ LTE B66
		(W/Kg)	
NSA N71+ LTE B66	SA N71	0.167	0.894
	LTE B66	0.727	

Note:

- The test separation of all above table is 10mm.
- Per KDB 447498 D01, the reported SAR is the measured SAR value adjusted for maximum tune-up tolerance.
 - Tune-up scaling Factor = tune-up limit power (mW) / EUT RF power (mW), where tune-up limit is the maximum rated power among all production units.
 - Scaled SAR(W/kg) = Measured SAR(W/kg) *Tune-up Scaling Factor
- When the user enables the personal Wireless router functions for the handsets, actual operations include simultaneous transmission of both the Wi-Fi transmitting frequency and thus cannot be evaluated for SAR under actual use conditions. The "Portable Hotspot" feature on the handset was NOT activated, to ensure the SAR measurements were evaluated for a single transmission frequency RF signal.



12.3 Repeated SAR

Band	Mode	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)
WCDMA Band IV	RMC	Bottom Side	1740	0.783	-2.27	23.5	23.19	0.840

Band	BW (MHz)	Mod.	RB Size	RB offset	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)
LTE Band 4	20M	QPSK	1	0	Bottom Side	1720	0.850	-2.87	24.5	23.50	1.071
			1	0	Bottom Side	1745	0.762	3.78	24.5	23.46	0.968
			1	0	Bottom Side	1745	0.609	-2.46	24.5	22.79	0.902
LTE Band 48	20M	QPSK	1	0	Right Tilt	3625	0.989	-1.22	23	22.65	1.071
			1	0	Right Tilt	3690	0.796	3.08	23	22.54	0.885
			50	0	Right Tilt	3625	0.833	-2.42	22	21.73	0.887
			1	0	Left Cheek	3625	0.948	-1.35	23	22.65	1.028
			1	0	Left Cheek	3690	0.754	-1.13	23	22.54	0.839
			50	0	Left Cheek	3625	0.763	-3.91	22	21.73	0.812
			1	0	Left Tilt	3625	0.962	-3.92	23	22.65	1.042
			1	0	Left Tilt	3690	0.719	-3.20	23	22.54	0.799
			50	0	Left Tilt	3625	0.772	-1.51	22	21.73	0.822

Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Result 1g (W/Kg)	Power Drift (%)	Max. Turn-up Power (dBm)	Meas. Output Power (dBm)	Scaled SAR (W/Kg)
SA N48	100M	DFT_QPSK	Full RB	Right Tilt	3624.99	0.672	2.73	23	22.23	0.802
			Full RB	Right Tilt	3649.98	0.757	-2.08	23	22.3	0.890
			Full RB	Left Tilt	3600	0.675	1.13	23	22.16	0.819
			Full RB	Left Tilt	3624.99	0.687	3.60	23	22.23	0.821
			Full RB	Left Tilt	3649.98	0.830	2.17	23	22.3	0.975
SA N77	100M	DFT_QPSK	Full RB	Left Tilt	3500	0.844	-3.26	24.00	23.97	0.850



12.4 Repeated SAR measurement

Band	Mode	Test Position	Freq.	Original Measured SAR 1g(W/kg)	1 st Repeated SAR 1g	Ratio
WCDMA Band IV	RMC	Bottom Side	1740	0.785	0.783	1.003

Band	BW (MHz)	Mod.	RB Size	RB offset	Test Position	Freq.	Original Measured SAR 1g (W/Kg)	1 st Repeated SAR 1g	Ratio
LTE Band 4	20M	QPSK	1	0	Bottom Side	1720	0.868	0.850	1.021
			1	0	Bottom Side	1745	0.770	0.762	1.011
			1	0	Bottom Side	1745	0.611	0.609	1.004
LTE Band 48	20M	QPSK	1	0	Right Tilt	3625	1.017	0.989	1.029
			1	0	Right Tilt	3690	0.816	0.796	1.025
			50	0	Right Tilt	3625	0.848	0.833	1.018
			1	0	Left Cheek	3625	0.967	0.948	1.020
			1	0	Left Cheek	3690	0.787	0.754	1.043
			50	0	Left Cheek	3625	0.781	0.763	1.023
			1	0	Left Tilt	3625	0.982	0.962	1.021
			1	0	Left Tilt	3690	0.752	0.719	1.046
			50	0	Left Tilt	3625	0.804	0.772	1.041

Band	BW (MHz)	Mod.	RB config	Test Position	Freq.	Original Measured SAR 1g(W/kg)	1 st Repeated SAR 1g	Ratio
SA N48	100M	DFT_QPSK	Full RB	Right Tilt	3624.99	0.691	0.672	1.028
			Full RB	Right Tilt	3649.98	0.79	0.757	1.043
			Full RB	Left Tilt	3600	0.693	0.675	1.026
			Full RB	Left Tilt	3624.99	0.722	0.687	1.050
			Full RB	Left Tilt	3649.98	0.844	0.830	1.017
SA N77	100M	DFT_QPSK	Full RB	Left Tilt	3500	0.883	0.844	1.046

Note:

- Per KDB 865664 D01, for each frequency band, repeated SAR measurement is required only when the measured SAR is ≥ 0.8 W/Kg.
- Per KDB 865664 D01, if the ratio of largest to smallest SAR for the original and first repeated measurement is ≤ 1.2 and the measured SAR < 1.45 W/Kg, only one repeated measurement is required.
- Perform a second repeated measurement only if the ratio of largest to smallest SAR for the original and first repeated measurements is > 1.20 or when the original or repeated measurement is ≥ 1.45 W/Kg.
- The ratio is the difference in percentage between original and repeated measured SAR.



12.5 Simultaneous Multi-band Transmission Evaluation:

Application Simultaneous Transmission information:

Position	Simultaneous State
Head	1. GSM + 2.4GHz WLAN/5G WLAN
	2. GSM + Bluetooth
	3. WCDMA + 2.4GHz WLAN/5G WLAN
	4. WCDMA + Bluetooth
	5. LTE + 2.4GHz WLAN/5G WLAN
	6. LTE + Bluetooth
	7.SA + 2.4GHz WLAN/5G WLAN
	8.SA + Bluetooth
	9.NSA + 2.4GHz WLAN/5G WLAN
	10.NSA + Bluetooth
Body	1. GSM + 2.4GHz WLAN/5G WLAN
	2. GSM + Bluetooth
	3. WCDMA + 2.4GHz WLAN/5G WLAN
	4. WCDMA + Bluetooth
	5. LTE + 2.4GHz WLAN/5G WLAN
	6. LTE + Bluetooth
	7.SA + 2.4GHz WLAN/5G WLAN
	8.SA + Bluetooth
	9.NSA + 2.4GHz WLAN/5G WLAN
	10.NSA + Bluetooth

NOTE:

1. Bluetooth and WLAN can't simultaneous transmission at the same time.
2. For simultaneous transmission at head and body exposure position, 2 transmitters simultaneous transmission was the worst state.
3. If the test separation distance is <5mm, 5mm is used for excluded SAR calculation.
4. KDB 447498 / 4.3.2 (2) when standalone SAR test exclusion applies to an antenna that transmits simultaneously with other antennas, the standalone SAR must be estimated according to following to determine simultaneous transmission SAR test exclusion:
 - a) $(\text{max. power of channel, including tune-up tolerance, mW}) / (\text{min. test separation distance, mm}) \cdot [\sqrt{f \text{ (GHz)}} / x]$ W/kg for test separation distances ≤ 50 mm;
Where $x = 7.5$ for 1-g SAR, and $x = 18.75$ for 10-g SAR.
 - b) 0.4W/Kg for 1-g SAR and 1.0W/Kg for 10-g SAR, when the separation distance is >50 mm.



Simultaneous Mode	Position	Mode	Max. 1-g SAR	1-g Sum SAR
			(W/kg)	(W/kg)
GSM + 2.4G WLAN	Head	GSM	0.217	0.453
		2.4G WLAN	0.236	
	Body	GSM	0.515	0.717
		2.4G WLAN	0.202	
GSM + Bluetooth	Head	GSM	0.217	0.267
		Bluetooth	0.050	
	Body	GSM	0.515	0.612
		Bluetooth	0.097	
GSM + 5G WLAN	Head	GSM	0.217	0.659
		5G WLAN	0.442	
	Body	GSM	0.515	0.901
		5G WLAN	0.386	
WCDMA + 2.4G WLAN	Head	WCDMA	0.349	0.585
		2.4G WLAN	0.236	
	Body	WCDMA	0.843	1.045
		2.4G WLAN	0.202	
WCDMA + Bluetooth	Head	WCDMA	0.349	0.399
		Bluetooth	0.050	
	Body	WCDMA	0.843	0.940
		Bluetooth	0.097	
WCDMA + 5G WLAN	Head	WCDMA	0.349	0.791
		5G WLAN	0.442	
	Body	WCDMA	0.843	1.229
		5G WLAN	0.386	
LTE + 2.4G WLAN	Head	LTE	1.012	1.248
		2.4G WLAN	0.236	
	Body	LTE	1.093	1.295
		2.4G WLAN	0.202	
LTE + Bluetooth	Head	LTE	1.102	1.152
		Bluetooth	0.050	
	Body	LTE	1.093	1.190
		Bluetooth	0.097	
LTE + 5G WLAN	Head	LTE	1.102	1.544
		5G WLAN	0.442	
	Body	LTE	1.093	1.479
		5G WLAN	0.386	
SA + 2.4G WLAN	Head	SA	0.992	1.228
		2.4G WLAN	0.236	
	Body	SA	0.733	0.935
		2.4G WLAN	0.202	
SA + Bluetooth	Head	SA	0.992	1.042
		Bluetooth	0.050	
	Body	SA	0.733	0.830
		Bluetooth	0.097	



SA+ 5G WLAN	Head	SA	0.992	1.434
		5G WLAN	0.442	
	Body	SA	0.733	1.119
		5G WLAN	0.386	
NSA + 2.4G WLAN	Head	NSA	0.578	0.814
		2.4G WLAN	0.236	
	Body	NSA	1.188	1.390
		2.4G WLAN	0.202	
NSA + Bluetooth	Head	NSA	0.578	0.628
		Bluetooth	0.050	
	Body	NSA	1.188	1.285
		Bluetooth	0.097	
NSA+ 5G WLAN	Head	NSA	0.578	1.020
		5G WLAN	0.442	
	Body	NSA	1.188	1.574
		5G WLAN	0.386	

Simultaneous transmission SAR test exclusion is determined for each operating configuration and exposure condition according to the reported standalone SAR of each applicable simultaneous transmitting antenna.

When the sum of SAR 1g of all simultaneously transmitting antennas in an operating mode and exposure condition combination is within the SAR limit (SAR-1g 1.6 W/kg), the simultaneous transmission SAR is not required. When the sum of SAR 1g is greater than the SAR limit (SAR-1g 1.6 W/kg), SAR test exclusion is determined by the SPLSR.



13. Equipment List

Kind of Equipment	Manufacturer	Type No.	Serial No.	Last Calibration	Calibrated Until
750MHz Dipole	MVG	DIP0G750	SN 06/22 DIP0G750-638	2022.02.11	2025.02.10
835MHz Dipole	MVG	DIP0G835	SN 06/22 DIP0G835-639	2022.02.11	2025.02.10
1800MHz Dipole	MVG	DIP1G800	SN 06/22 DIP1G800-640	2022.02.11	2025.02.10
1900MHz Dipole	MVG	DIP1G900	SN 06/22 DIP1G900-641	2022.02.11	2025.02.10
2300MHz Dipole	MVG	DIP2G300	SN 06/22 DIP2G100-644	2022.02.11	2025.02.10
2450MHz Dipole	MVG	DIP2G450	SN 06/22 DIP2G450-645	2022.02.11	2025.02.10
2600MHz Dipole	MVG	DIP2G600	SN 06/22 DIP2G600-646	2022.02.11	2025.02.10
3500MHz Dipole	MVG	DIP3G500	SN 06/22 DIP3G500-647	2022.02.11	2025.02.10
3700MHz Dipole	MVG	DIP3G700	SN 06/22 DIP3G700-648	2022.02.11	2025.02.10
4200MHz Dipole	MVG	DIP4G200	SN 06/22 DIP4G200-650	2022.02.11	2025.02.10
5000MHz Dipole	MVG	DIP5G000	SN 06/22 DIP5G000-653	2022.02.11	2025.02.10
E-Field Probe	MVG	EPGO364	SN 04/22 EPGO364	2023.02.10	2024.02.09
Liquid Calibration Kit	MVG	OCPG 87	SN 06/22 OCPG87	2023.02.10	2024.02.09
Antenna	MVG	ANTA 73	SN 06/22 ANTA 73	N/A	N/A
Ellipsoid Phantom	MVG	ELLI 51	SN 06/22 ELLI 51	N/A	N/A
Phantom	MVG	SAM 148	SN 06/22 SAM148	N/A	N/A
Phone holder	MVG	MSH 117	SN 06/22 MSH 117	N/A	N/A
Laptop holder	MVG	LSH 36	SN 06/22 LSH 38	N/A	N/A
Directional coupler	SHW	SHWDCP	202203280013	N/A	N/A
Network Analyzer	Agilent	E5071C	MY46418070	2023.03.27	2024.03.26
Multi Meter	Keithley	DMM6500	DMM6500	2023.03.27	2024.03.26
Signal Generator	Keithley	N5182B	MY59100717	2023.04.07	2024.04.06
Wireless Communication Test Set	R&S	CMW500	137737	2023.04.14	2024.04.13
Power Sensor	R&S	Z11	116184	2023.03.27	2024.03.26
Temperature hygrometer	N/A	ST-W2318	N/A	2023.04.24	2024.04.23
Thermograph	N/A	TP101	N/A	2023.04.25	2024.04.24



Appendix A. System Validation Plots

System Performance Check Data (750MHz)

Type: Phone measurement (Complete)

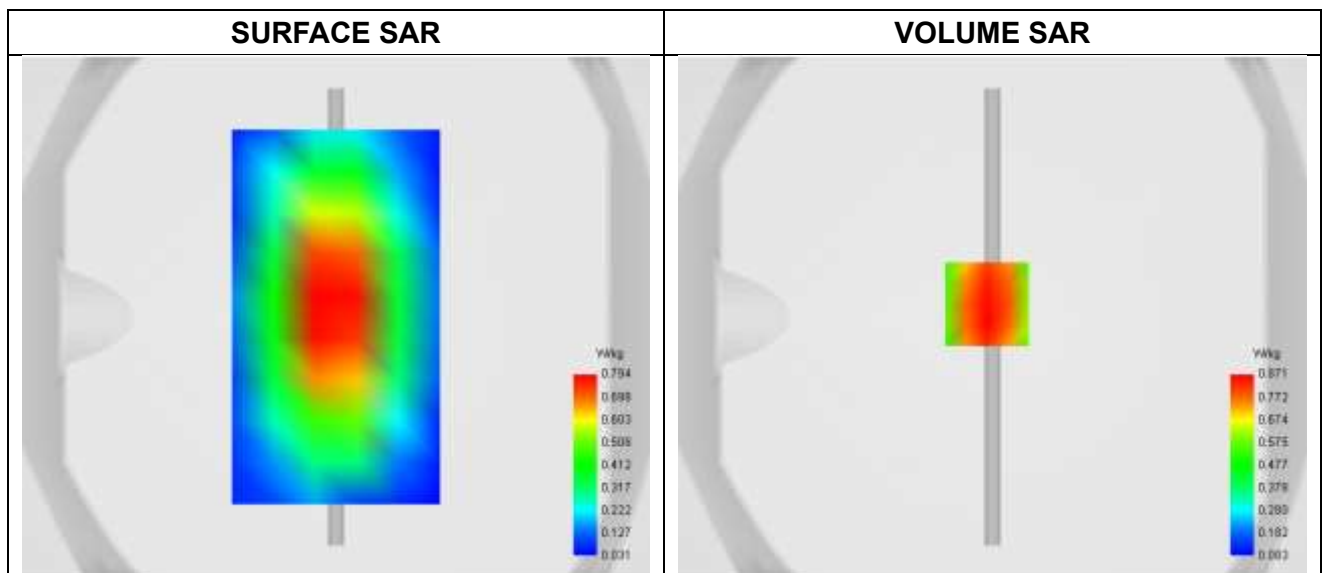
Area scan resolution: dx=8mm,dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-06-21

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW750
Channels	Middle
Signal	CW
Frequency (MHz)	750.000
Relative permittivity	42.24
Conductivity (S/m)	0.85
Probe	SN 04/22 EPGO364
ConvF	1.69
Crest factor:	1:1

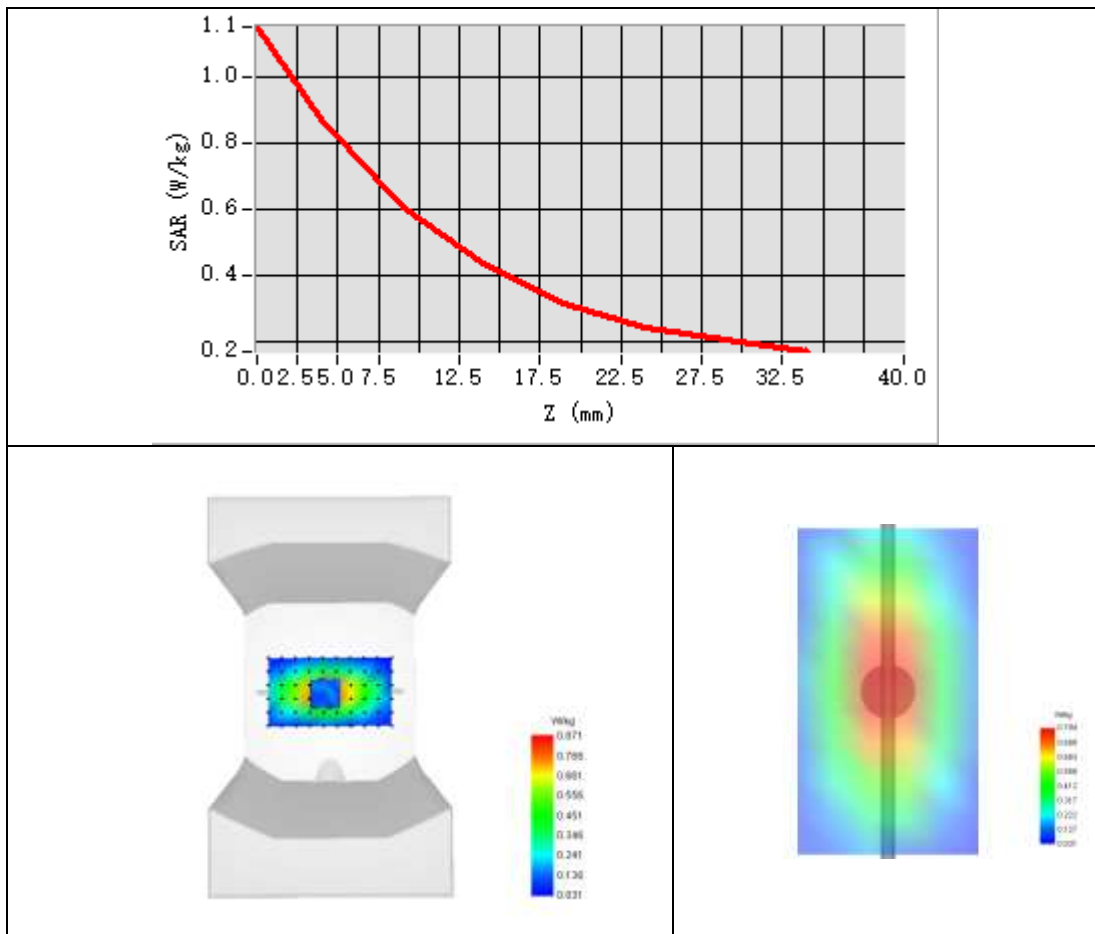


Maximum location: X=-2.00, Y=5.00 ; SAR Peak: 1.20 W/kg

SAR 10g (W/Kg)	0.580
SAR 1g (W/Kg)	0.886



Z Axis Scan





System Performance Check Data (750MHz)

Type: Phone measurement (Complete)

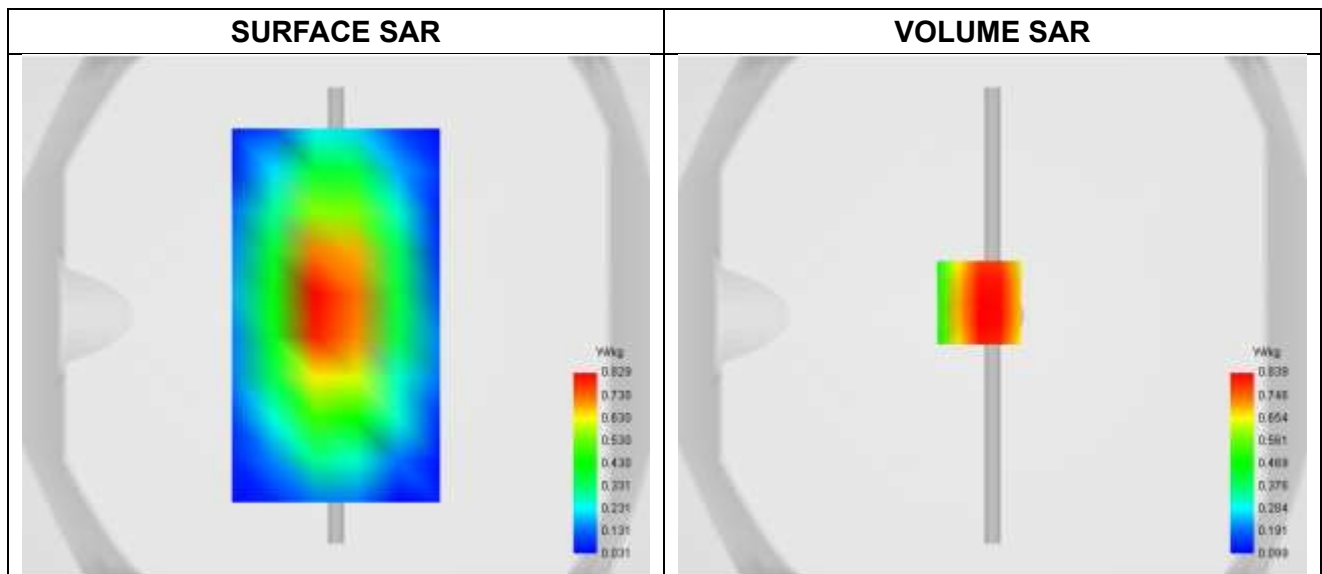
Area scan resolution: dx=8mm,dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-09

Experimental conditions.

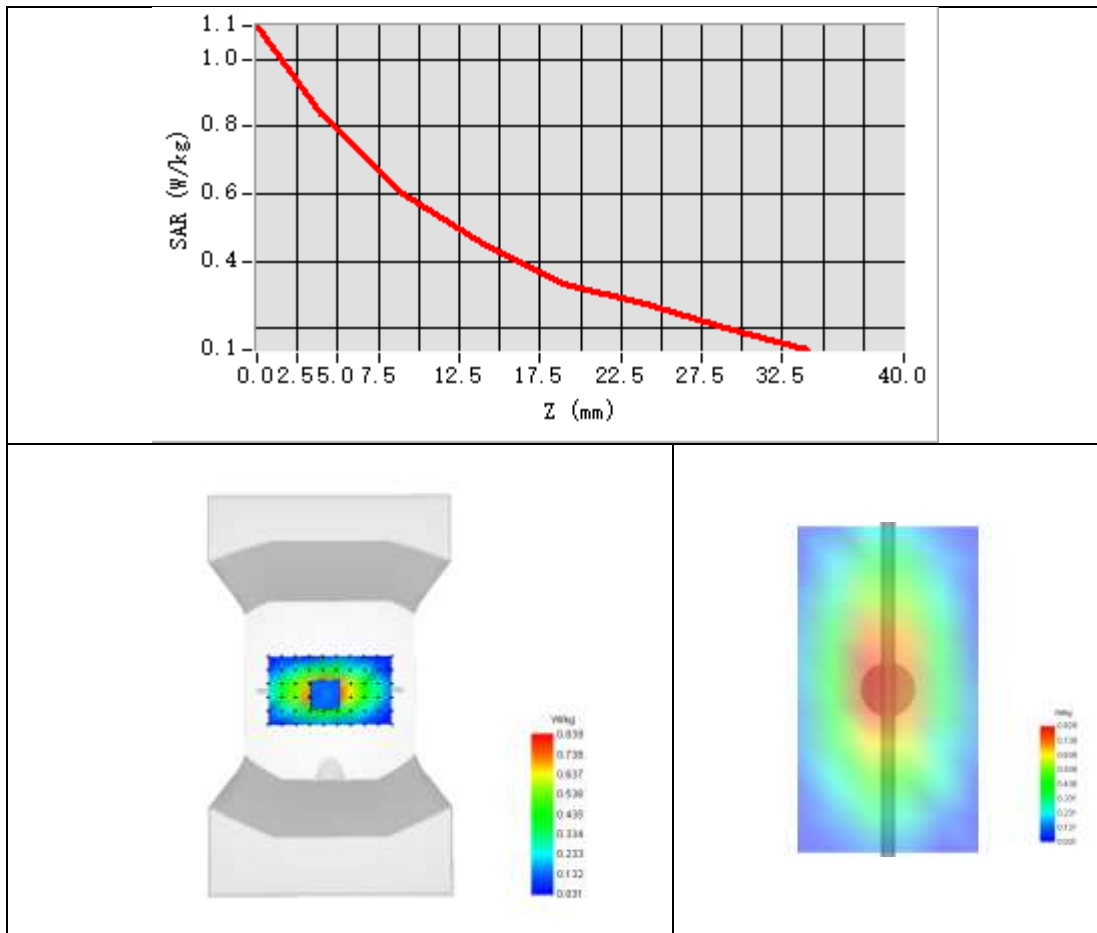
Phantom	Validation plane
Device Position	Dipole
Band	CW750
Channels	Middle
Signal	CW
Frequency (MHz)	750.000
Relative permittivity	42.61
Conductivity (S/m)	0.91
Probe	SN 04/22 EPGO364
ConvF	1.69
Crest factor:	1:1



Maximum location: X=-5.00, Y=5.00 ; SAR Peak: 1.17 W/kg

SAR 10g (W/Kg)	0.565
SAR 1g (W/Kg)	0.802

Z Axis Scan





System Performance Check Data (835MHz)

Type: Phone measurement (Complete)

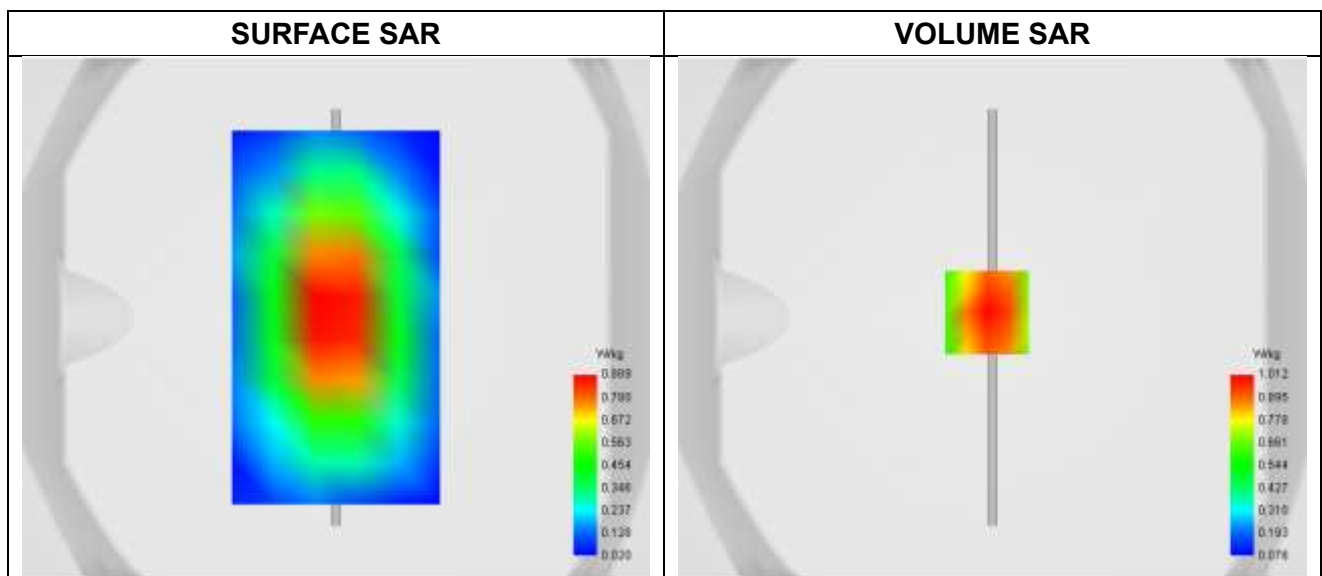
Area scan resolution: dx=8mm,dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-10

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW835
Channels	Middle
Signal	CW
Frequency (MHz)	835.000
Relative permittivity	41.69
Conductivity (S/m)	0.88
Probe	SN 04/22 EPGO364
ConvF	1.72
Crest factor:	1:1

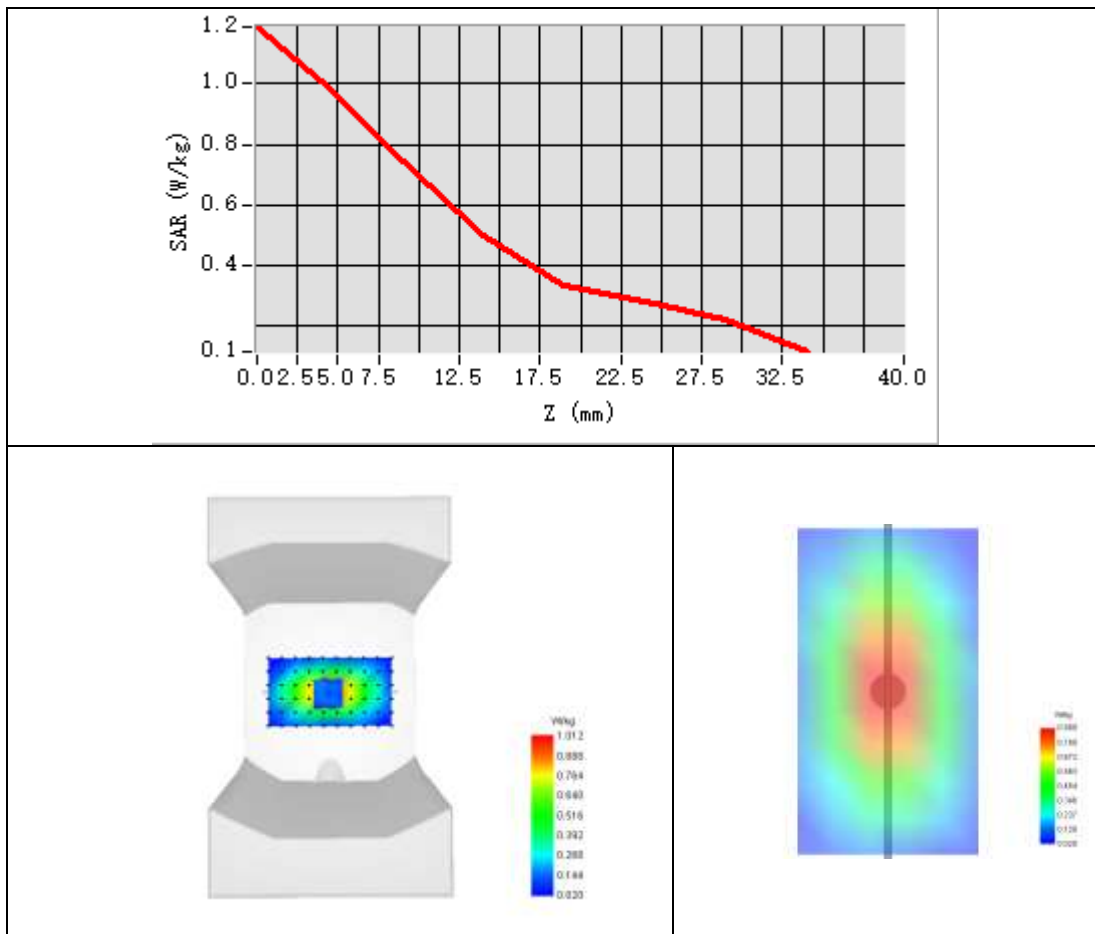


Maximum location: X=-2.00, Y=2.00 ; SAR Peak: 1.37 W/kg

SAR 10g (W/Kg)	0.655
0.991SAR 1g (W/Kg)	0.991



Z Axis Scan





System Performance Check Data (835MHz)

Type: Phone measurement (Complete)

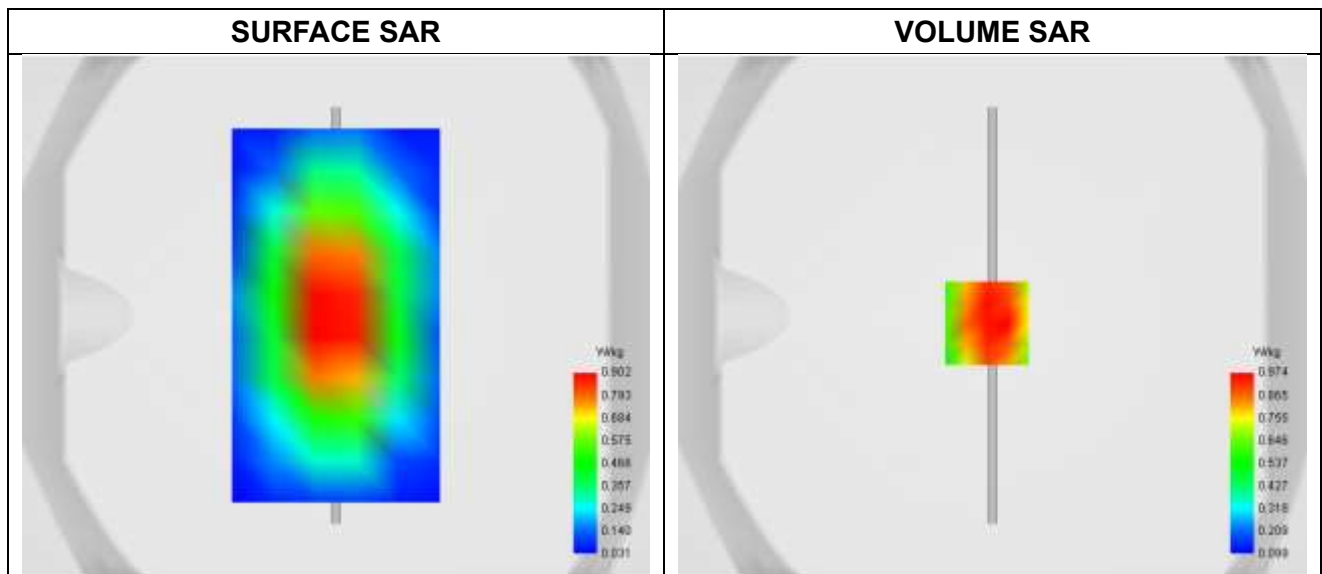
Area scan resolution: dx=8mm,dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-17

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW835
Channels	Middle
Signal	CW
Frequency (MHz)	835.000
Relative permittivity	41.66
Conductivity (S/m)	0.92
Probe	SN 04/22 EPGO364
ConvF	1.72
Crest factor:	1:1

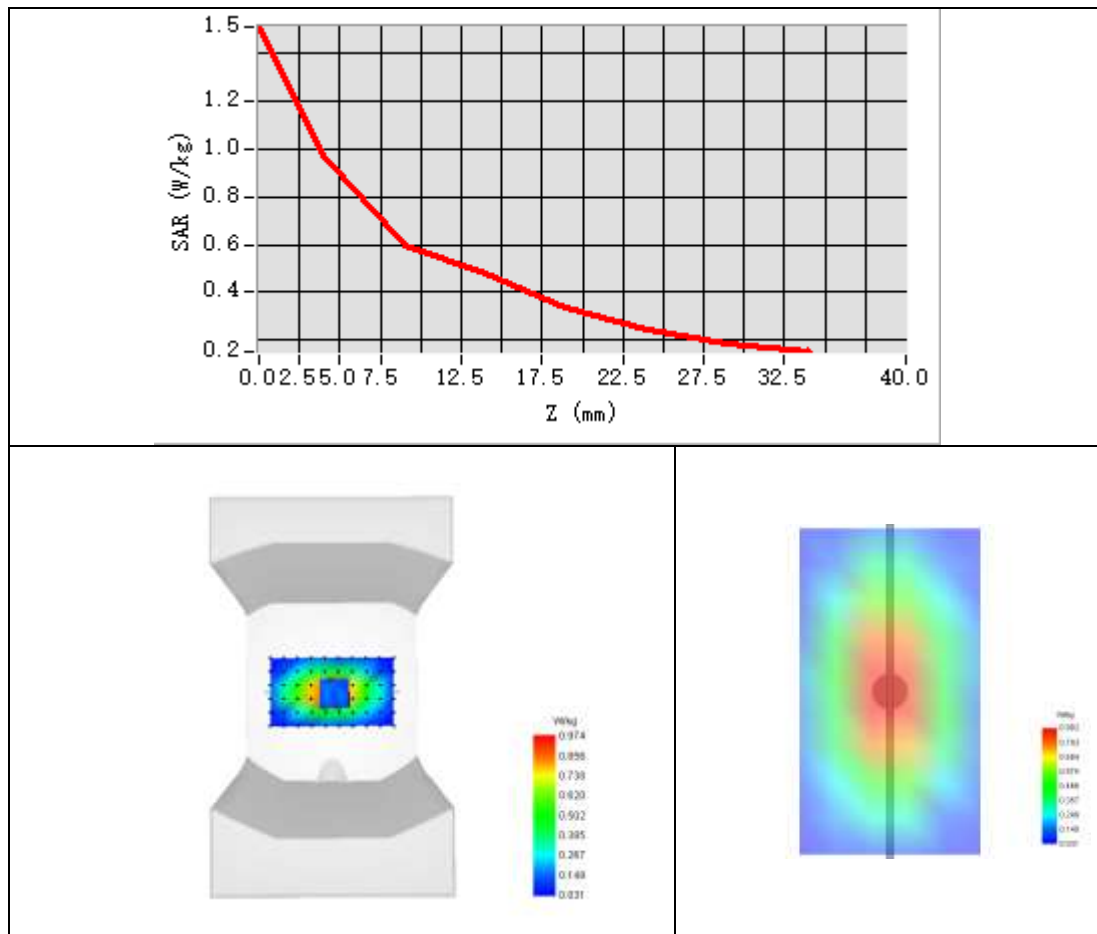


Maximum location: X=-2.00, Y=-3.00 ; SAR Peak: 1.36 W/kg

SAR 10g (W/Kg)	0.636
SAR 1g (W/Kg)	0.969



Z Axis Scan





System Performance Check Data (1800MHz)

Type: Phone measurement (Complete)

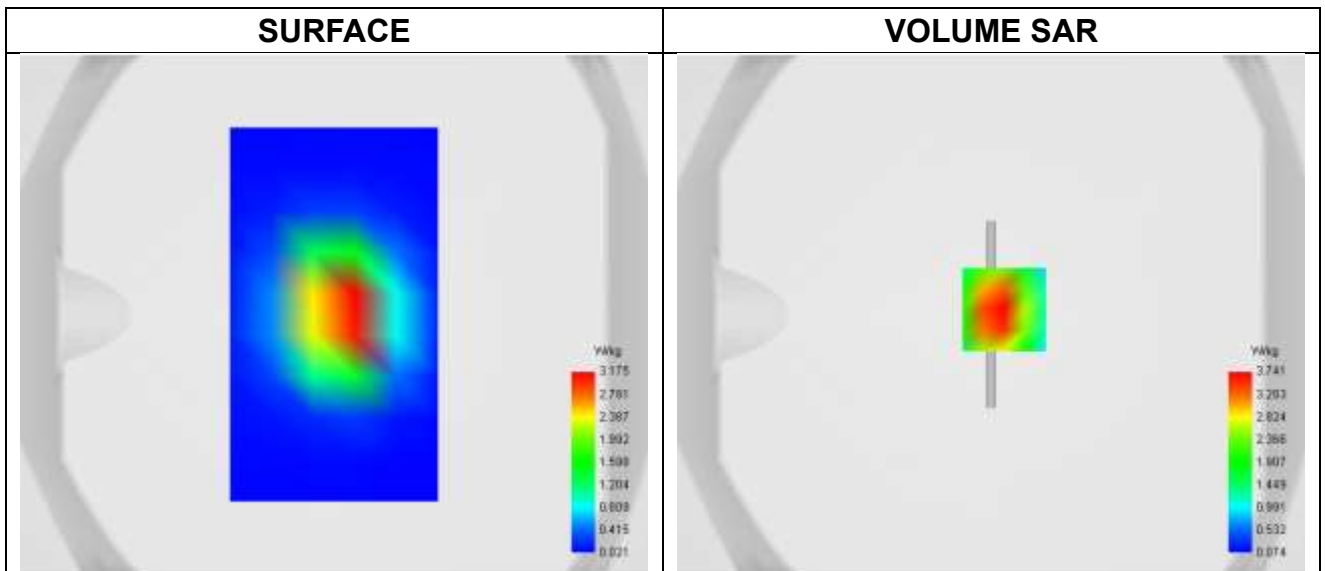
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-11

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW1800
Channels	Middle
Signal	CW
Frequency (MHz)	1800.000
Relative permittivity	40.86
Conductivity (S/m)	1.44
Probe	SN 04/22 EPGO364
ConvF	1.95
Crest factor:	1:1

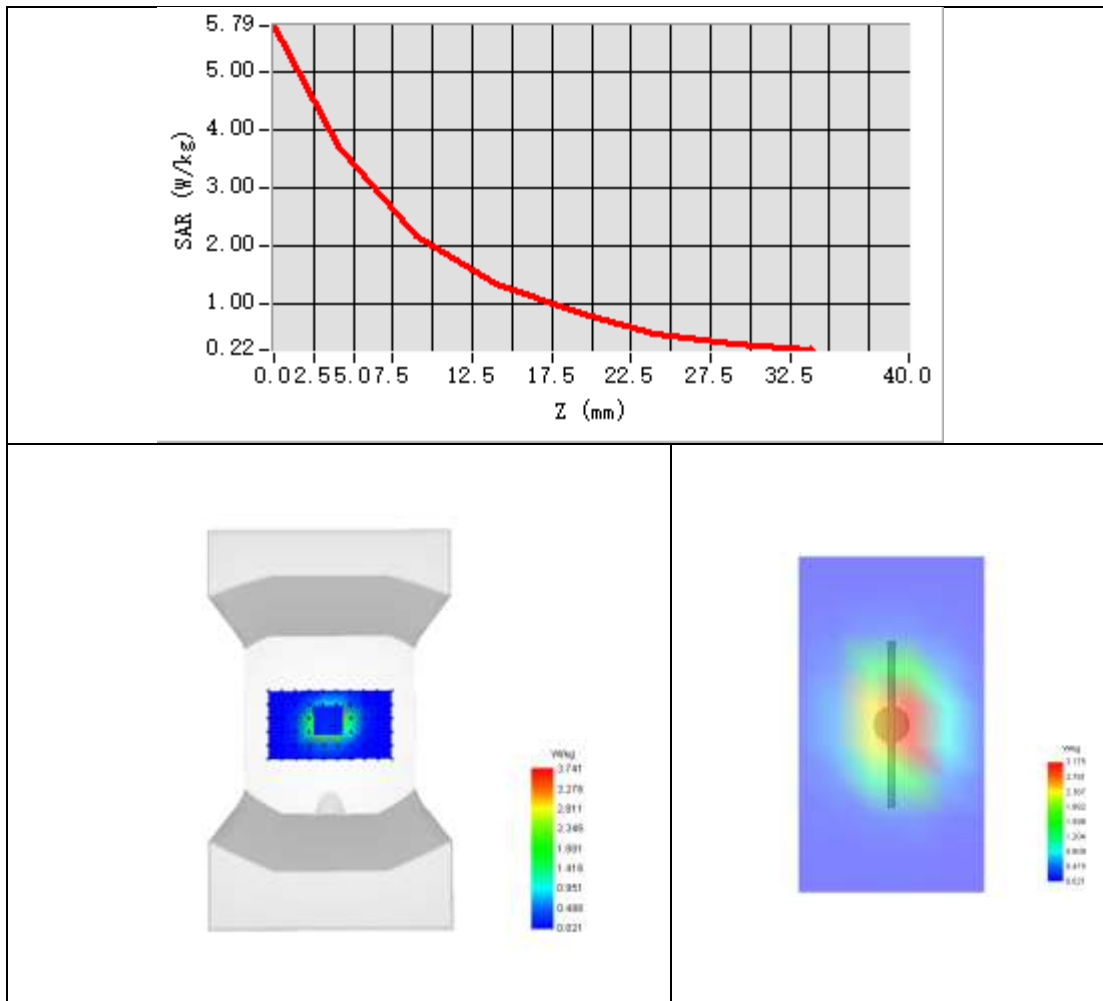


Maximum location: X=5.00, Y=3.00 ; SAR Peak: 5.94 W/kg

SAR 10g (W/Kg)	2.009
SAR 1g (W/Kg)	3.910



Z Axis Scan





System Performance Check Data (1900MHz)

Type: Phone measurement (Complete)

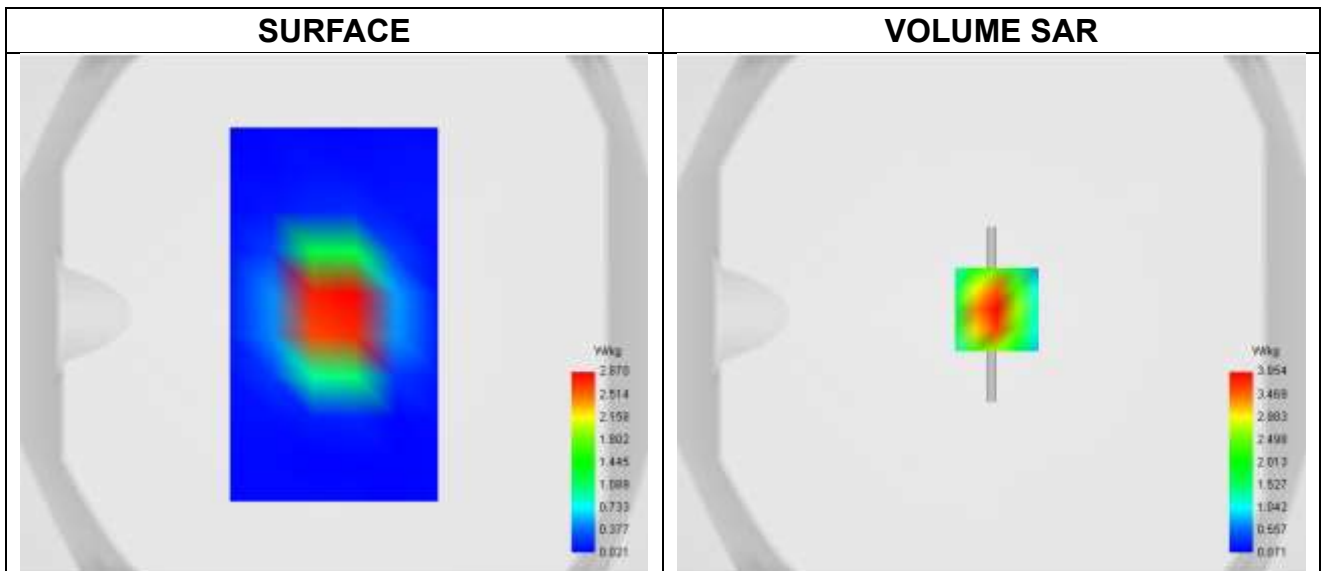
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-12

Experimental conditions.

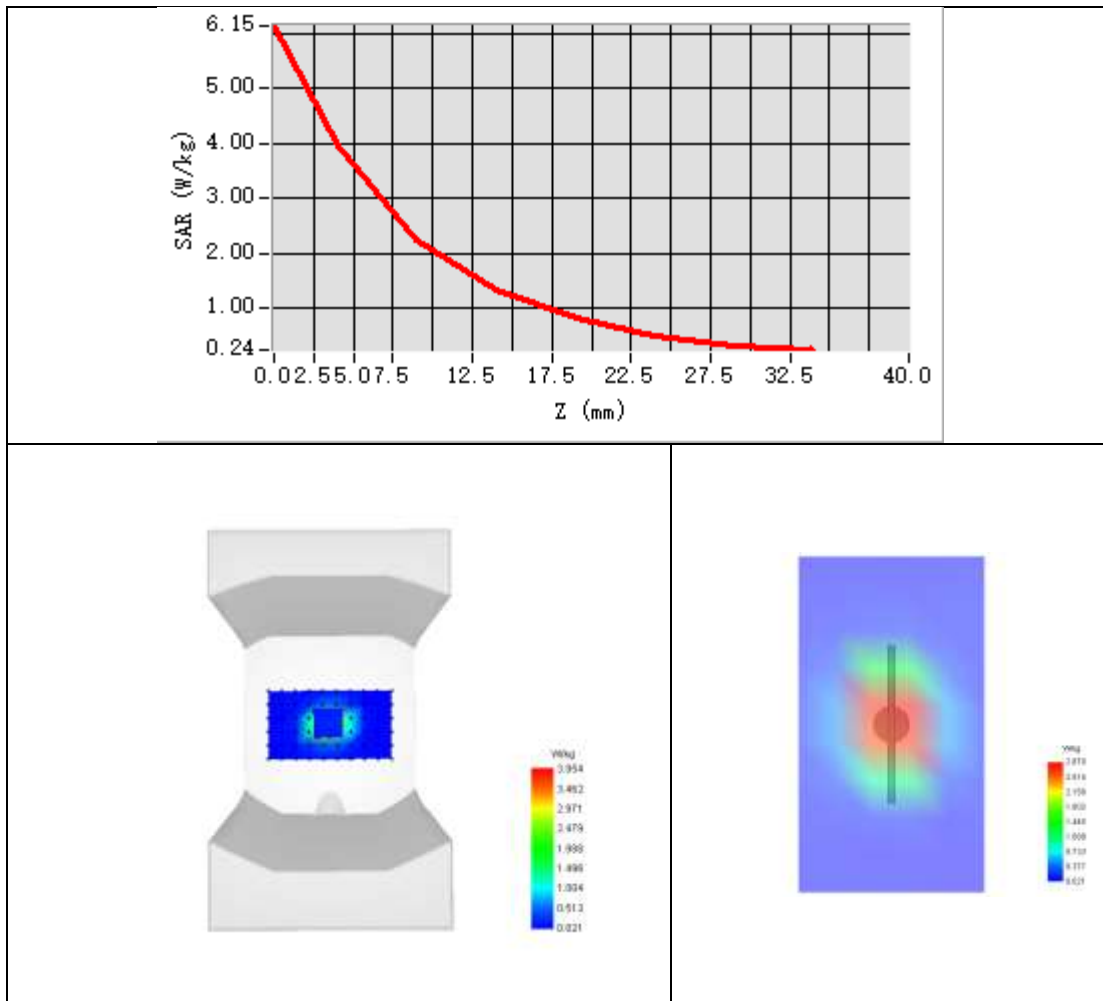
Phantom	Validation plane
Device Position	Dipole
Band	CW1900
Channels	Middle
Signal	CW
Frequency (MHz)	1900.000
Relative permittivity	40.57
Conductivity (S/m)	1.39
Probe	SN 04/22 EPGO364
ConvF	2.25
Crest factor:	1:1



Maximum location: X=2.00, Y=2.00 ; SAR Peak: 6.20 W/kg

SAR 10g (W/Kg)	2.070
SAR 1g (W/Kg)	4.094

Z Axis Scan





System Performance Check Data (2300MHz)

Type: Phone measurement (Complete)

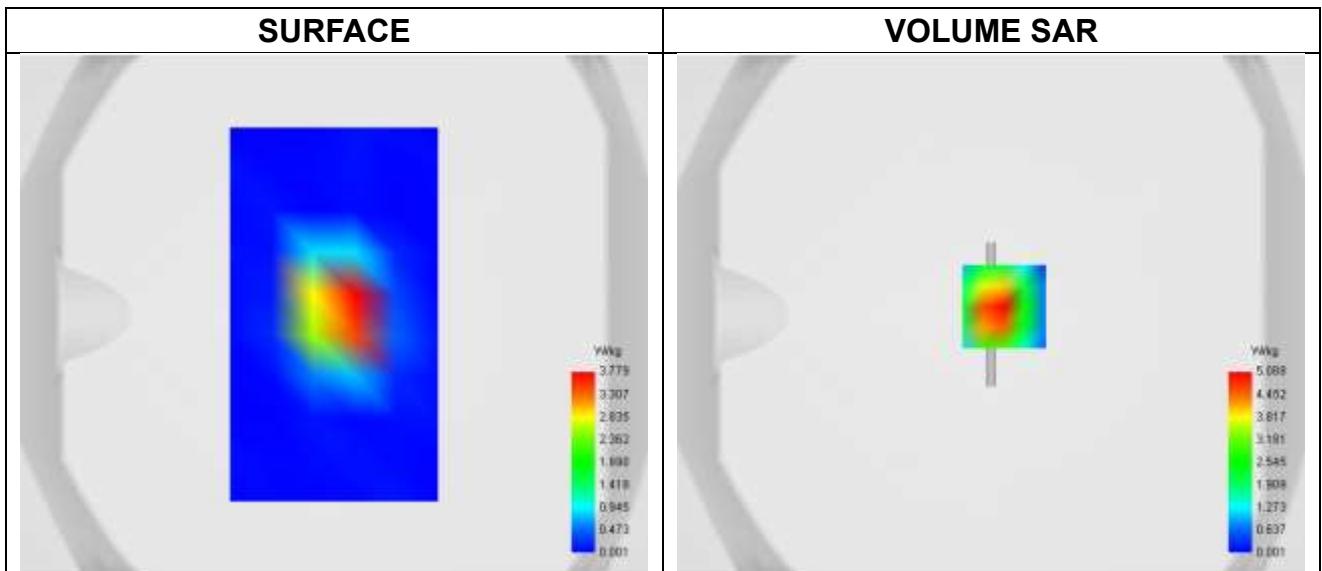
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-13

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW2300
Channels	Middle
Signal	CW
Frequency (MHz)	2300.000
Relative permittivity	39.79
Conductivity (S/m)	1.71
Probe	SN 04/22 EPGO364
ConvF	2.32
Crest factor:	1:1

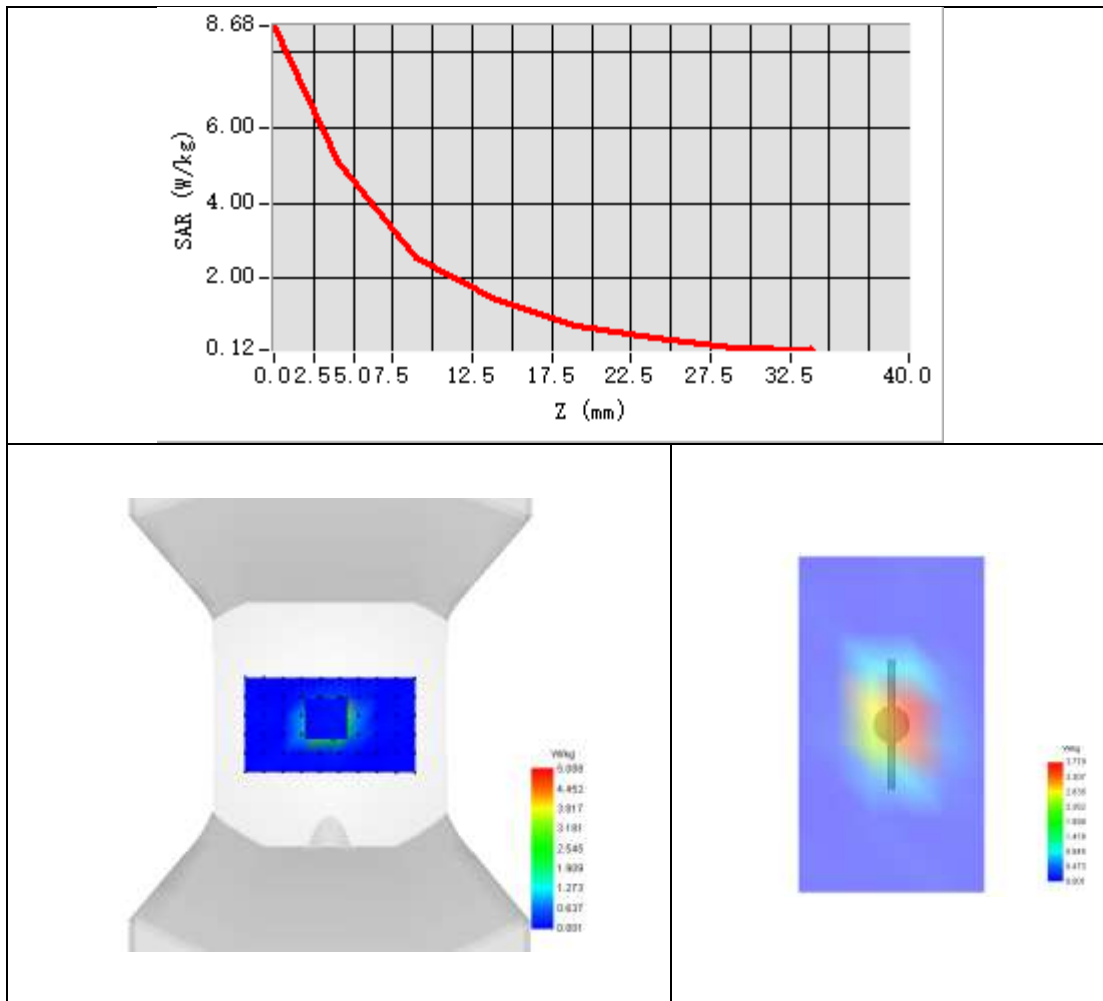


Maximum location: X=5.00, Y=2.00 ; SAR Peak: 8.58 W/kg

SAR 10g (W/Kg)	2.332
SAR 1g (W/Kg)	5.131



Z Axis Scan





System Performance Check Data (2450MHz)

Type: Phone measurement (Complete)

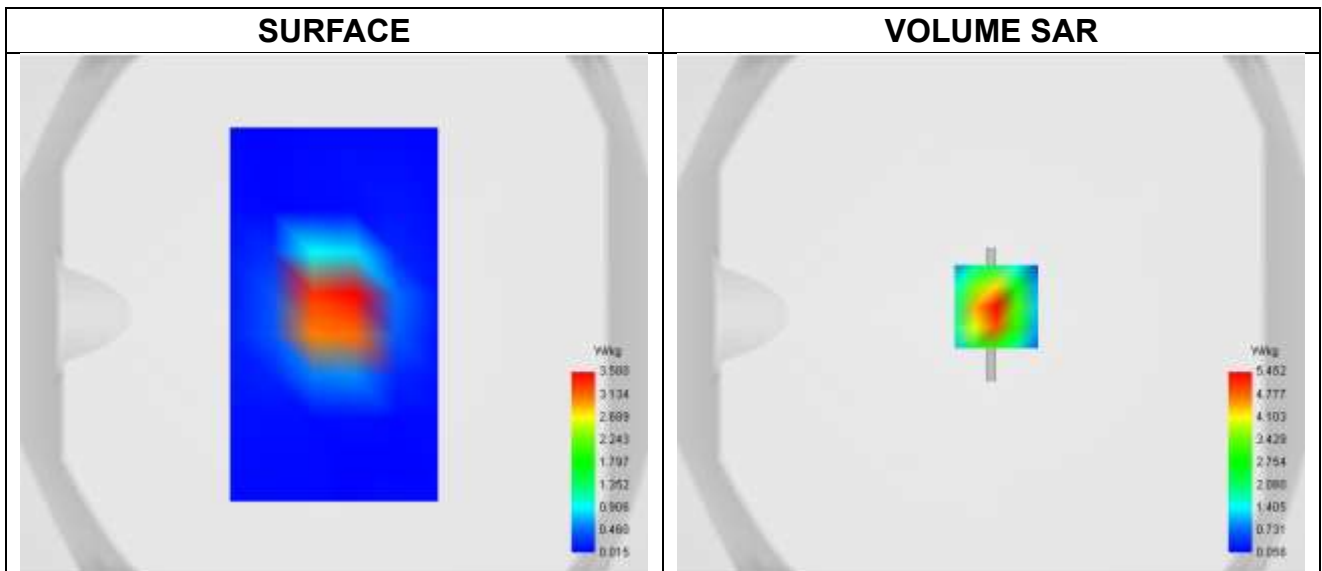
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-14

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW2450
Channels	Middle
Signal	CW
Frequency (MHz)	2450.000
Relative permittivity	39.76
Conductivity (S/m)	1.85
Probe	SN 04/22 EPGO364
ConvF	2.33
Crest factor:	1:1

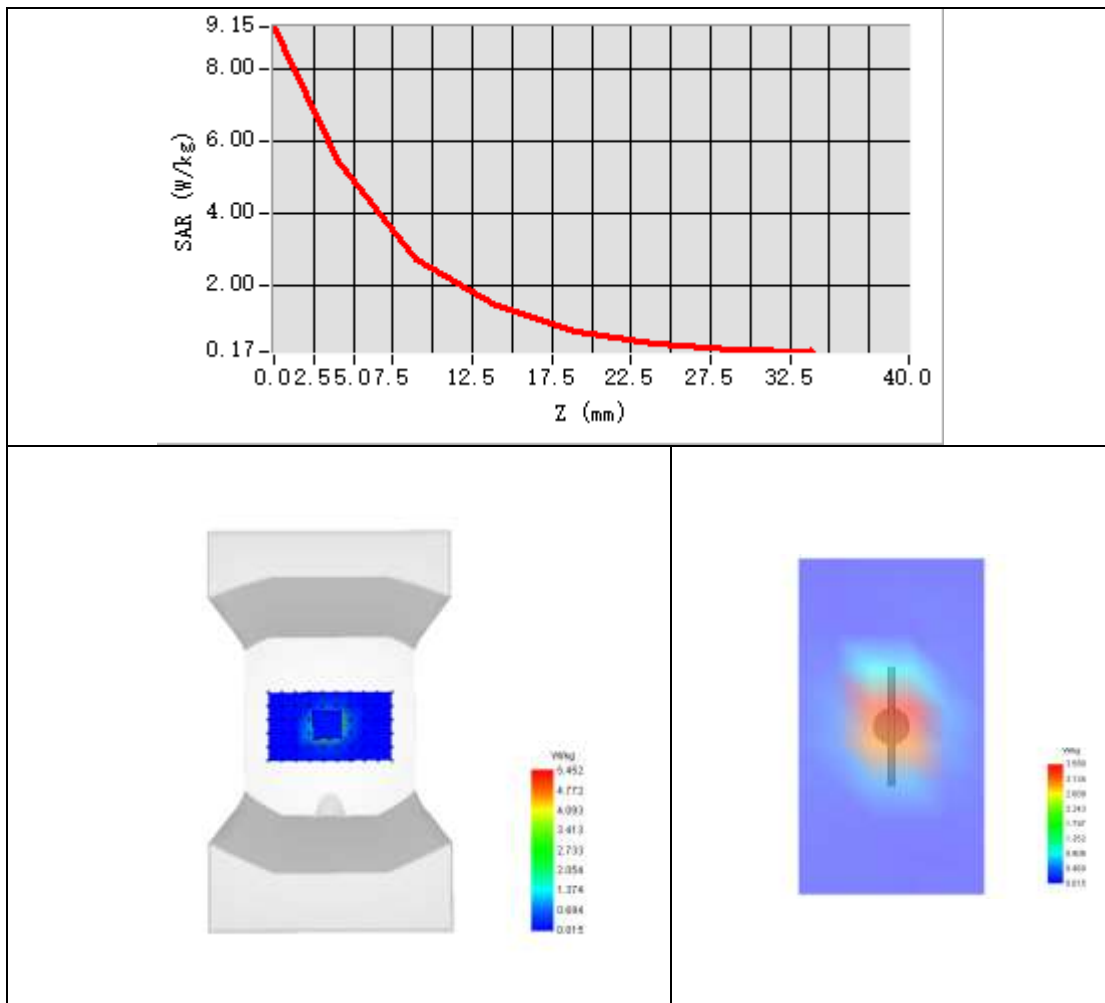


Maximum location: X=2.00, Y=3.00 ; SAR Peak: 9.30 W/kg

SAR 10g (W/Kg)	2.363
SAR 1g (W/Kg)	5.461



Z Axis Scan





System Performance Check Data (2450MHz)

Type: Phone measurement (Complete)

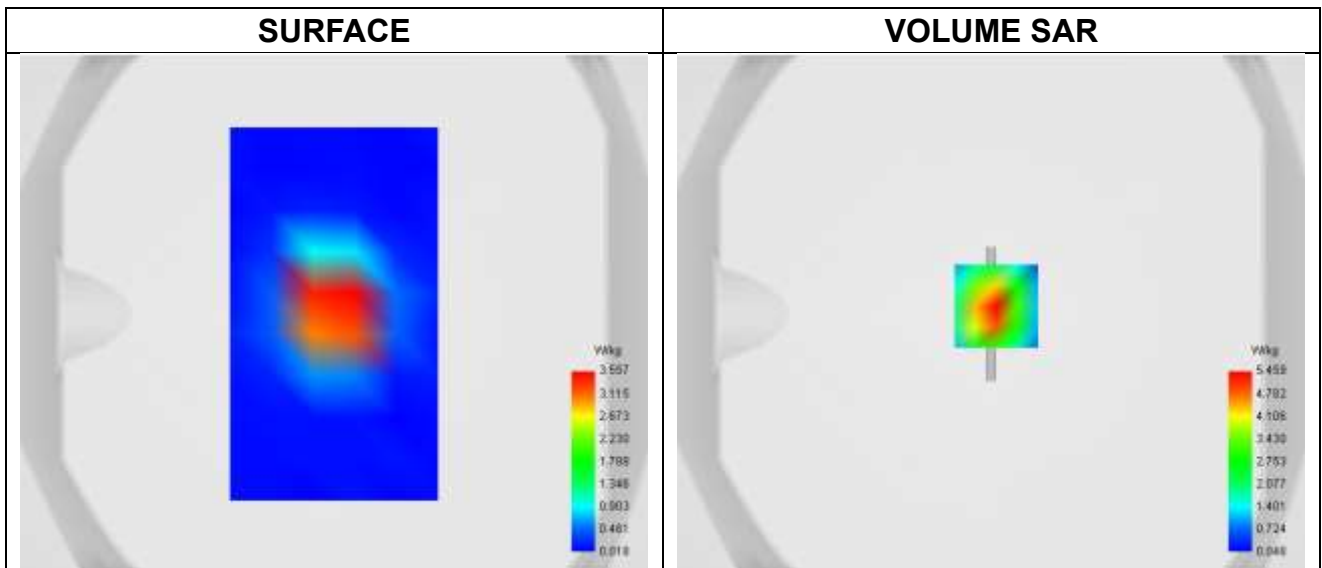
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-16

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW2450
Channels	Middle
Signal	CW
Frequency (MHz)	2450.000
Relative permittivity	38.59
Conductivity (S/m)	1.75
Probe	SN 04/22 EPGO364
ConvF	2.33
Crest factor:	1:1

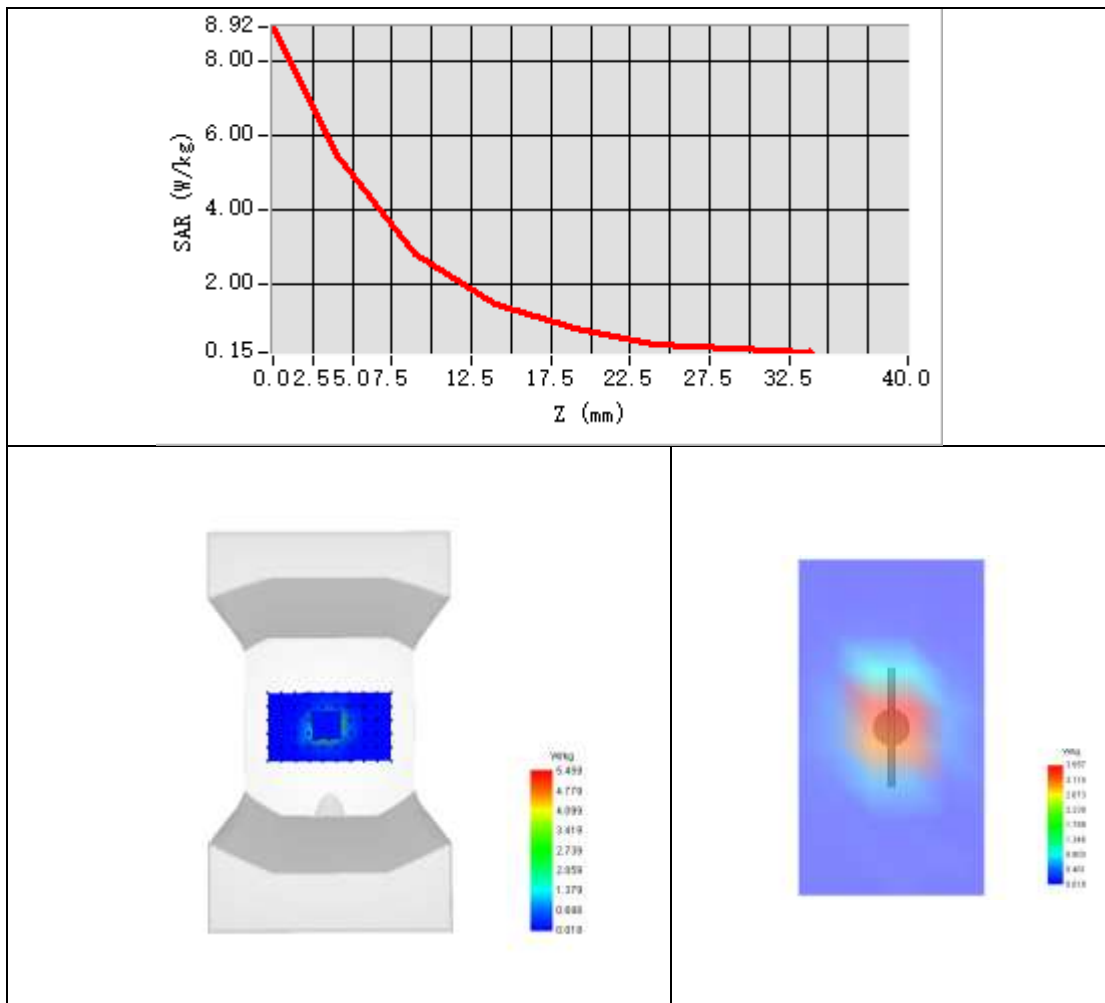


Maximum location: X=2.00, Y=3.00 ; SAR Peak: 9.08 W/kg

SAR 10g (W/Kg)	2.247
SAR 1g (W/Kg)	5.389



Z Axis Scan





System Performance Check Data (2600MHz)

Type: Phone measurement (Complete)

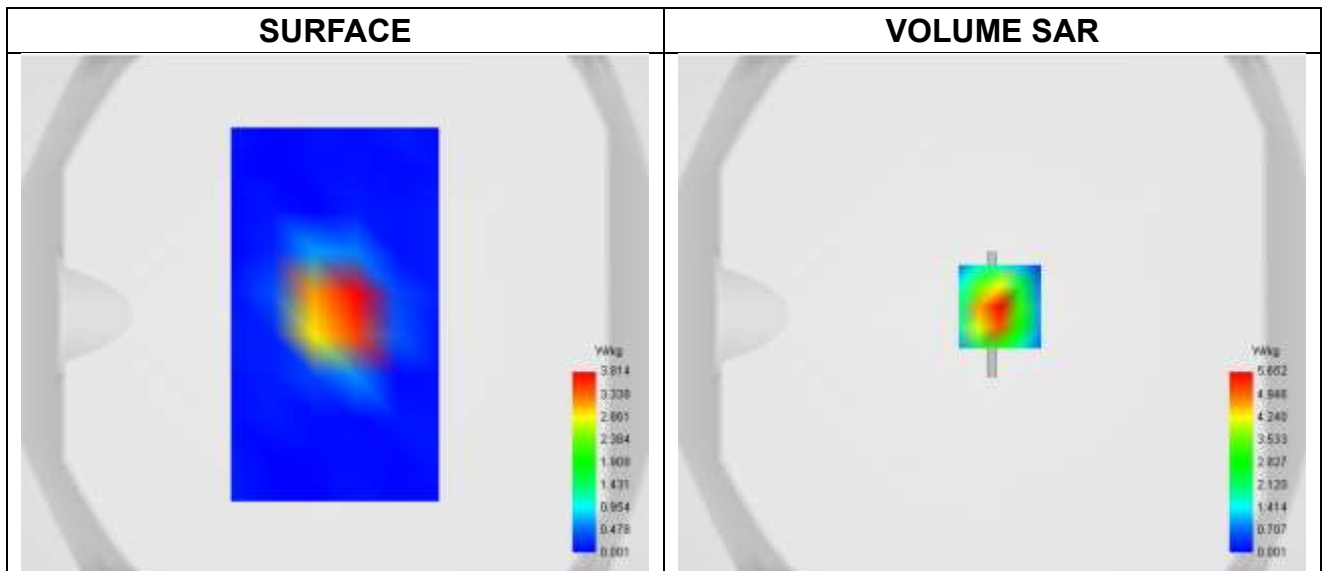
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=8mm, dy=8mm, dz=5mm

Date of measurement: 2023-07-13

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW2600
Channels	Middle
Signal	CW
Frequency (MHz)	2600.000
Relative permittivity	40.58
Conductivity (S/m)	1.92
Probe	SN 04/22 EPGO364
ConvF	2.36
Crest factor:	1:1

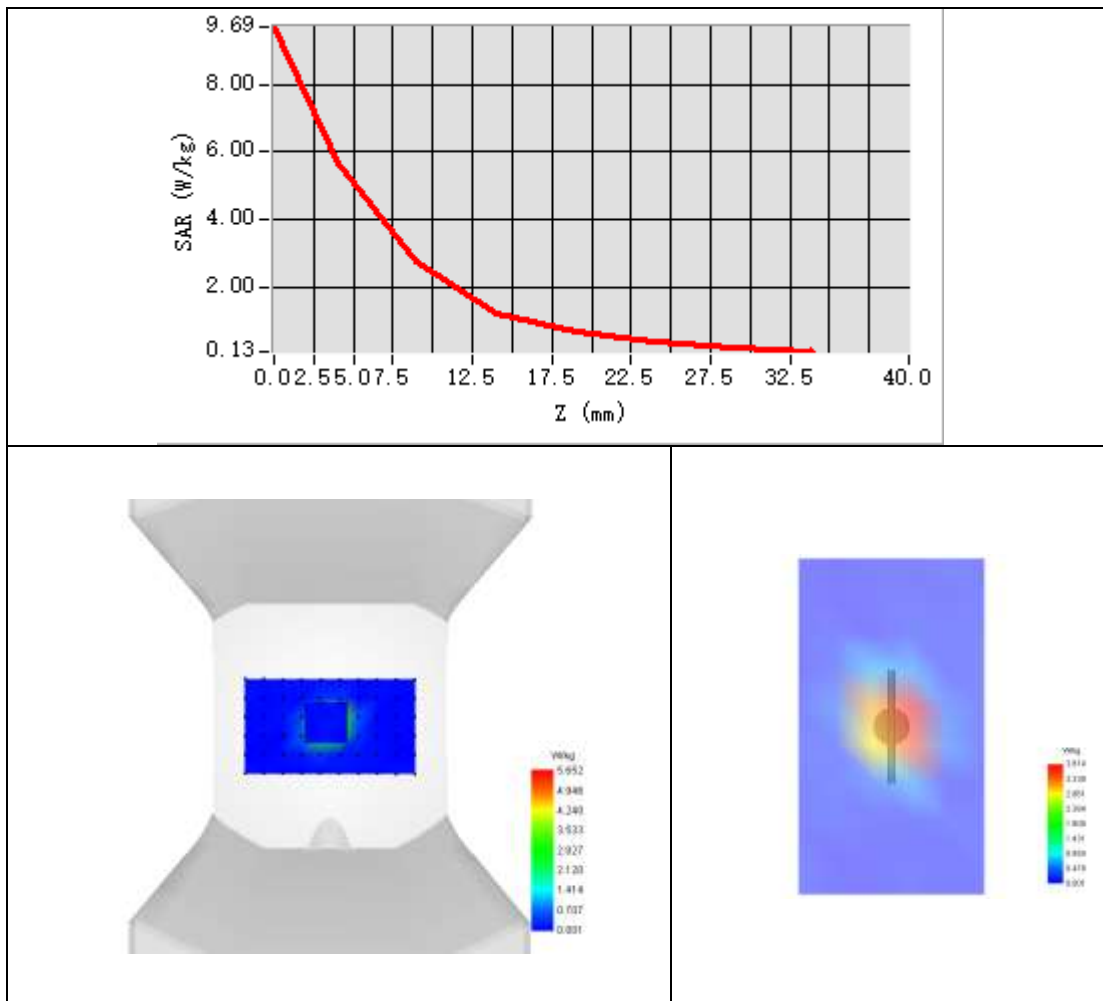


Maximum location: X=3.00, Y=3.00 ; SAR Peak: 9.85 W/kg

SAR 10g (W/Kg)	2.390
SAR 1g (W/Kg)	5.657



Z Axis Scan





System Performance Check Data (3500MHz)

Type: Phone measurement (Complete)

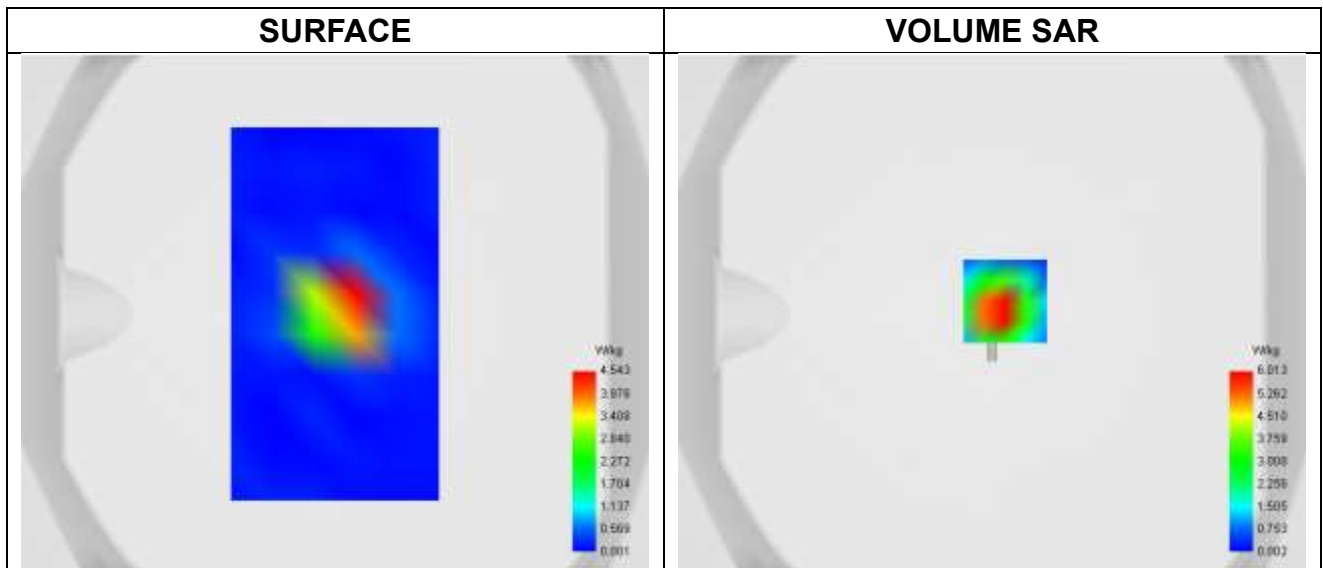
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-09

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW3500
Channels	Middle
Signal	CW
Frequency (MHz)	3500.000
Relative permittivity	38.13
Conductivity (S/m)	2.96
Probe	SN 04/22 EPGO364
ConvF	1.85
Crest factor:	1:1

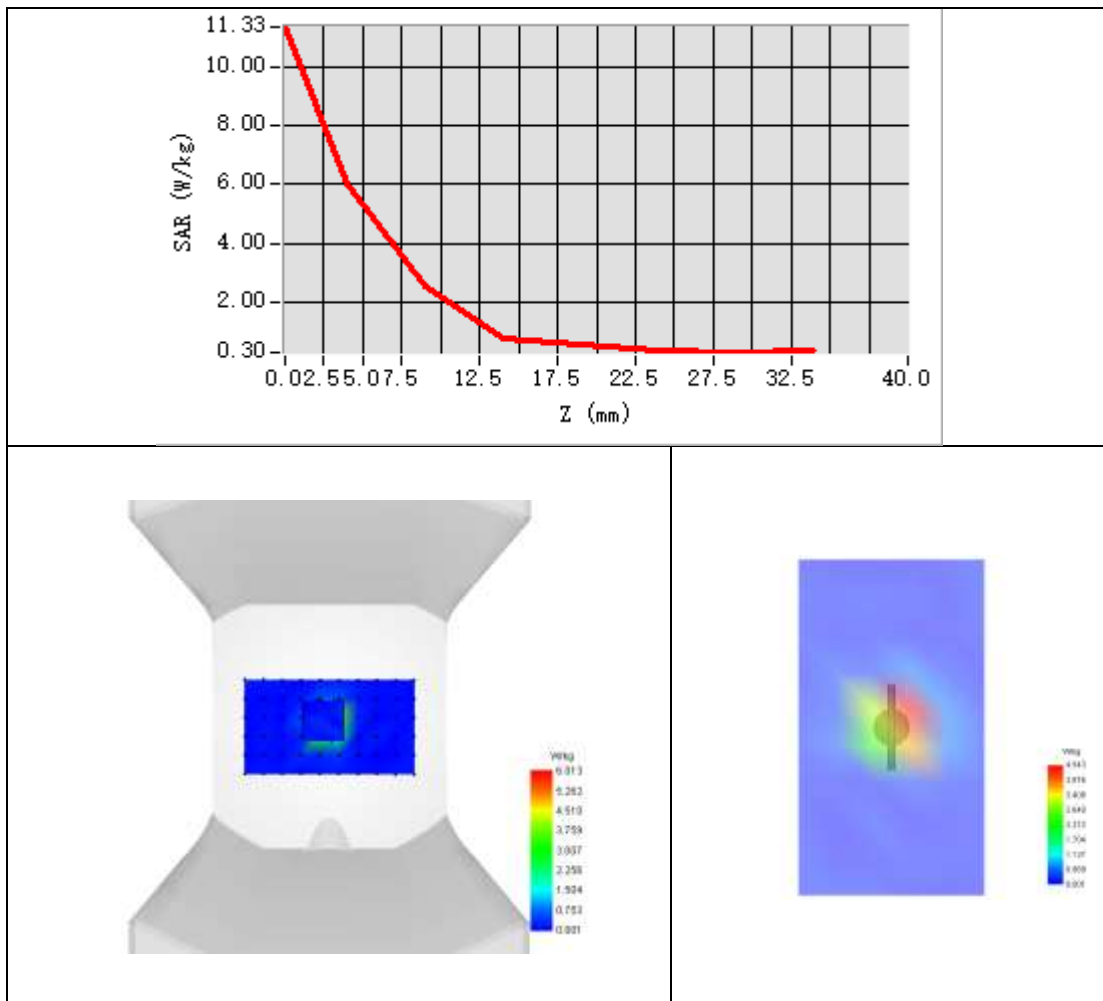


Maximum location: X=5.00, Y=5.00 ; SAR Peak: 12.88 W/kg

SAR 10g (W/Kg)	2.405
SAR 1g (W/Kg)	6.627



Z Axis Scan





System Performance Check Data (3500MHz)

Type: Phone measurement (Complete)

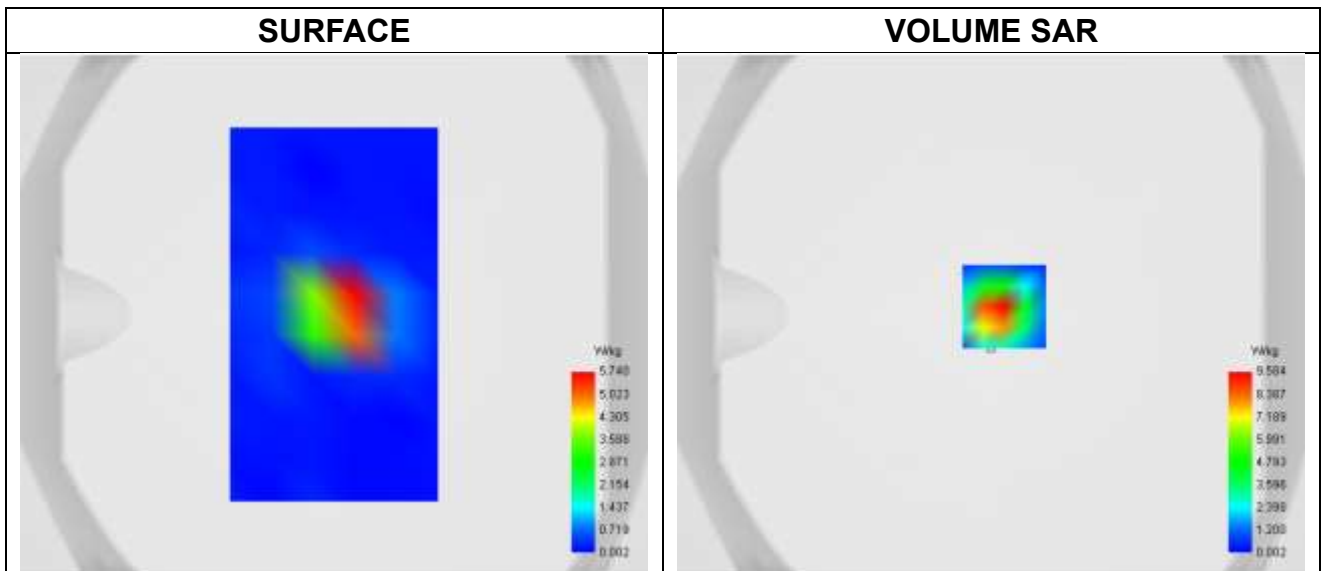
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-17

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW3500
Channels	Middle
Signal	CW
Frequency (MHz)	3500.000
Relative permittivity	37.42
Conductivity (S/m)	3.76
Probe	SN 04/22 EPGO364
ConvF	1.85
Crest factor:	1:1

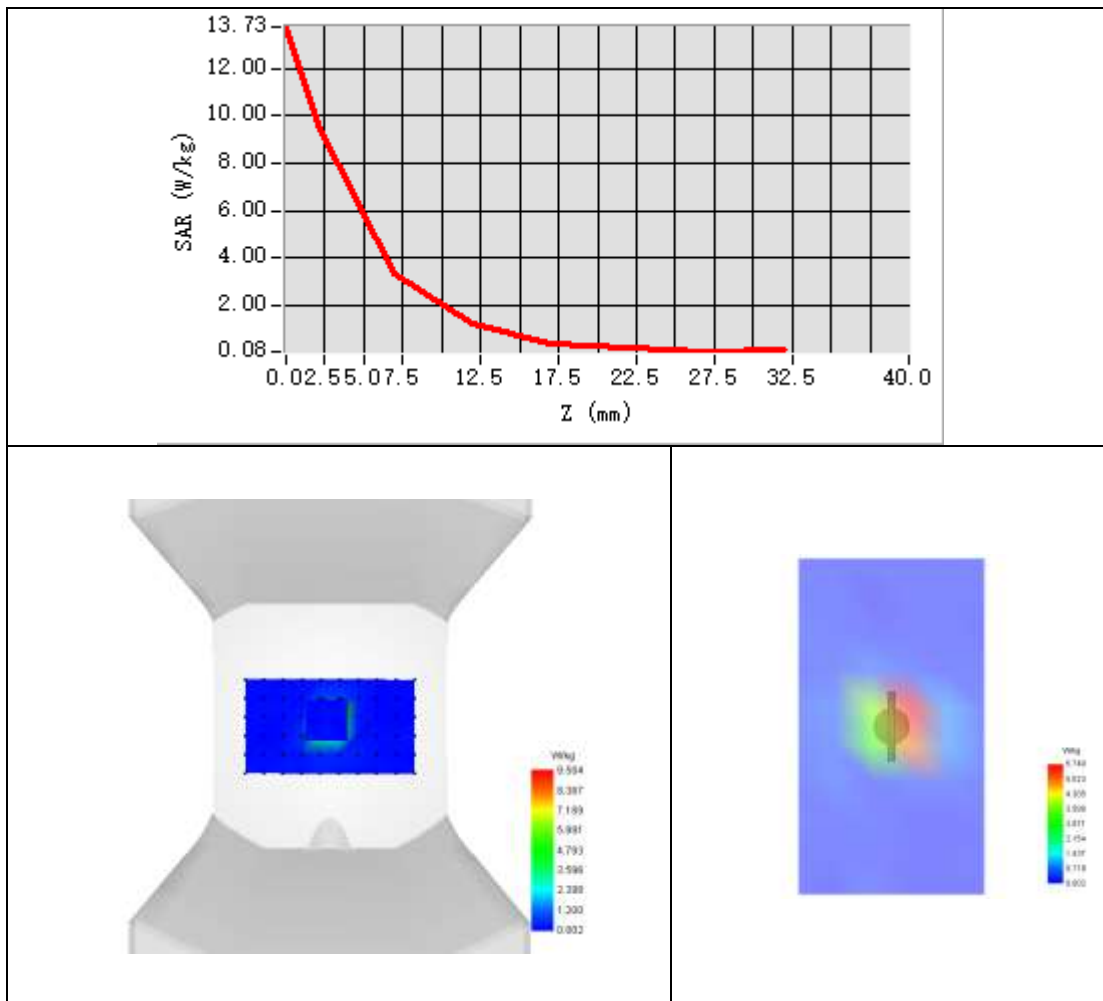


Maximum location: X=5.00, Y=3.00 ; SAR Peak: 15.87 W/kg

SAR 10g (W/Kg)	2.280
SAR 1g (W/Kg)	6.696



Z Axis Scan





System Performance Check Data (3700MHz)

Type: Phone measurement (Complete)

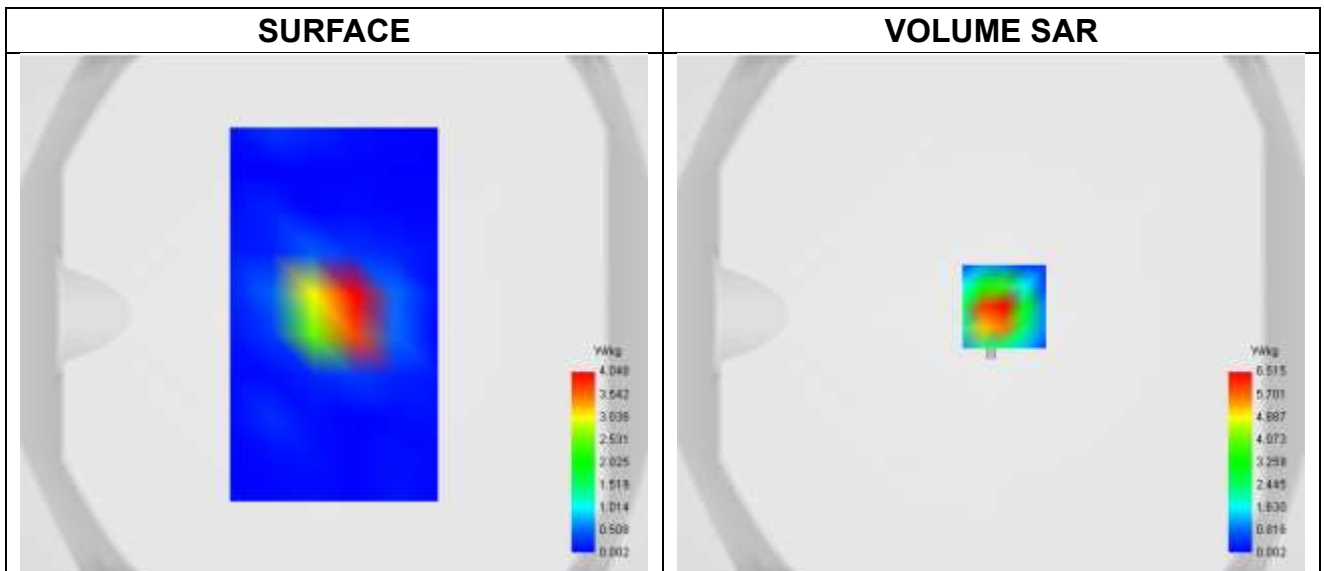
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-15

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW3700
Channels	Middle
Signal	CW
Frequency (MHz)	3700.000
Relative permittivity	38.52
Conductivity (S/m)	3.20
Probe	SN 04/22 EPGO364
ConvF	1.82
Crest factor:	1:1

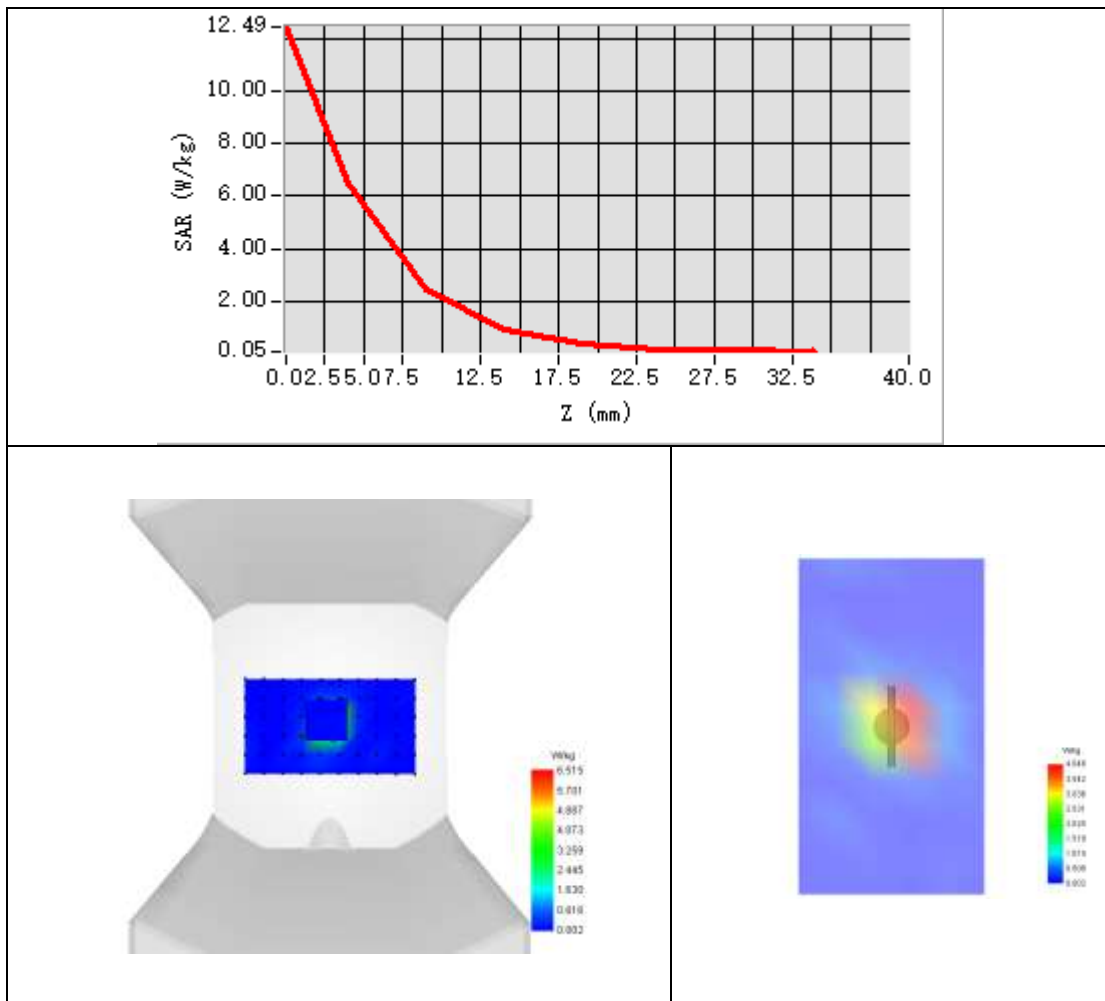


Maximum location: X=5.00, Y=3.00 ; SAR Peak: 14.04 W/kg

SAR 10g (W/Kg)	2.368
SAR 1g (W/Kg)	6.598



Z Axis Scan





System Performance Check Data (5200MHz)

Type: Phone measurement (Complete)

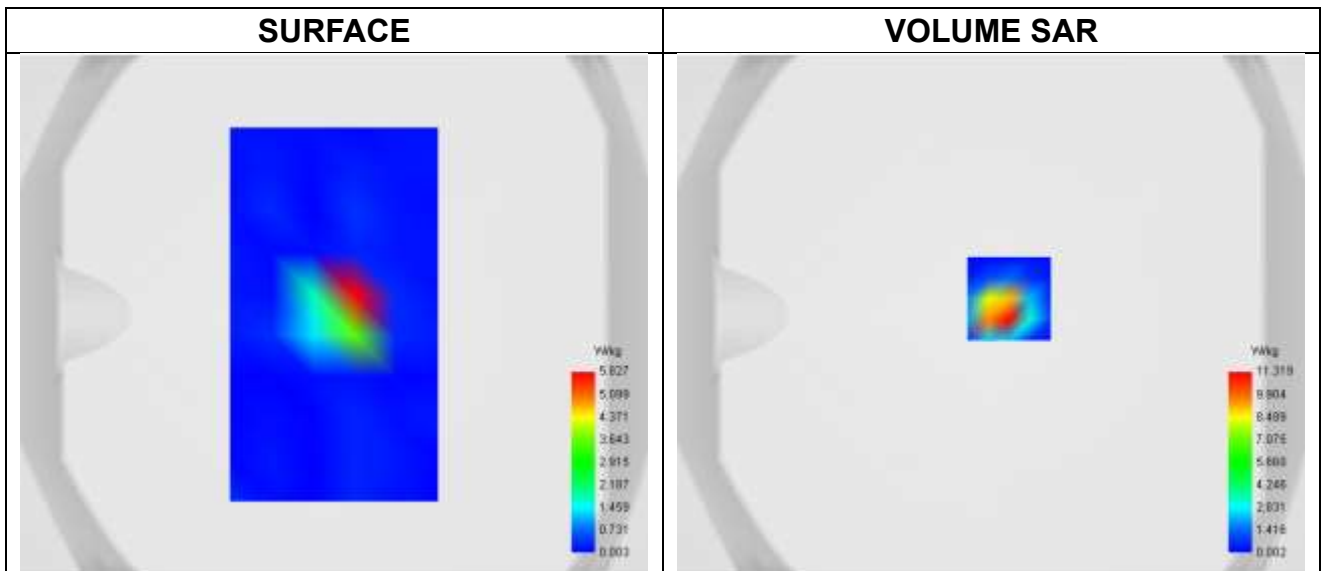
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-18

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5200
Channels	Middle
Signal	CW
Frequency (MHz)	5200.000
Relative permittivity	36.37
Conductivity (S/m)	4.68
Probe	SN 04/22 EPGO364
ConvF	1.95
Crest factor:	1:1

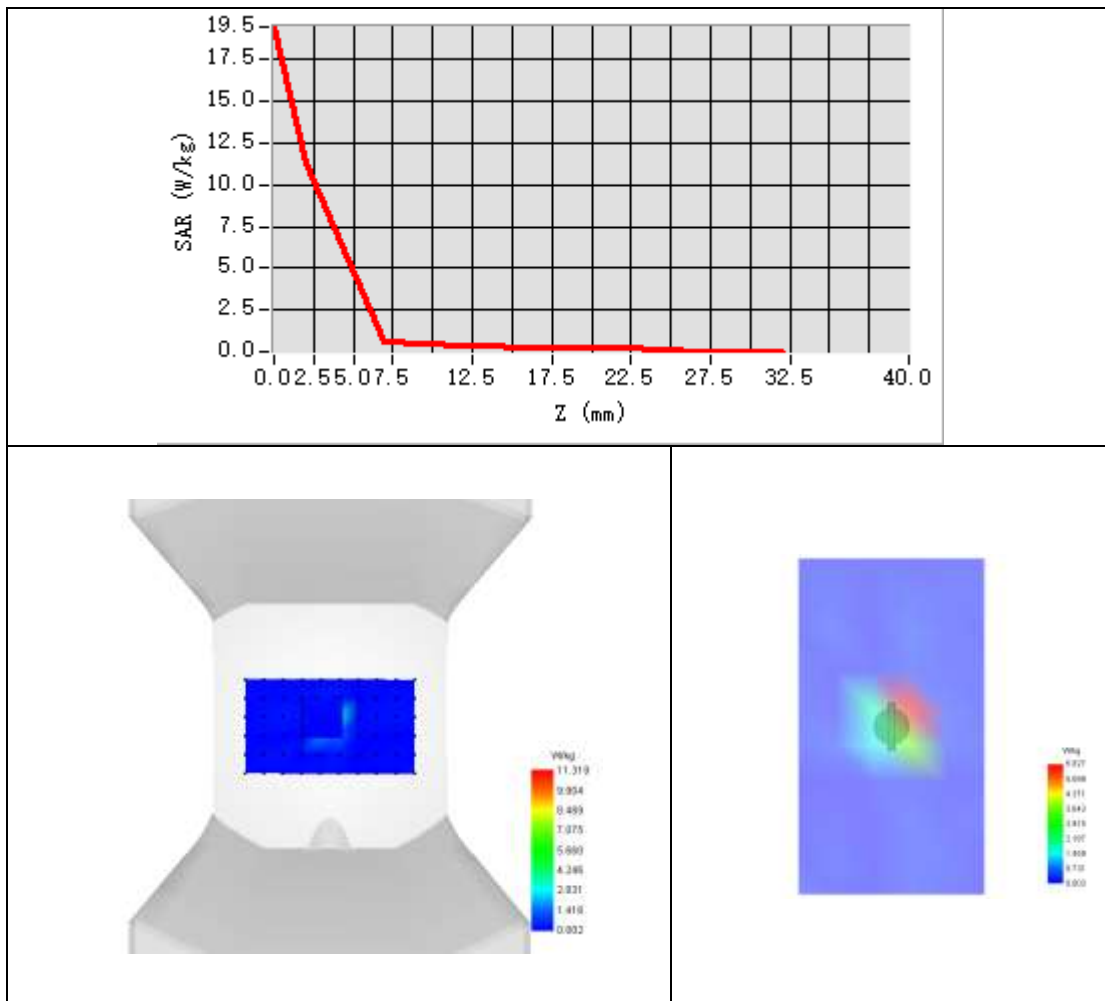


Maximum location: X=7.00, Y=6.00 ; SAR Peak: 21.88 W/kg

SAR 10g (W/Kg)	2.198
SAR 1g (W/Kg)	7.773



Z Axis Scan





System Performance Check Data (5200MHz)

Type: Phone measurement (Complete)

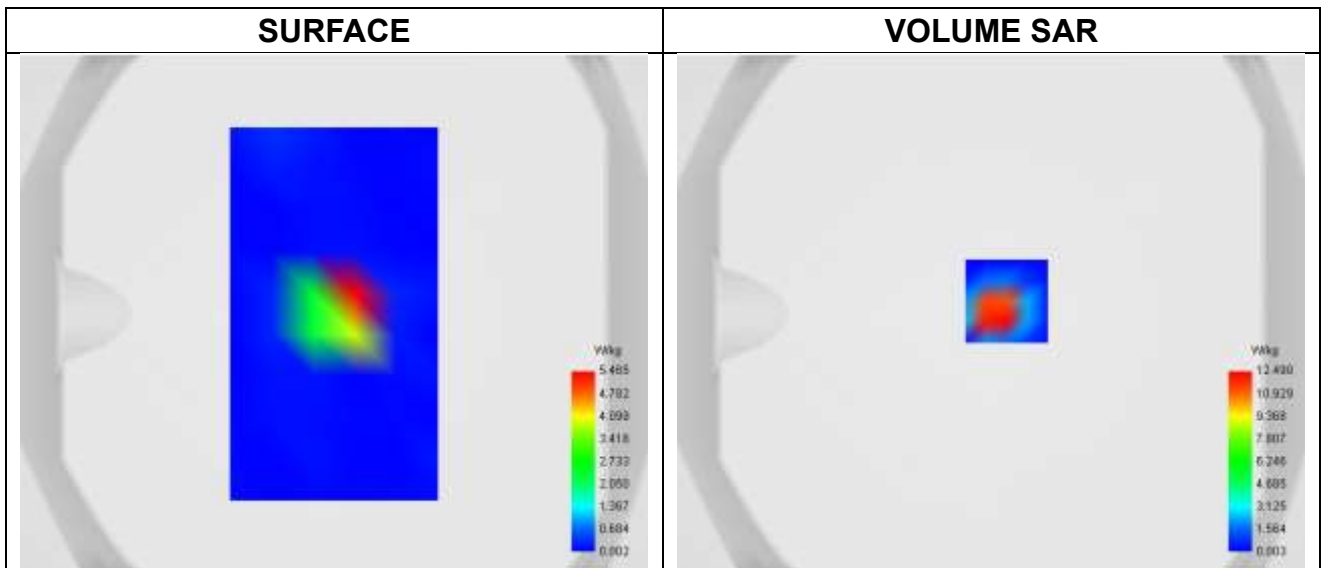
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-23

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5200
Channels	Middle
Signal	CW
Frequency (MHz)	5200.000
Relative permittivity	37.16
Conductivity (S/m)	4.68
Probe	SN 04/22 EPGO364
ConvF	1.91
Crest factor:	1:1

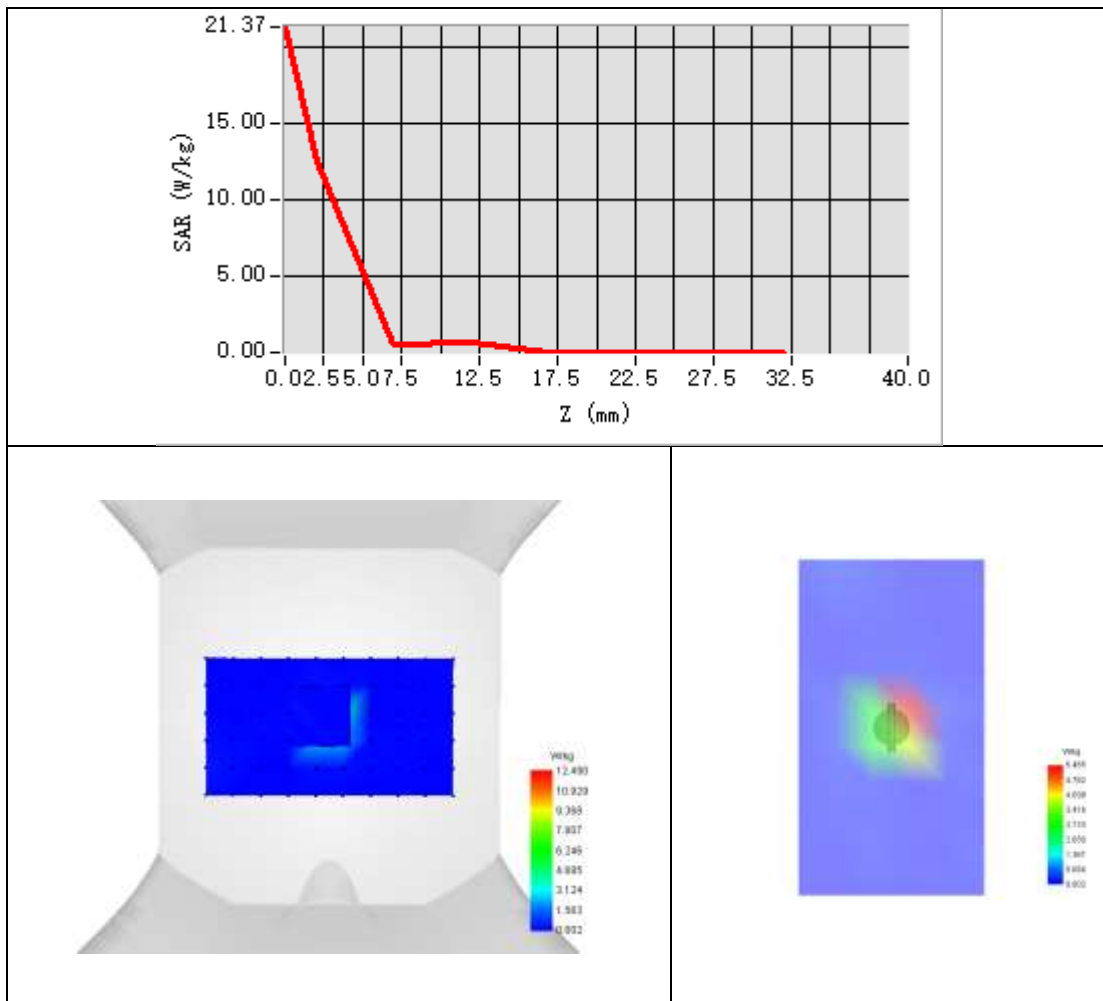


Maximum location: X=6.00, Y=5.00 ; SAR Peak: 26.47 W/kg

SAR 10g (W/Kg)	1.883
SAR 1g (W/Kg)	7.275



Z Axis Scan





System Performance Check Data (5300MHz)

Type: Phone measurement (Complete)

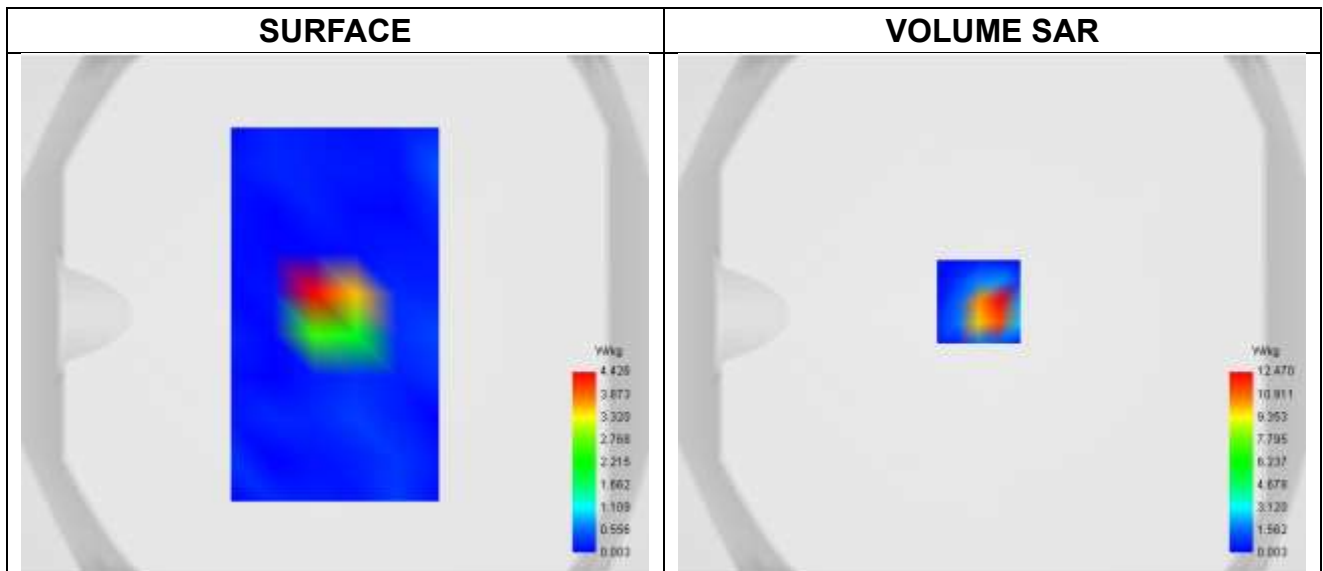
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-21

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5300
Channels	Middle
Signal	CW
Frequency (MHz)	5300.000
Relative permittivity	36.31
Conductivity (S/m)	4.81
Probe	SN 04/22 EPGO364
ConvF	1.85
Crest factor:	1:1

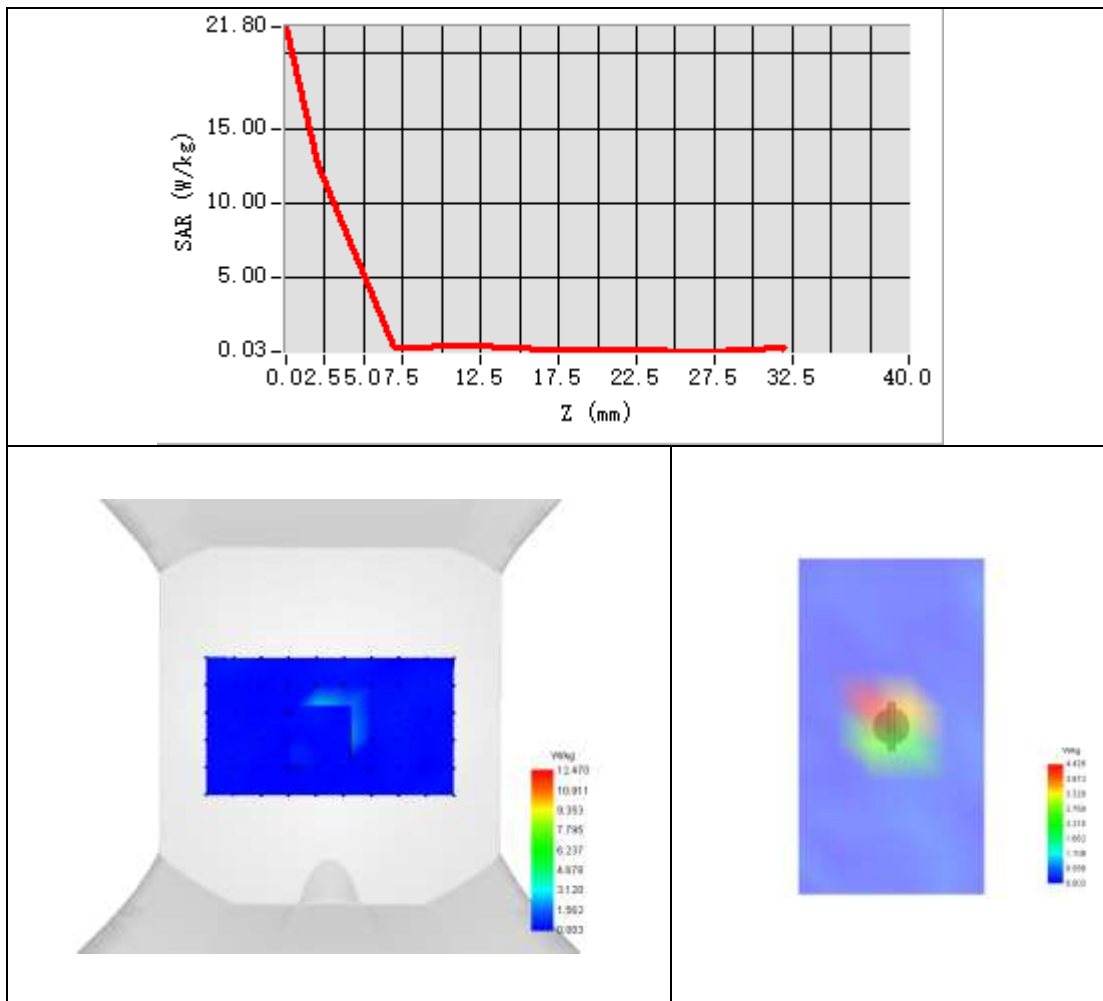


Maximum location: X=-5.00, Y=5.00 ; SAR Peak: 25.45 W/kg

SAR 10g (W/Kg)	2.224
SAR 1g (W/Kg)	8.048



Z Axis Scan





System Performance Check Data (5300MHz)

Type: Phone measurement (Complete)

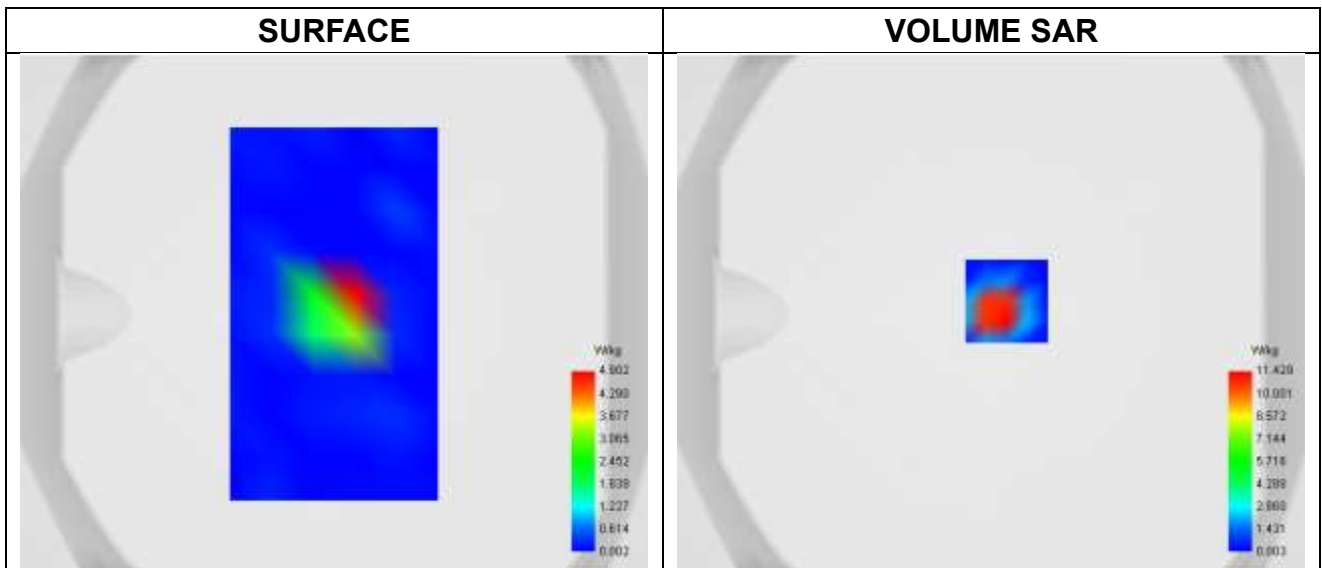
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-22

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5300
Channels	Middle
Signal	CW
Frequency (MHz)	5300.000
Relative permittivity	37.13
Conductivity (S/m)	4.78
Probe	SN 04/22 EPGO364
ConvF	1.80
Crest factor:	1:1

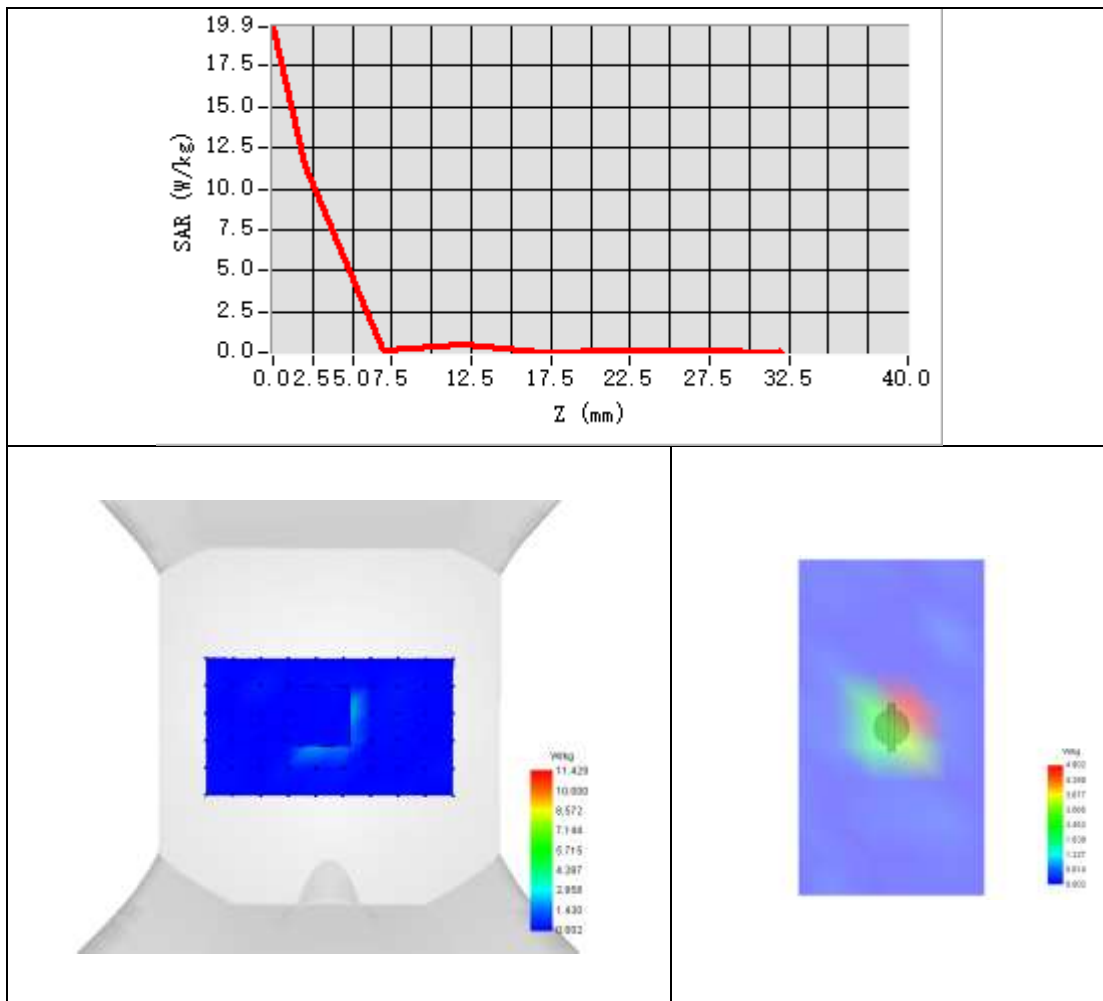


Maximum location: X=6.00, Y=5.00 ; SAR Peak: 24.70 W/kg

SAR 10g (W/Kg)	1.930
SAR 1g (W/Kg)	7.790



Z Axis Scan





System Performance Check Data (5600MHz)

Type: Phone measurement (Complete)

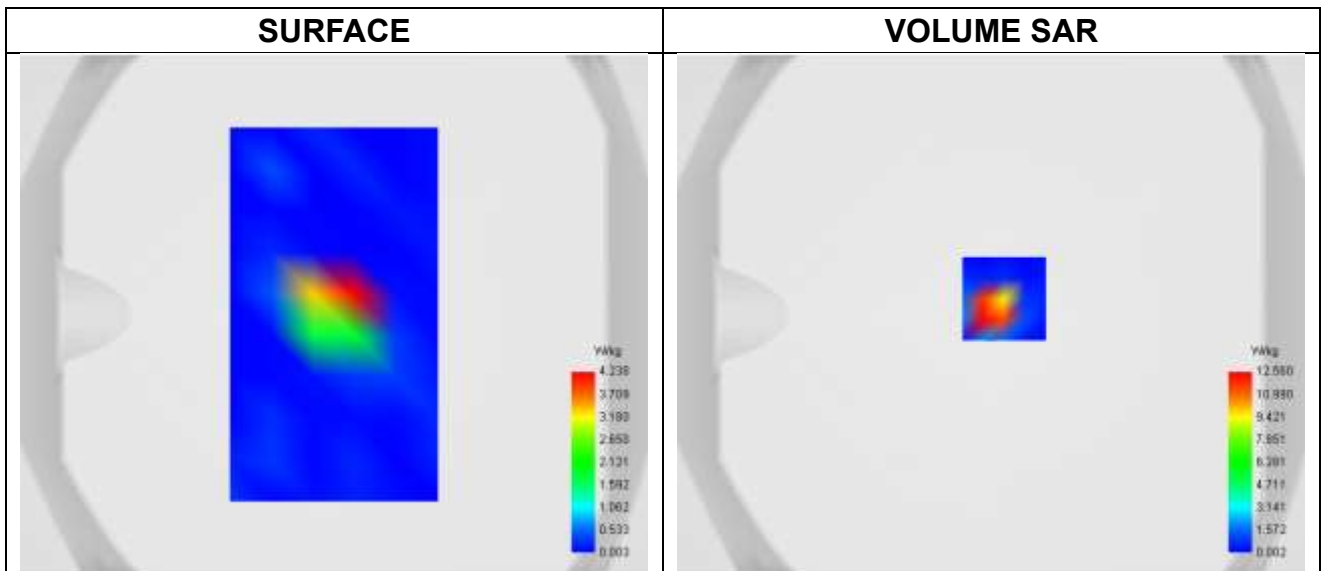
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-24

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5600
Channels	Middle
Signal	CW
Frequency (MHz)	5600.000
Relative permittivity	36.73
Conductivity (S/m)	5.09
Probe	SN 04/22 EPGO364
ConvF	1.86
Crest factor:	1:1

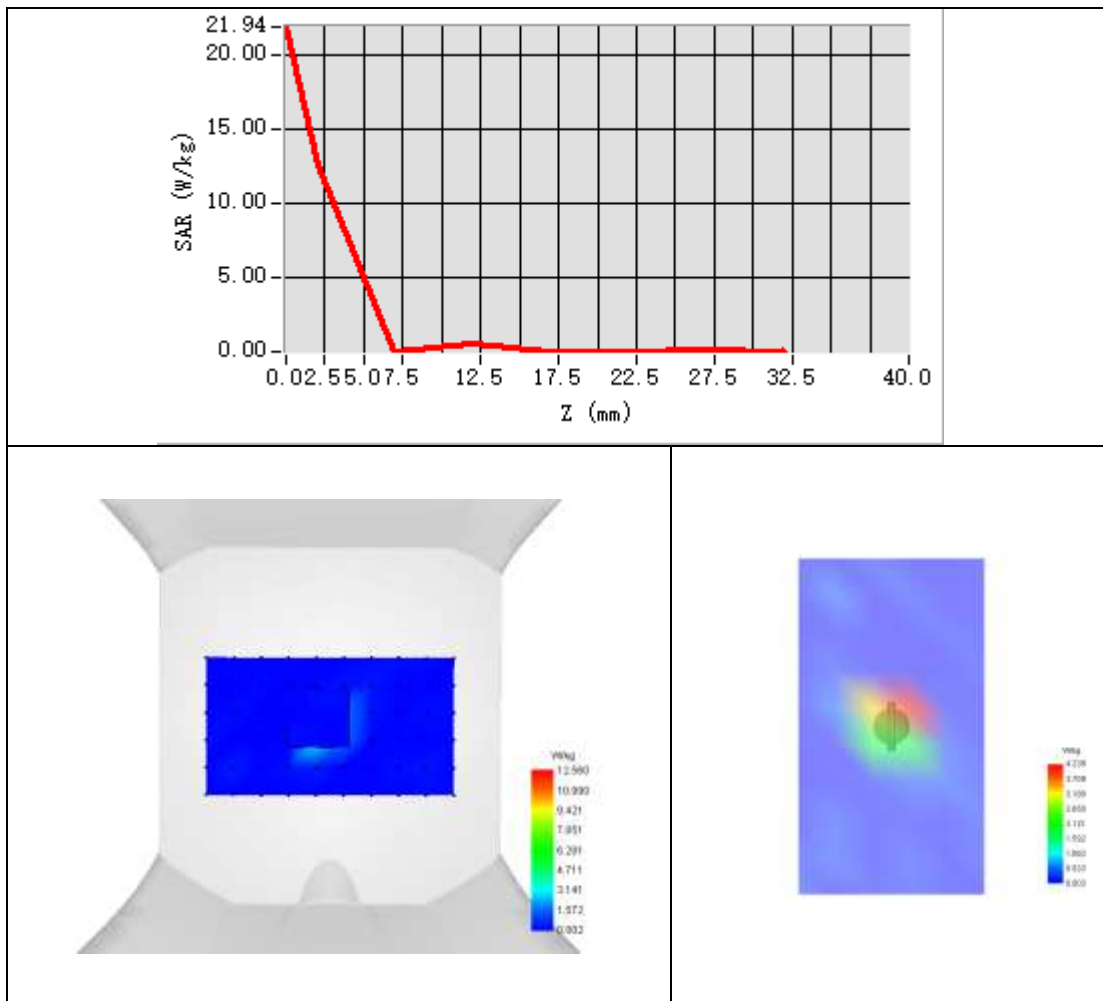


Maximum location: X=5.00, Y=6.00 ; SAR Peak: 27.34 W/kg

SAR 10g (W/Kg)	2.257
SAR 1g (W/Kg)	7.857



Z Axis Scan





System Performance Check Data (5600MHz)

Type: Phone measurement (Complete)

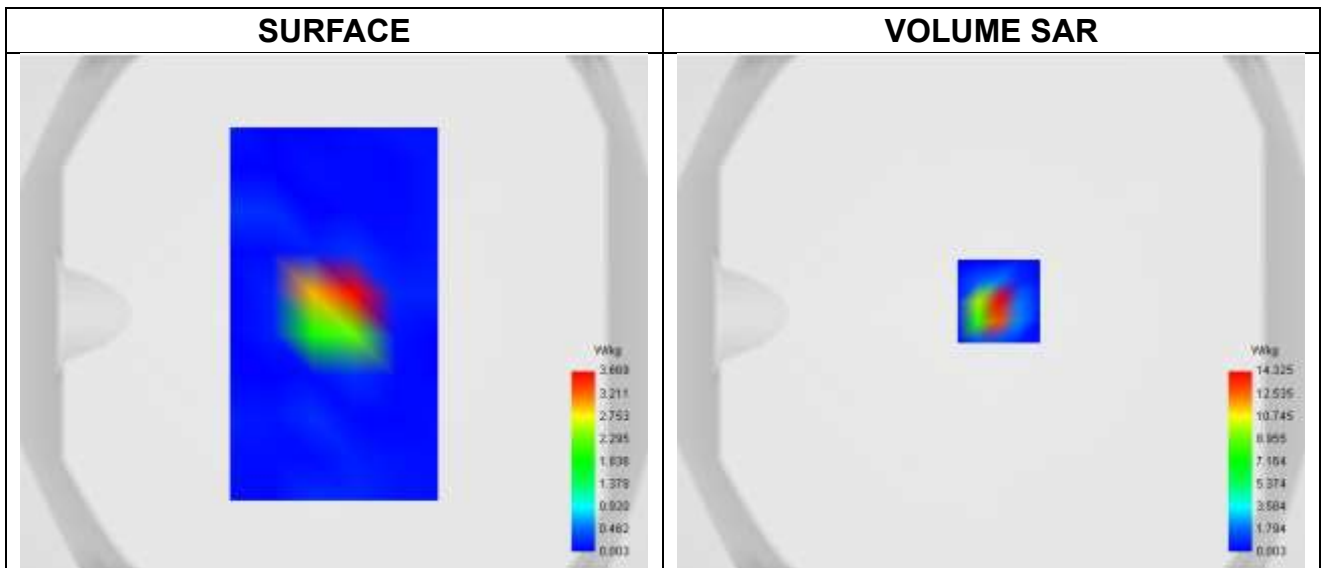
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-25

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5600
Channels	Middle
Signal	CW
Frequency (MHz)	5600.000
Relative permittivity	36.09
Conductivity (S/m)	5.06
Probe	SN 04/22 EPGO364
ConvF	1.76
Crest factor:	1:1

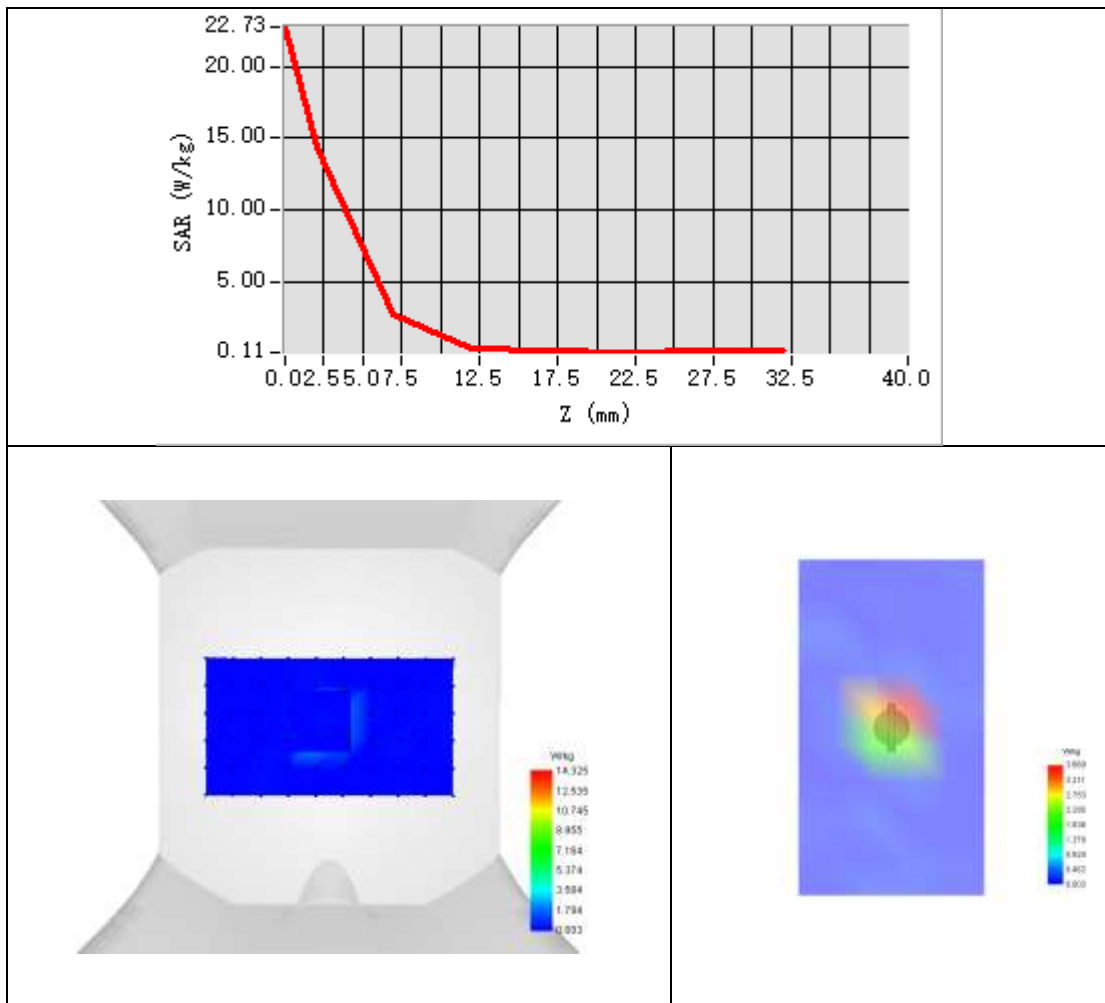


Maximum location: X=3.00, Y=5.00 ; SAR Peak: 28.11 W/kg

SAR 10g (W/Kg)	2.094
SAR 1g (W/Kg)	8.306



Z Axis Scan





System Performance Check Data (5800MHz)

Type: Phone measurement (Complete)

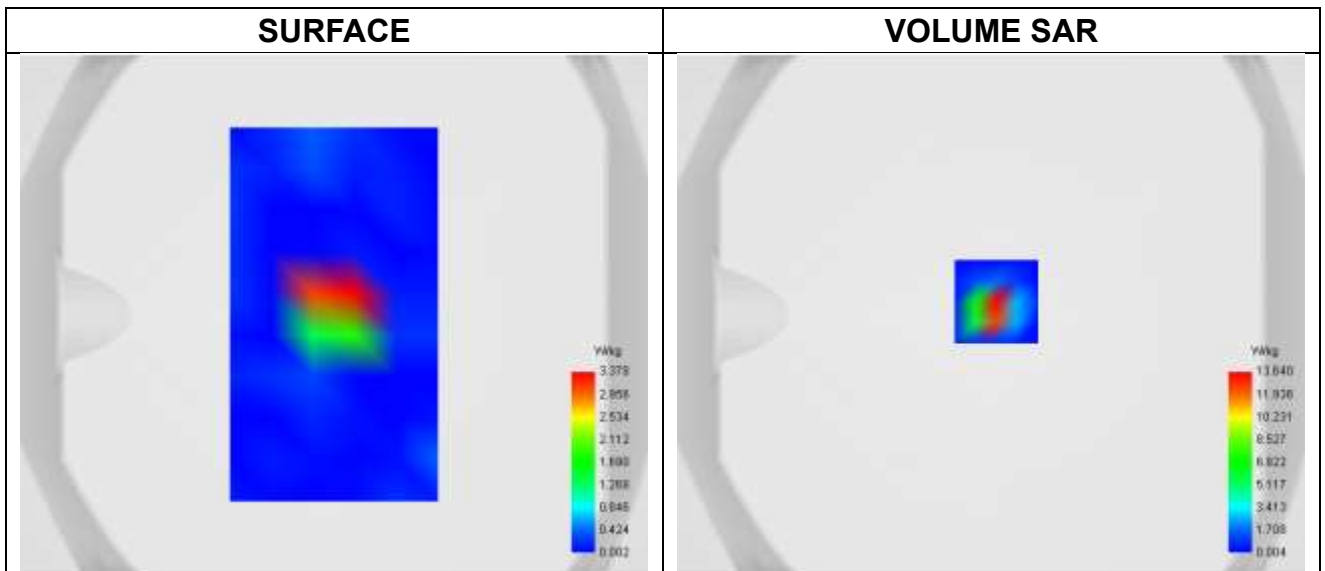
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-26

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5800
Channels	Middle
Signal	CW
Frequency (MHz)	5800.000
Relative permittivity	35.87
Conductivity (S/m)	5.23
Probe	SN 04/22 EPGO364
ConvF	1.73
Crest factor:	1:1

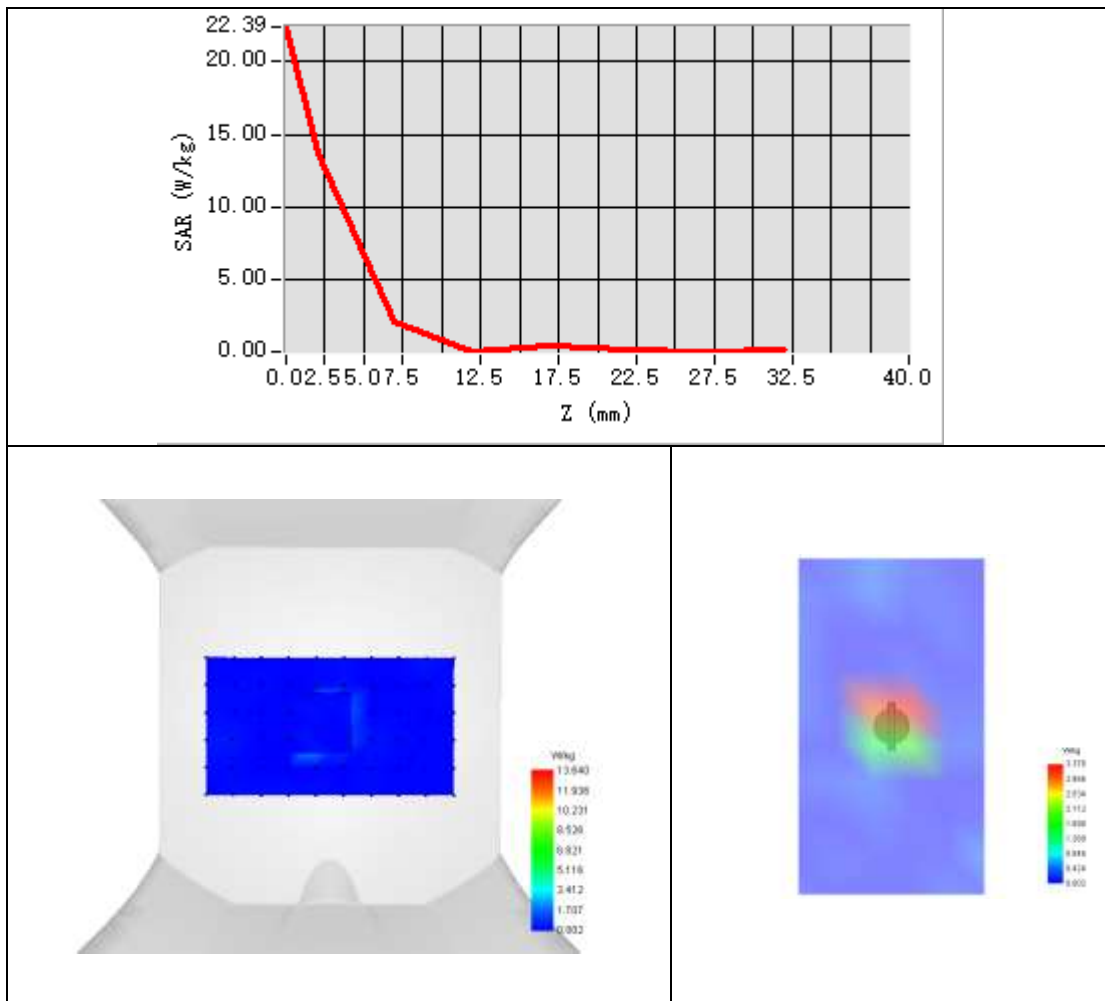


Maximum location: X=2.00, Y=5.00 ; SAR Peak: 27.22 W/kg

SAR 10g (W/Kg)	2.110
SAR 1g (W/Kg)	7.503



Z Axis Scan





System Performance Check Data (5800MHz)

Type: Phone measurement (Complete)

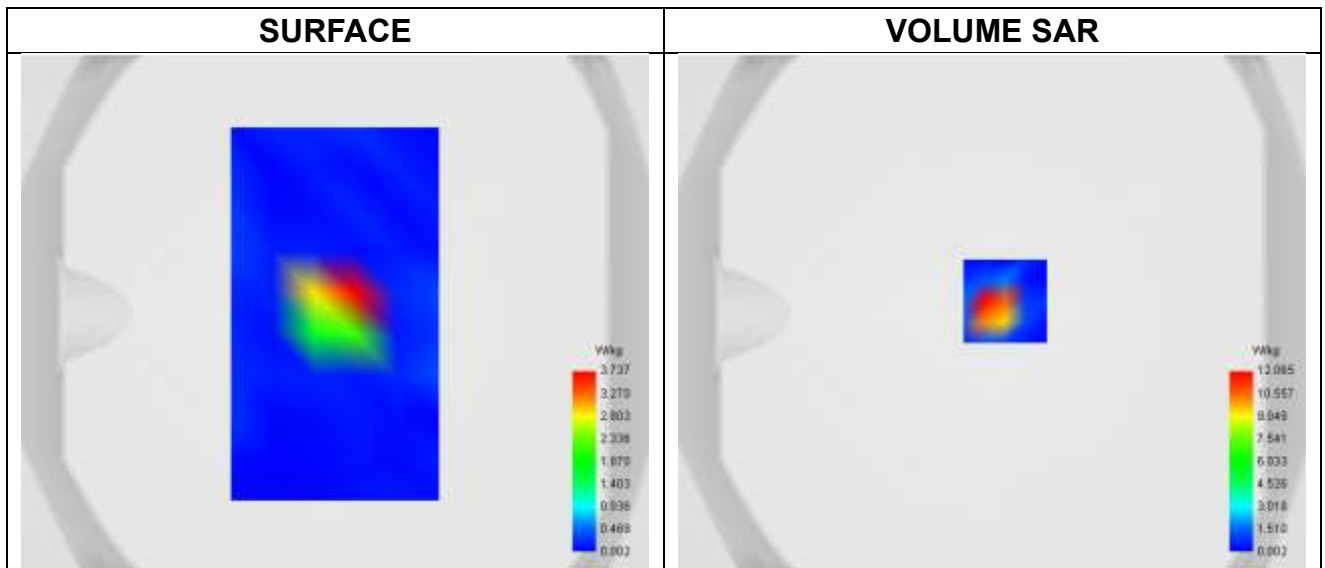
Area scan resolution: dx=8mm, dy=8mm

Zoom scan resolution: dx=4mm, dy=4mm, dz=2mm

Date of measurement: 2023-07-08

Experimental conditions.

Phantom	Validation plane
Device Position	Dipole
Band	CW5800
Channels	Middle
Signal	CW
Frequency (MHz)	5800.000
Relative permittivity	35.61
Conductivity (S/m)	5.30
Probe	SN 04/22 EPGO364
ConvF	1.70
Crest factor:	1:1

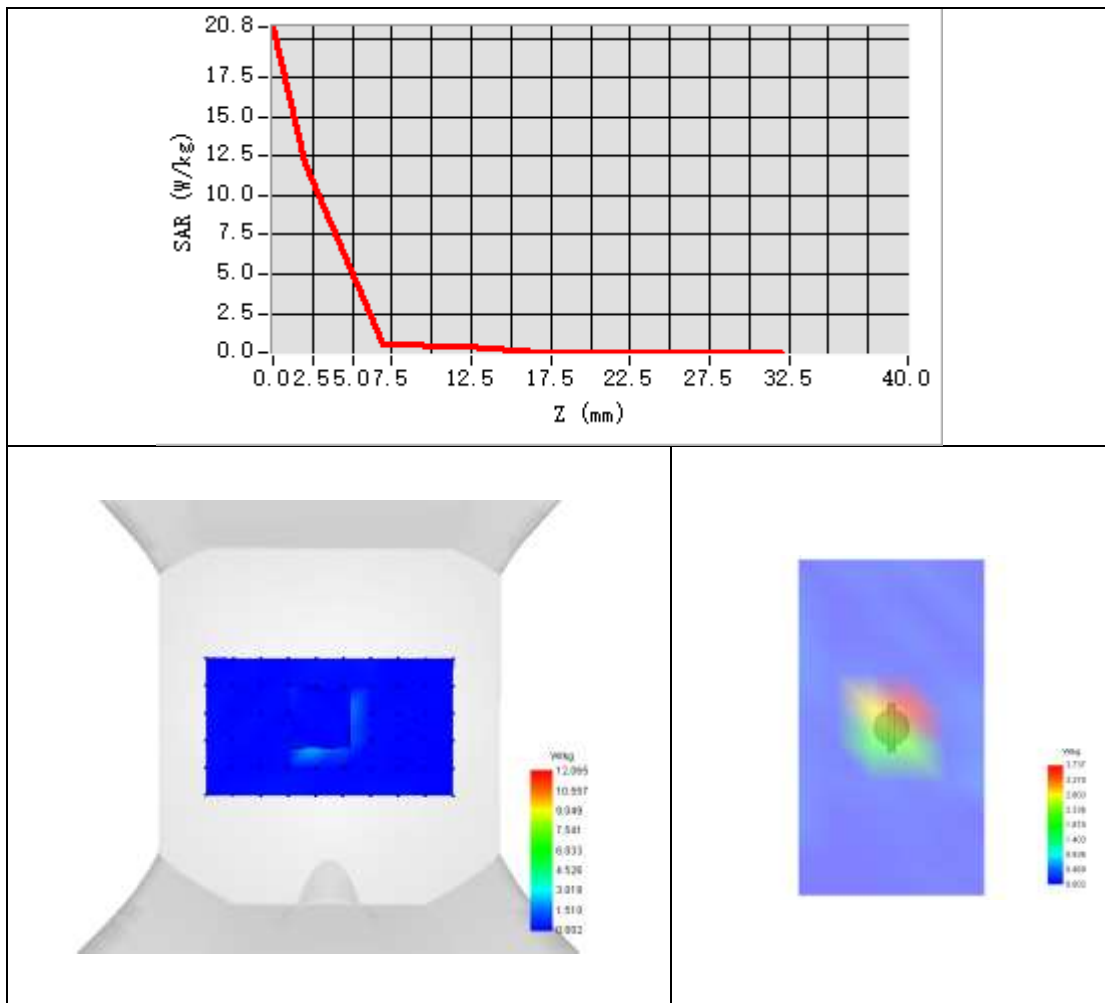


Maximum location: X=5.00, Y=5.00 ; SAR Peak: 25.68 W/kg

SAR 10g (W/Kg)	1.883
SAR 1g (W/Kg)	7.275



Z Axis Scan



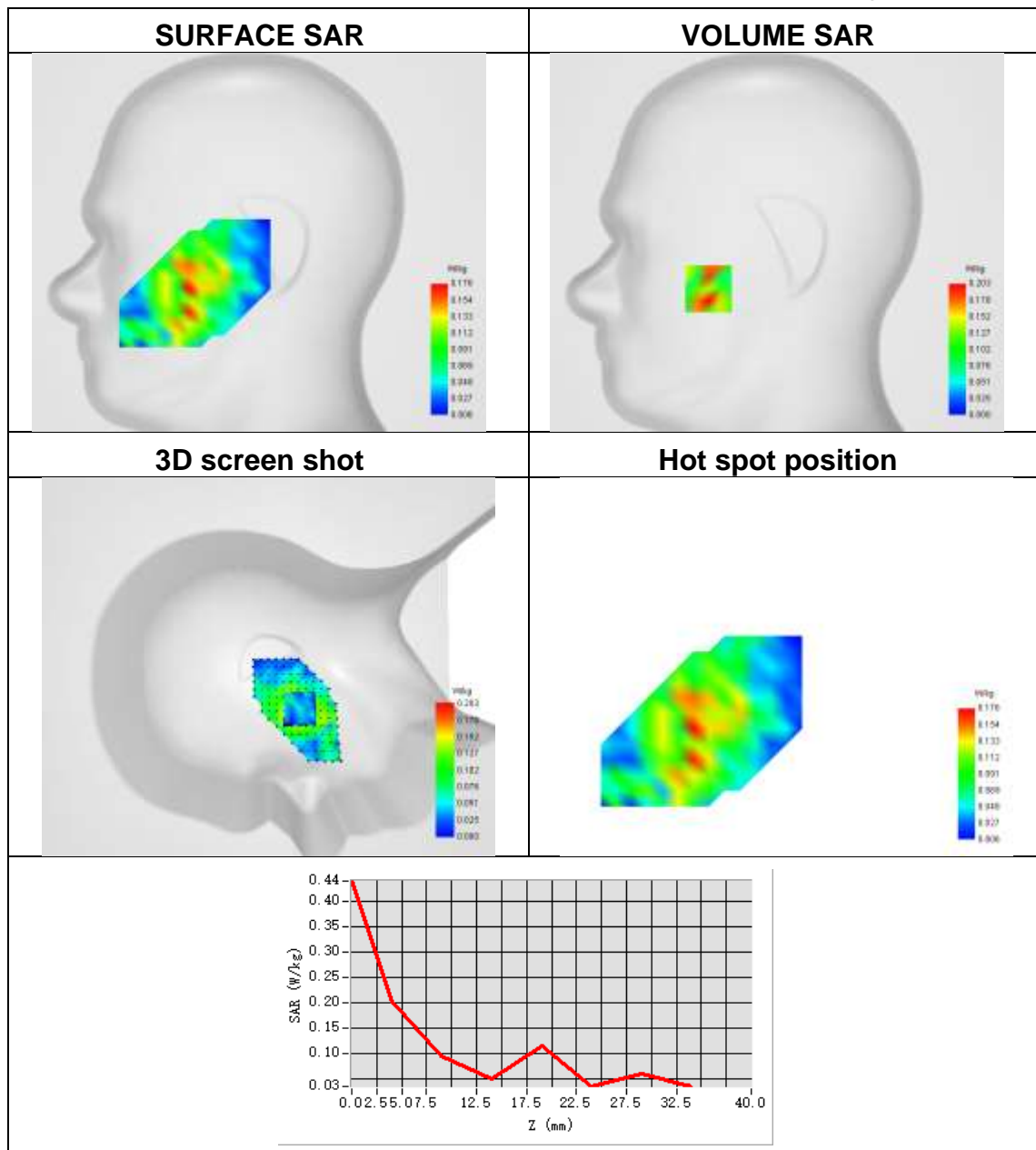


Appendix B. SAR Test Plots

Plot 1:

Test Date	2023-07-17
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right Cheek
Device Position	Cheek
Band	GSM850
Signal	TDMA (GSM)
Frequency	848.8
SAR 10g (W/Kg)	0.098
SAR 1g (W/Kg)	0.202

Maximum location: X=-47.00, Y=-32.00 ; SAR Peak: 0.52 W/kg

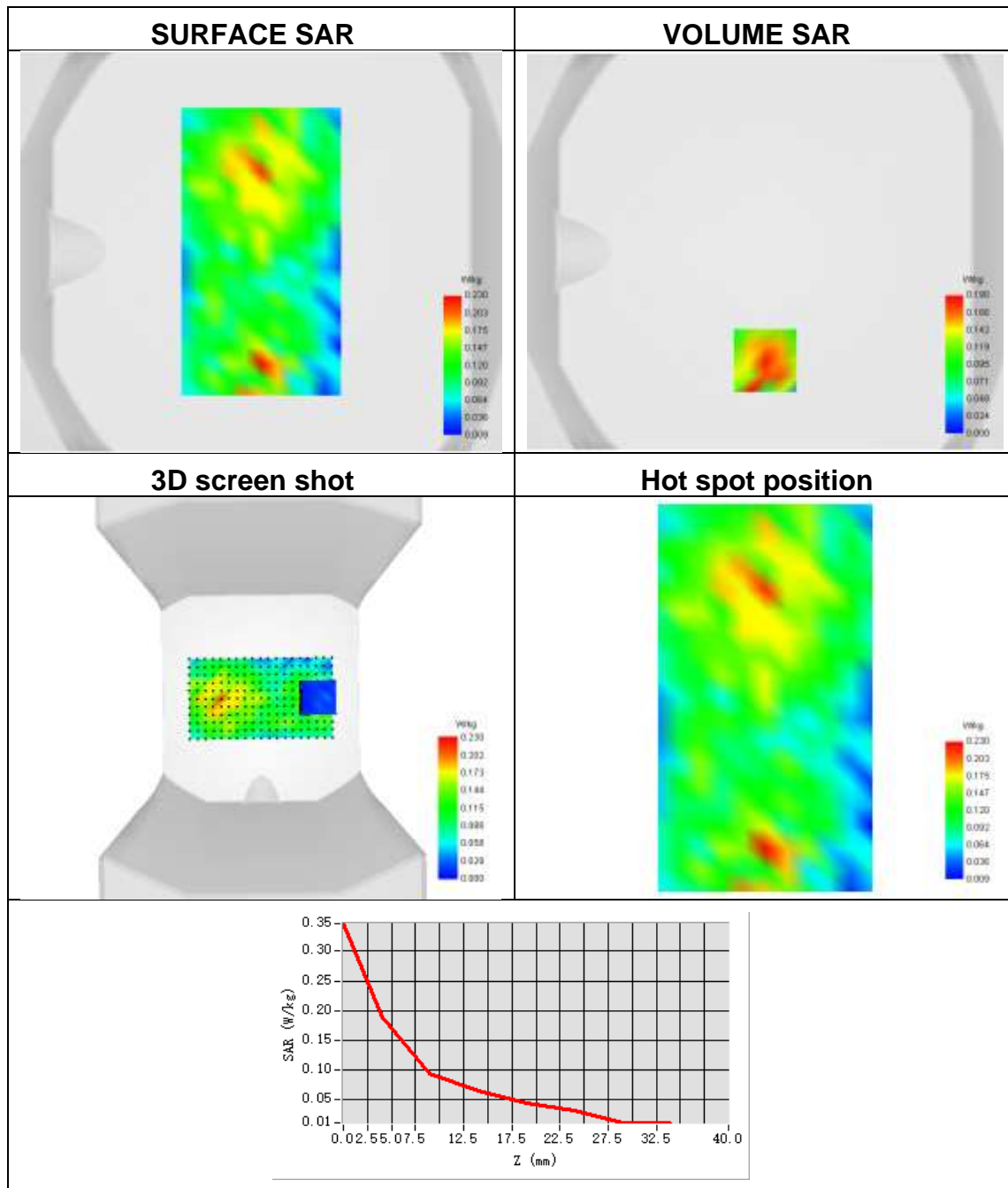




Plot 2:

Test Date	2023-07-17
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	GPRS850
Signal	TDMA (EGPRS)
Frequency	848.8
SAR 10g (W/Kg)	0.102
SAR 1g (W/Kg)	0.200

Maximum location: X=0.00, Y=-55.00 ; SAR Peak: 0.37 W/kg

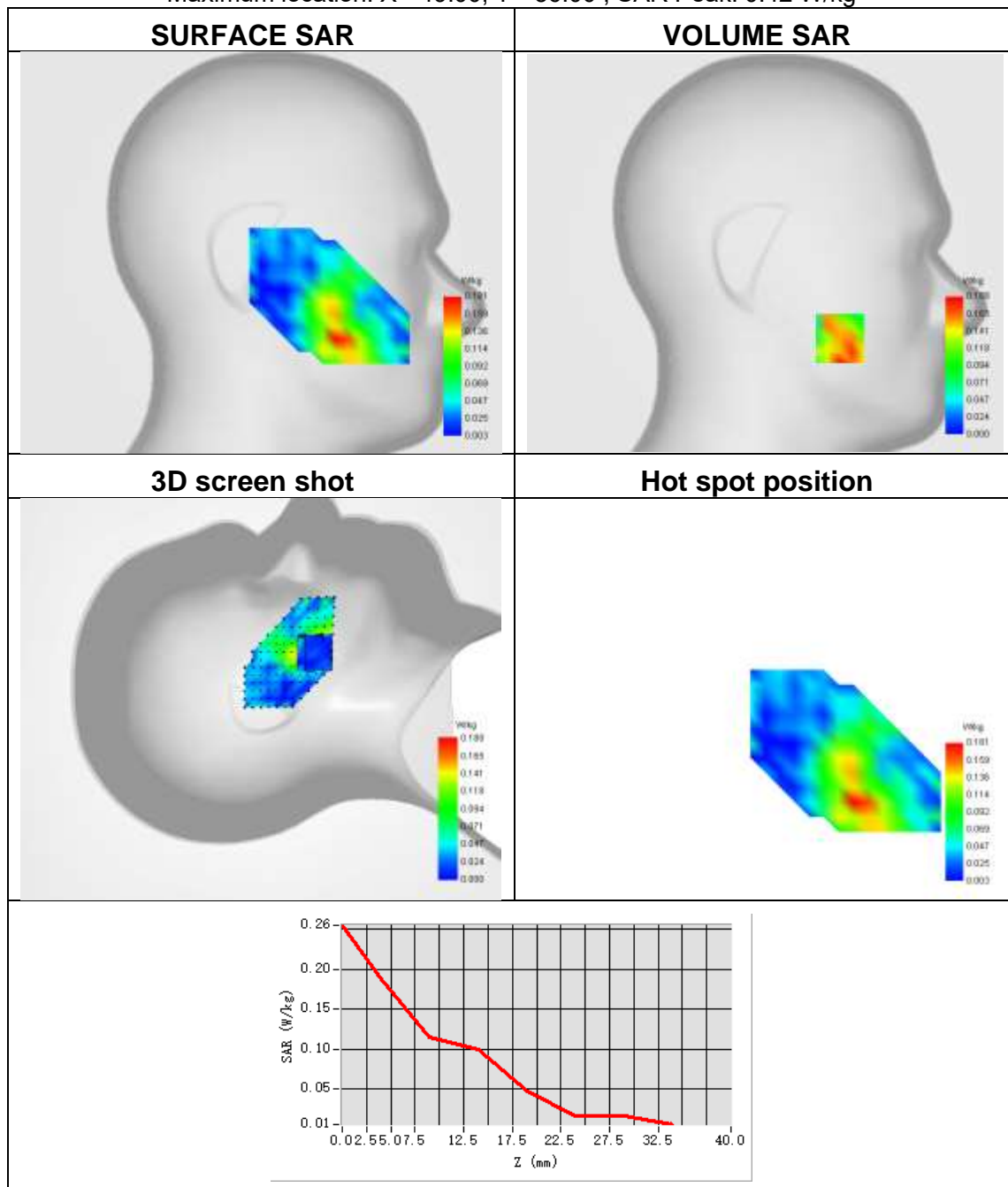




Plot 3:

Test Date	2023-07-12
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	GSM1900
Signal	TDMA (GSM)
Frequency	1909.8
SAR 10g (W/Kg)	0.083
SAR 1g (W/Kg)	0.179

Maximum location: X=-49.00, Y=-56.00 ; SAR Peak: 0.42 W/kg

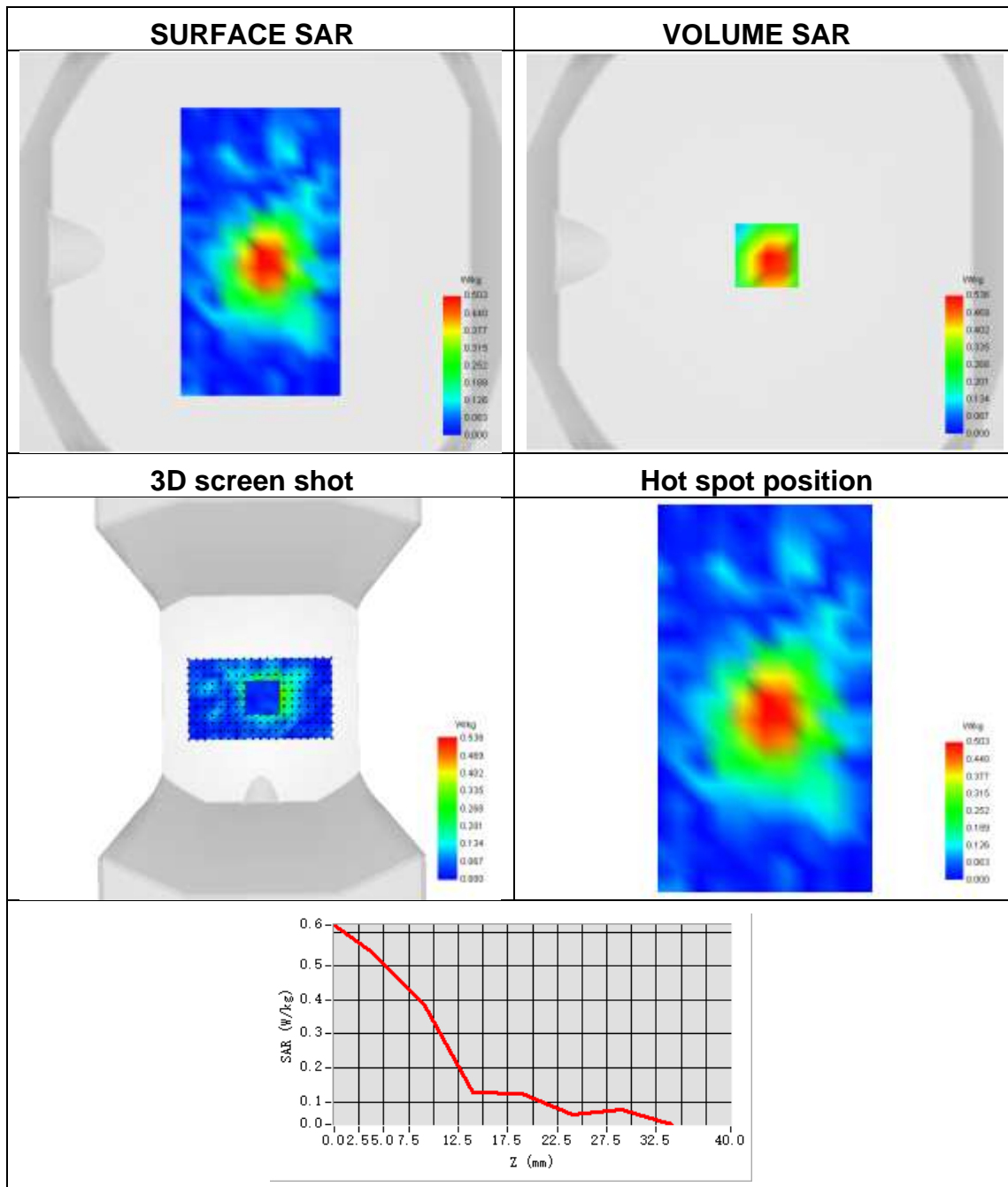




Plot 4:

Test Date	2023-07-12
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	GPRS1900
Signal	TDMA (EGPRS)
Frequency	1880
SAR 10g (W/Kg)	0.267
SAR 1g (W/Kg)	0.499

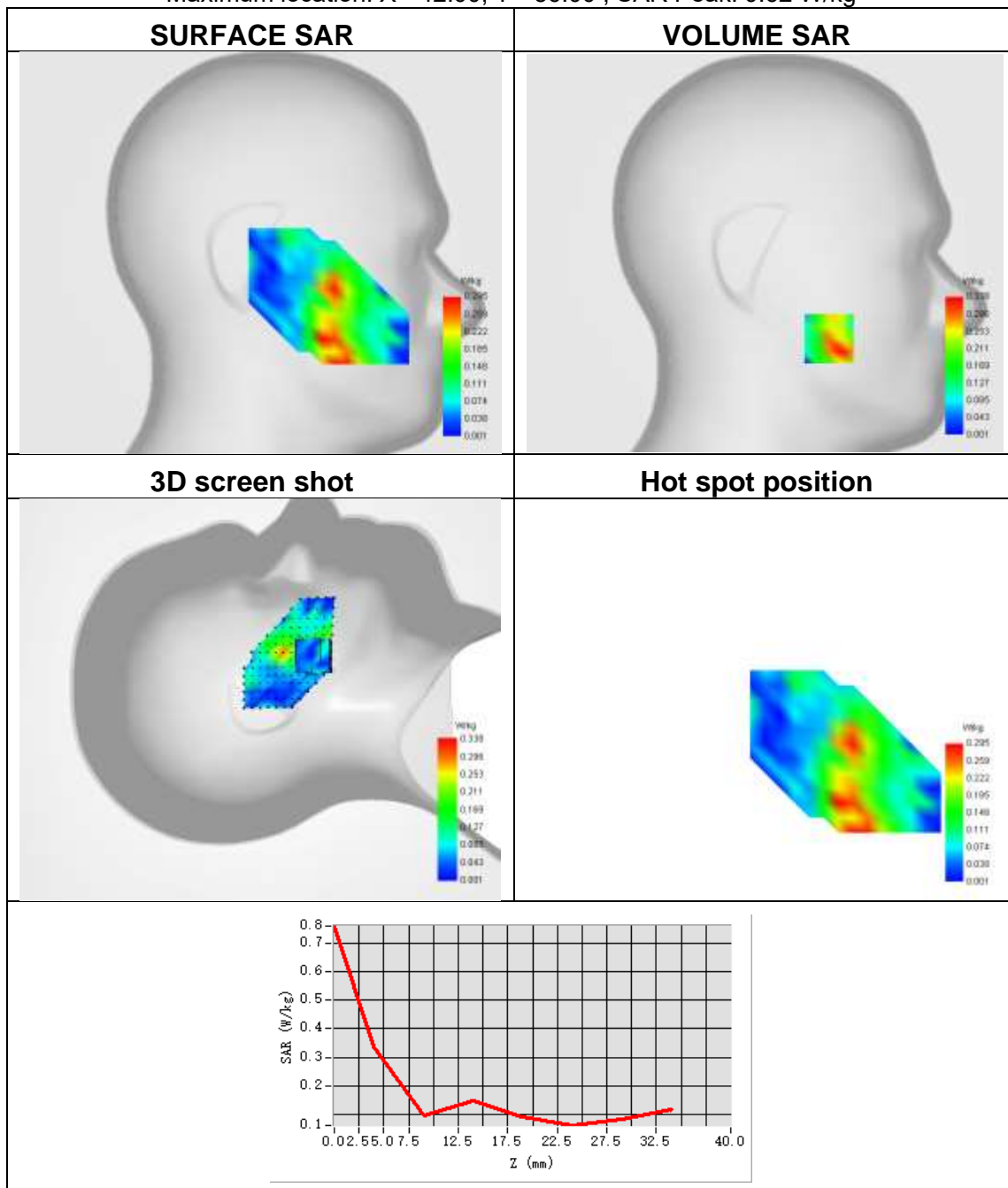
Maximum location: X=1.00, Y=-2.00 ; SAR Peak: 0.88 W/kg



Plot 5:

Test Date	2023-07-12
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	Band 2 (1900)
Signal	WCDMA
Frequency	1880
SAR 10g (W/Kg)	0.148
SAR 1g (W/Kg)	0.319

Maximum location: X=-42.00, Y=-56.00 ; SAR Peak: 0.62 W/kg

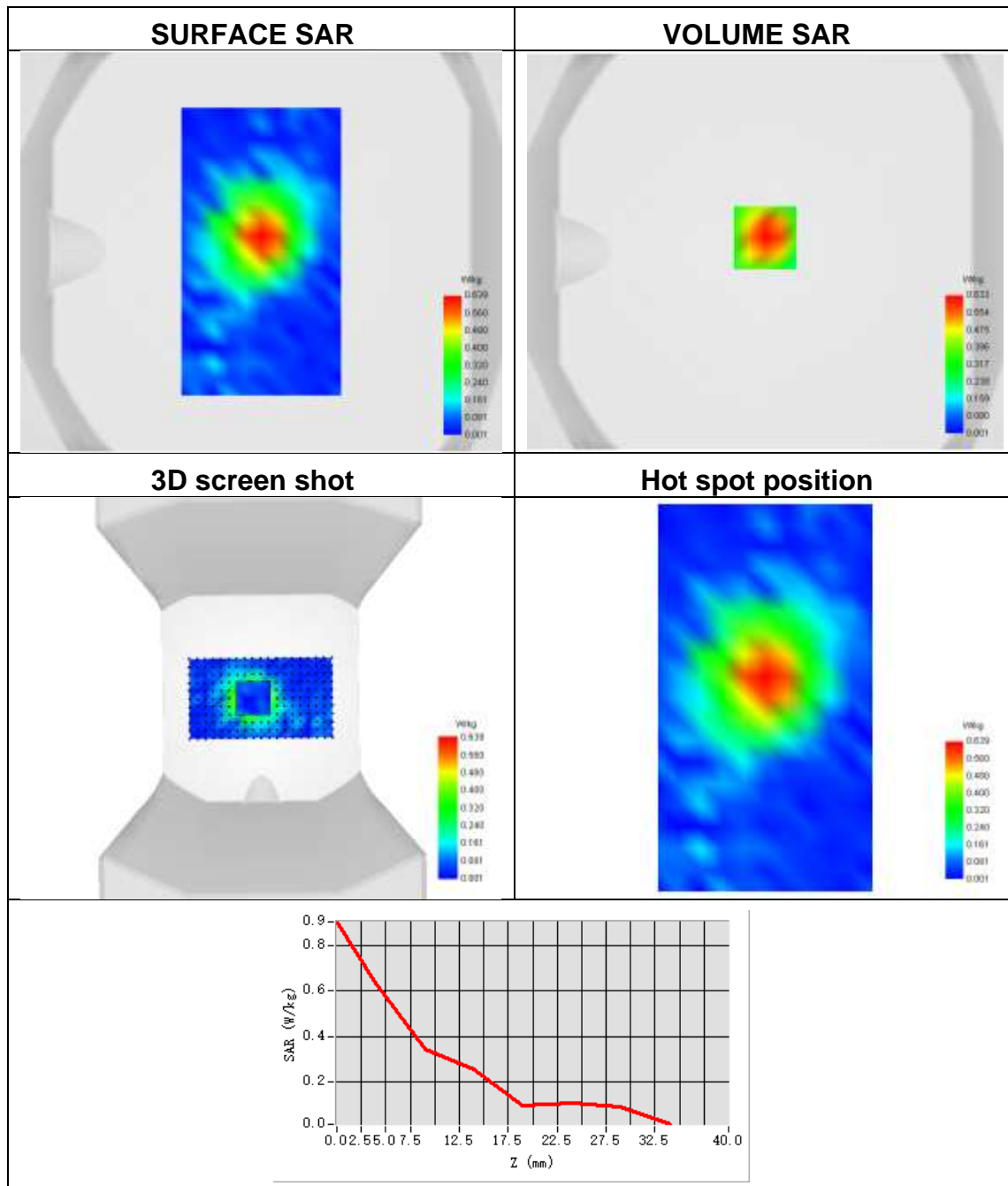




Plot 6:

Test Date	2023-07-12
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	Band 2 (1900)
Signal	WCDMA
Frequency	1880
SAR 10g (W/Kg)	0.327
SAR 1g (W/Kg)	0.581

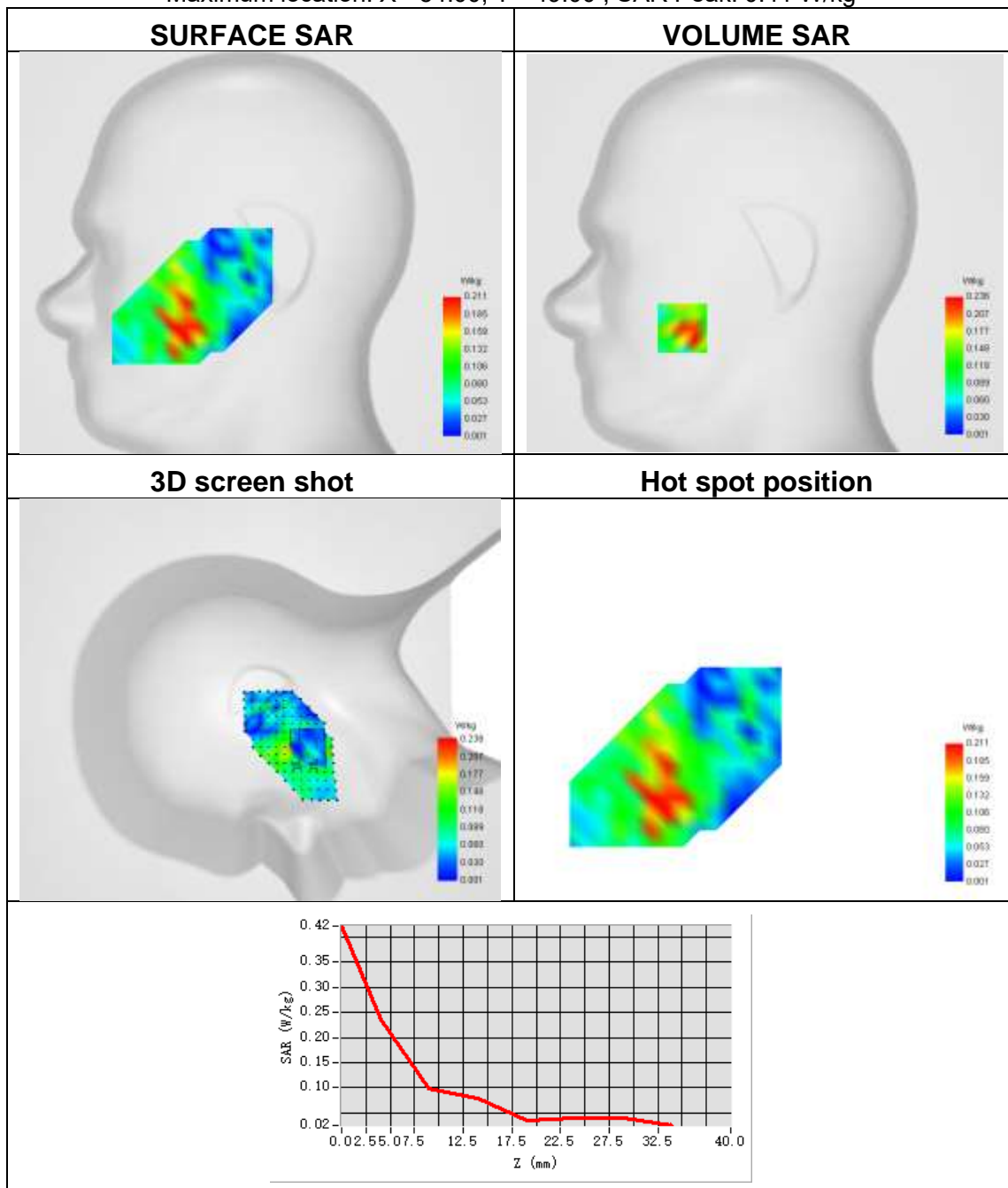
Maximum location: X=0.00, Y=7.00 ; SAR Peak: 0.93 W/kg



Plot 7:

Test Date	2023-07-11
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right Cheek
Device Position	Cheek
Band	Band 4 (1700)
Signal	WCDMA
Frequency	1740
SAR 10g (W/Kg)	0.117
SAR 1g (W/Kg)	0.223

Maximum location: X=-54.00, Y=-49.00 ; SAR Peak: 0.41 W/kg

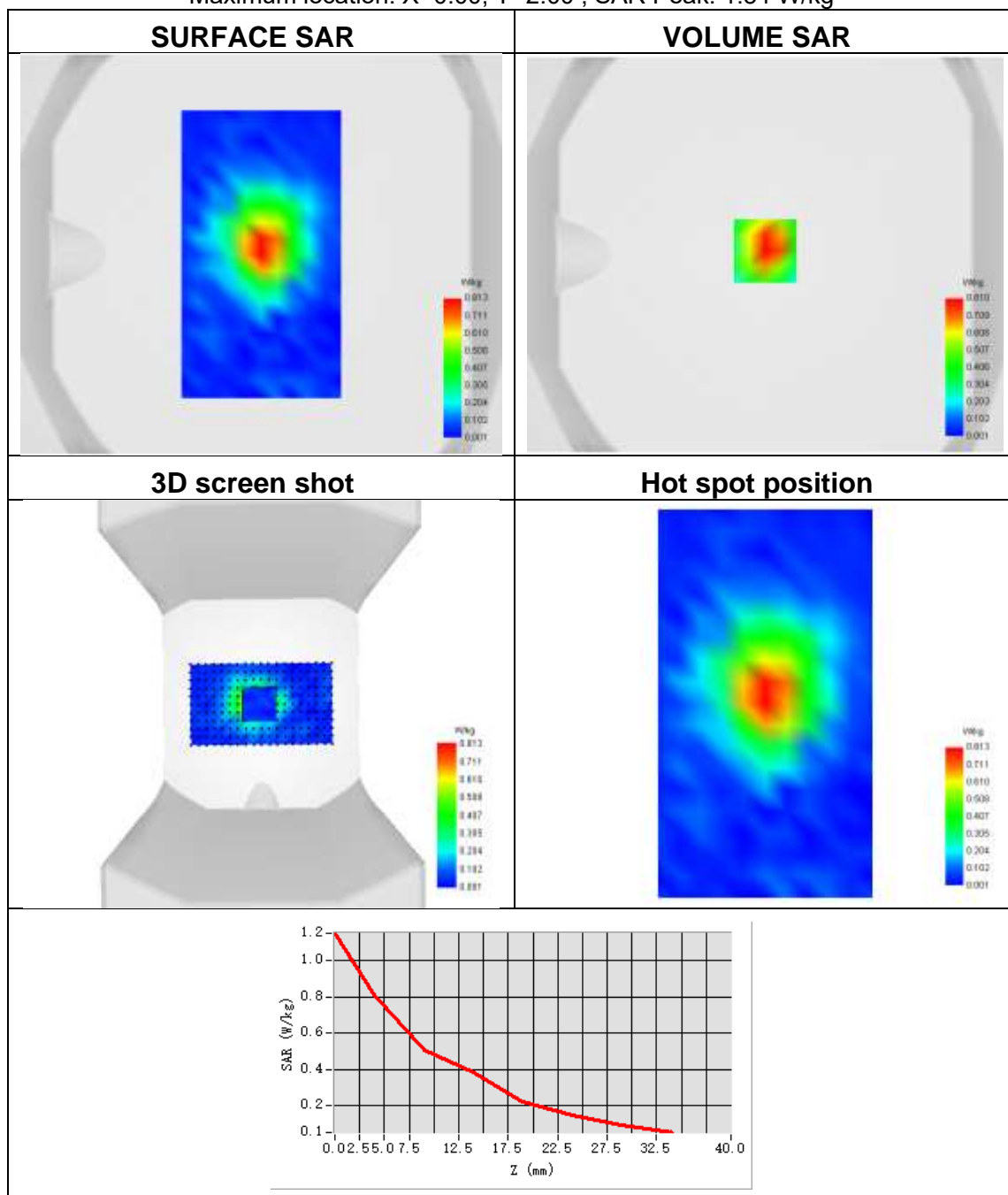




Plot 8:

Test Date	2023-07-11
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	Band 4 (1700)
Signal	WCDMA
Frequency	1740
SAR 10g (W/Kg)	0.420
SAR 1g (W/Kg)	0.785

Maximum location: X=0.00, Y=2.00 ; SAR Peak: 1.34 W/kg

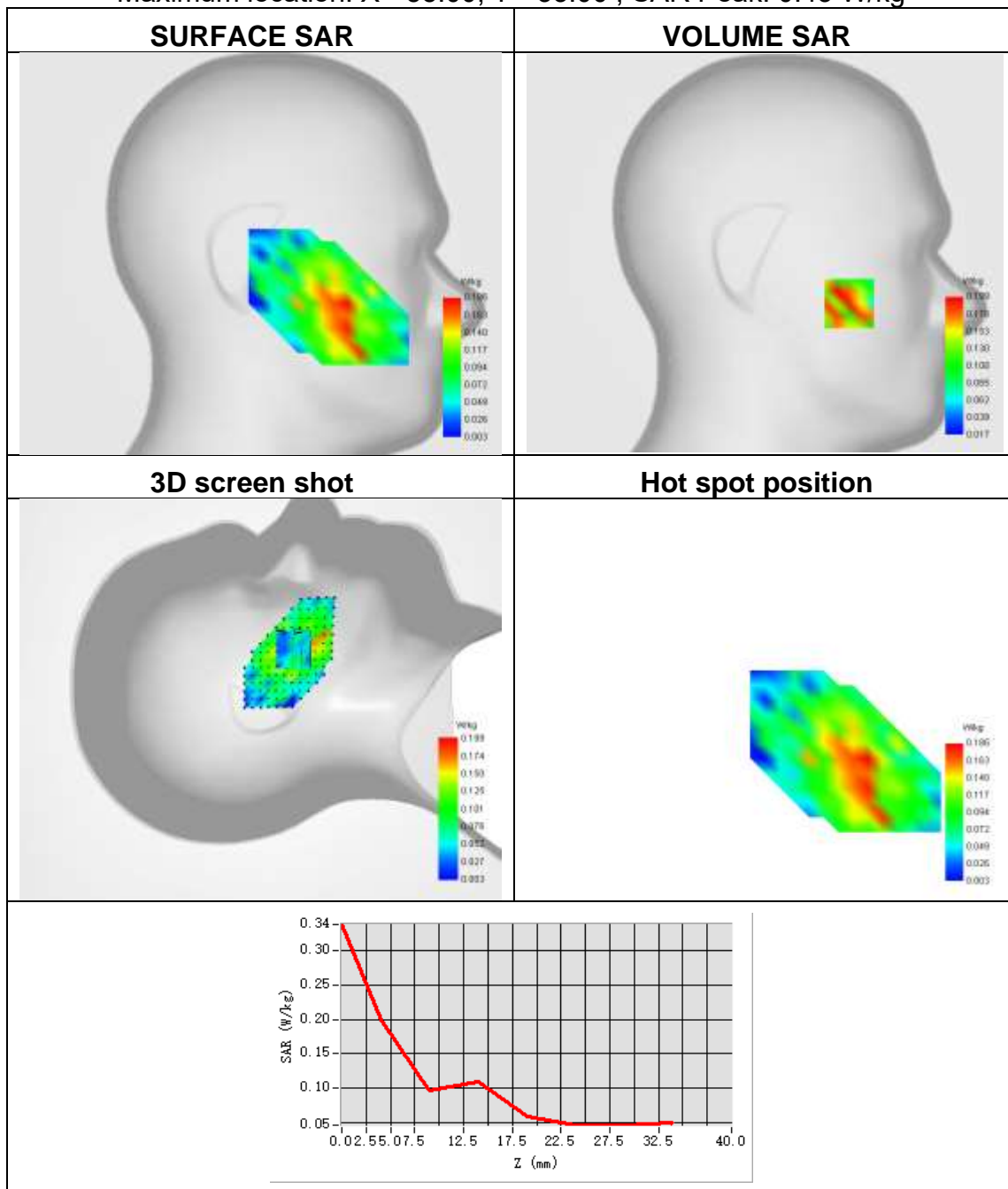




Plot 9:

Test Date	2022-12-05
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	Band 5 (850)
Signal	WCDMA
Frequency	826.4
SAR 10g (W/Kg)	0.120
SAR 1g (W/Kg)	0.208

Maximum location: X=-55.00, Y=-33.00 ; SAR Peak: 0.43 W/kg

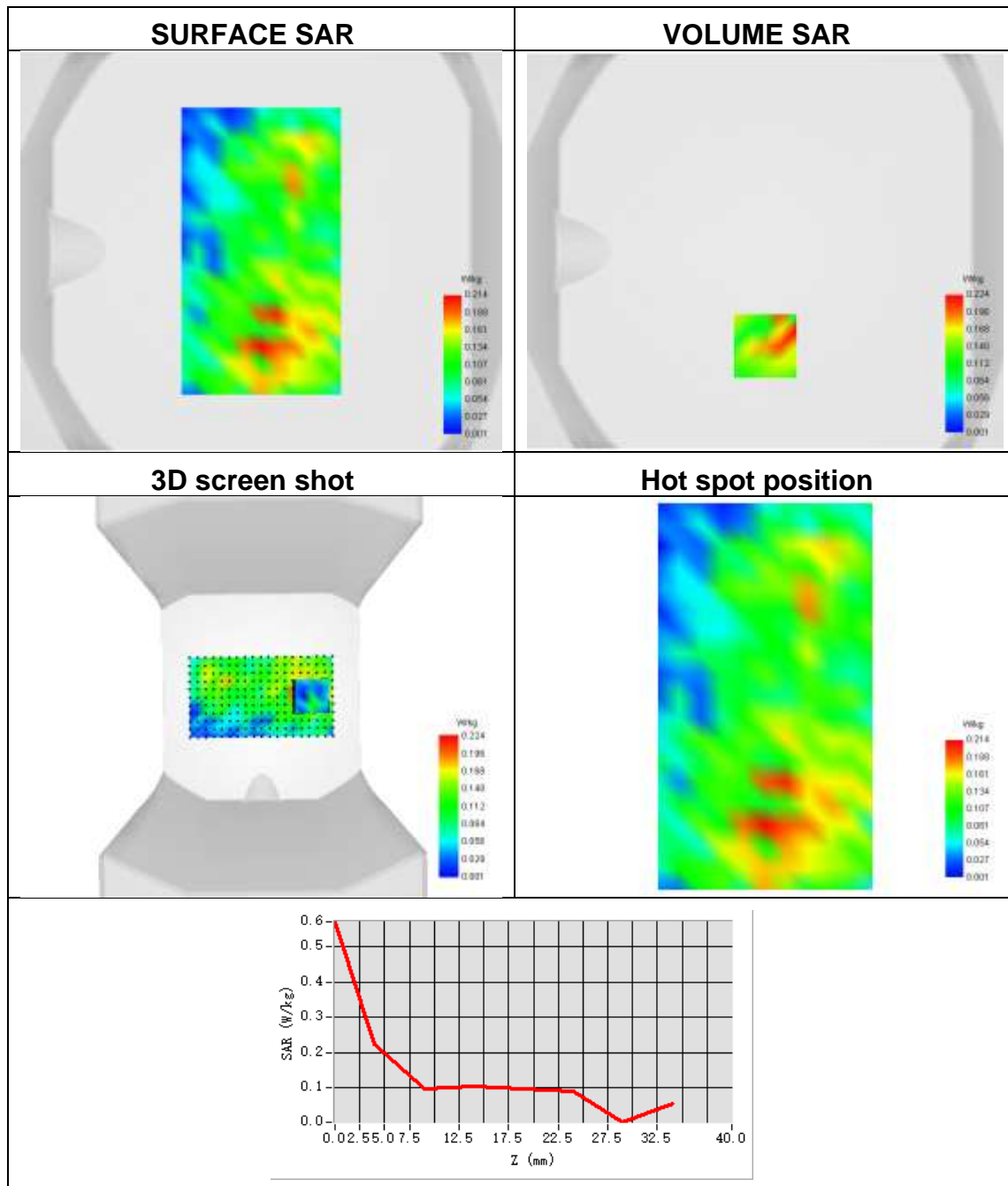




Plot 10:

Test Date	2023-07-10
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	Band 5 (850)
Signal	WCDMA
Frequency	826.4
SAR 10g (W/Kg)	0.104
SAR 1g (W/Kg)	0.204

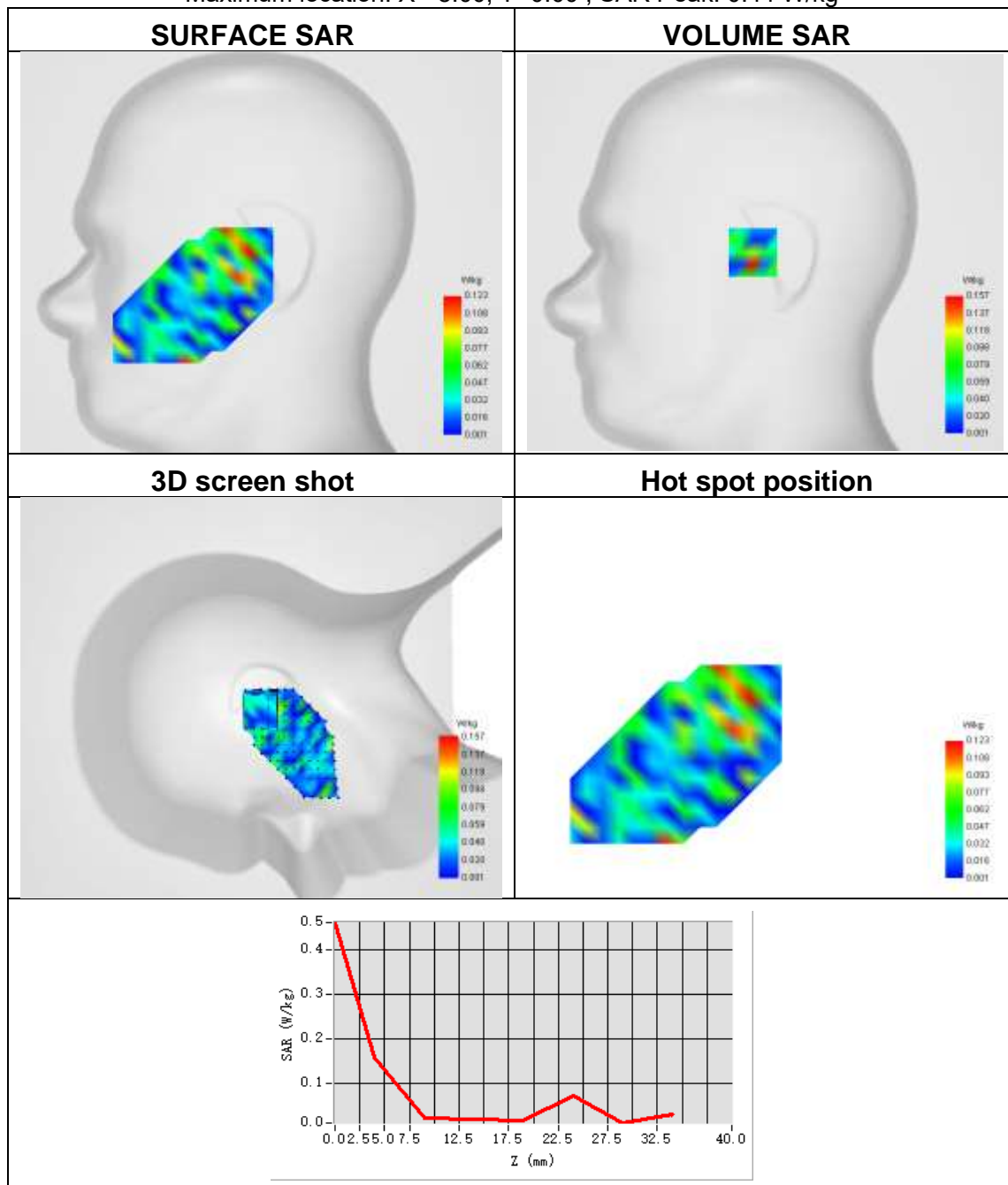
Maximum location: X=0.00, Y=-48.00 ; SAR Peak: 0.44 W/kg



Plot 11:

Test Date	2023-07-14
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right
Device Position	Tilt
Band	IEEE 802.11b ISM ANT A
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.052
SAR 1g (W/Kg)	0.141

Maximum location: X=-8.00, Y=0.00 ; SAR Peak: 0.44 W/kg

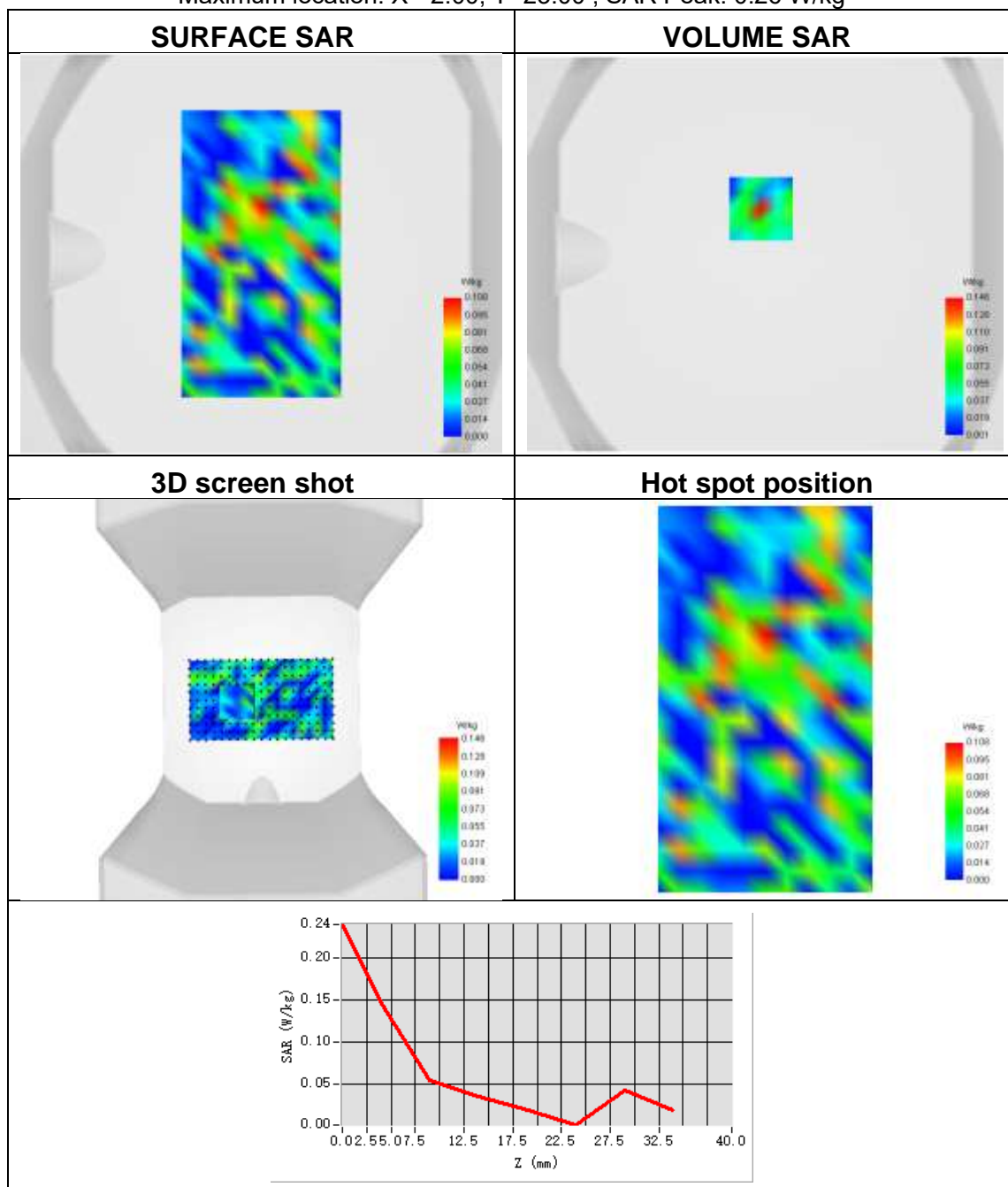




Plot 12:

Test Date	2023-07-14
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11b ISM ANT A
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.050
SAR 1g (W/Kg)	0.118

Maximum location: X=-2.00, Y=23.00 ; SAR Peak: 0.26 W/kg

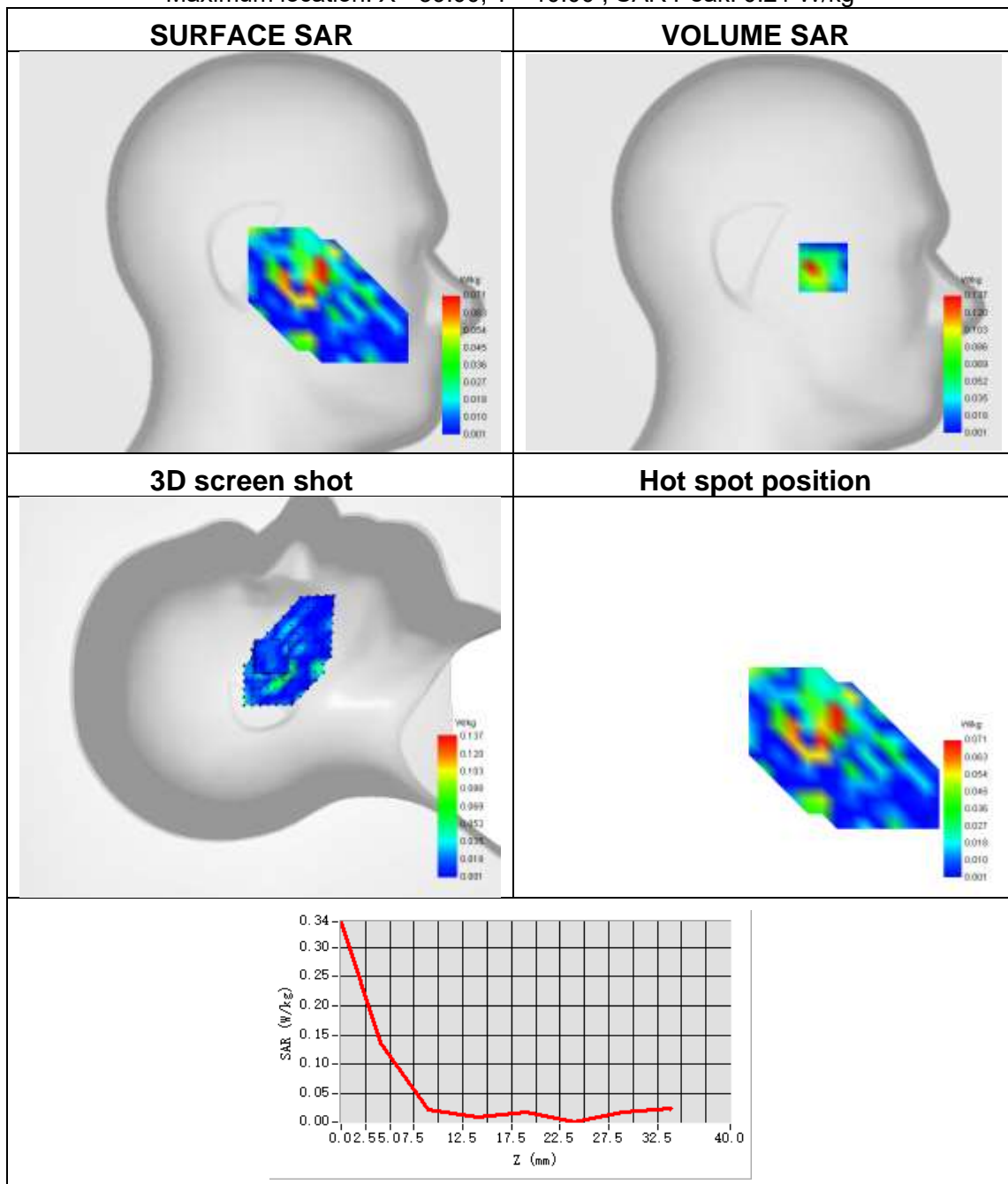




Plot 13:

Test Date	2023-07-14
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	IEEE 802.11b ISM ANT B
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.044
SAR 1g (W/Kg)	0.109

Maximum location: X=-39.00, Y=-10.00 ; SAR Peak: 0.21 W/kg

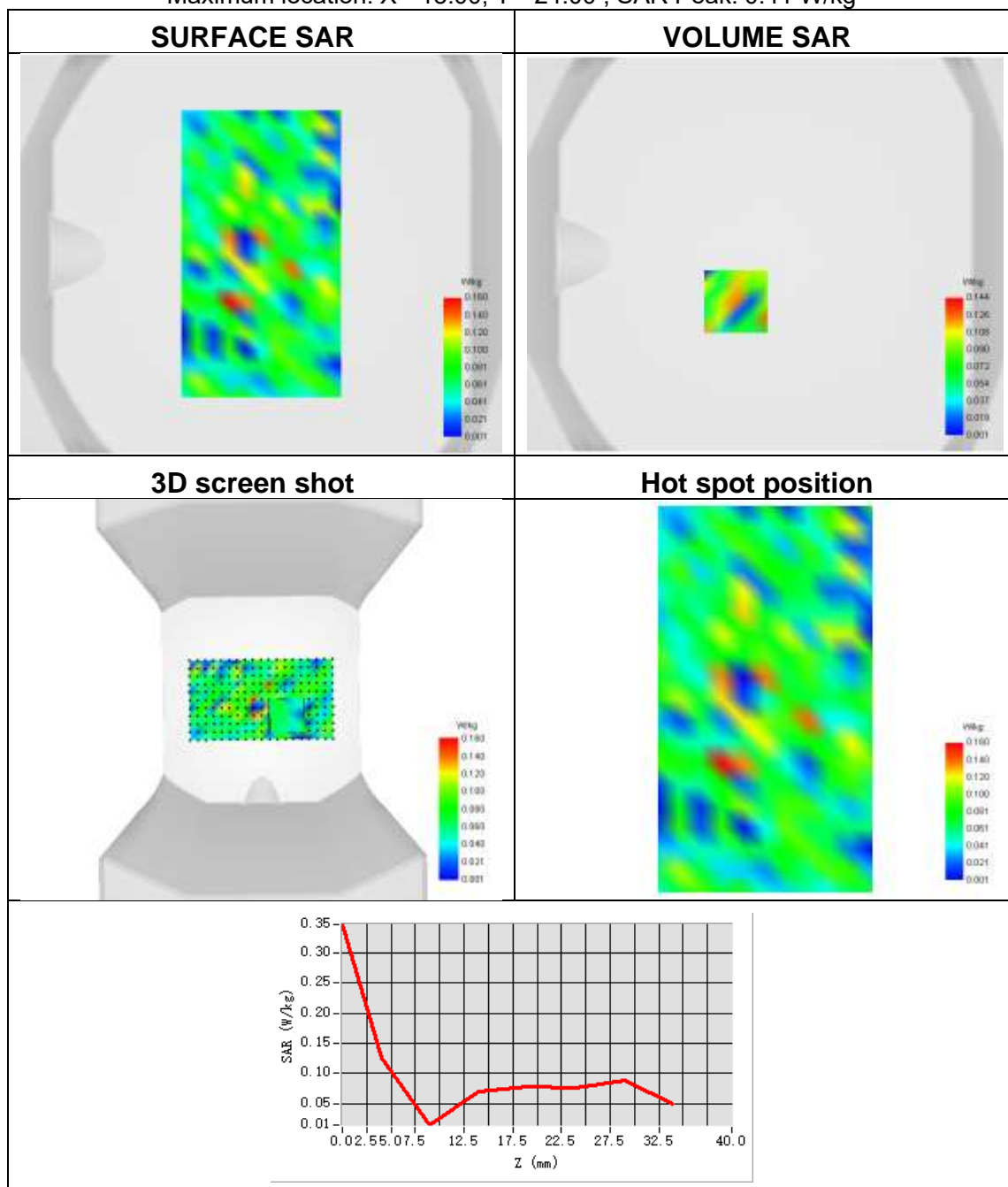




Plot 14:

Test Date	2023-07-14
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11b ISM ANT B
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.069
SAR 1g (W/Kg)	0.157

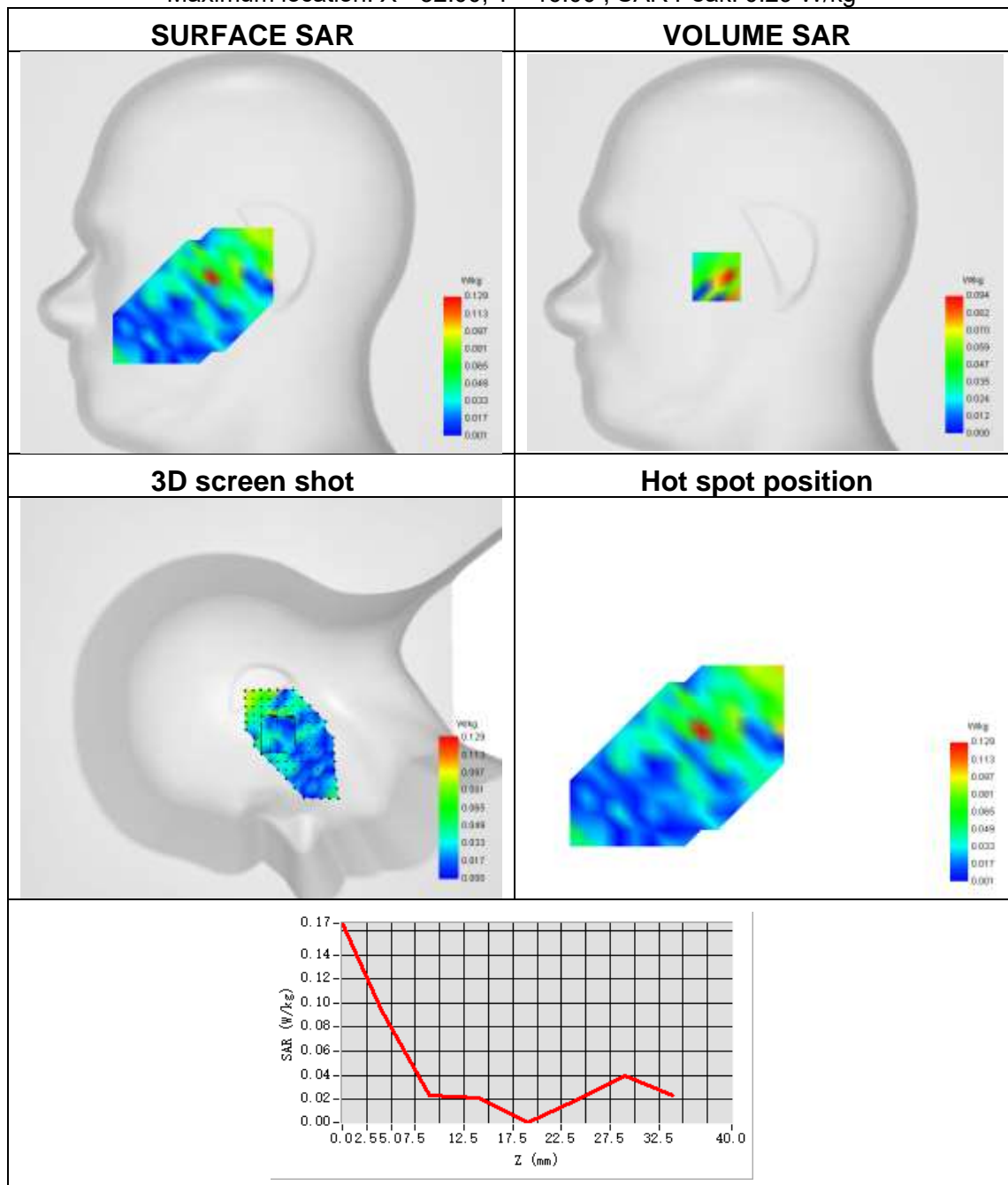
Maximum location: X=-15.00, Y=-24.00 ; SAR Peak: 0.41 W/kg



Plot 15:

Test Date	2023-07-16
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.045
SAR 1g (W/Kg)	0.110

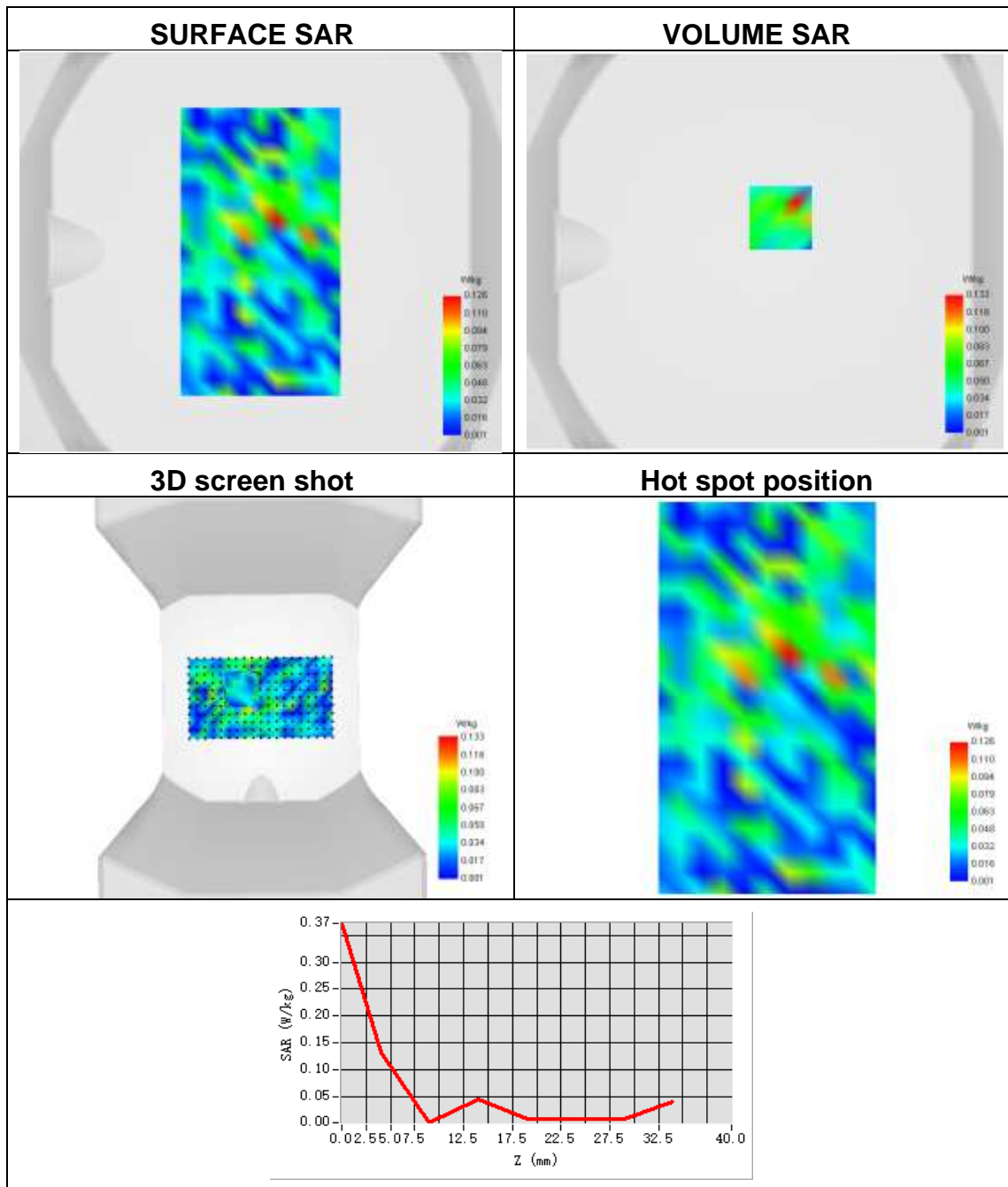
Maximum location: X=-32.00, Y=-16.00 ; SAR Peak: 0.29 W/kg



Plot 16:

Test Date	2023-07-16
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.044
SAR 1g (W/Kg)	0.113

Maximum location: X=8.00, Y=17.00 ; SAR Peak: 0.28 W/kg

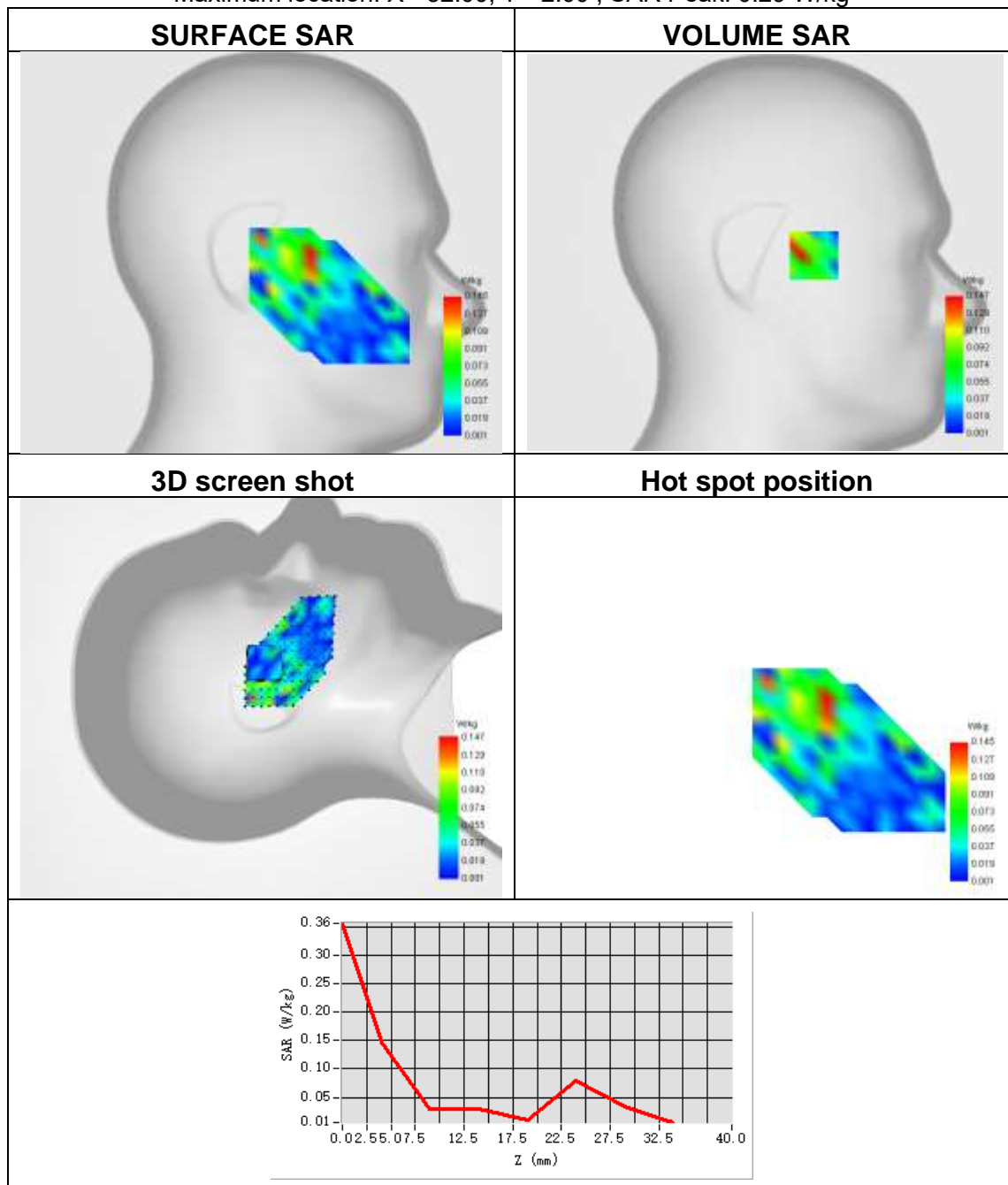




Plot 17:

Test Date	2023-07-16
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	IEEE 802.11 n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.054
SAR 1g (W/Kg)	0.120

Maximum location: X=-32.00, Y=-2.00 ; SAR Peak: 0.29 W/kg

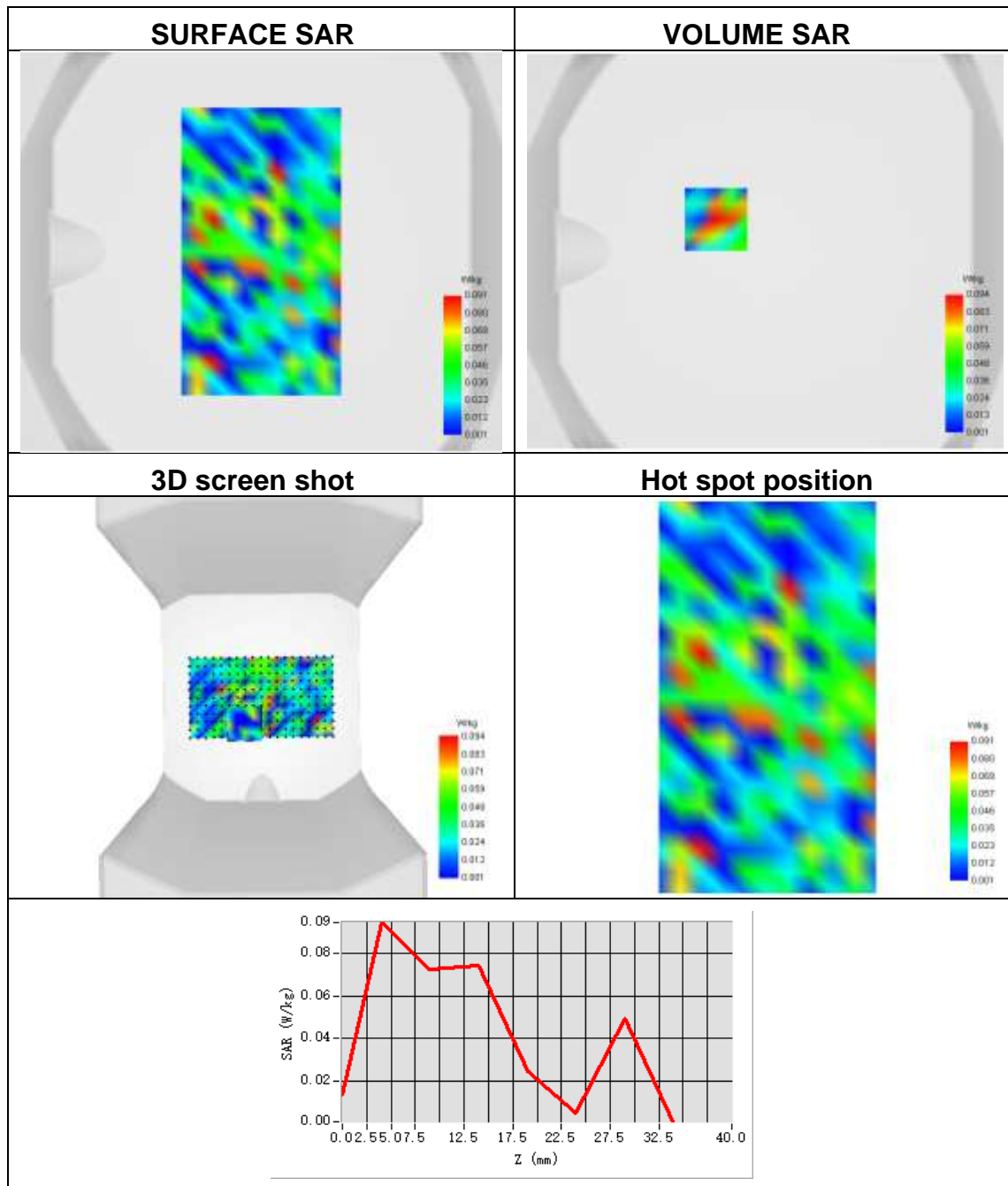




Plot 18:

Test Date	2023-07-16
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Front Side
Band	IEEE 802.11 n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	2412
SAR 10g (W/Kg)	0.043
SAR 1g (W/Kg)	0.084

Maximum location: X=-25.00, Y=16.00 ; SAR Peak: 0.18 W/kg

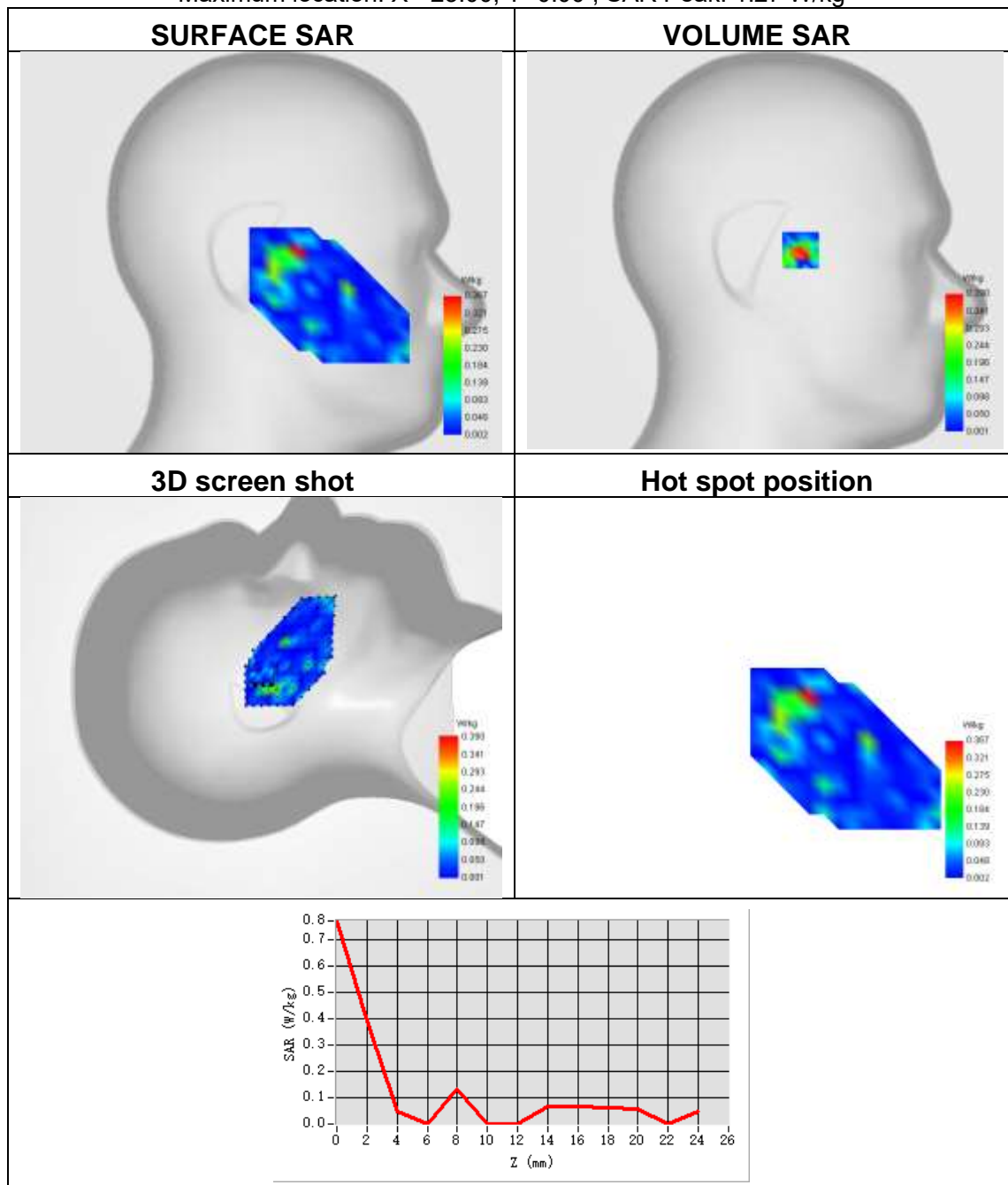




Plot 19:

Test Date	2023-07-18
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Left head
Device Position	Cheek
Band	IEEE 802.11n-HT40 ANT A
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.089
SAR 1g (W/Kg)	0.280

Maximum location: X=-23.00, Y=0.00 ; SAR Peak: 1.27 W/kg

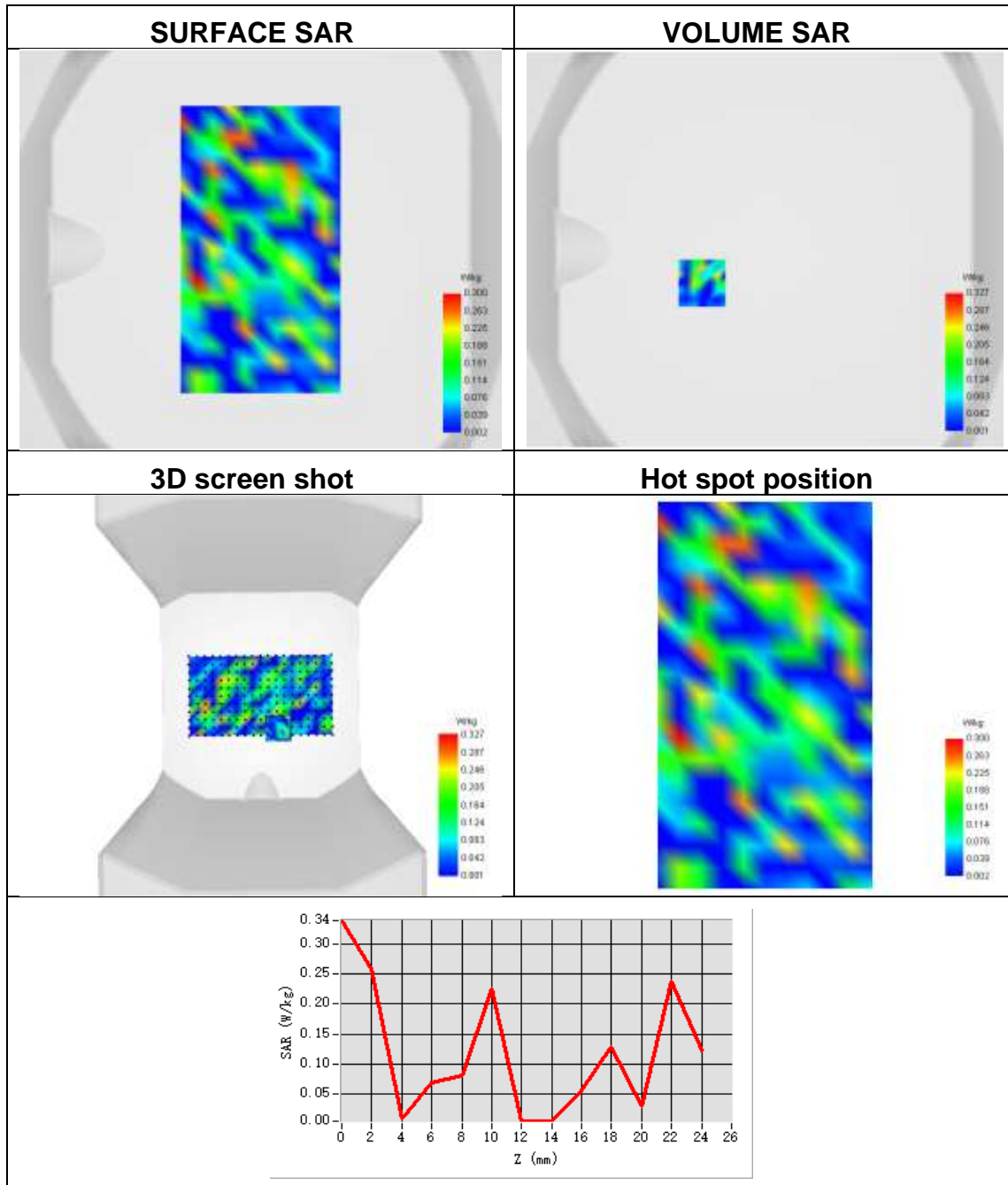




Plot 20:

Test Date	2023-07-18
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Front Side
Band	IEEE 802.11 n-HT40 ANT A
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.071
SAR 1g (W/Kg)	0.184

Maximum location: X=-32.00, Y=-17.00 ; SAR Peak: 1.12 W/kg

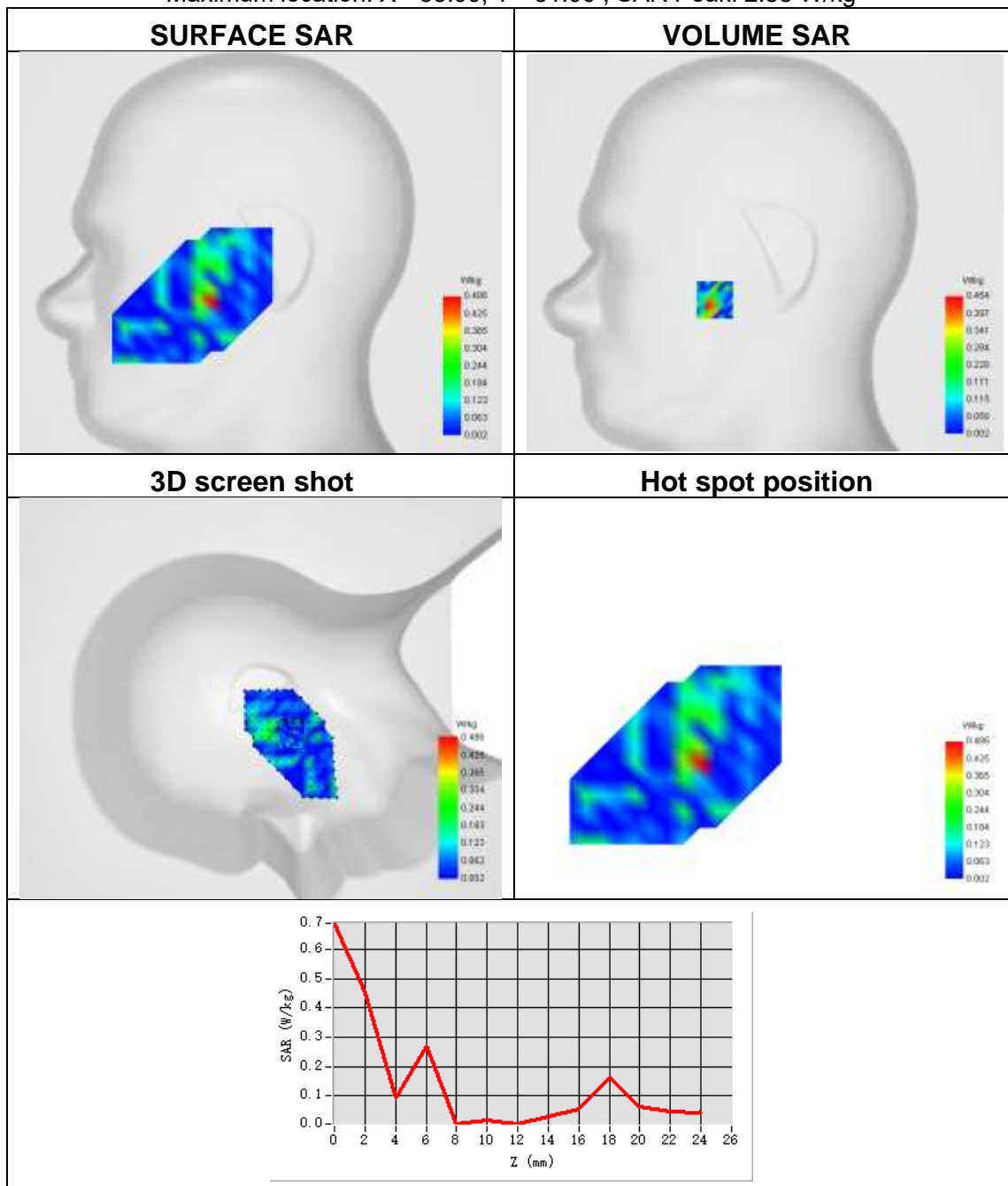




Plot 21:

Test Date	2023-07-18
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT40 ANT B
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.092
SAR 1g (W/Kg)	0.337

Maximum location: X=-33.00, Y=-31.00 ; SAR Peak: 2.58 W/kg

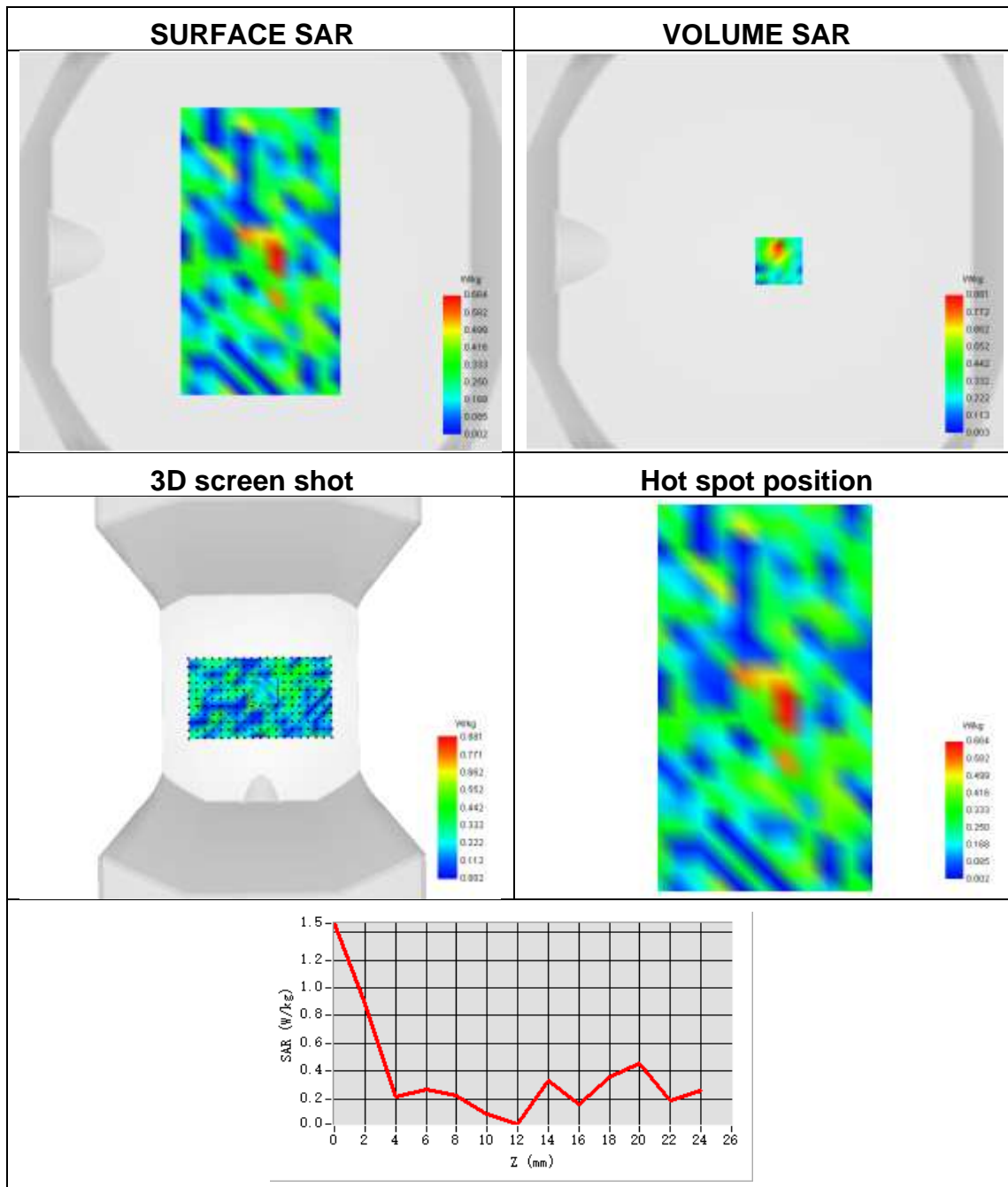




Plot 22:

Test Date	2023-07-18
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11a U-NII ANT B
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.187
SAR 1g (W/Kg)	0.419

Maximum location: X=7.00, Y=-5.00 ; SAR Peak: 2.74 W/kg

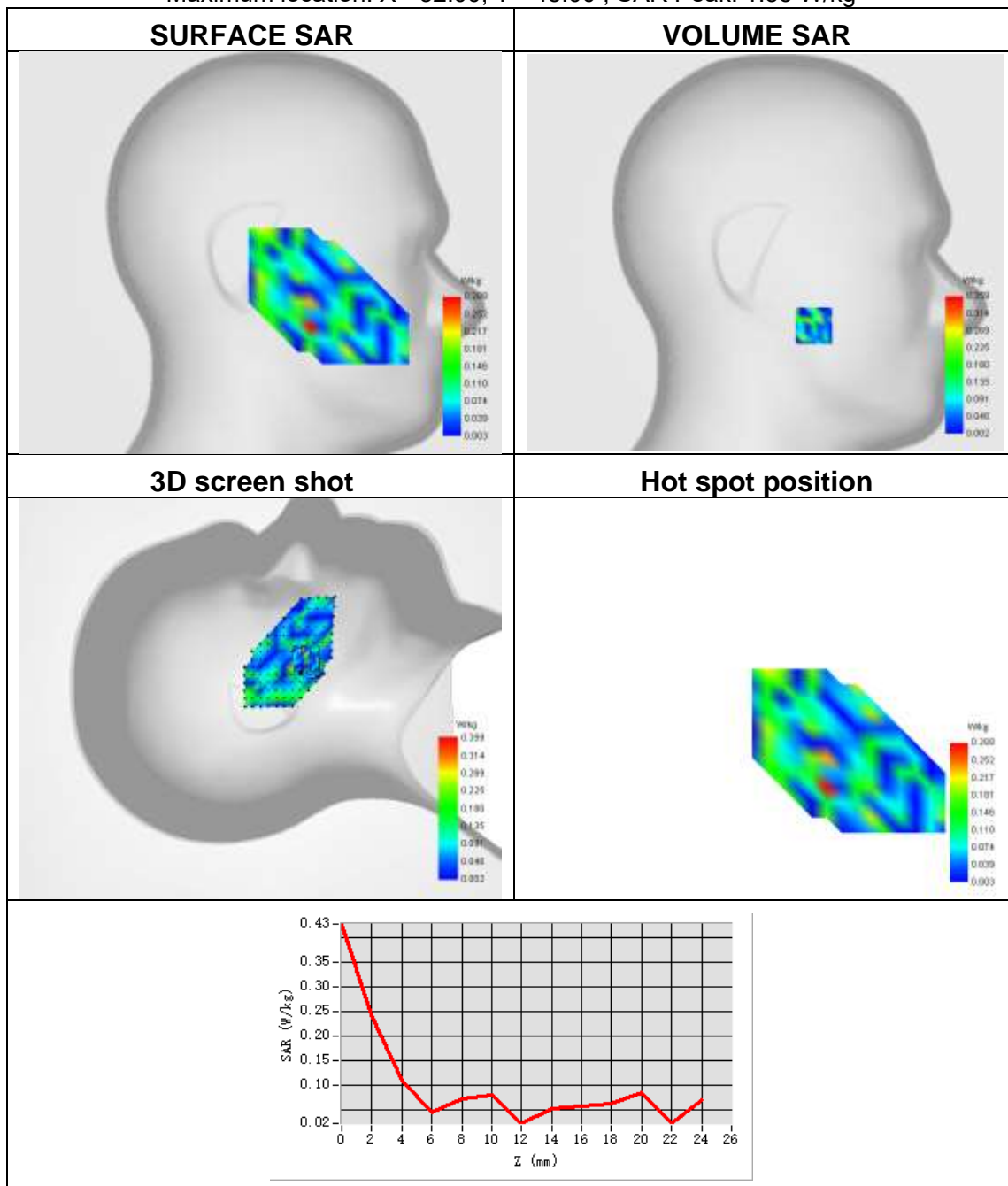




Plot 23:

Test Date	2023-07-23
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Left head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.067
SAR 1g (W/Kg)	0.161

Maximum location: X=-32.00, Y=-48.00 ; SAR Peak: 1.55 W/kg

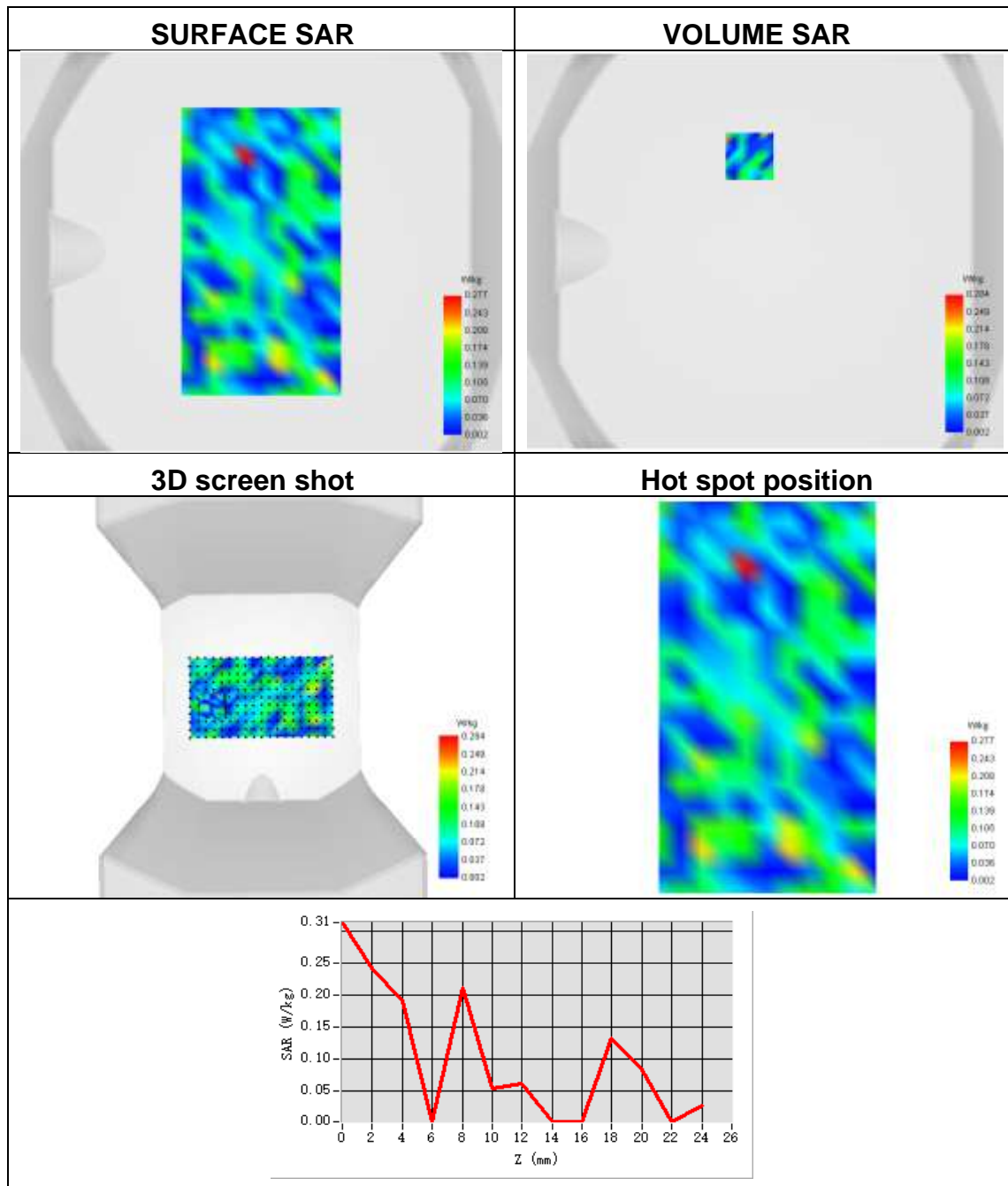




Plot 24:

Test Date	2023-07-23
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Front Side
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.056
SAR 1g (W/Kg)	0.121

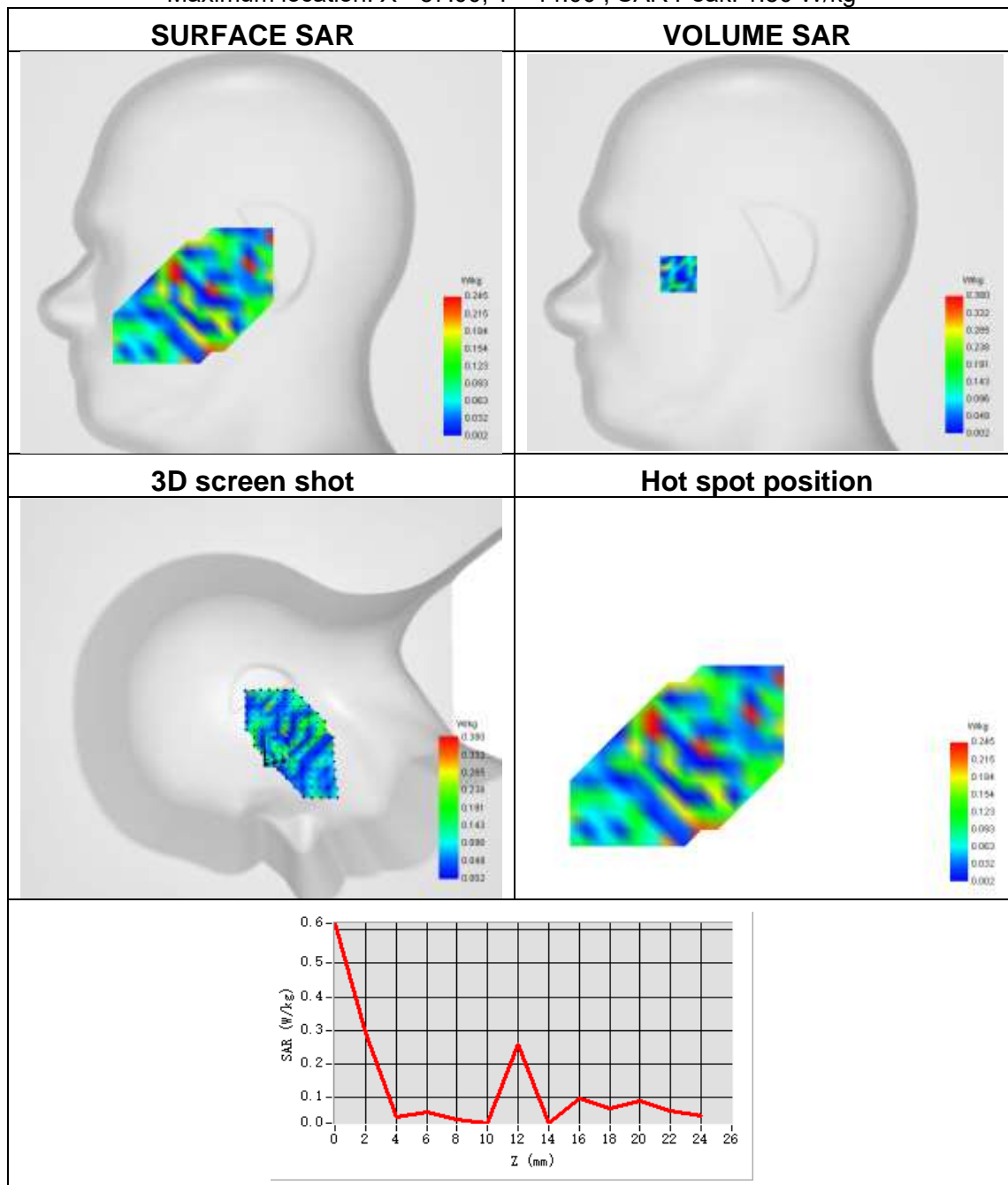
Maximum location: X=-8.00, Y=48.00 ; SAR Peak: 1.22 W/kg



Plot 25:

Test Date	2023-07-23
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.051
SAR 1g (W/Kg)	0.129

Maximum location: X=-57.00, Y=-14.00 ; SAR Peak: 1.50 W/kg

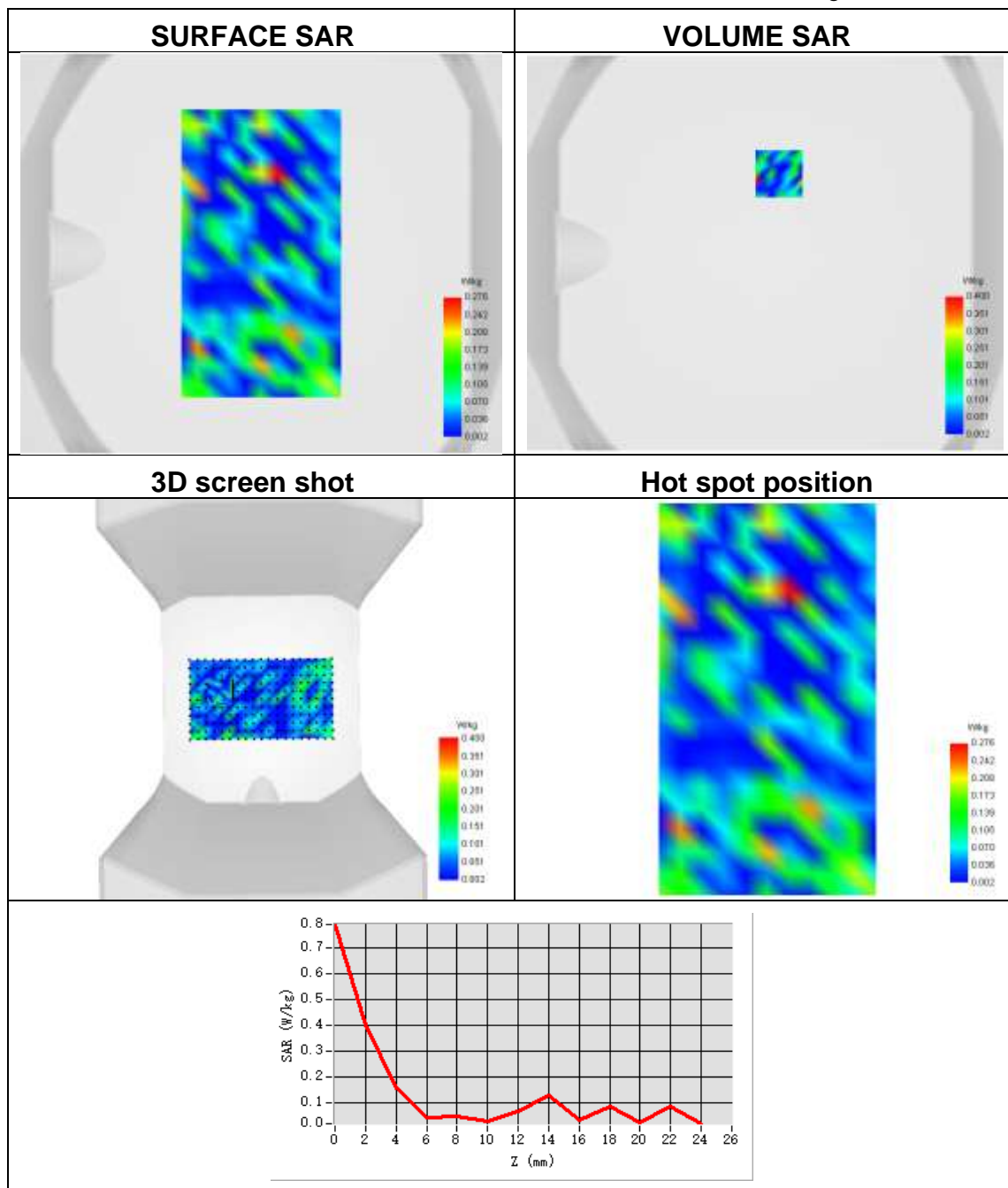




Plot 26:

Test Date	2023-07-23
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11 n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5190
SAR 10g (W/Kg)	0.059
SAR 1g (W/Kg)	0.128

Maximum location: X=7.00, Y=40.00 ; SAR Peak: 1.44 W/kg

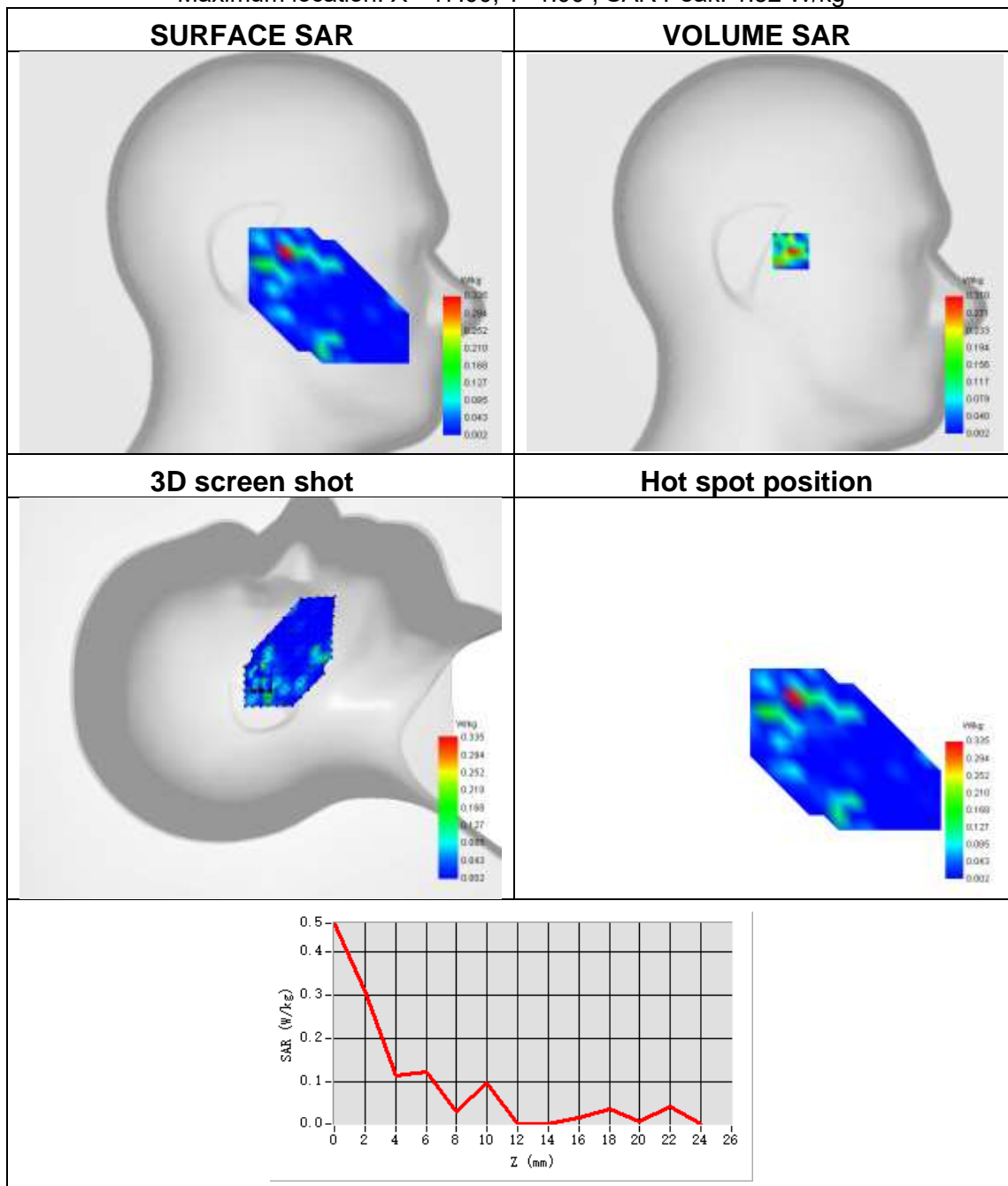




Plot 27:

Test Date	2023-07-21
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Left head
Device Position	Tilt
Band	IEEE 802.11a U-NII ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.066
SAR 1g (W/Kg)	0.248

Maximum location: X=-17.00, Y=1.00 ; SAR Peak: 1.52 W/kg

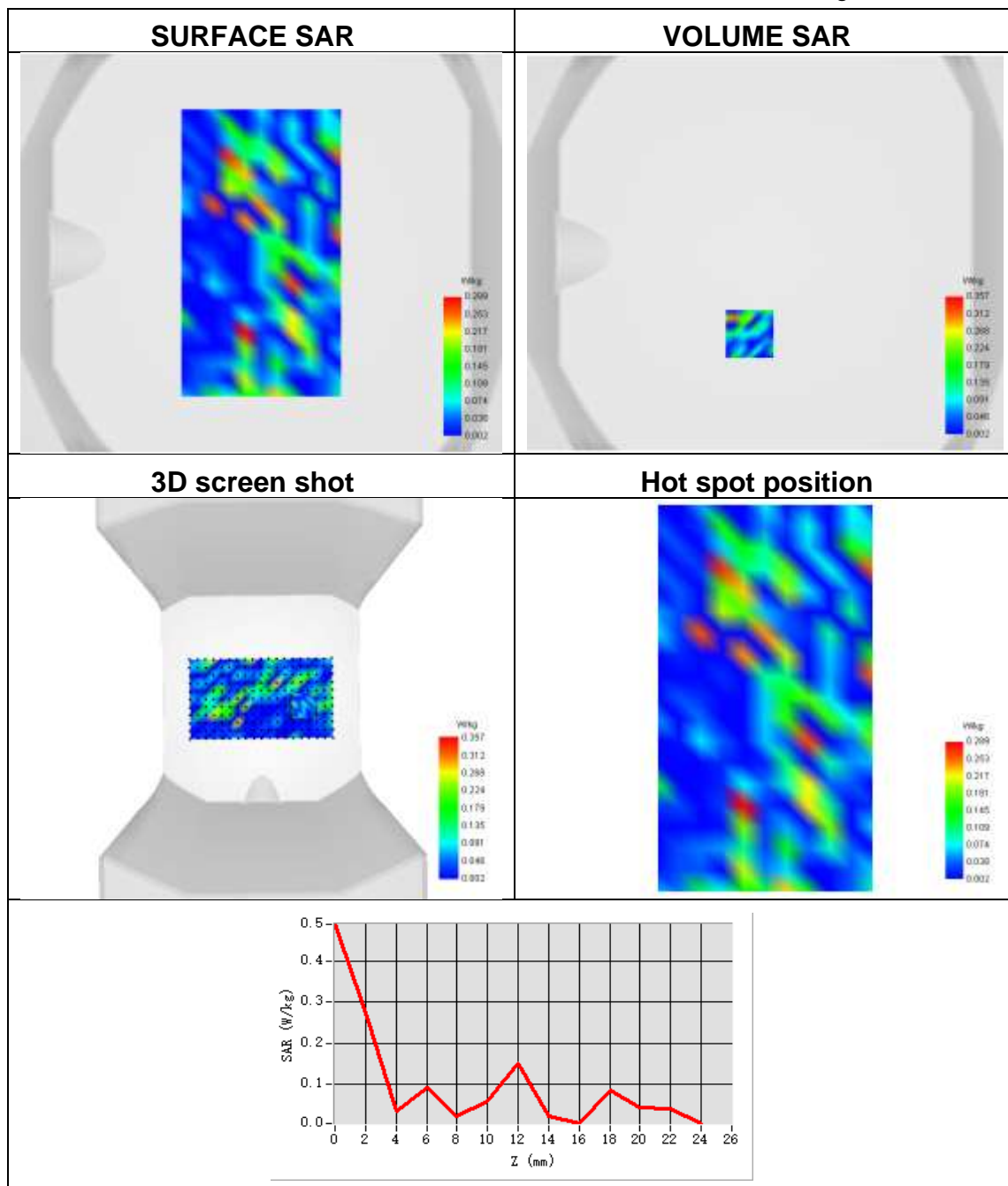




Plot 28:

Test Date	2023-07-21
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Top Side
Band	IEEE 802.11a U-NII ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.056
SAR 1g (W/Kg)	0.166

Maximum location: X=-8.00, Y=-41.00 ; SAR Peak: 1.19 W/kg

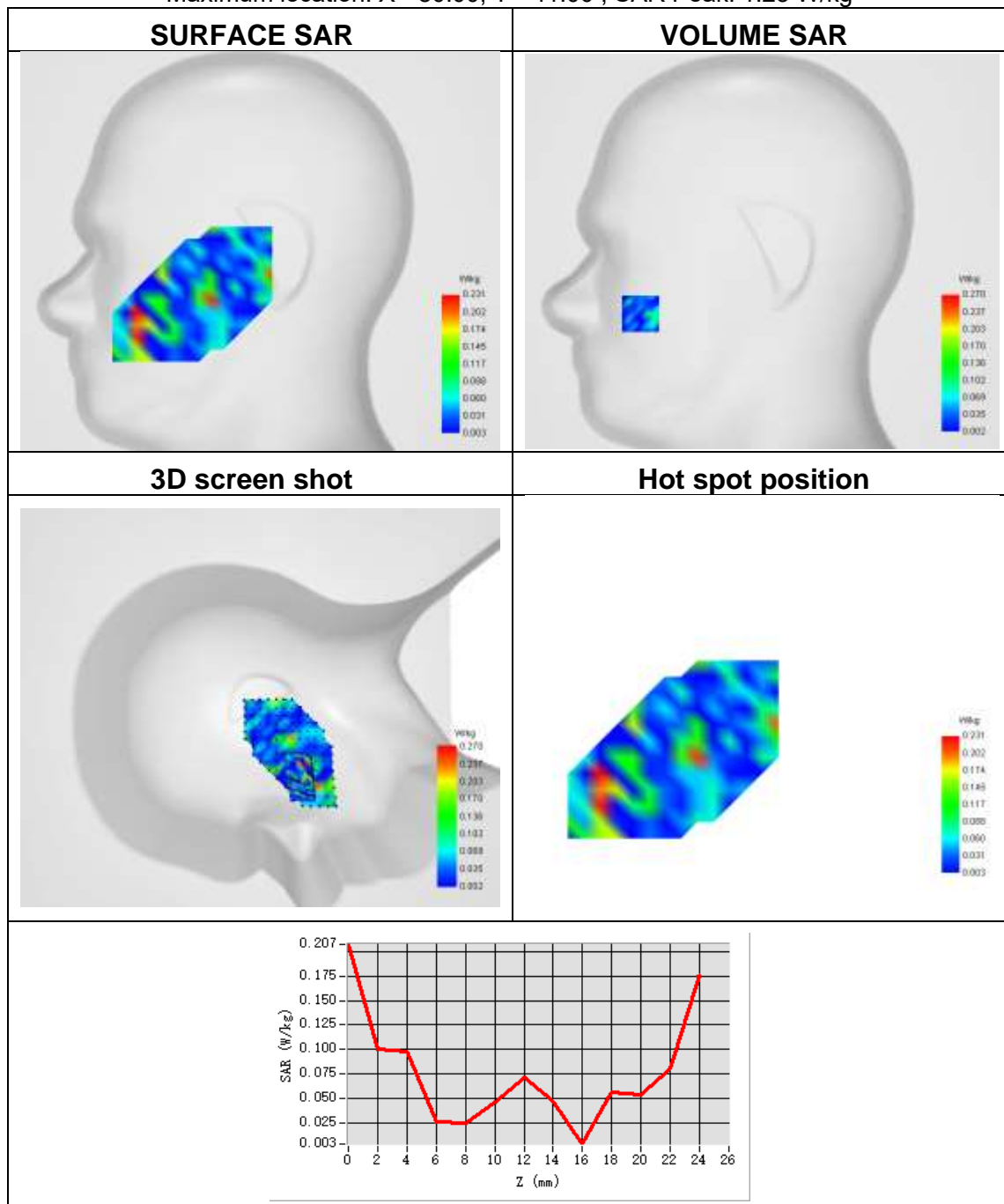




Plot 29:

Test Date	2023-07-22
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11a U-NII ANT B
Signal	IEEE 802.11
Frequency	5260
SAR 10g (W/Kg)	0.037
SAR 1g (W/Kg)	0.112

Maximum location: X=-80.00, Y=-41.00 ; SAR Peak: 1.23 W/kg

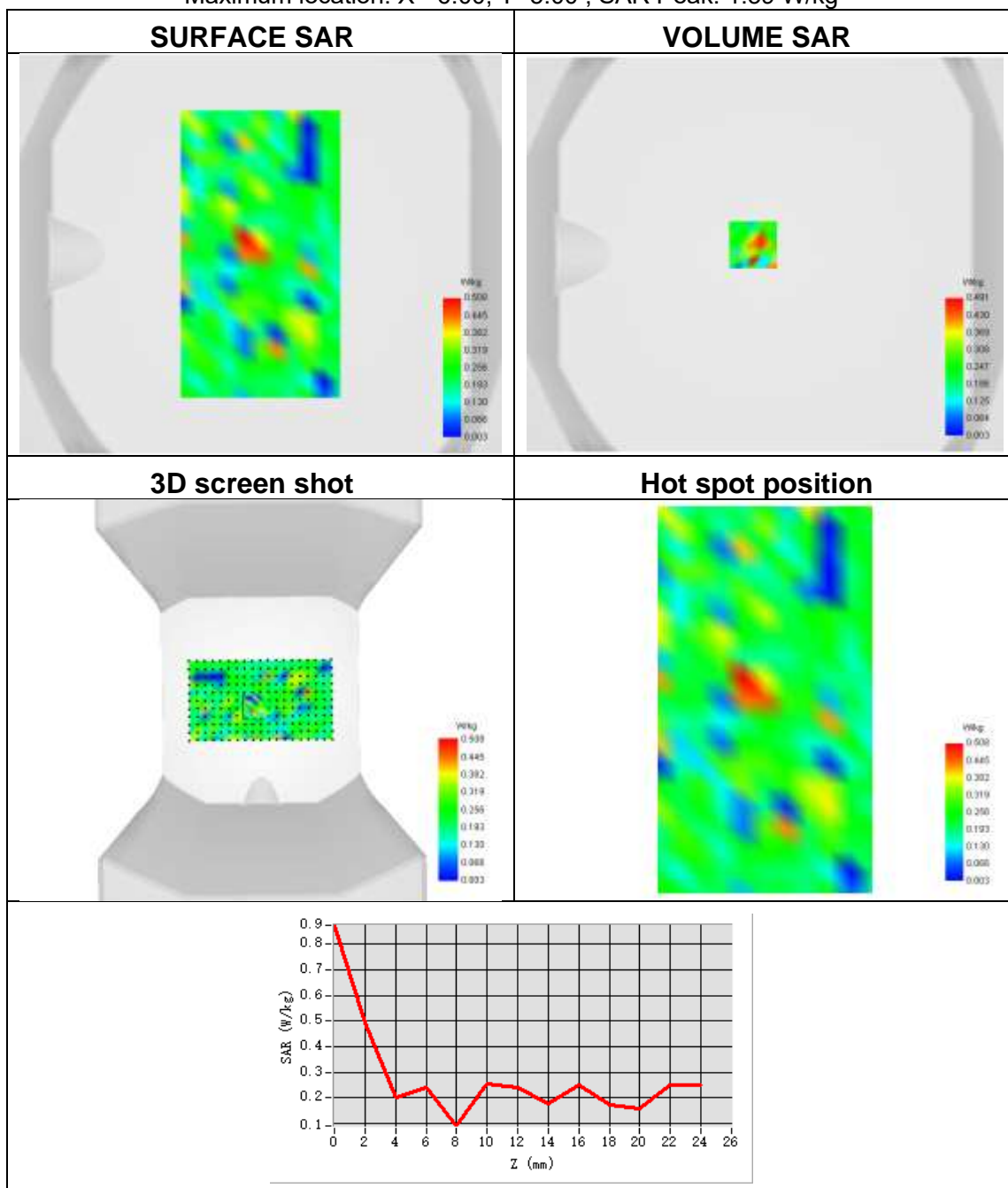




Plot 30:

Test Date	2023-07-22
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11a U-NII ANT B
Signal	IEEE 802.11 a
Frequency	5260
SAR 10g (W/Kg)	0.212
SAR 1g (W/Kg)	0.351

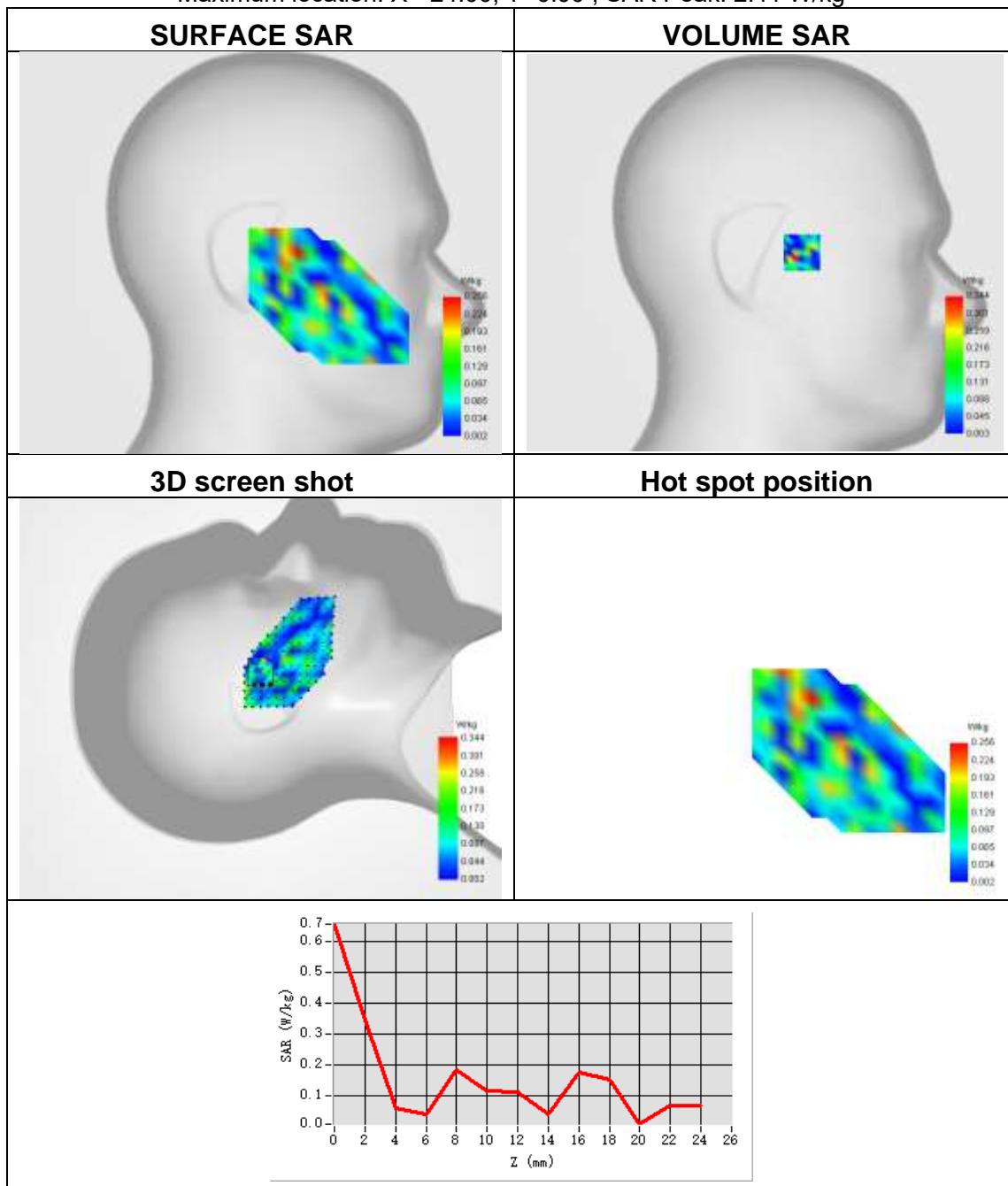
Maximum location: X=-6.00, Y=5.00 ; SAR Peak: 1.39 W/kg



Plot 31:

Test Date	2023-07-22
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Left head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.080
SAR 1g (W/Kg)	0.226

Maximum location: X=-24.00, Y=0.00 ; SAR Peak: 2.41 W/kg

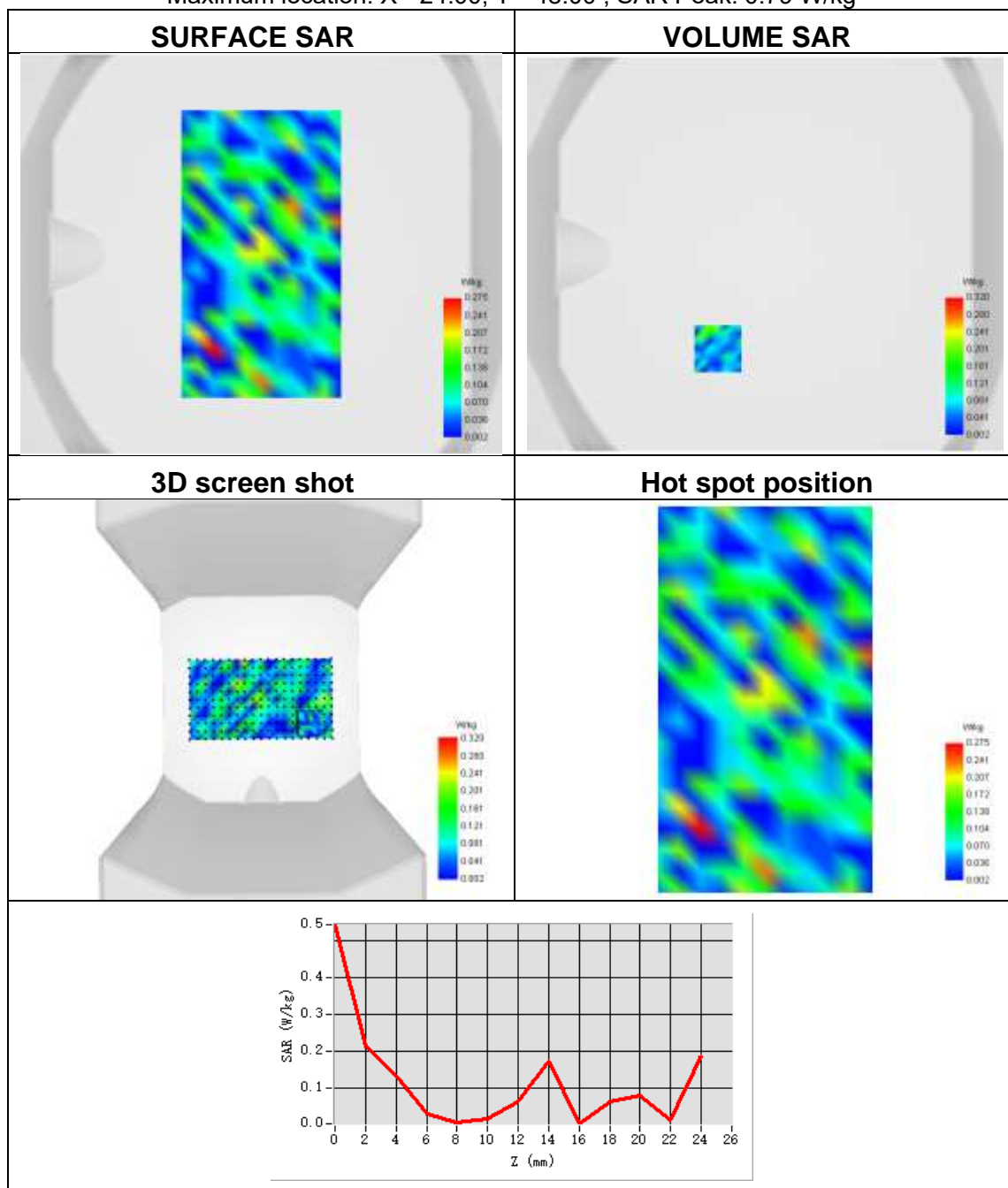




Plot 32:

Test Date	2023-07-22
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Top Side
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.053
SAR 1g (W/Kg)	0.123

Maximum location: X=-24.00, Y=-48.00 ; SAR Peak: 0.79 W/kg

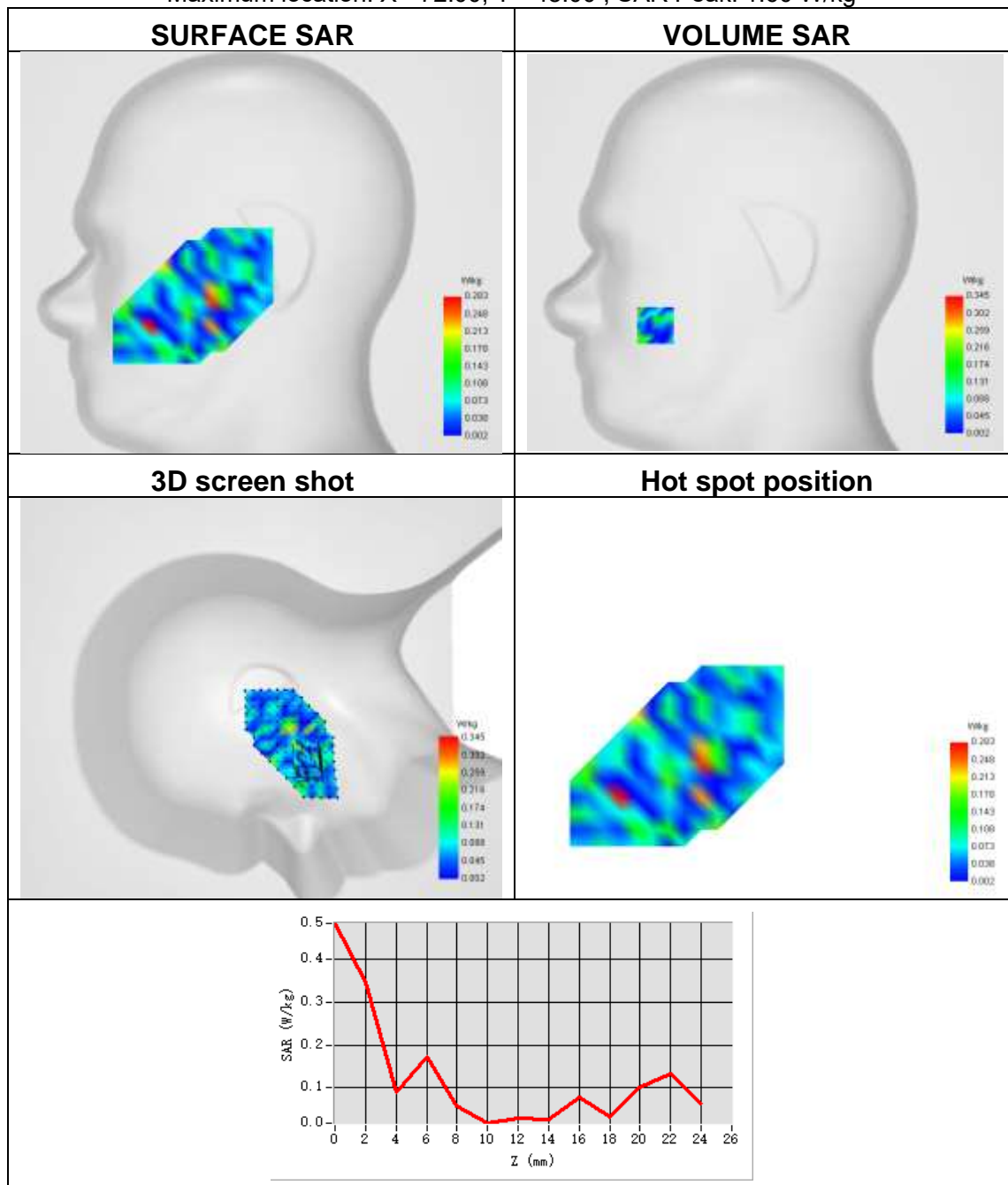




Plot 33:

Test Date	2023-07-22
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.047
SAR 1g (W/Kg)	0.134

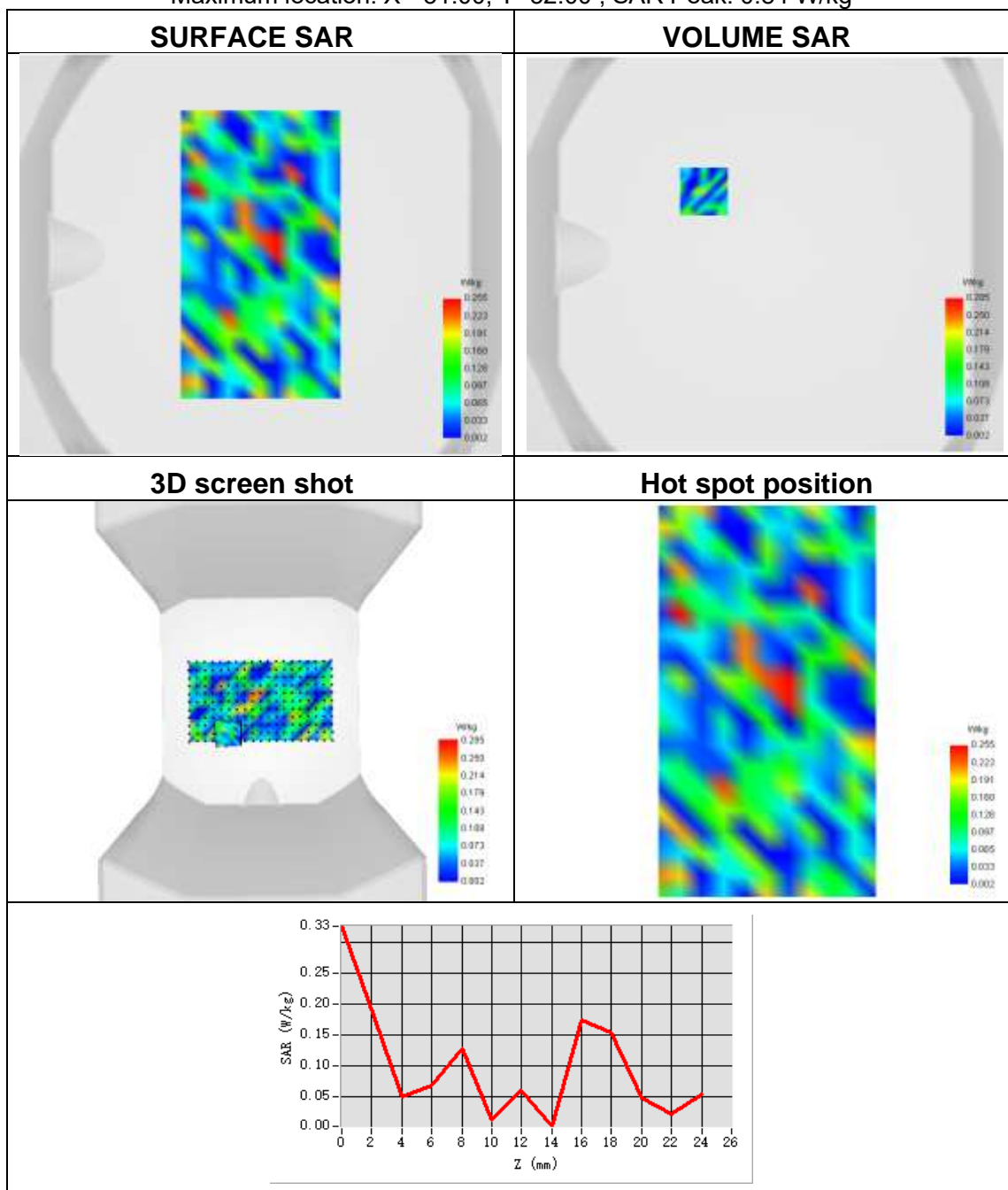
Maximum location: X=-72.00, Y=-48.00 ; SAR Peak: 1.60 W/kg



Plot 34:

Test Date	2023-07-22
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5320
SAR 10g (W/Kg)	0.046
SAR 1g (W/Kg)	0.107

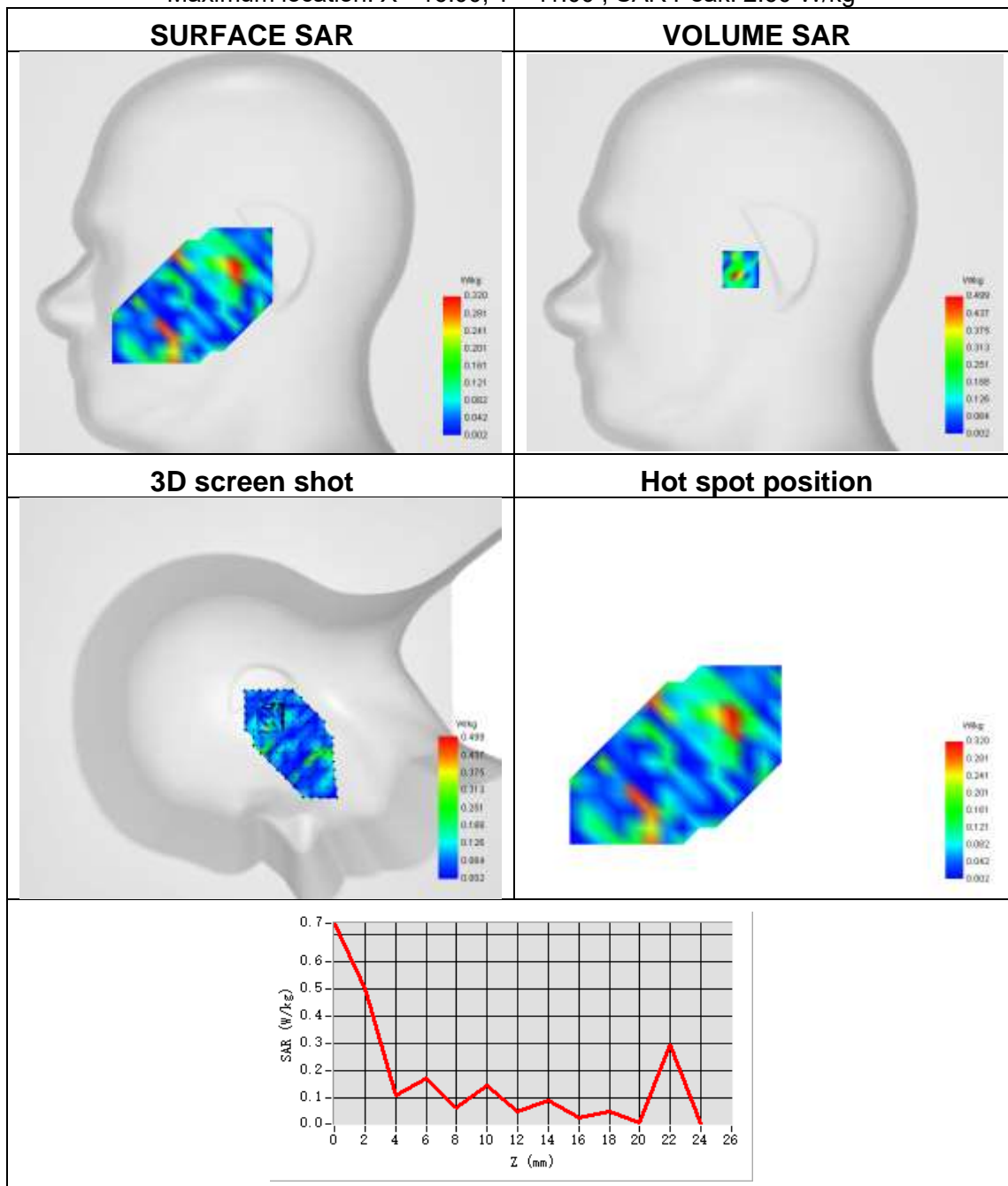
Maximum location: X=-31.00, Y=32.00 ; SAR Peak: 0.84 W/kg



Plot 35:

Test Date	2023-07-24
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 ANT A
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.098
SAR 1g (W/Kg)	0.296

Maximum location: X=-16.00, Y=-11.00 ; SAR Peak: 2.60 W/kg

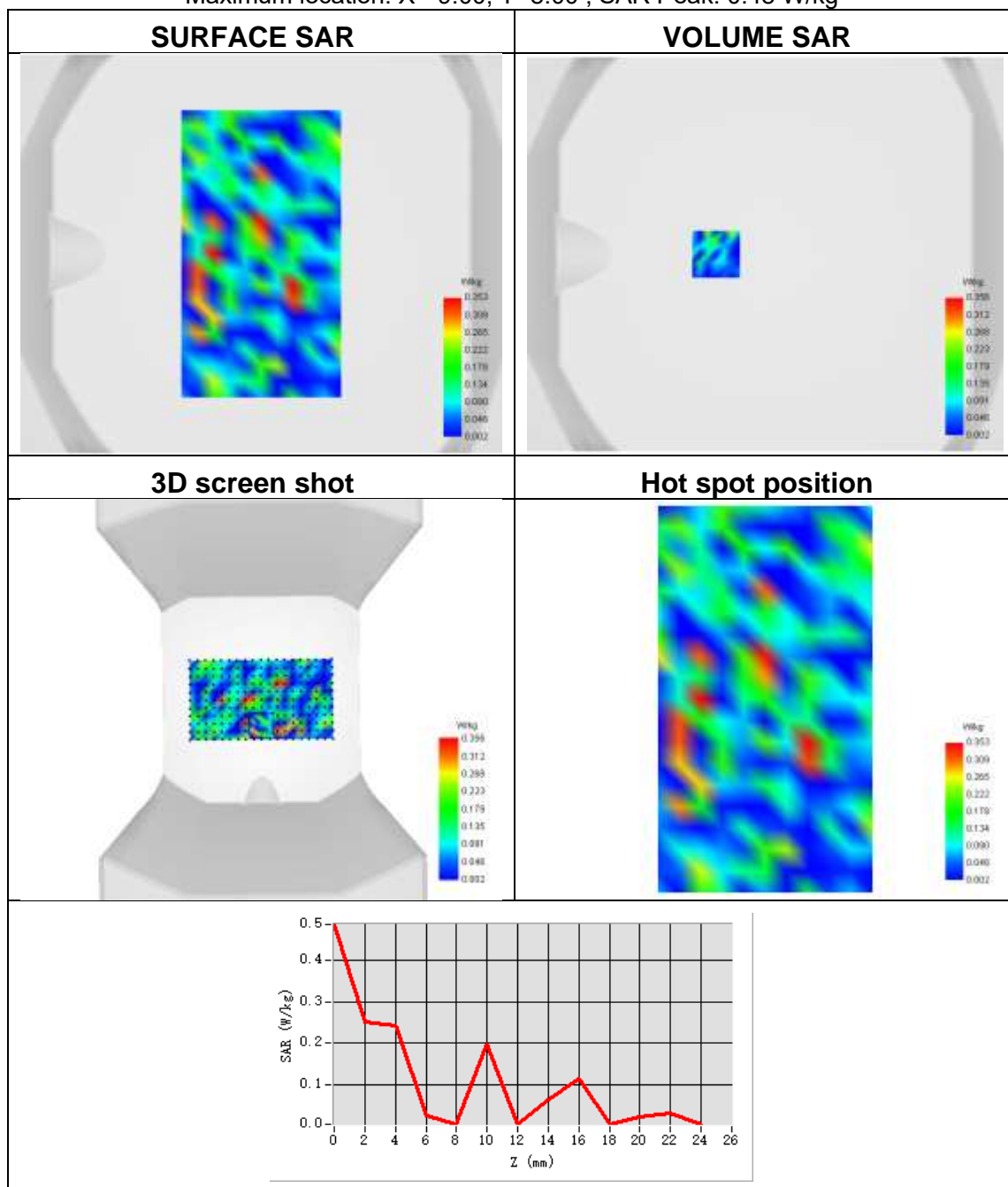




Plot 36:

Test Date	2023-07-24
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11 n-HT20 ANT A
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.062
SAR 1g (W/Kg)	0.172

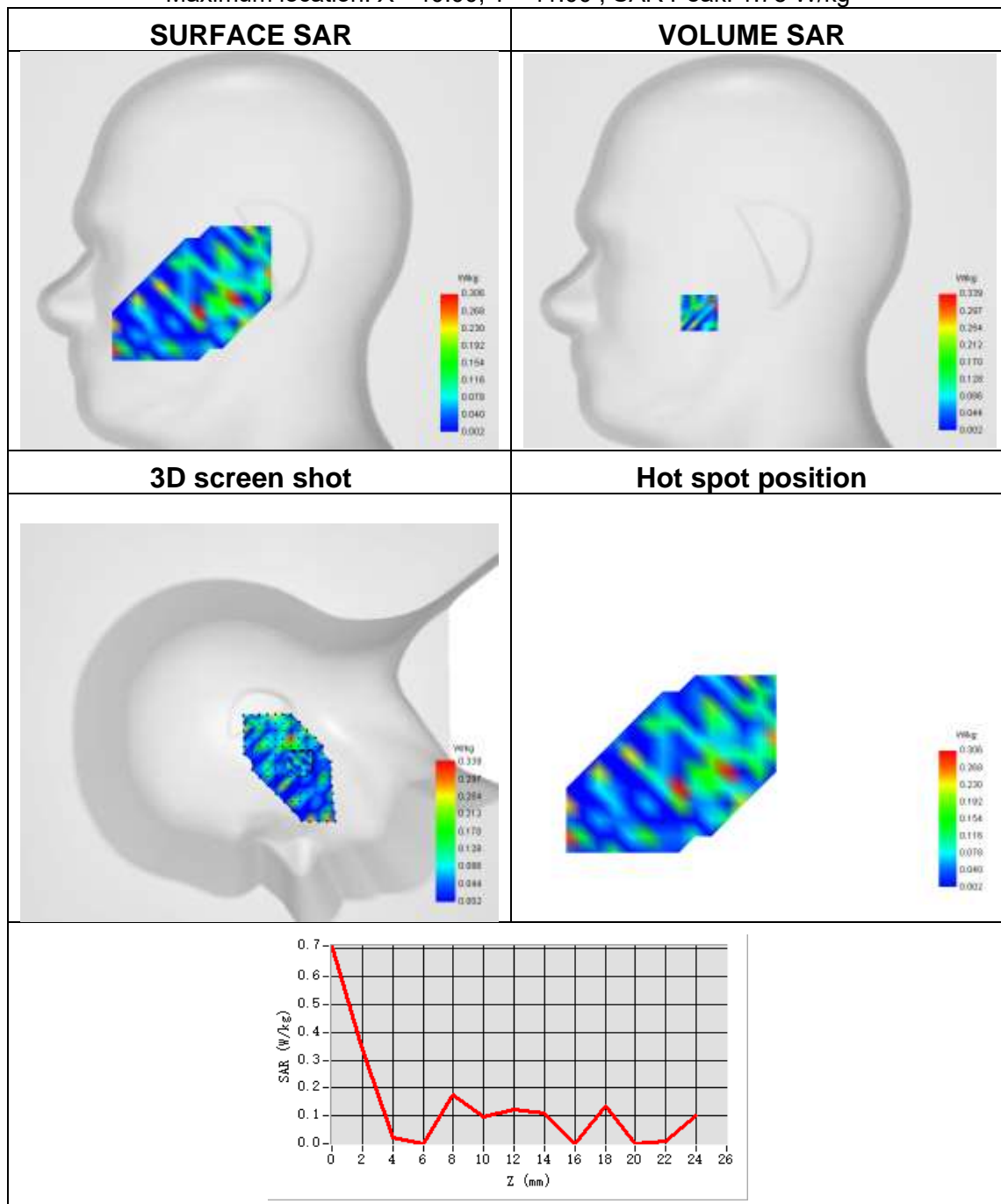
Maximum location: X=-9.00, Y=8.00 ; SAR Peak: 0.48 W/kg



Plot 37:

Test Date	2023-07-24
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 ANT B
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.075
SAR 1g (W/Kg)	0.211

Maximum location: X=-40.00, Y=-41.00 ; SAR Peak: 1.75 W/kg

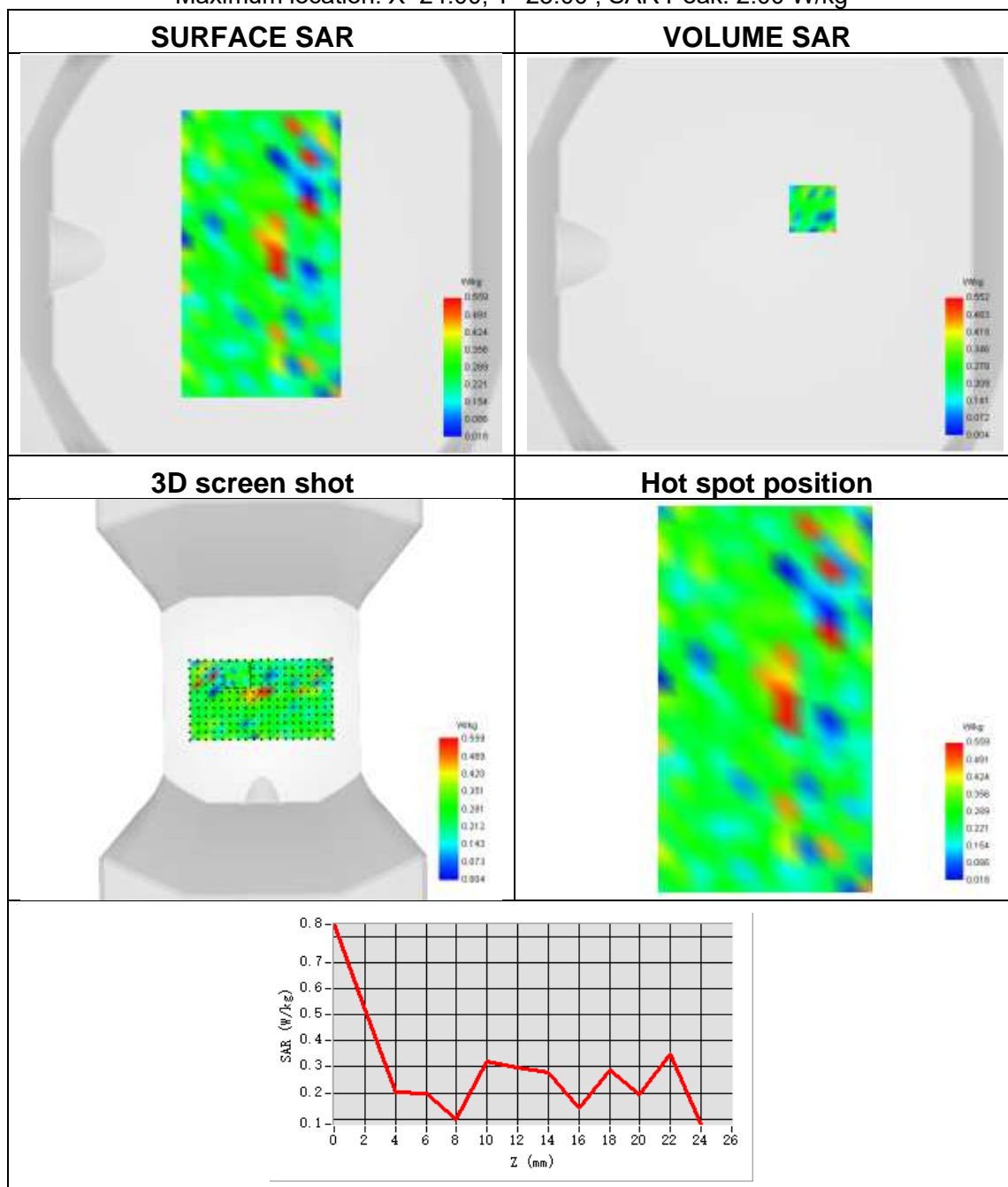




Plot 38:

Test Date	2023-07-24
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11 n-HT20 ANT B
Signal	IEEE 802.11
Frequency	5510.000
SAR 10g (W/Kg)	0.170
SAR 1g (W/Kg)	0.284

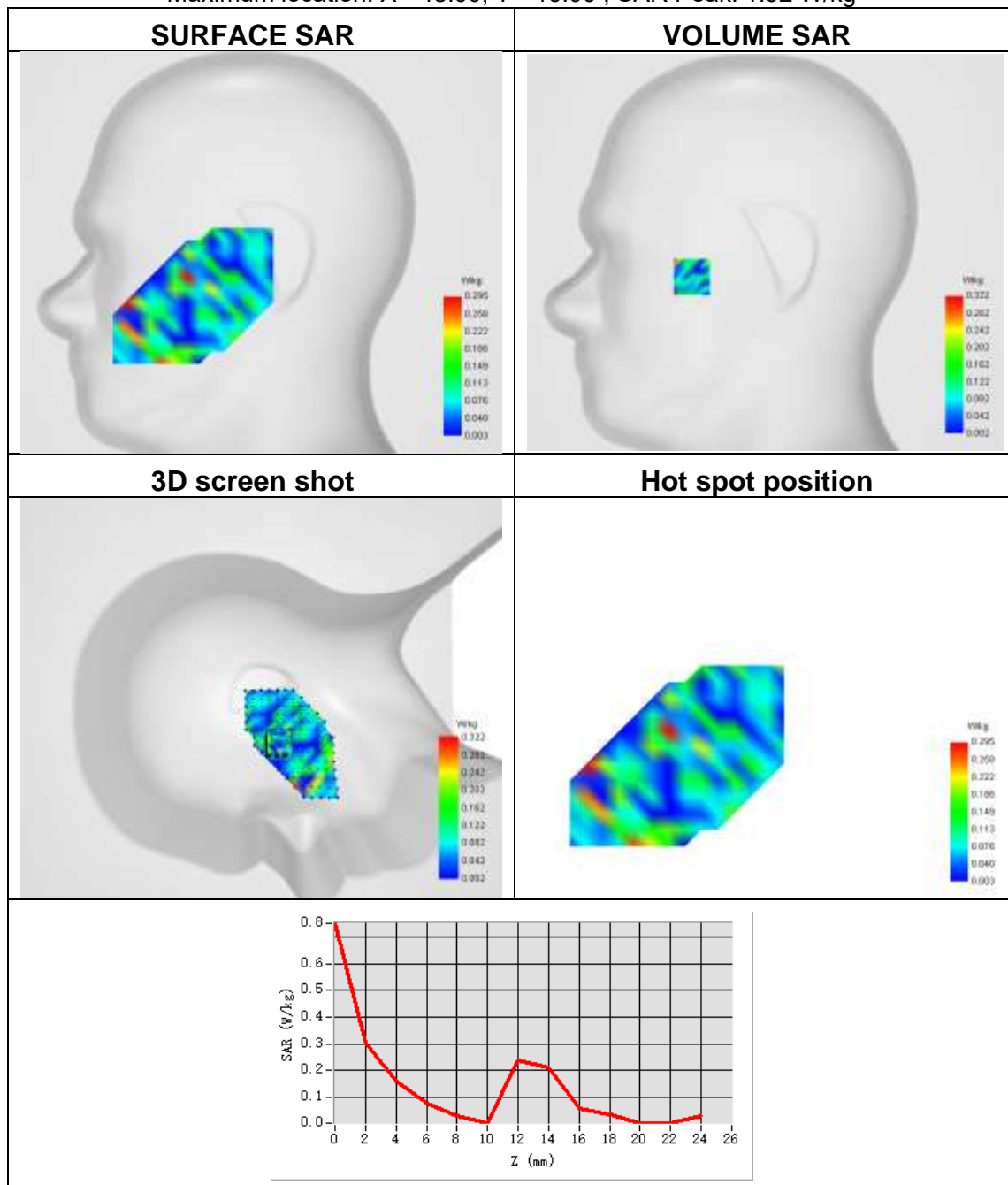
Maximum location: X=24.00, Y=23.00 ; SAR Peak: 2.00 W/kg



Plot 39:

Test Date	2023-07-25
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.075
SAR 1g (W/Kg)	0.157

Maximum location: X=-48.00, Y=-16.00 ; SAR Peak: 1.92 W/kg

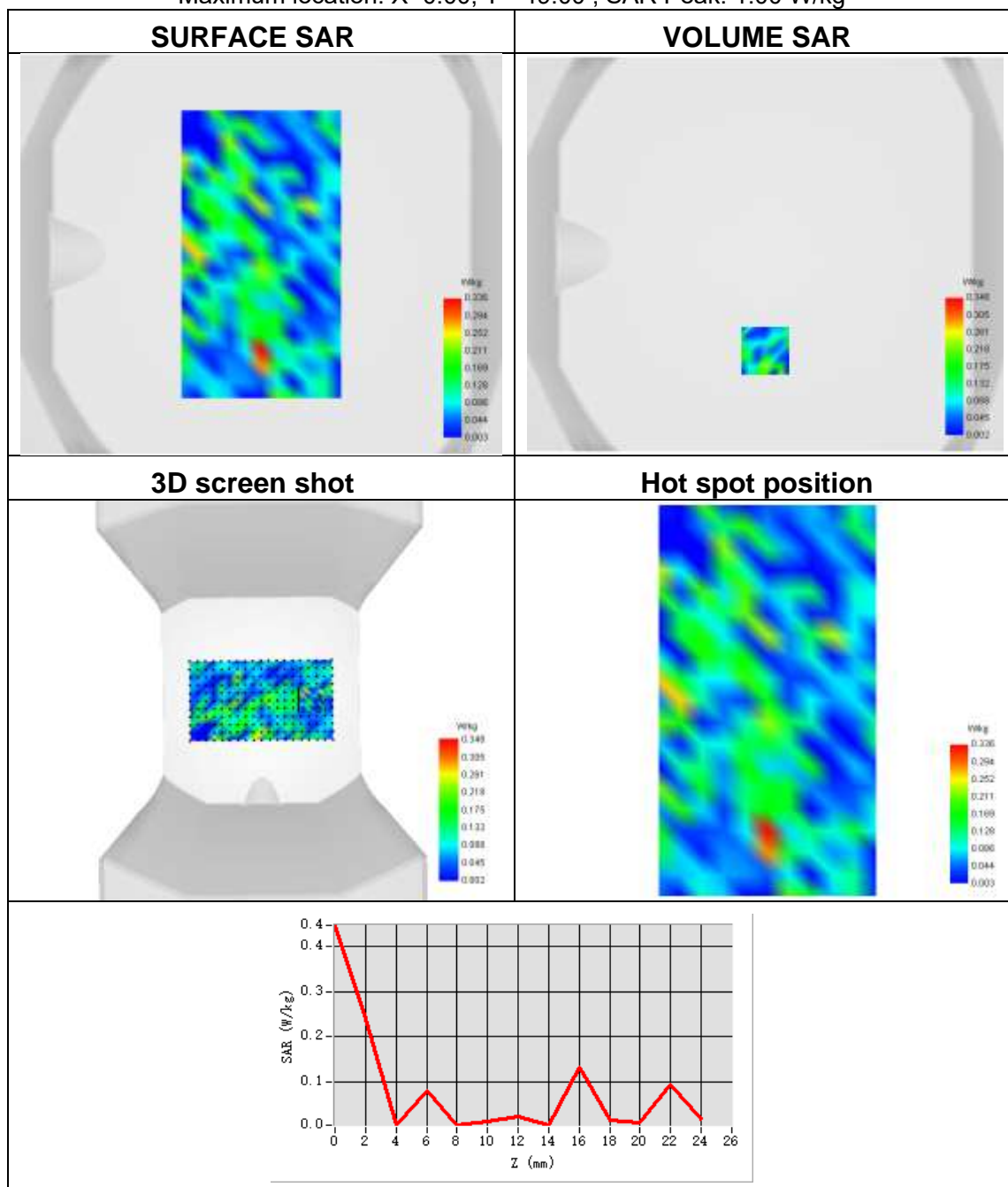




Plot 40:

Test Date	2023-07-25
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.060
SAR 1g (W/Kg)	0.073

Maximum location: X=0.00, Y=-49.00 ; SAR Peak: 1.00 W/kg

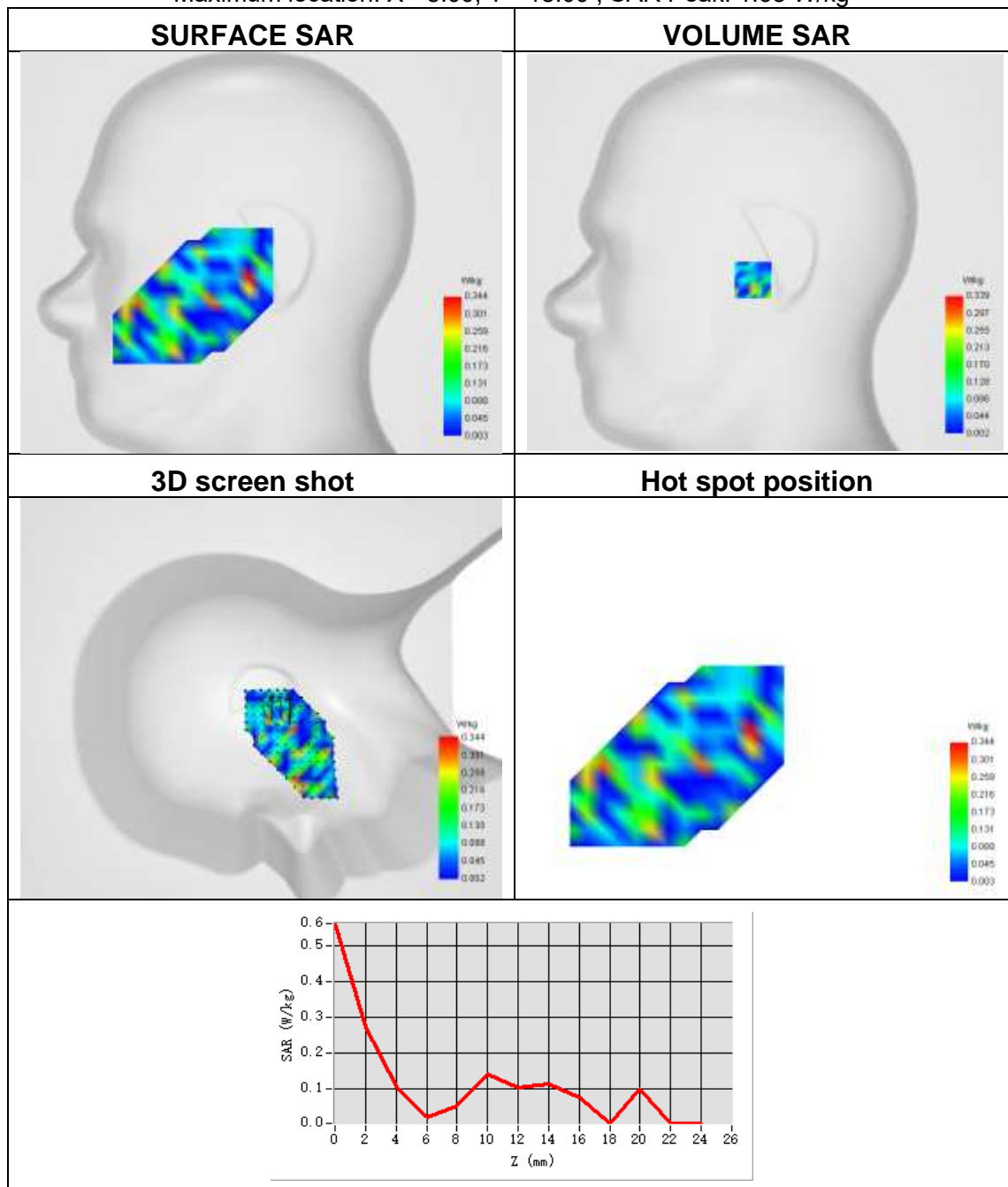




Plot 41:

Test Date	2023-07-25
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.099
SAR 1g (W/Kg)	0.254

Maximum location: X=-8.00, Y=-18.00 ; SAR Peak: 1.68 W/kg

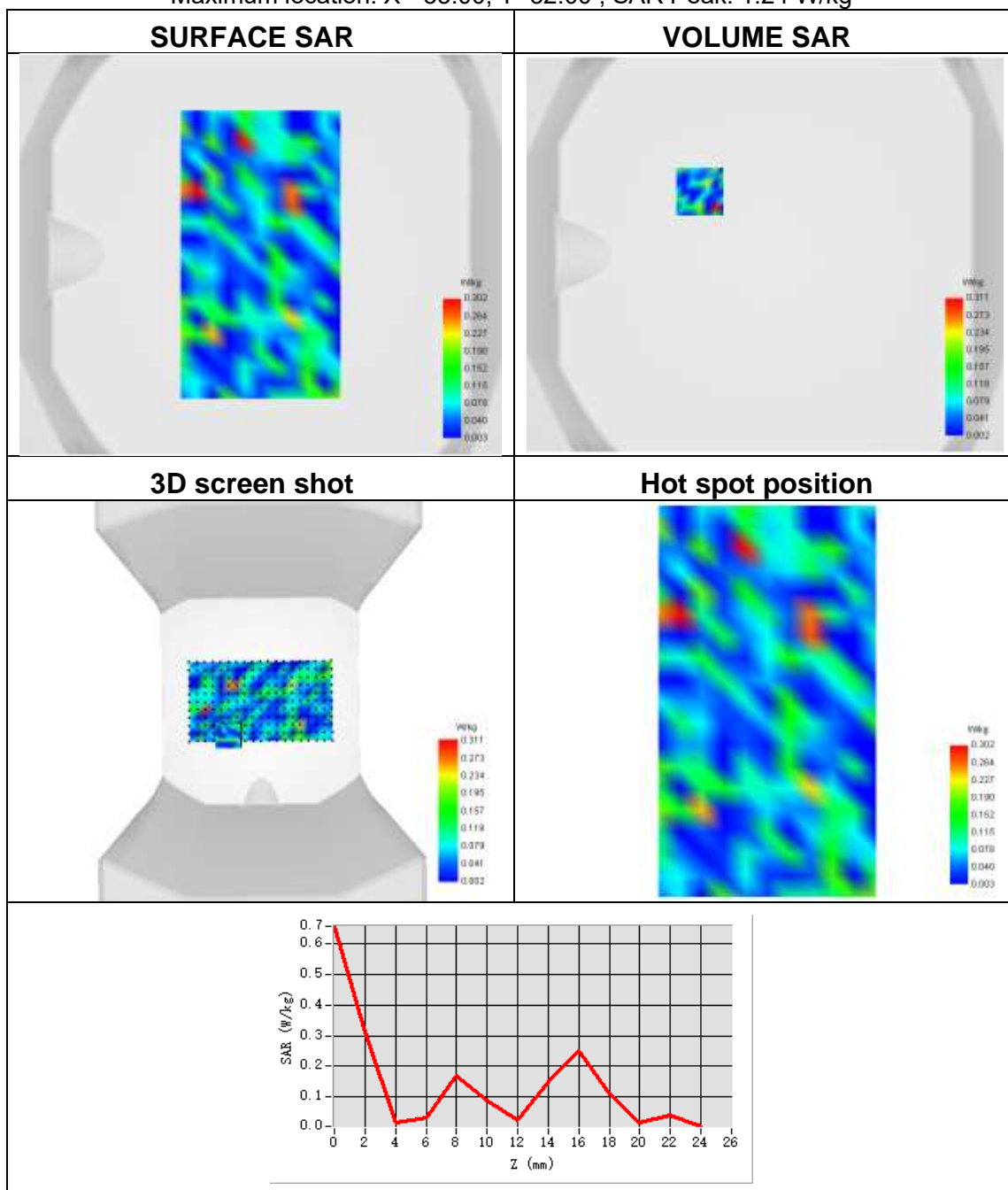




Plot 42:

Test Date	2023-07-25
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5700
SAR 10g (W/Kg)	0.049
SAR 1g (W/Kg)	0.094

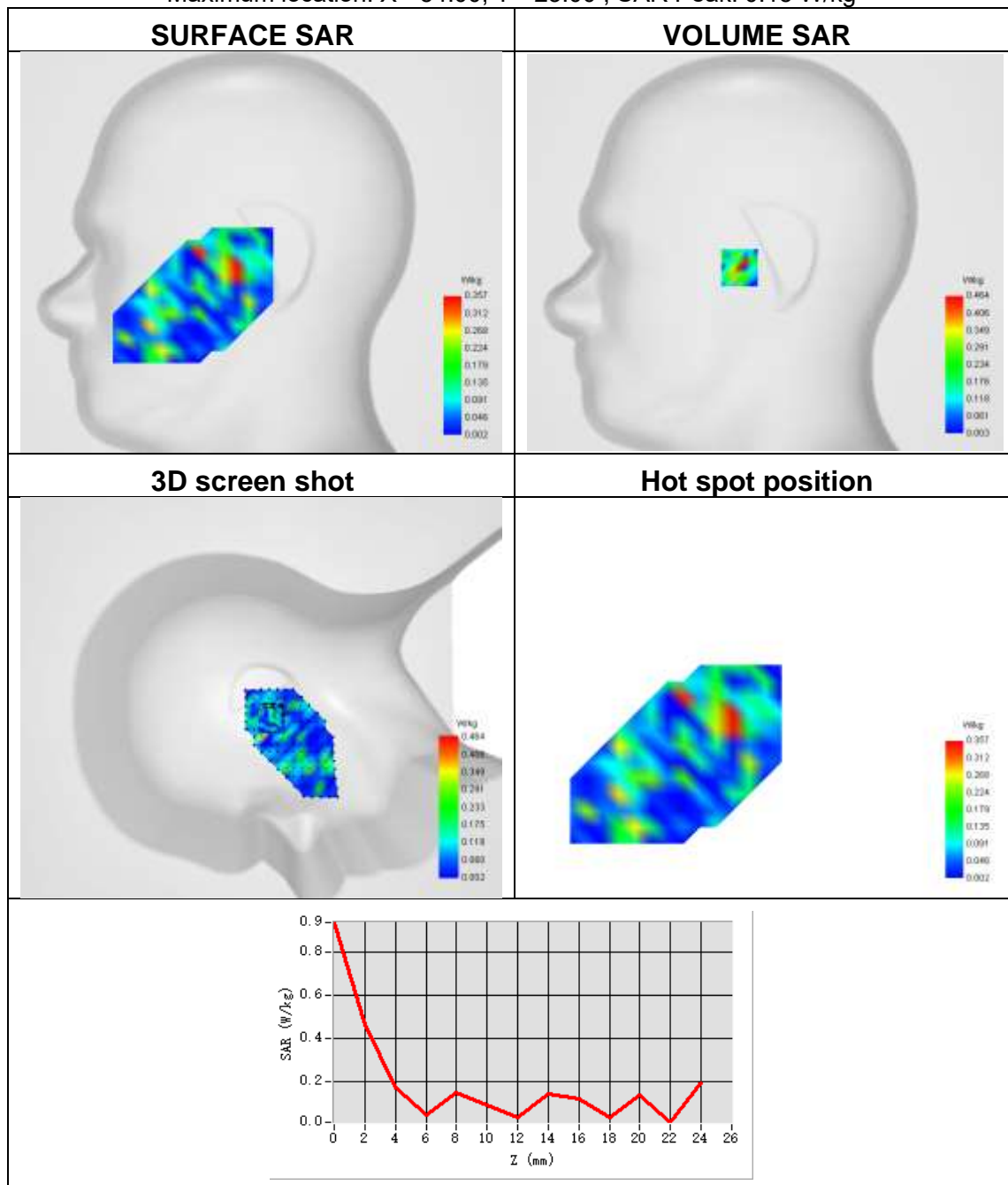
Maximum location: X=-33.00, Y=32.00 ; SAR Peak: 1.24 W/kg



Plot 43:

Test Date	2023-07-26
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11a U-NII ANT A
Signal	IEEE 802.11
Frequency	5745
SAR 10g (W/Kg)	0.127
SAR 1g (W/Kg)	0.298

Maximum location: X=-54.00, Y=-23.00 ; SAR Peak: 0.15 W/kg

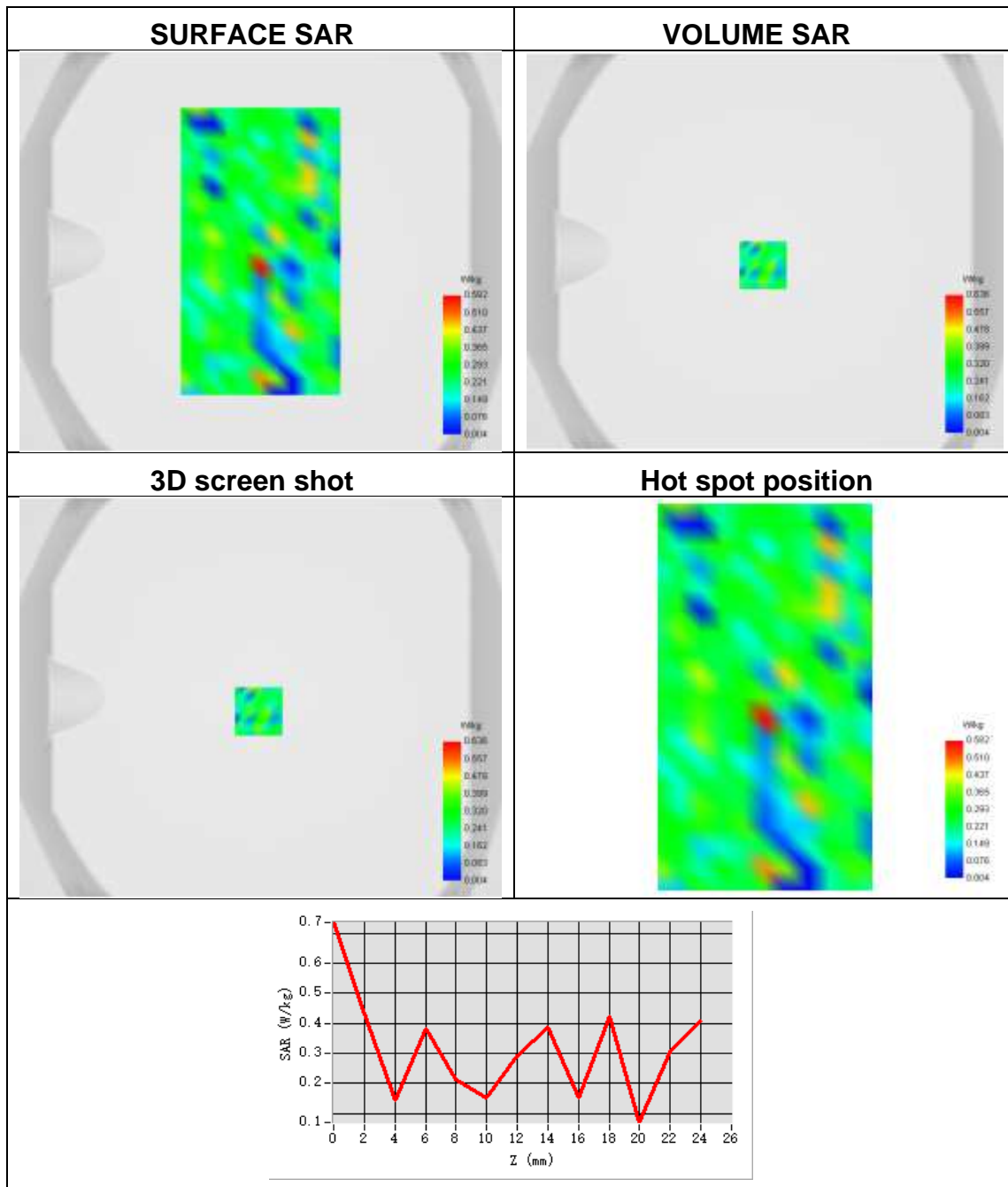




Plot 44:

Test Date	2023-07-26
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11a U-NII ANT A
Signal	IEEE 802.11
Frequency	5745
SAR 10g (W/Kg)	0.181
SAR 1g (W/Kg)	0.318

Maximum location: X=7.00, Y=50.00 ; SAR Peak: 0.48 W/kg

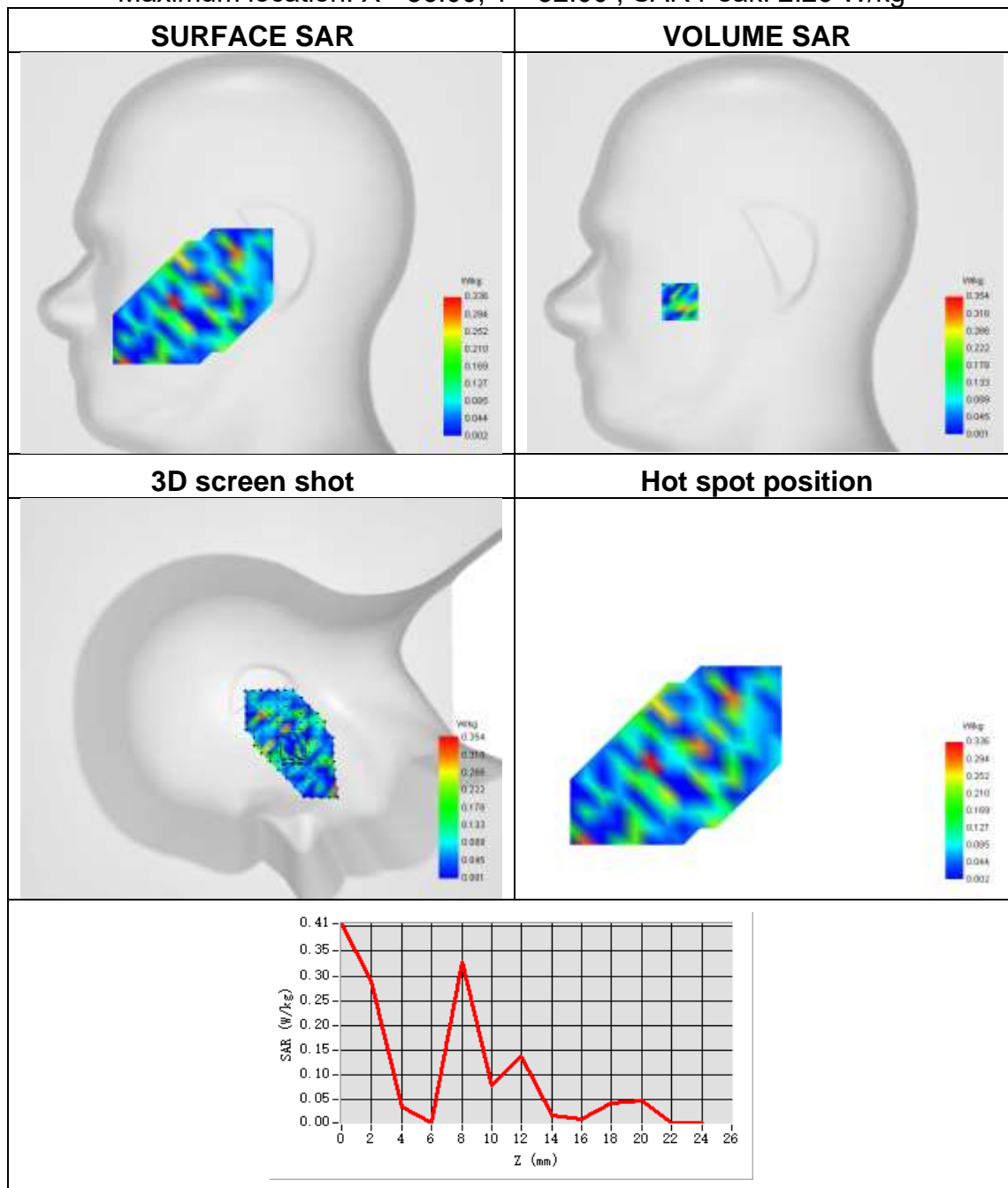




Plot 45:

Test Date	2023-07-26
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 ANT B
Signal	IEEE 802.11
Frequency	5745
SAR 10g (W/Kg)	0.079
SAR 1g (W/Kg)	0.305

Maximum location: X=-56.00, Y=-32.00 ; SAR Peak: 2.26 W/kg

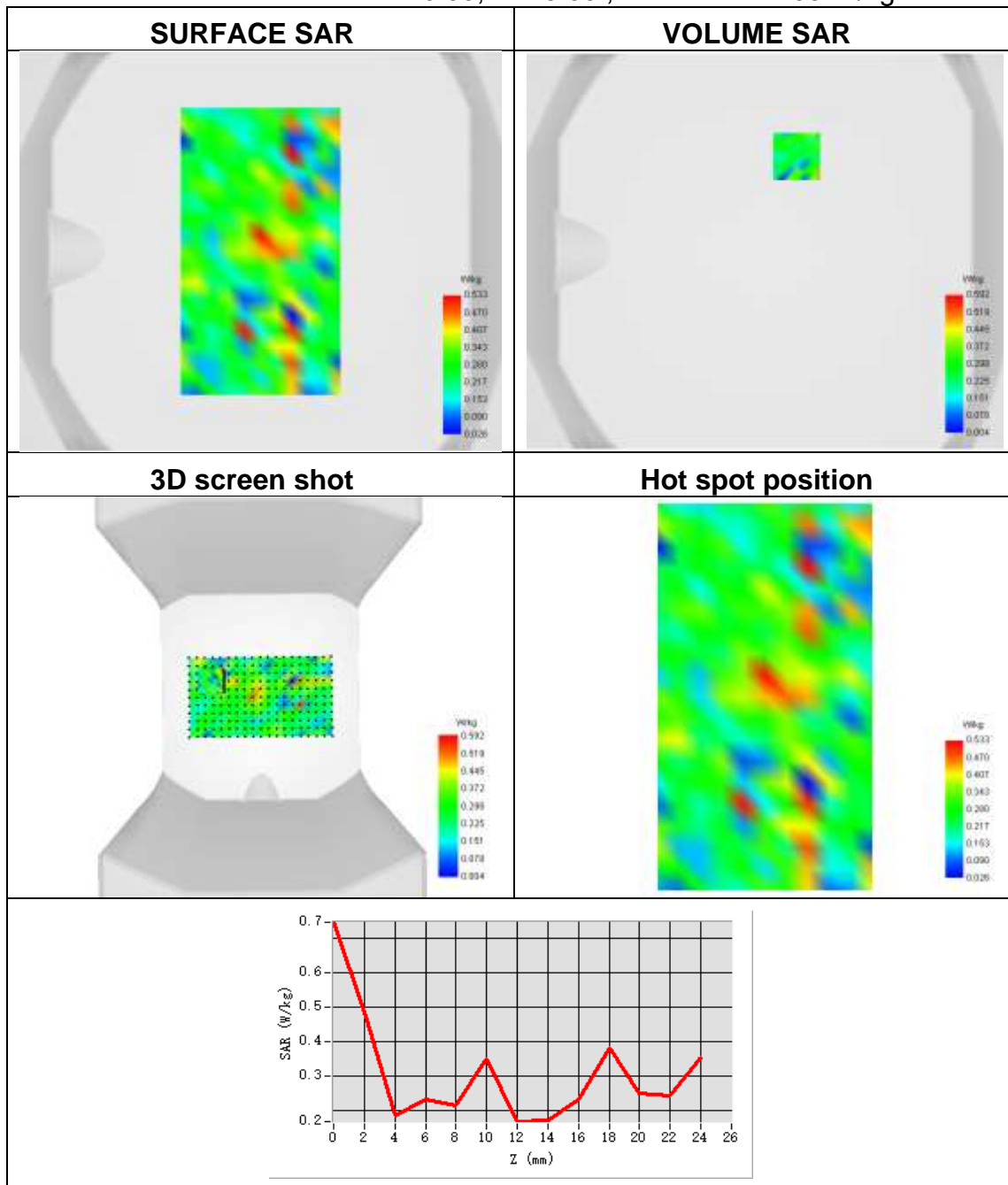




Plot 46:

Test Date	2023-07-26
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11n-HT20 ANT B
Signal	IEEE 802.11
Frequency	5745
SAR 10g (W/Kg)	0.171
SAR 1g (W/Kg)	0.296

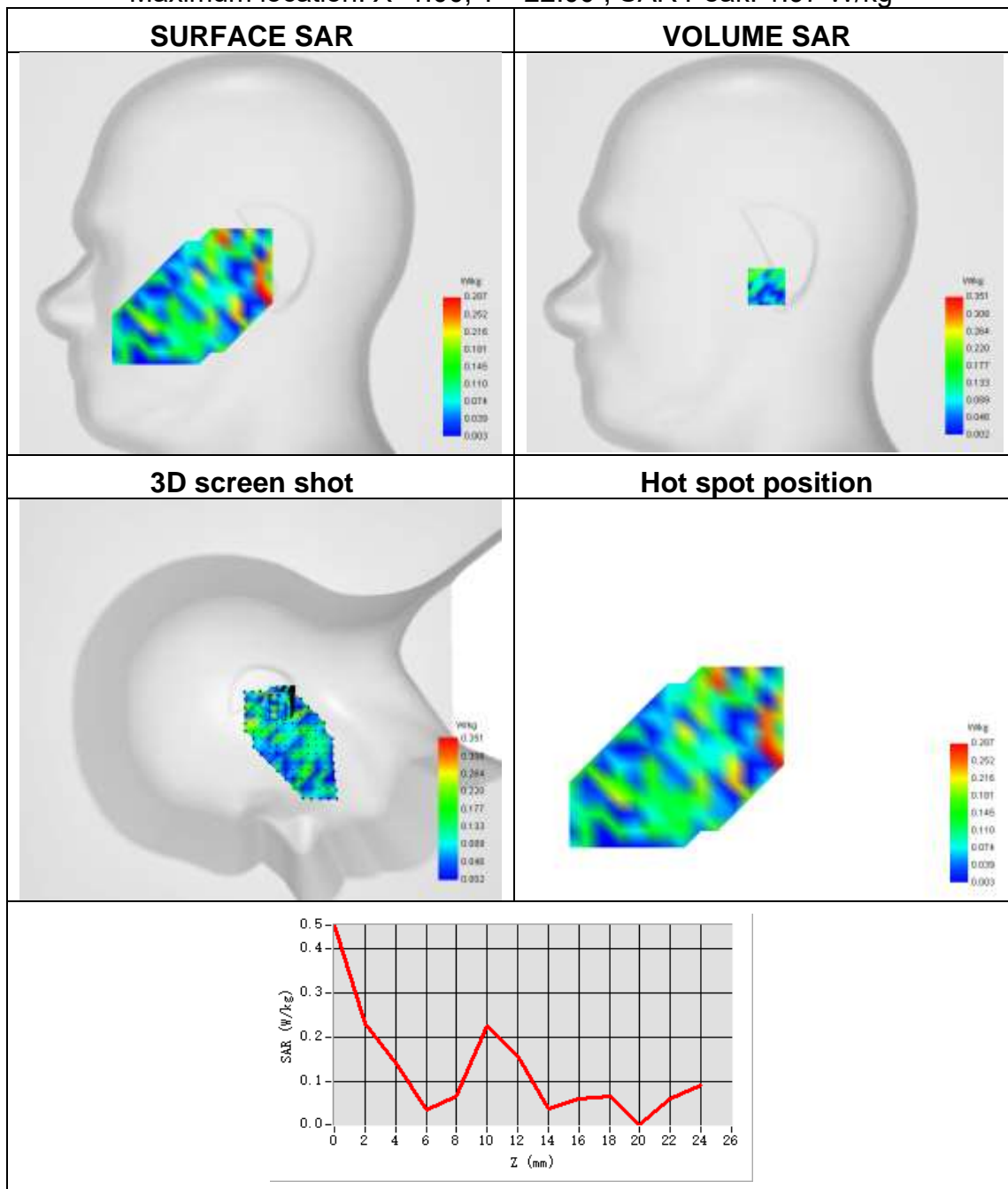
Maximum location: X=16.00, Y=48.00 ; SAR Peak: 2.03 W/kg



Plot 47:

Test Date	2023-07-08
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5785
SAR 10g (W/Kg)	0.085
SAR 1g (W/Kg)	0.215

Maximum location: X=1.00, Y=-22.00 ; SAR Peak: 1.97 W/kg

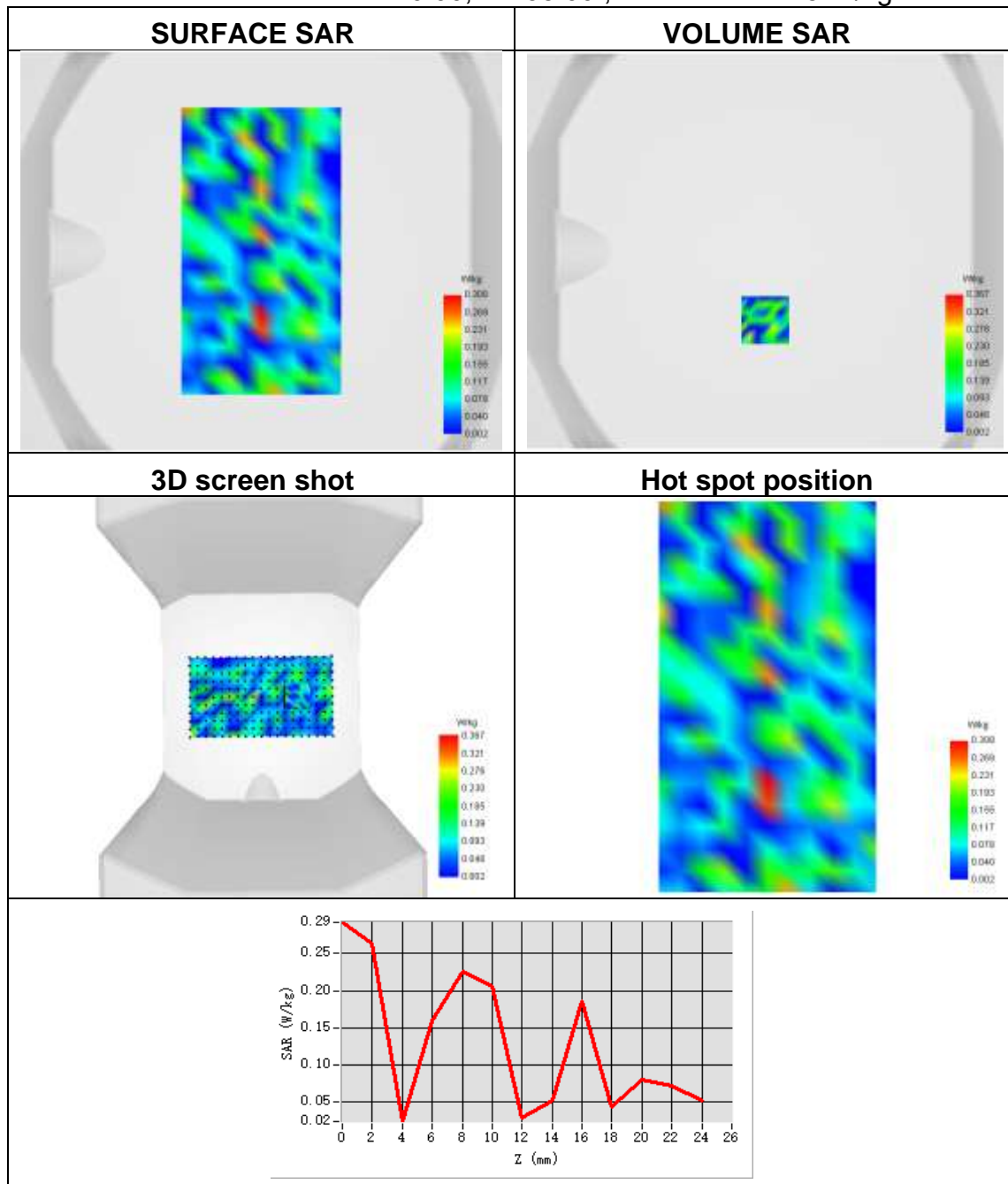




Plot 48:

Test Date	2023-07-08
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Back Side
Band	IEEE 802.11 n-HT20 MIMO ANT A
Signal	IEEE 802.11
Frequency	5785
SAR 10g (W/Kg)	0.075
SAR 1g (W/Kg)	0.113

Maximum location: X=0.00, Y=-35.00 ; SAR Peak: 1.15 W/kg

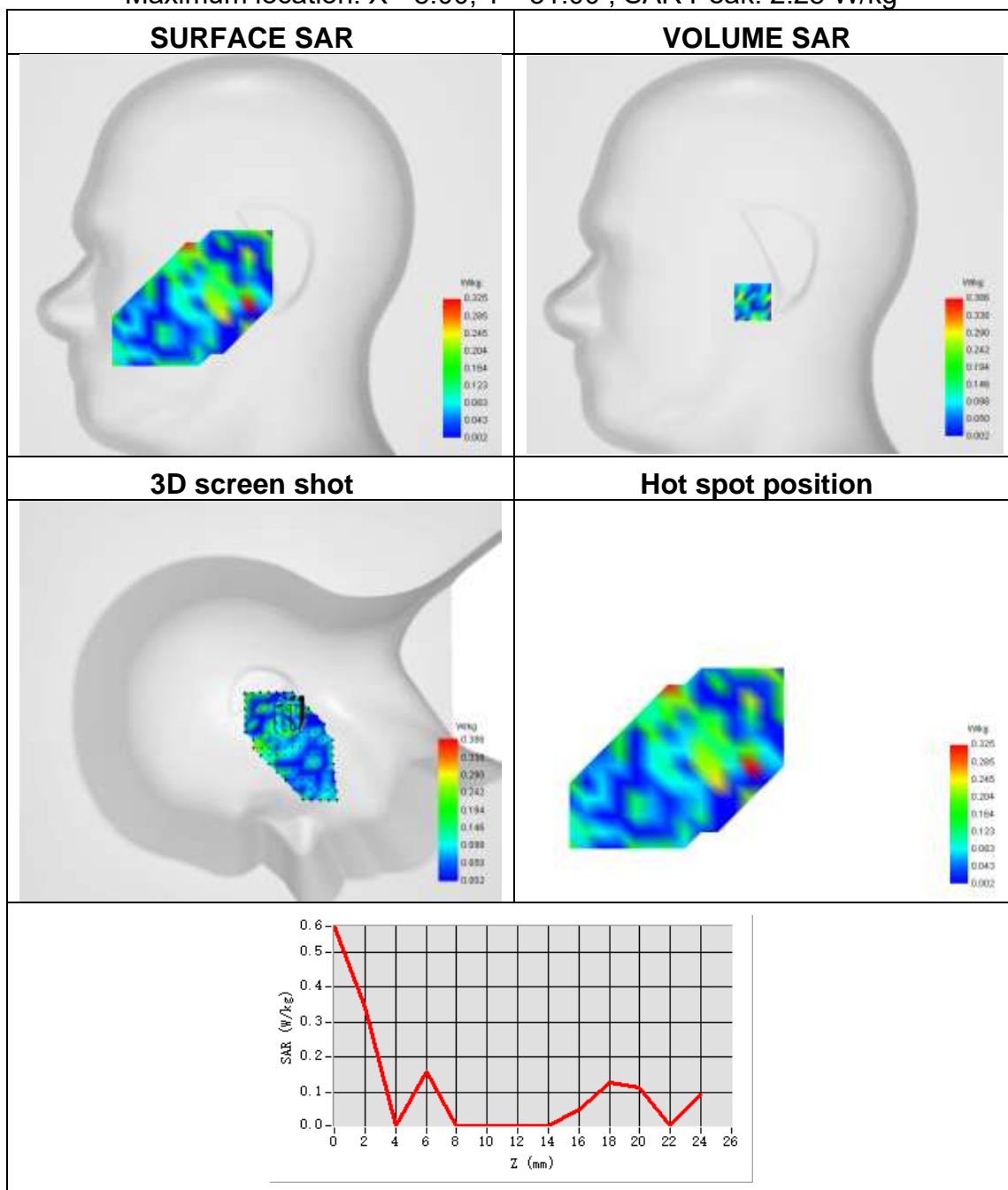




Plot 49:

Test Date	2023-07-08
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Right head
Device Position	Tilt
Band	IEEE 802.11n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5785
SAR 10g (W/Kg)	0.085
SAR 1g (W/Kg)	0.109

Maximum location: X=-8.00, Y=-31.00 ; SAR Peak: 2.28 W/kg

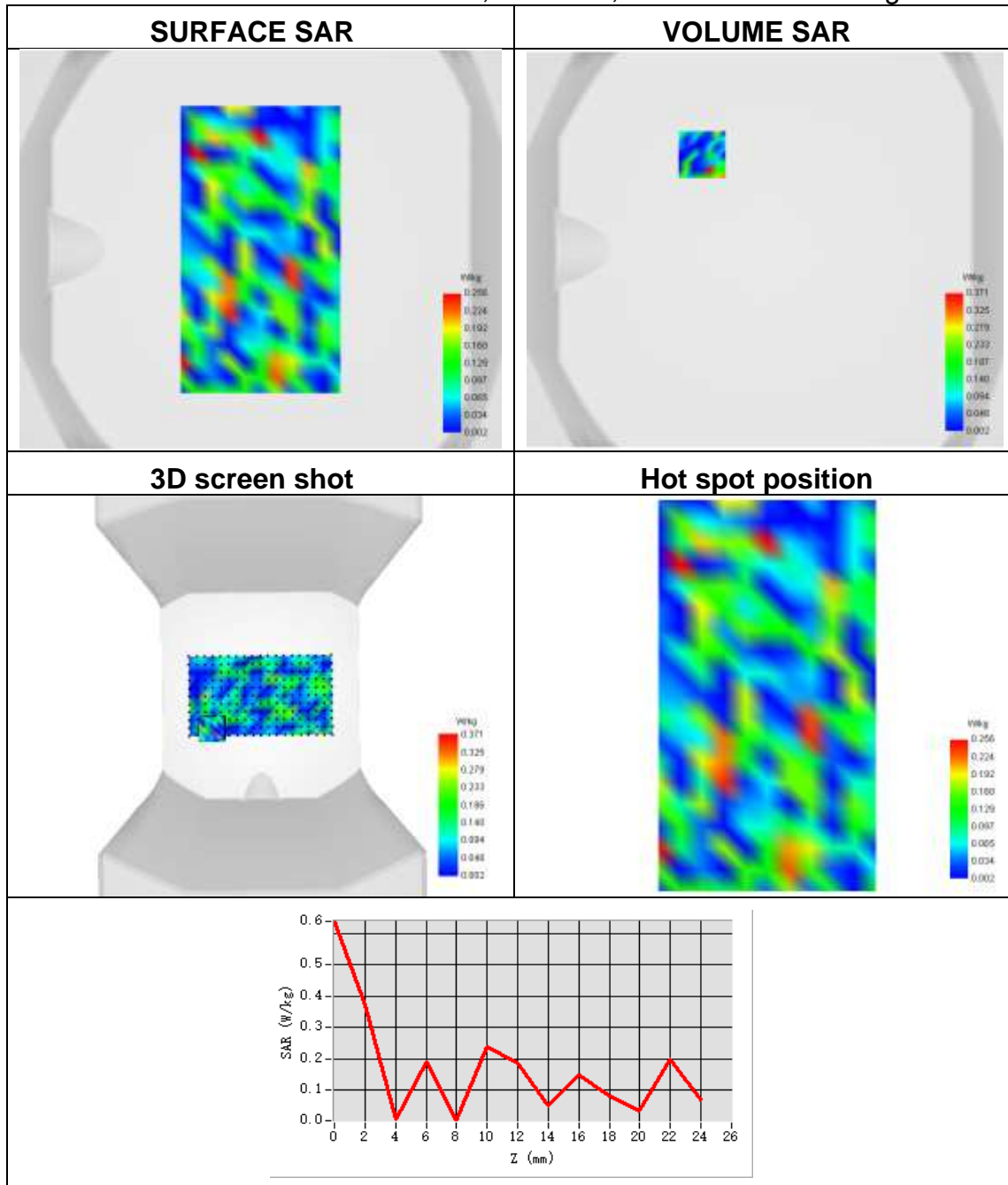




Plot 50:

Test Date	2023-07-08
Area Scan	surf_sam_plan.txt
Zoom Scan	7x7x12,dx=4mm dy=4mm dz=2mm
Phantom	Validation plane
Device Position	Right Side
Band	IEEE 802.11 n-HT20 MIMO ANT B
Signal	IEEE 802.11
Frequency	5785
SAR 10g (W/Kg)	0.069
SAR 1g (W/Kg)	0.179

Maximum location: X=-32.00, Y=48.00 ; SAR Peak: 1.01 W/kg

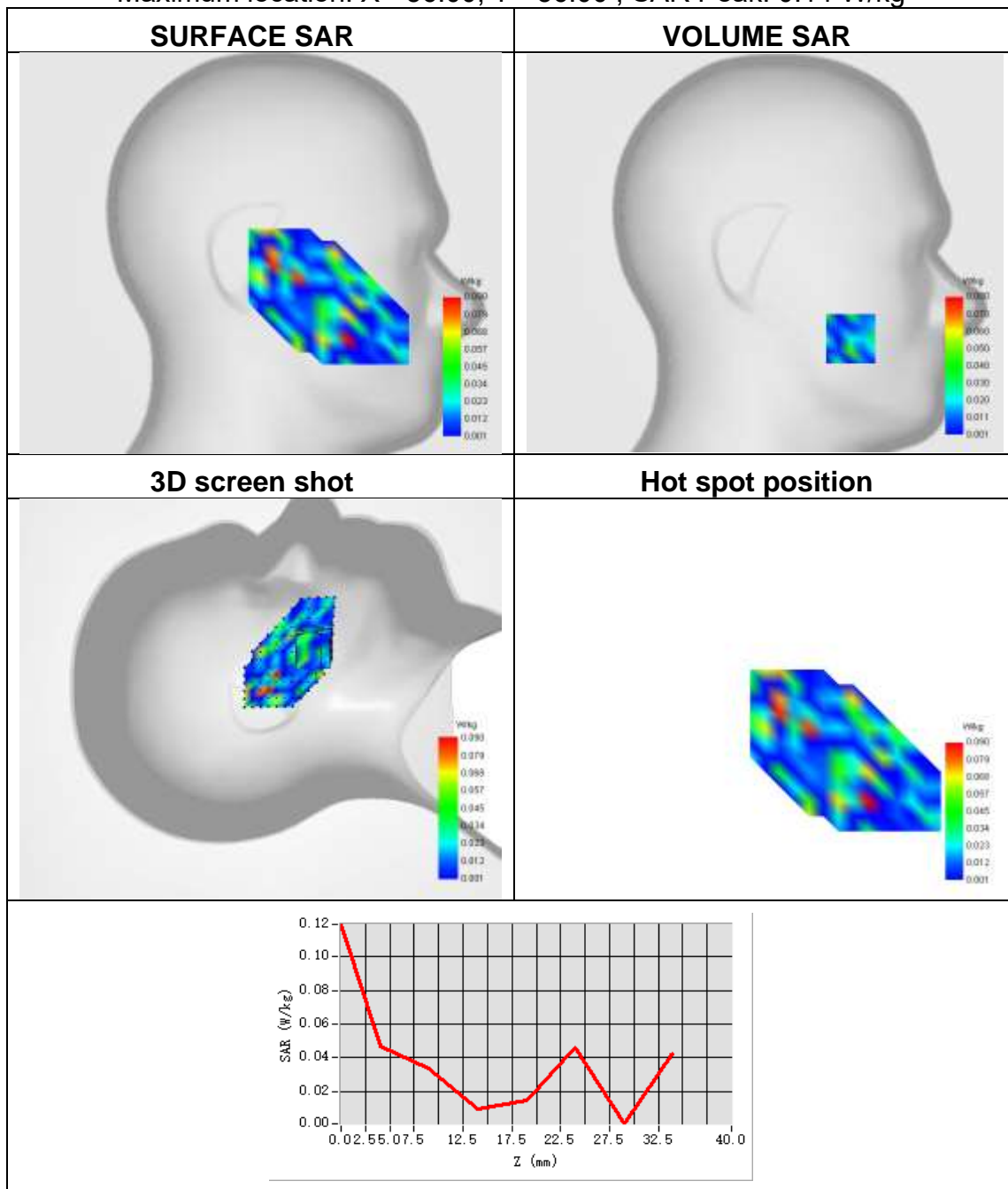




Plot 1:

Test Date	2023-07-14
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	Bluetooth
Signal	GFSK
Frequency	2441
SAR 10g (W/Kg)	0.022
SAR 1g (W/Kg)	0.044

Maximum location: X=-56.00, Y=-56.00 ; SAR Peak: 0.14 W/kg

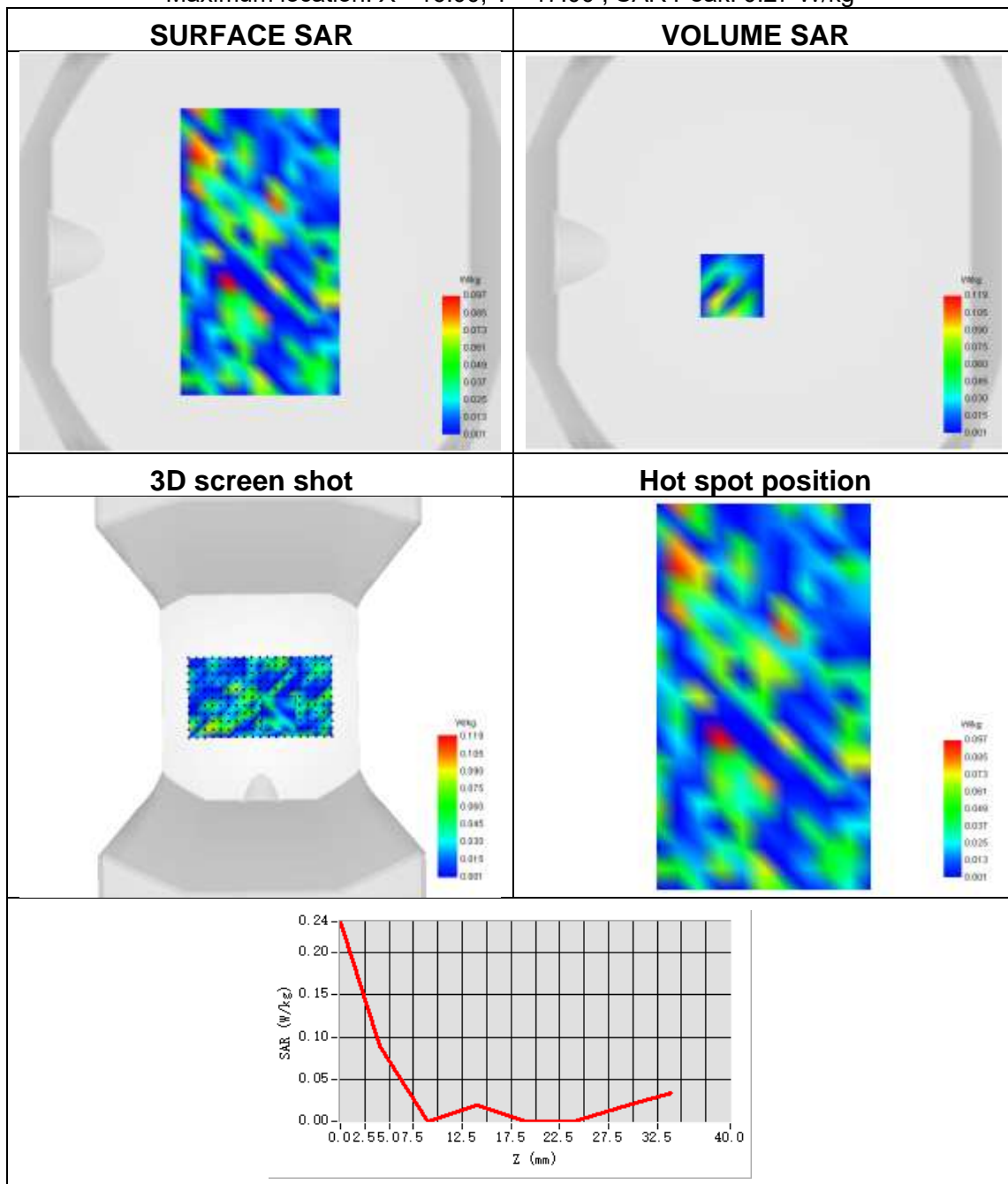




Plot 52:

Test Date	2023-07-14
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	Bluetooth
Signal	GFSK
Frequency	2441
SAR 10g (W/Kg)	0.031
SAR 1g (W/Kg)	0.085

Maximum location: X=-16.00, Y=-17.00 ; SAR Peak: 0.27 W/kg

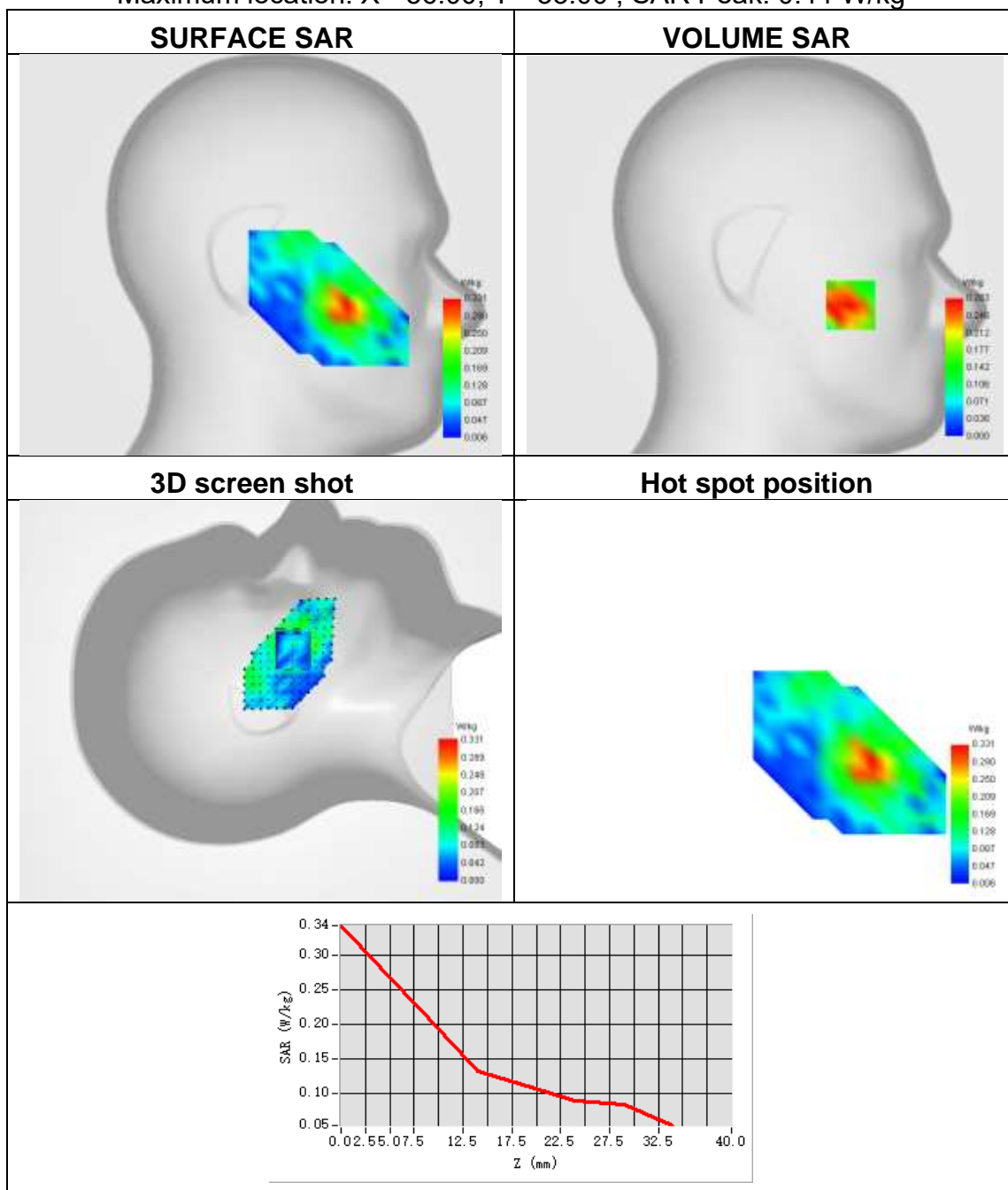




Plot 53:

Test Date	2023-07-12
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	LTE band 2
Signal	LTE FDD
Frequency	1860
SAR 10g (W/Kg)	0.160
SAR 1g (W/Kg)	0.271

Maximum location: X=-56.00, Y=-33.00 ; SAR Peak: 0.44 W/kg

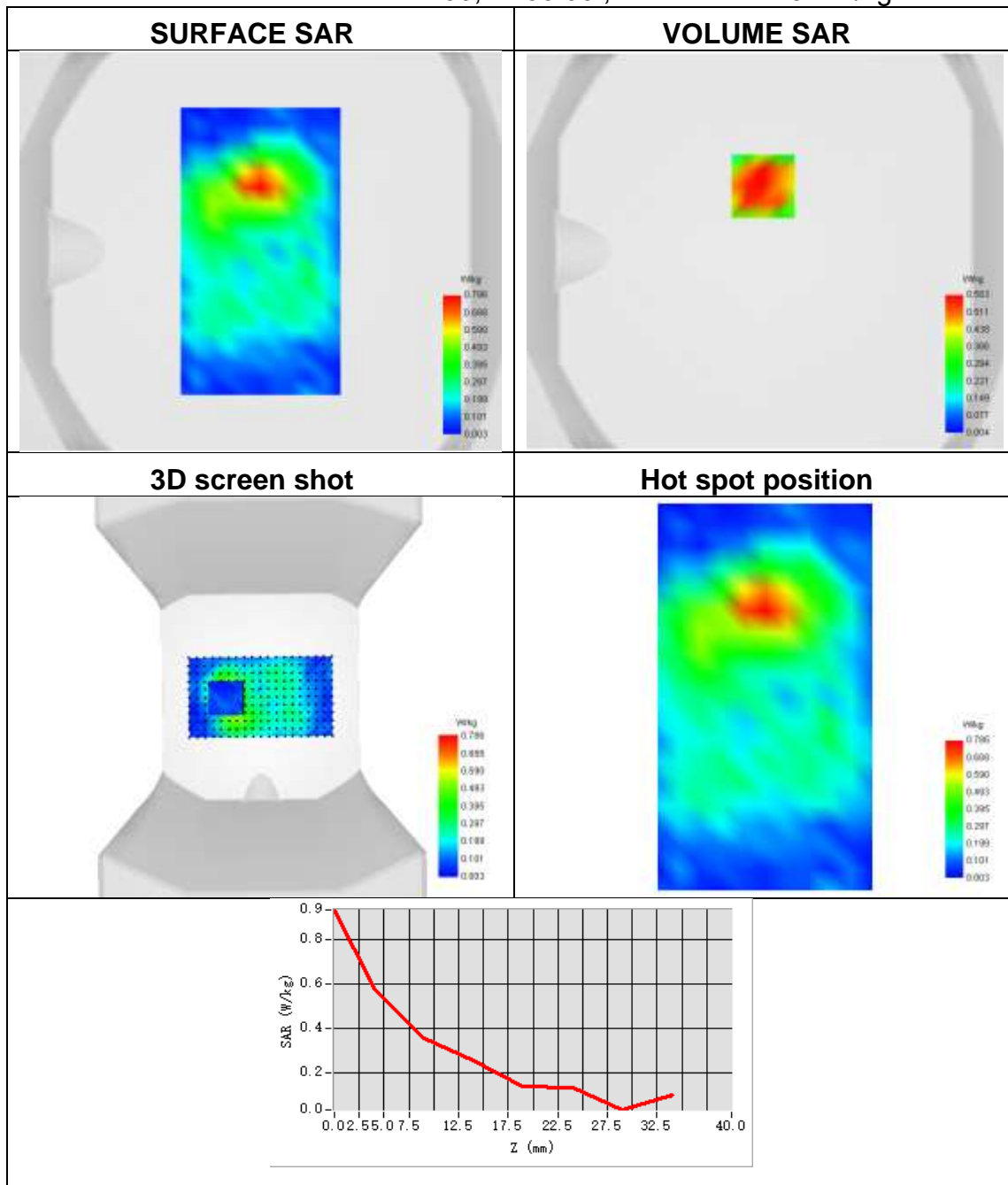




Plot 54:

Test Date	2023-07-12
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	LTE band 2
Signal	LTE FDD
Frequency	1860
SAR 10g (W/Kg)	0.304
SAR 1g (W/Kg)	0.582

Maximum location: X=-1.00, Y=33.00 ; SAR Peak: 1.07 W/kg

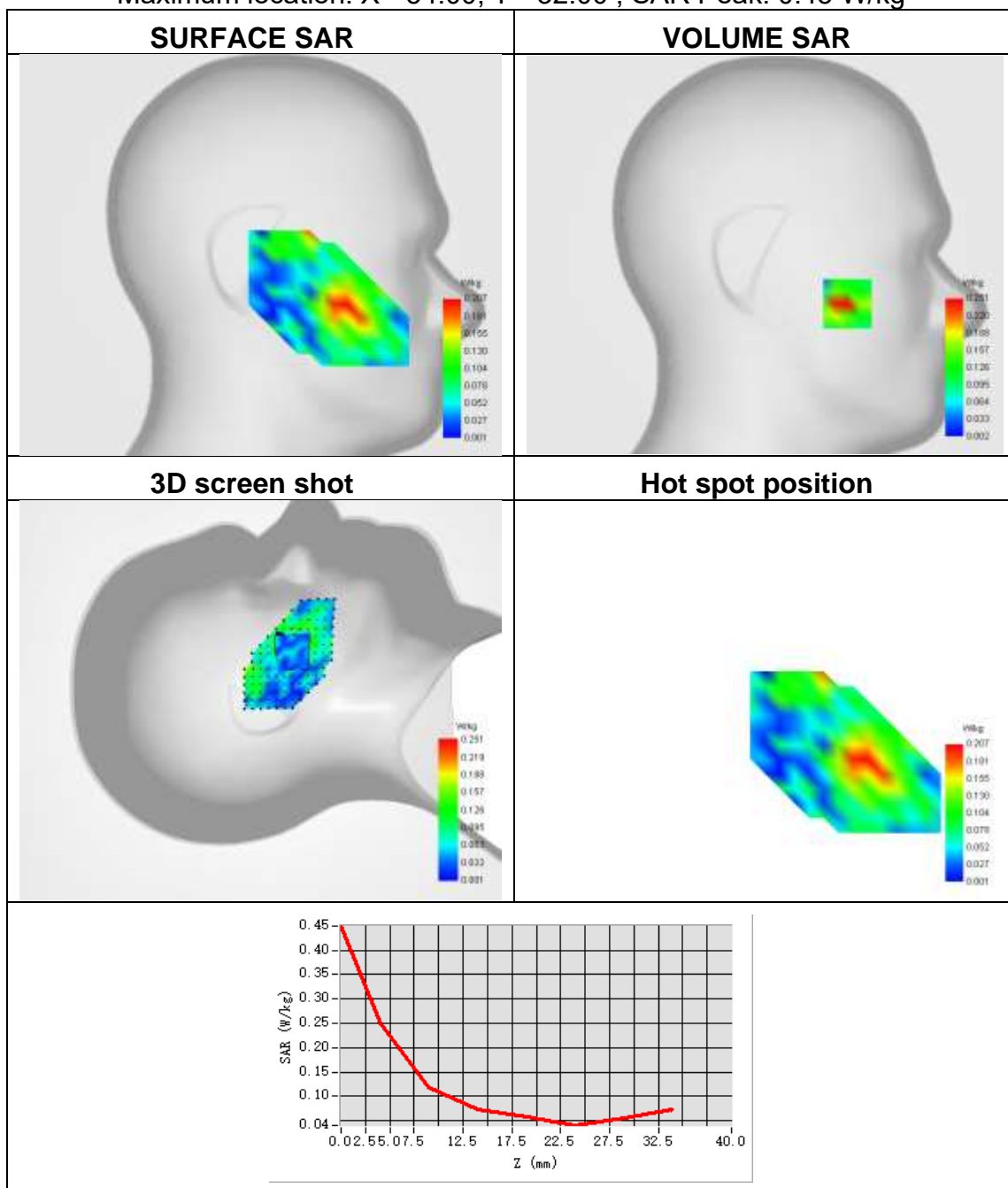




Plot 55:

Test Date	2023-07-11
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	LTE band 4
Signal	LTE FDD
Frequency	1720
SAR 10g (W/Kg)	0.111
SAR 1g (W/Kg)	0.237

Maximum location: X=-54.00, Y=-32.00 ; SAR Peak: 0.45 W/kg

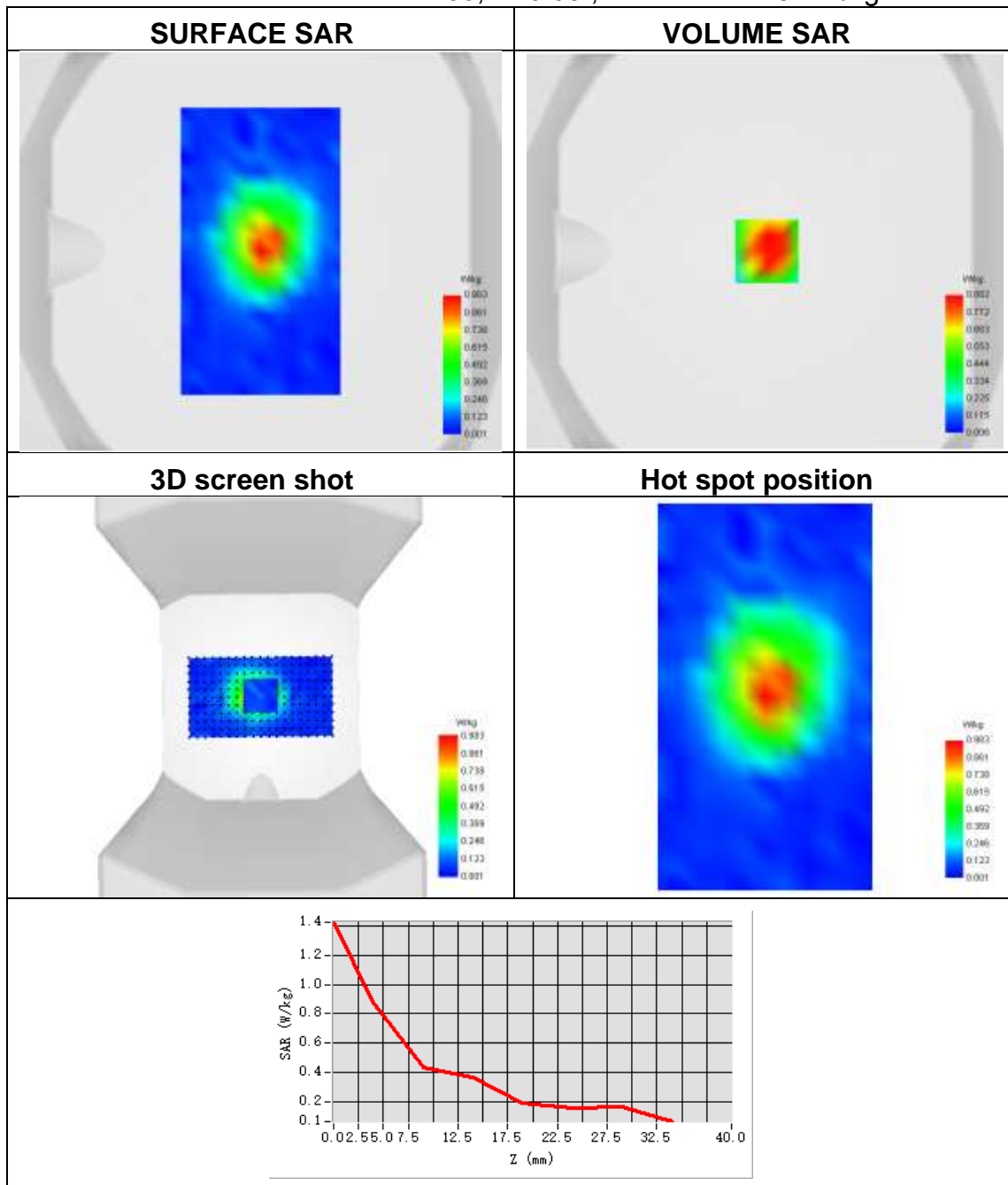




Plot 56:

Test Date	2023-07-11
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	LTE band 4
Signal	LTE FDD
Frequency	1720
SAR 10g (W/Kg)	0.483
SAR 1g (W/Kg)	0.868

Maximum location: X=1.00, Y=0.00 ; SAR Peak: 1.51 W/kg

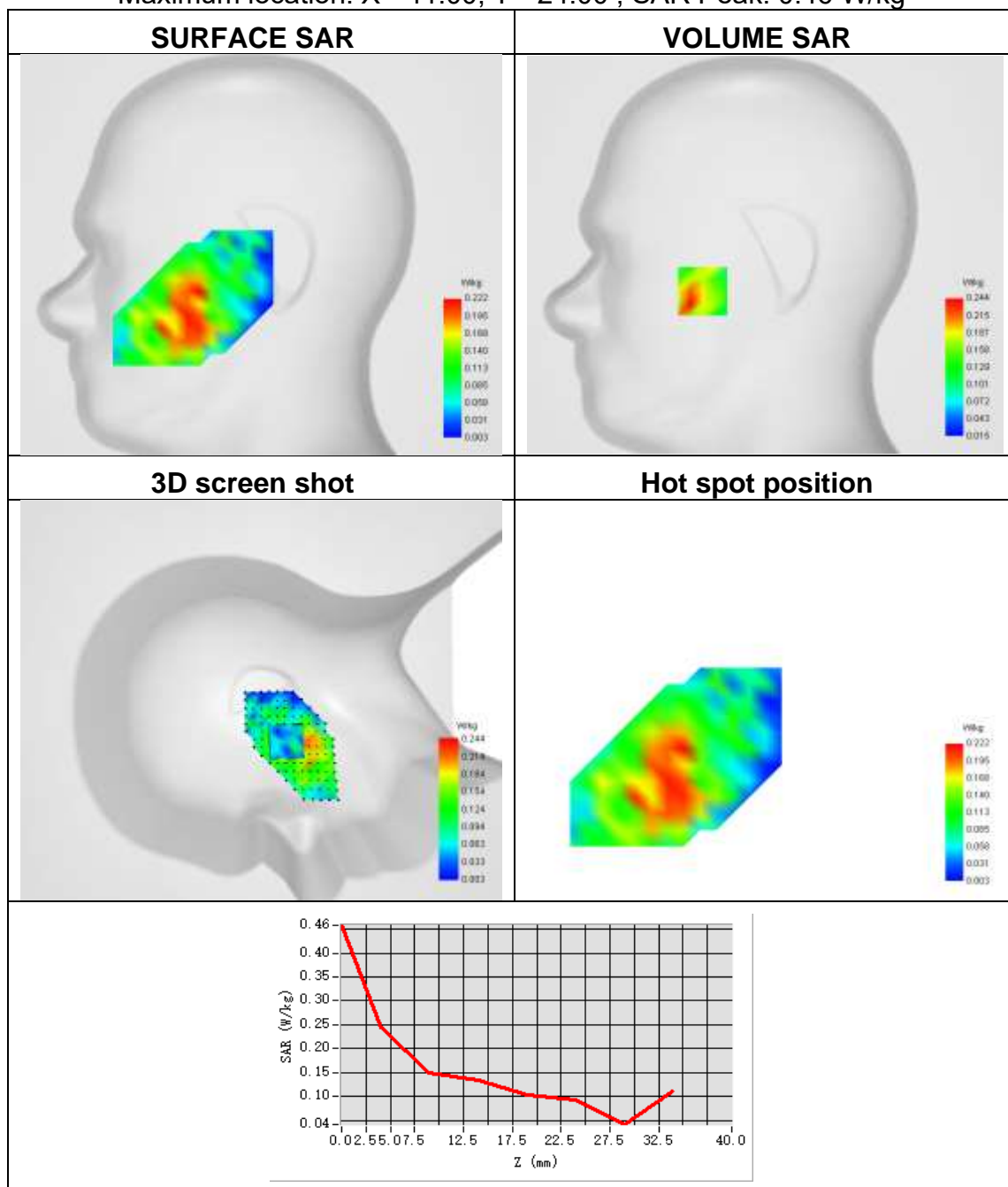




Plot 57:

Test Date	2023-07-17
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right head
Device Position	Cheek
Band	LTE band 5
Signal	LTE FDD
Frequency	844
SAR 10g (W/Kg)	0.127
SAR 1g (W/Kg)	0.236

Maximum location: X=-41.00, Y=-24.00 ; SAR Peak: 0.46 W/kg

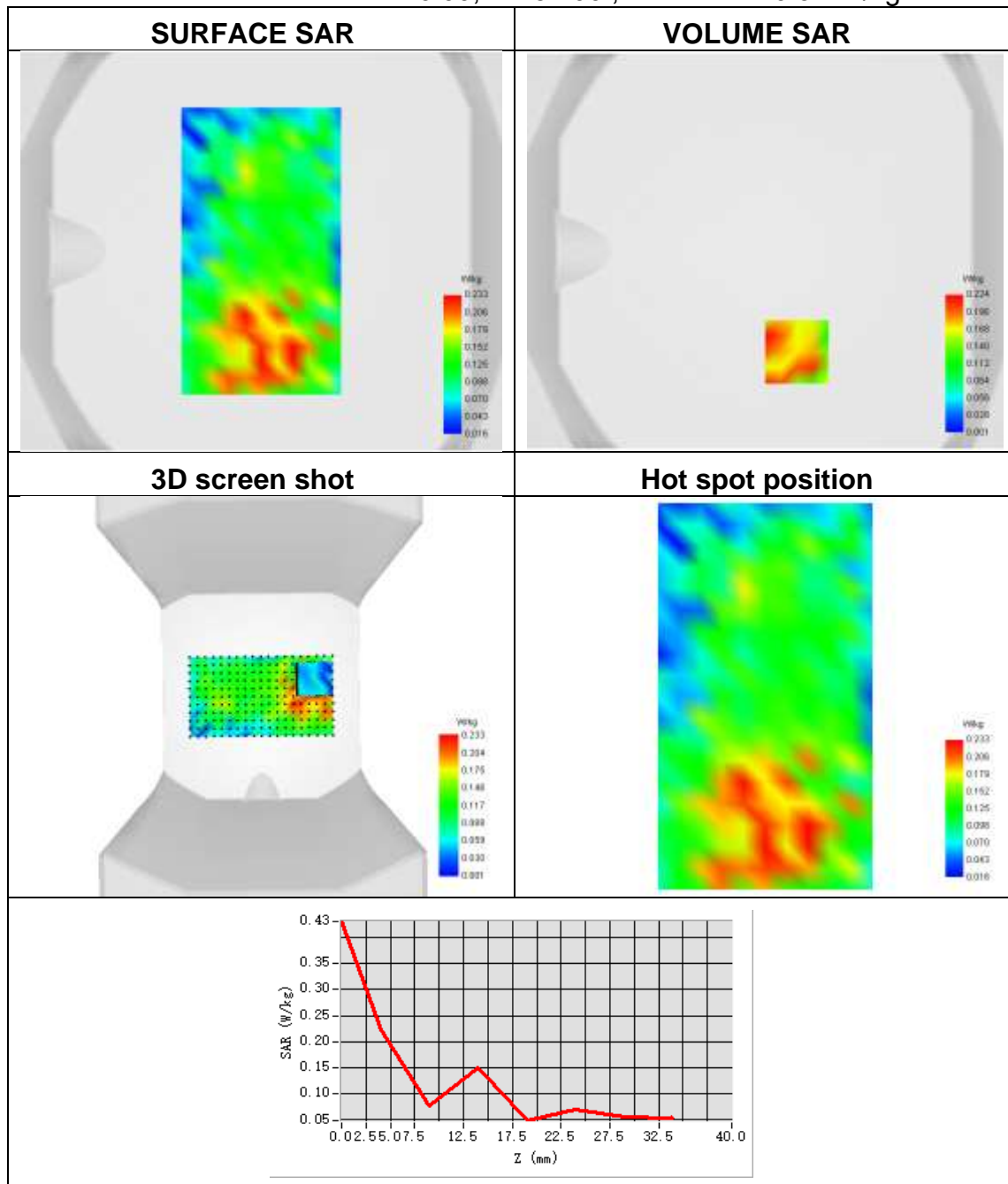




Plot 58:

Test Date	2023-07-17
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	LTE band 5
Signal	LTE FDD
Frequency	844
SAR 10g (W/Kg)	0.109
SAR 1g (W/Kg)	0.201

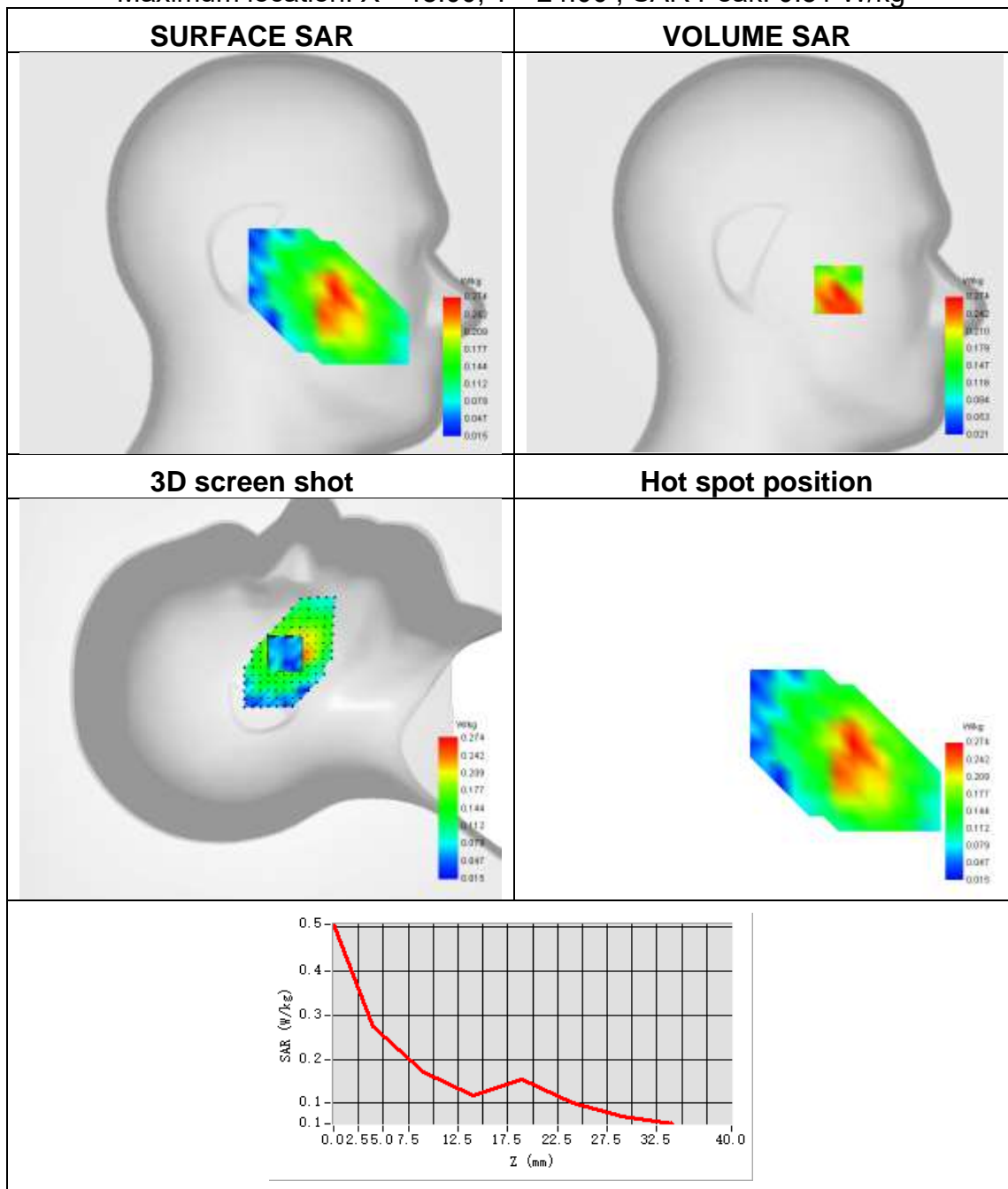
Maximum location: X=16.00, Y=-51.00 ; SAR Peak: 0.34 W/kg



Plot 59:

Test Date	2023-07-09
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	LTE band 12
Signal	LTE FDD
Frequency	704
SAR 10g (W/Kg)	0.028
SAR 1g (W/Kg)	0.054

Maximum location: X=-48.00, Y=-24.00 ; SAR Peak: 0.51 W/kg

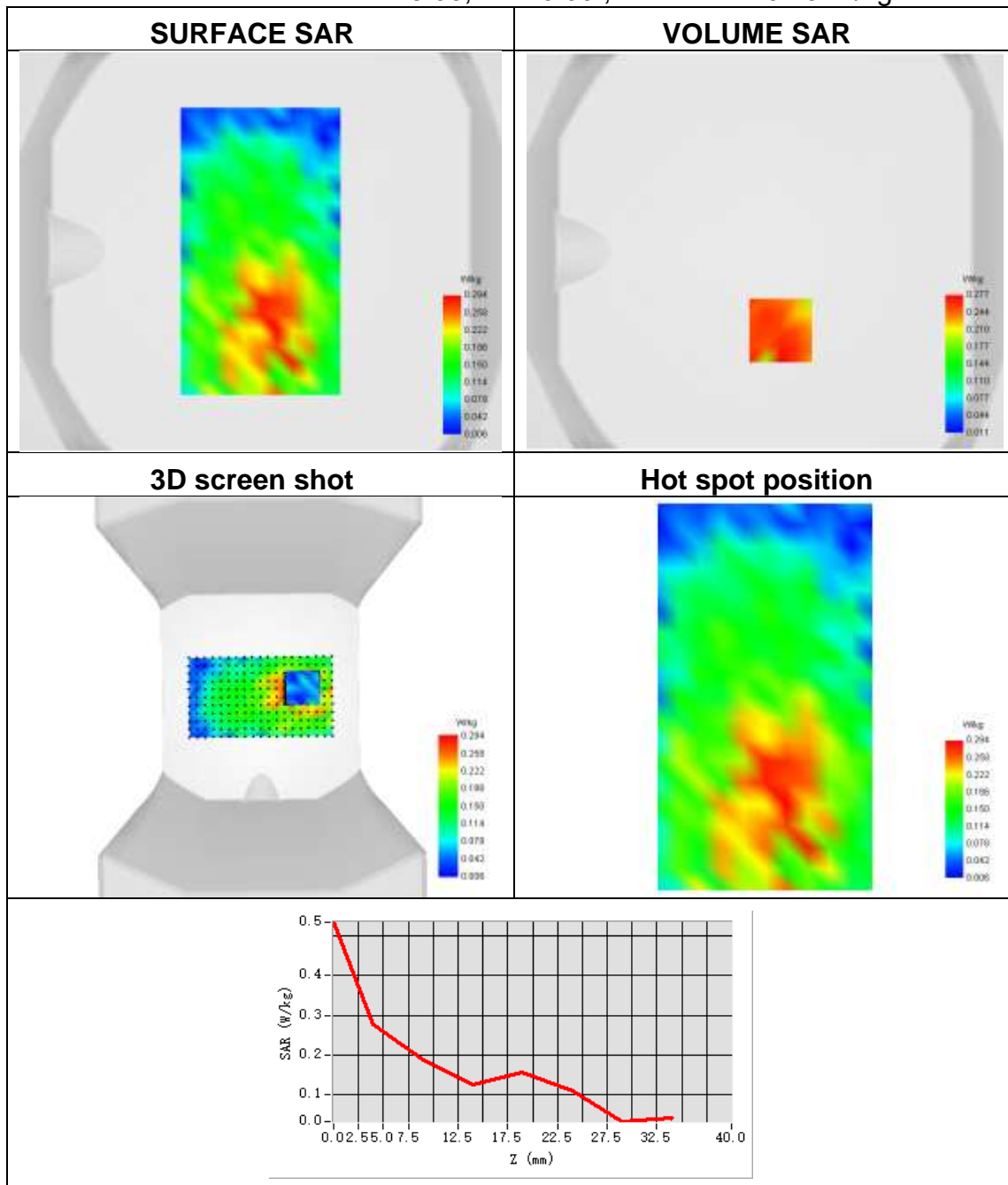




Plot 60:

Test Date	2023-07-09
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Front side
Band	LTE band 12
Signal	LTE FDD
Frequency	704
SAR 10g (W/Kg)	0.169
SAR 1g (W/Kg)	0.299

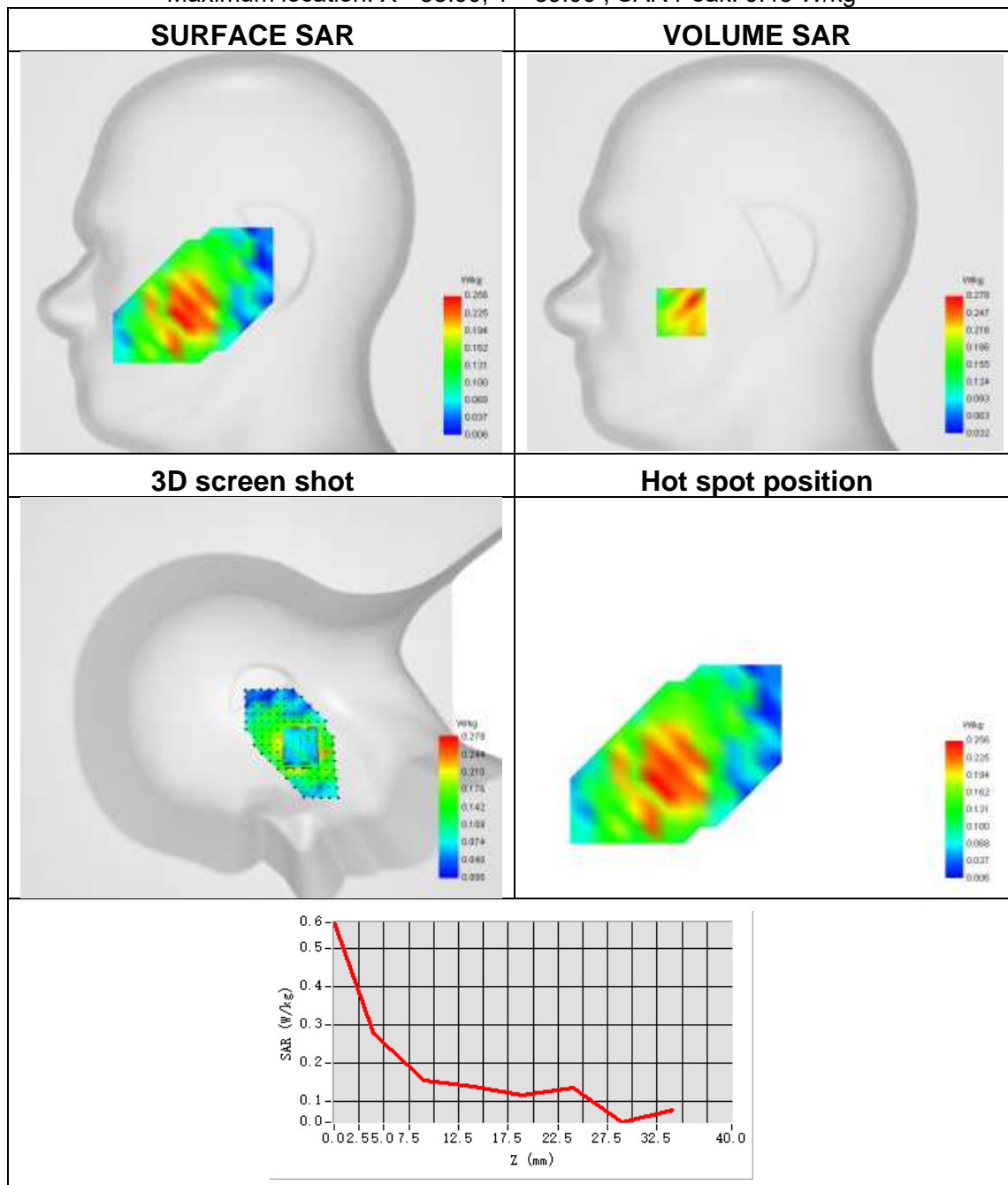
Maximum location: X=8.00, Y=-40.00 ; SAR Peak: 0.49 W/kg



Plot 61:

Test Date	2023-07-09
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right head
Device Position	Cheek
Band	LTE band 17
Signal	LTE FDD
Frequency	711
SAR 10g (W/Kg)	0.147
SAR 1g (W/Kg)	0.269

Maximum location: X=-55.00, Y=-39.00 ; SAR Peak: 0.43 W/kg

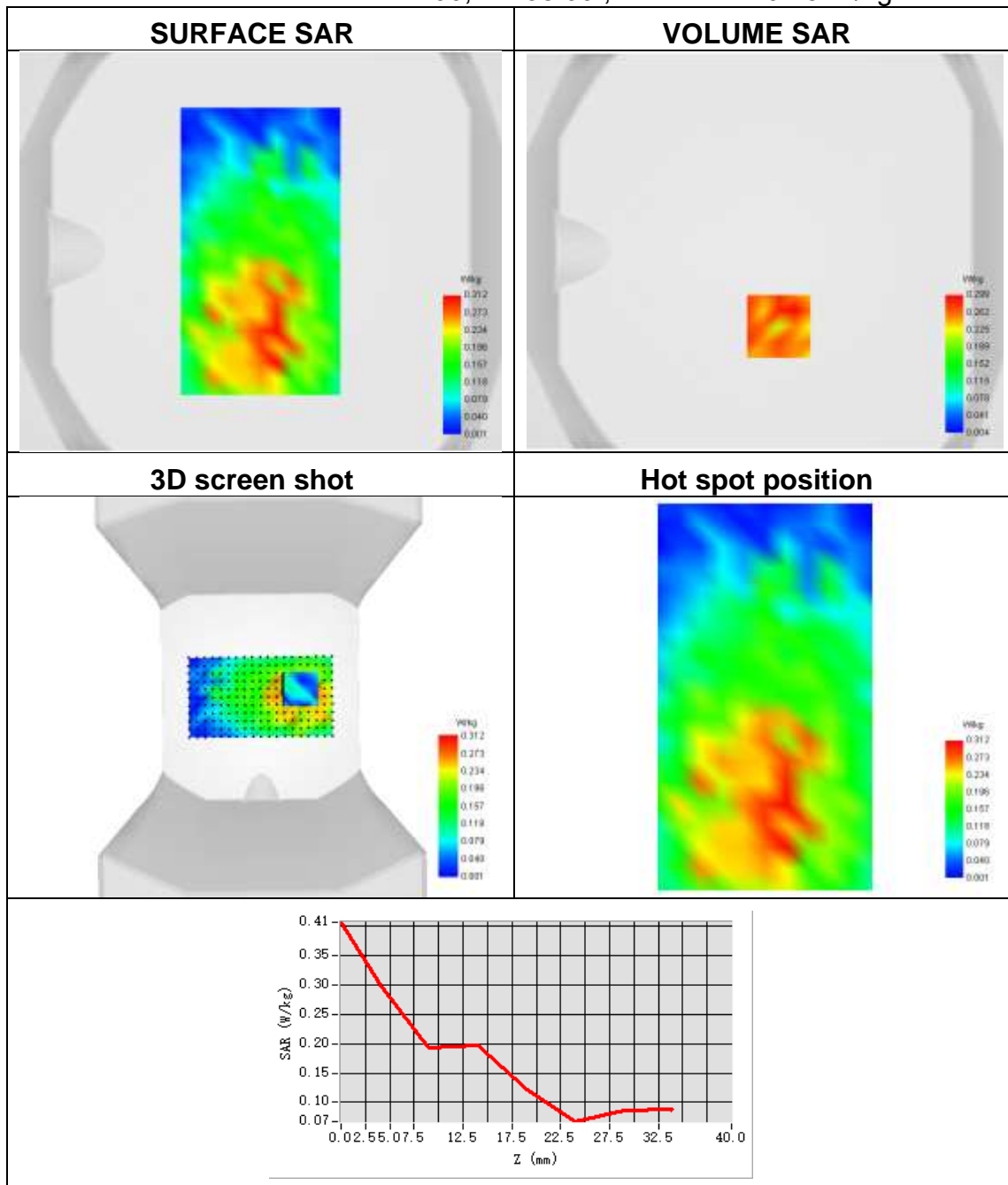




Plot 62:

Test Date	2023-07-09
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	LTE band 17
Signal	LTE FDD
Frequency	711
SAR 10g (W/Kg)	0.173
SAR 1g (W/Kg)	0.310

Maximum location: X=7.00, Y=-38.00 ; SAR Peak: 0.45 W/kg

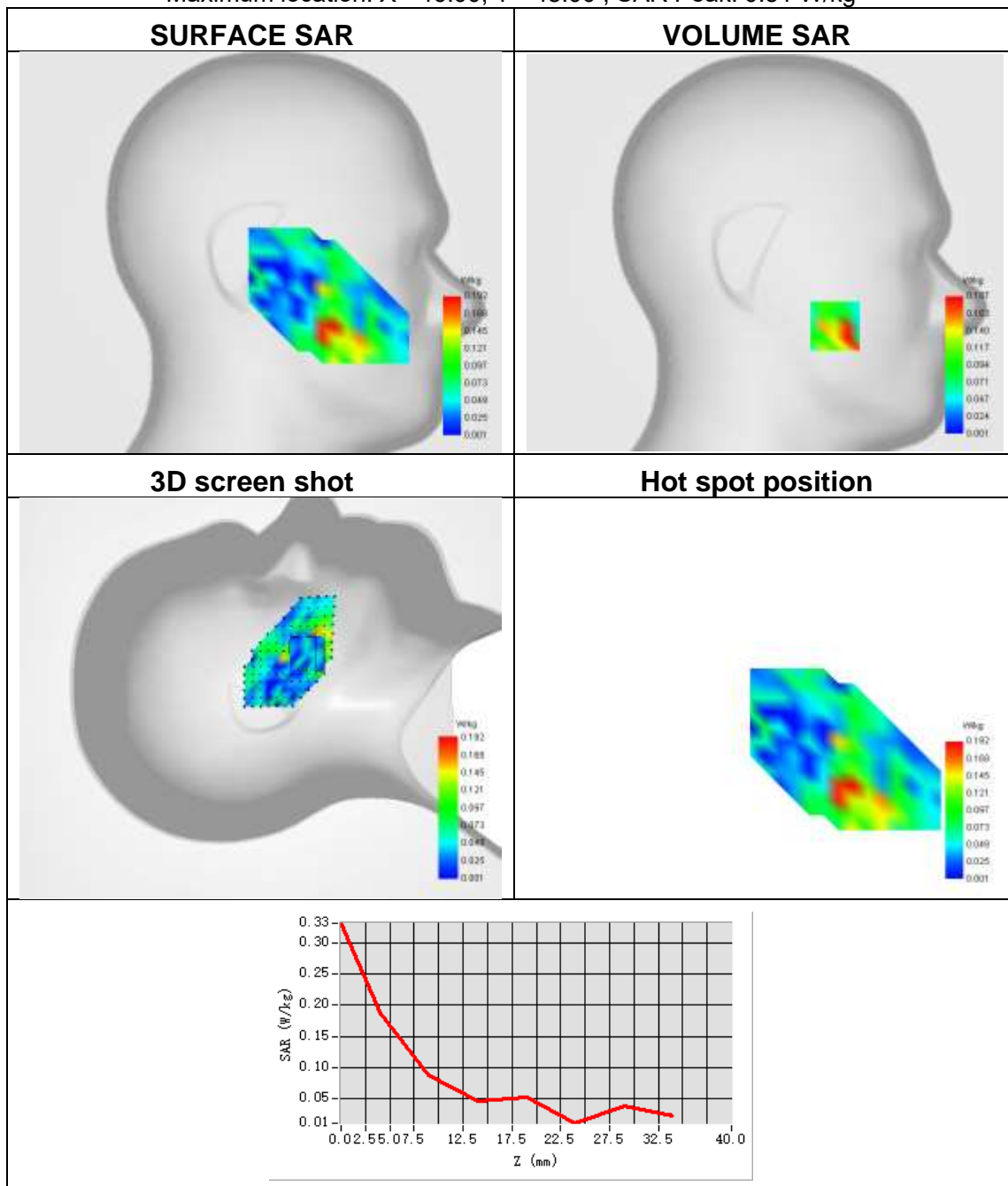




Plot 63:

Test Date	2023-07-13
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	LTE band 30
Signal	LTE FDD
Frequency	2310
SAR 10g (W/Kg)	0.072
SAR 1g (W/Kg)	0.158

Maximum location: X=-46.00, Y=-48.00 ; SAR Peak: 0.51 W/kg

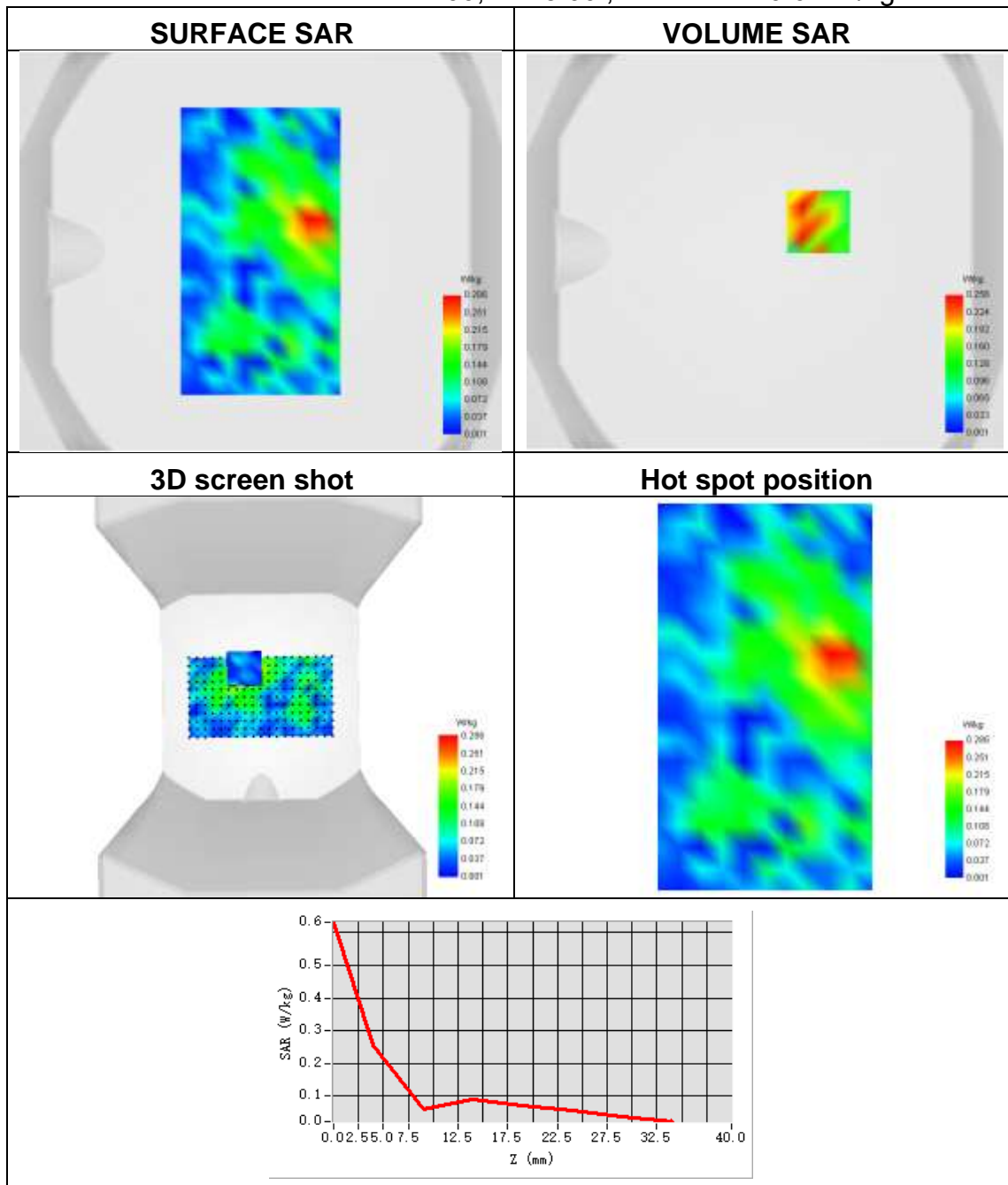




Plot 64:

Test Date	2023-07-13
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Front side
Band	LTE band 30
Signal	LTE FDD
Frequency	2310
SAR 10g (W/Kg)	0.124
SAR 1g (W/Kg)	0.271

Maximum location: X=27.00, Y=15.00 ; SAR Peak: 0.61 W/kg

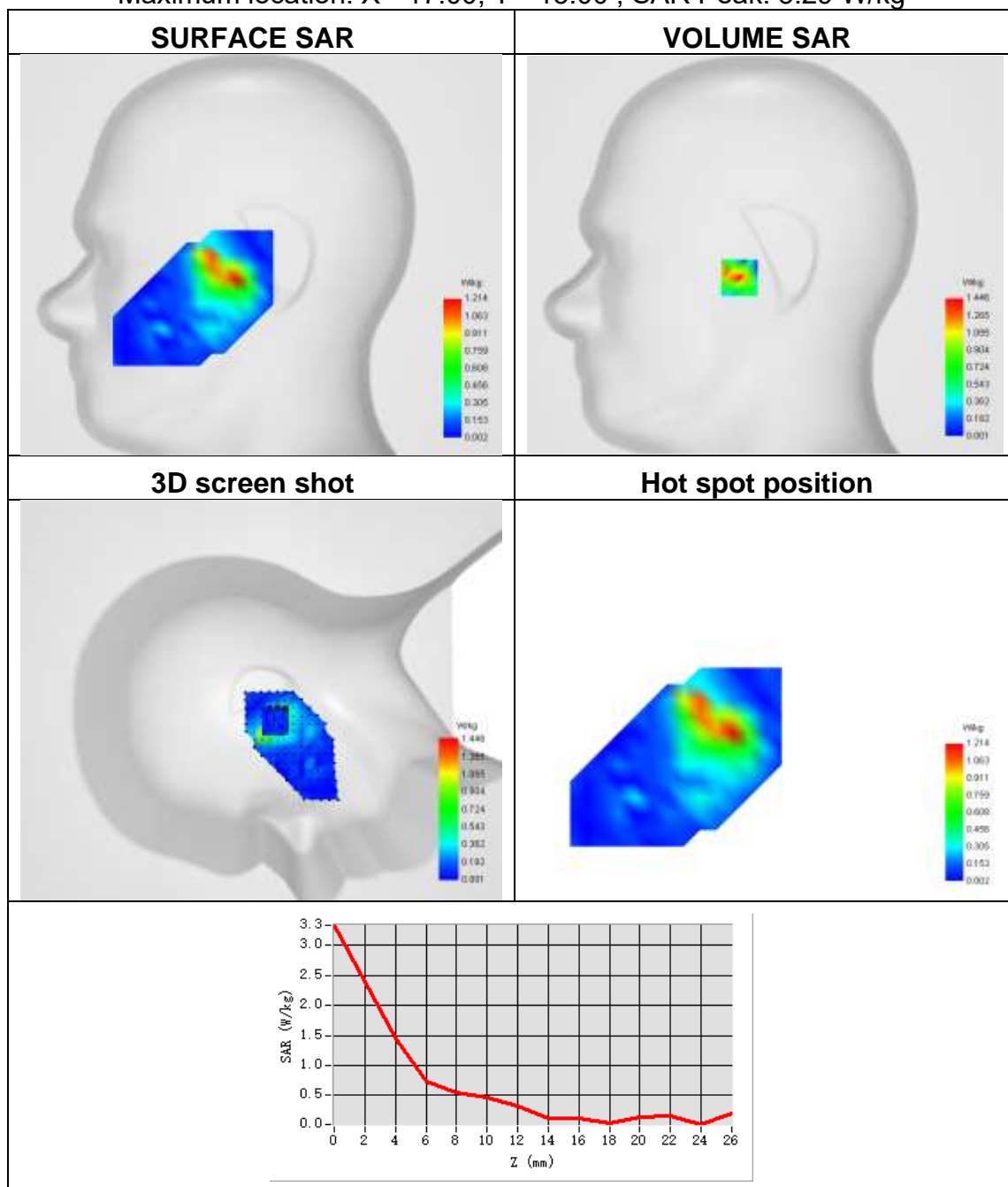




Plot 65:

Test Date	2023-07-15
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Right head
Device Position	Tilt
Band	LTE band 48
Signal	LTE TDD
Frequency	3625
SAR 10g (W/Kg)	0.413
SAR 1g (W/Kg)	1.071

Maximum location: X=-17.00, Y=-15.00 ; SAR Peak: 3.29 W/kg

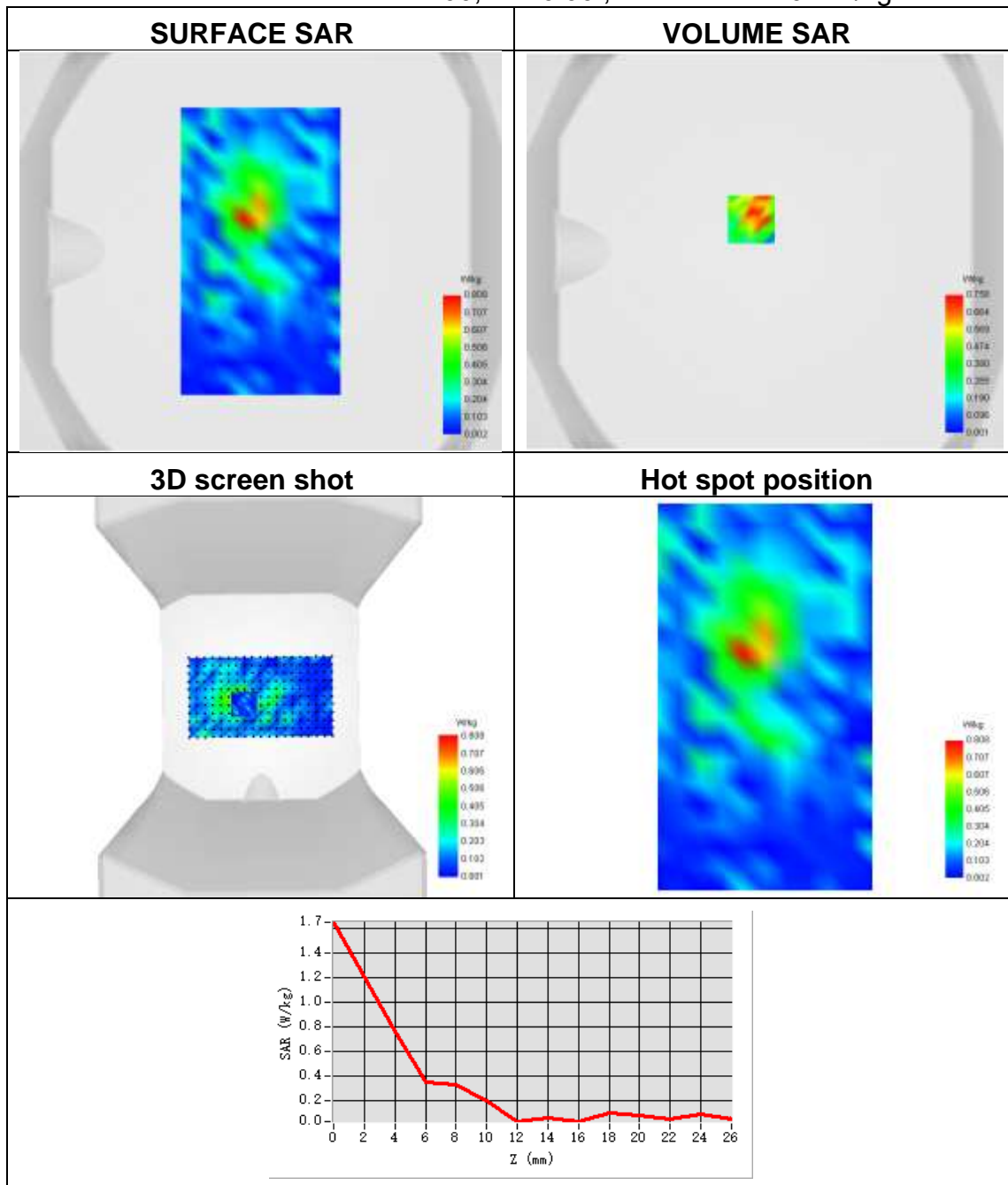




Plot 66:

Test Date	2023-07-15
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	LTE band 48
Signal	LTE TDD
Frequency	3625
SAR 10g (W/Kg)	0.234
SAR 1g (W/Kg)	0.607

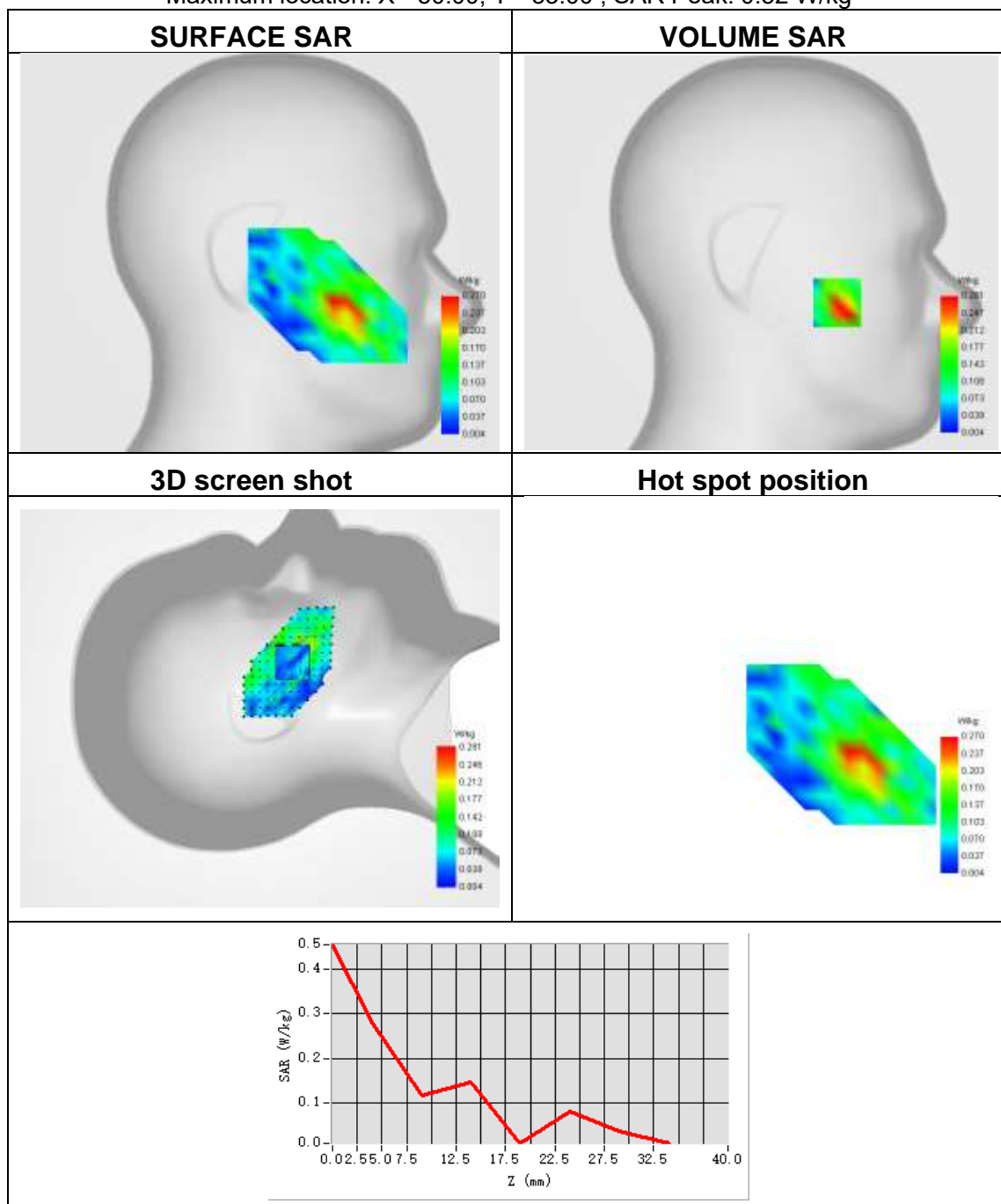
Maximum location: X=-7.00, Y=16.00 ; SAR Peak: 2.01 W/kg



Plot 67:

Test Date	2023-07-11
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	LTE band 66
Signal	LTE FDD
Frequency	1745
SAR 10g (W/Kg)	0.151
SAR 1g (W/Kg)	0.289

Maximum location: X=-50.00, Y=-33.00 ; SAR Peak: 0.52 W/kg

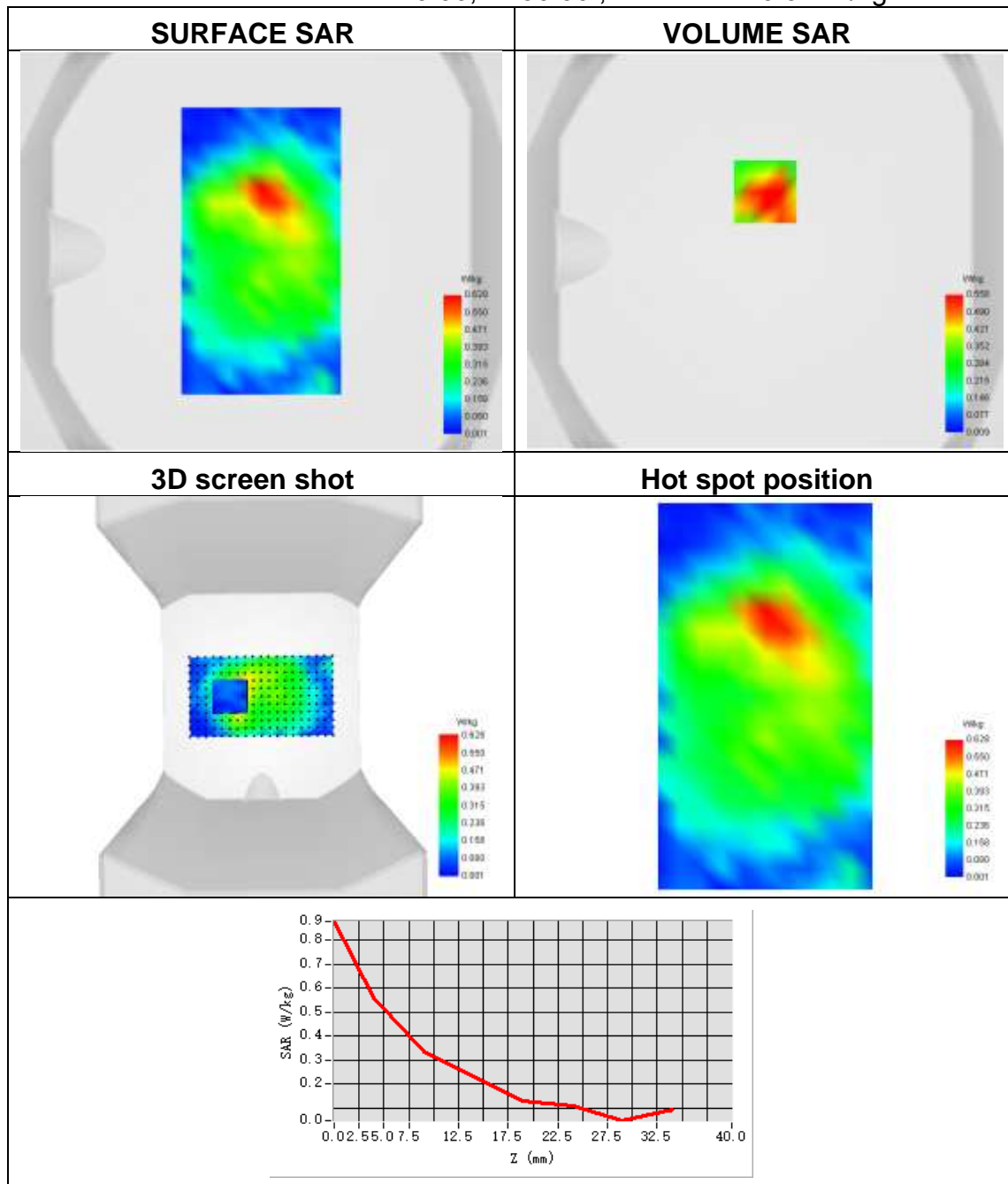




Plot 68:

Test Date	2023-07-11
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Bottom Side
Band	LTE band 66
Signal	LTE FDD
Frequency	1745
SAR 10g (W/Kg)	0.312
SAR 1g (W/Kg)	0.571

Maximum location: X=0.00, Y=30.00 ; SAR Peak: 0.97 W/kg

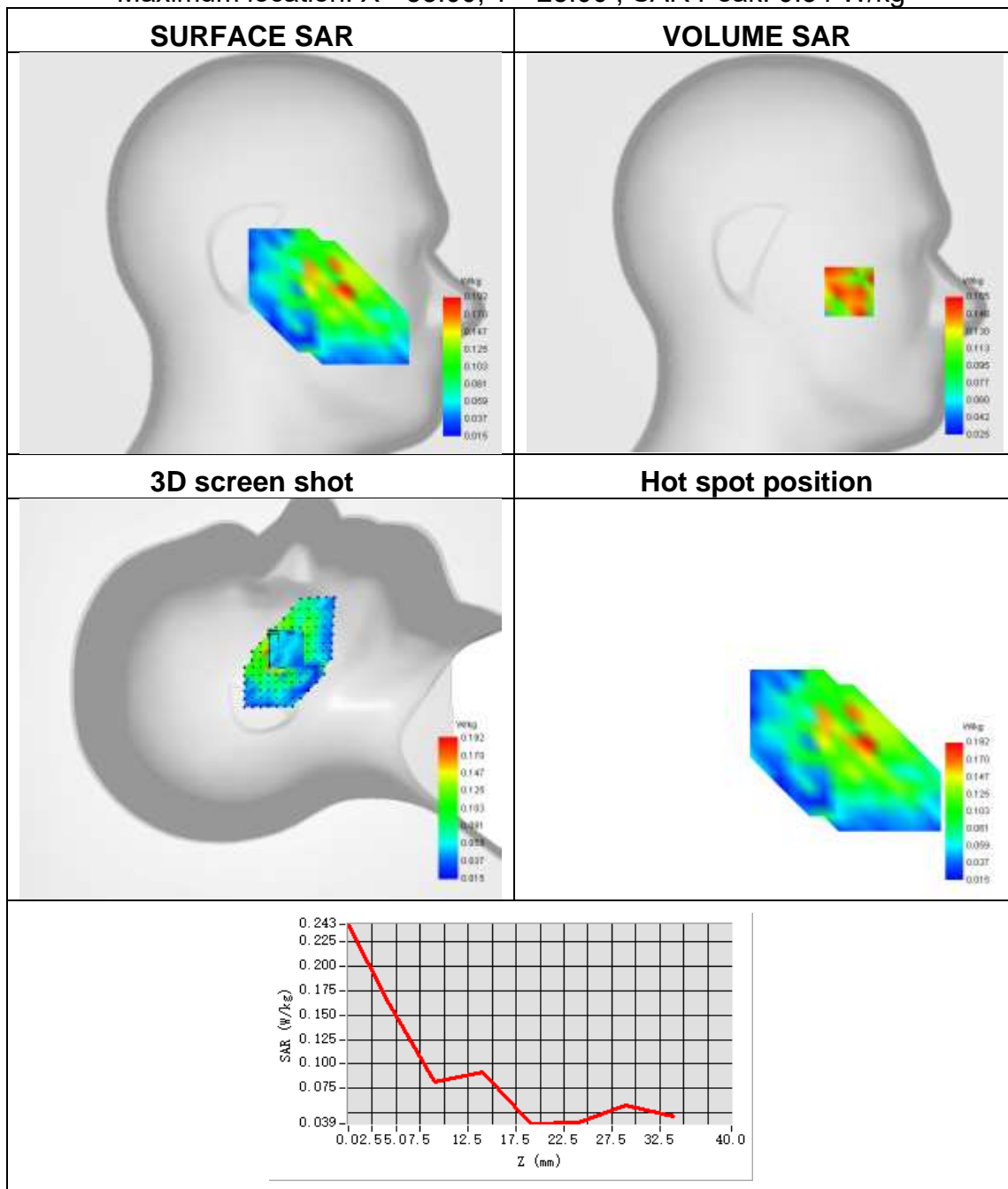




Plot 69:

Test Date	2023-06-21
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	LTE band 71
Signal	LTE FDD
Frequency	680.5
SAR 10g (W/Kg)	0.110
SAR 1g (W/Kg)	0.192

Maximum location: X=-55.00, Y=-25.00 ; SAR Peak: 0.34 W/kg

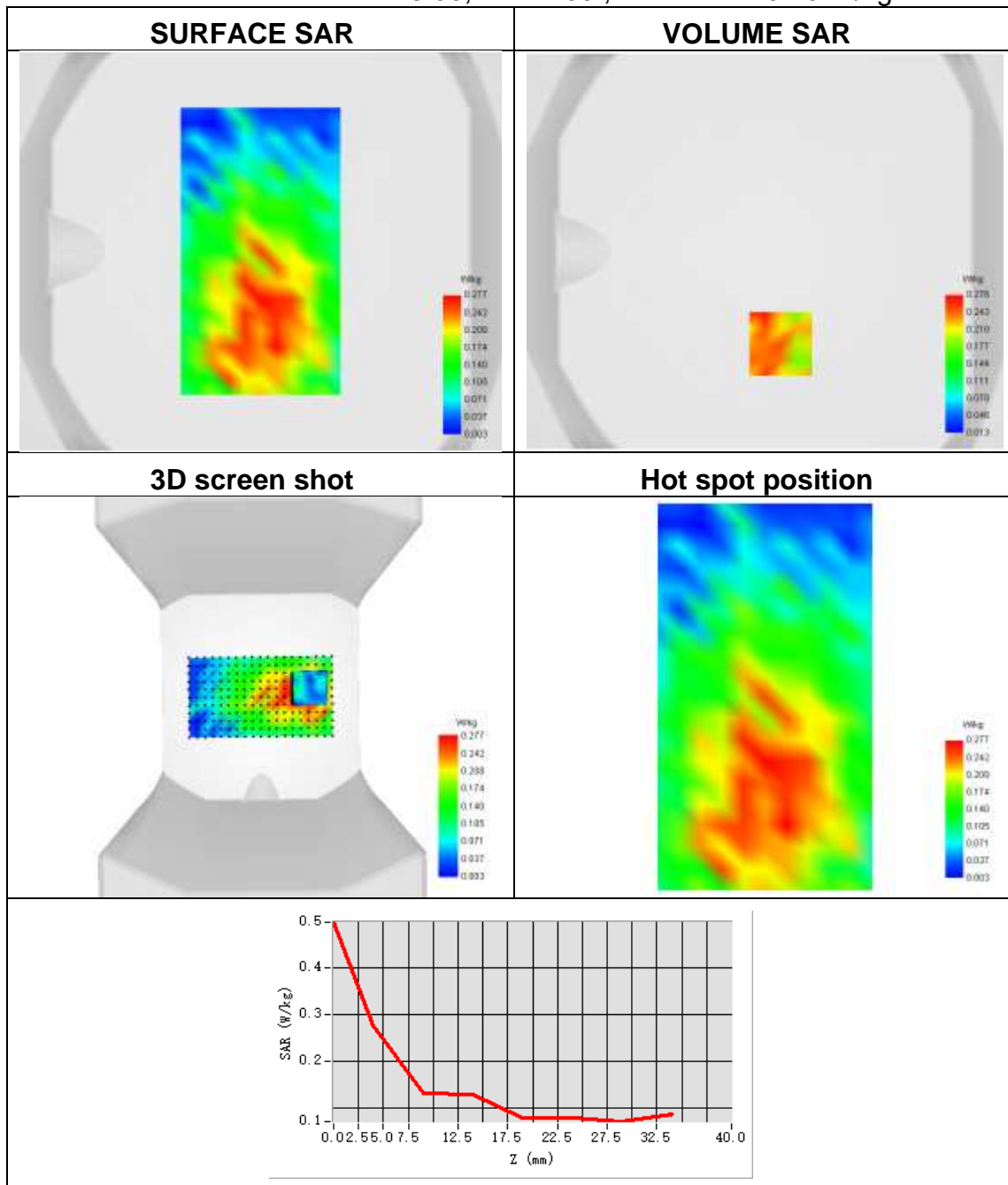




Plot 70:

Test Date	2023-06-21
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	LTE band 71
Signal	LTE FDD
Frequency	680.5
SAR 10g (W/Kg)	0.164
SAR 1g (W/Kg)	0.288

Maximum location: X=8.00, Y=-47.00 ; SAR Peak: 0.46 W/kg

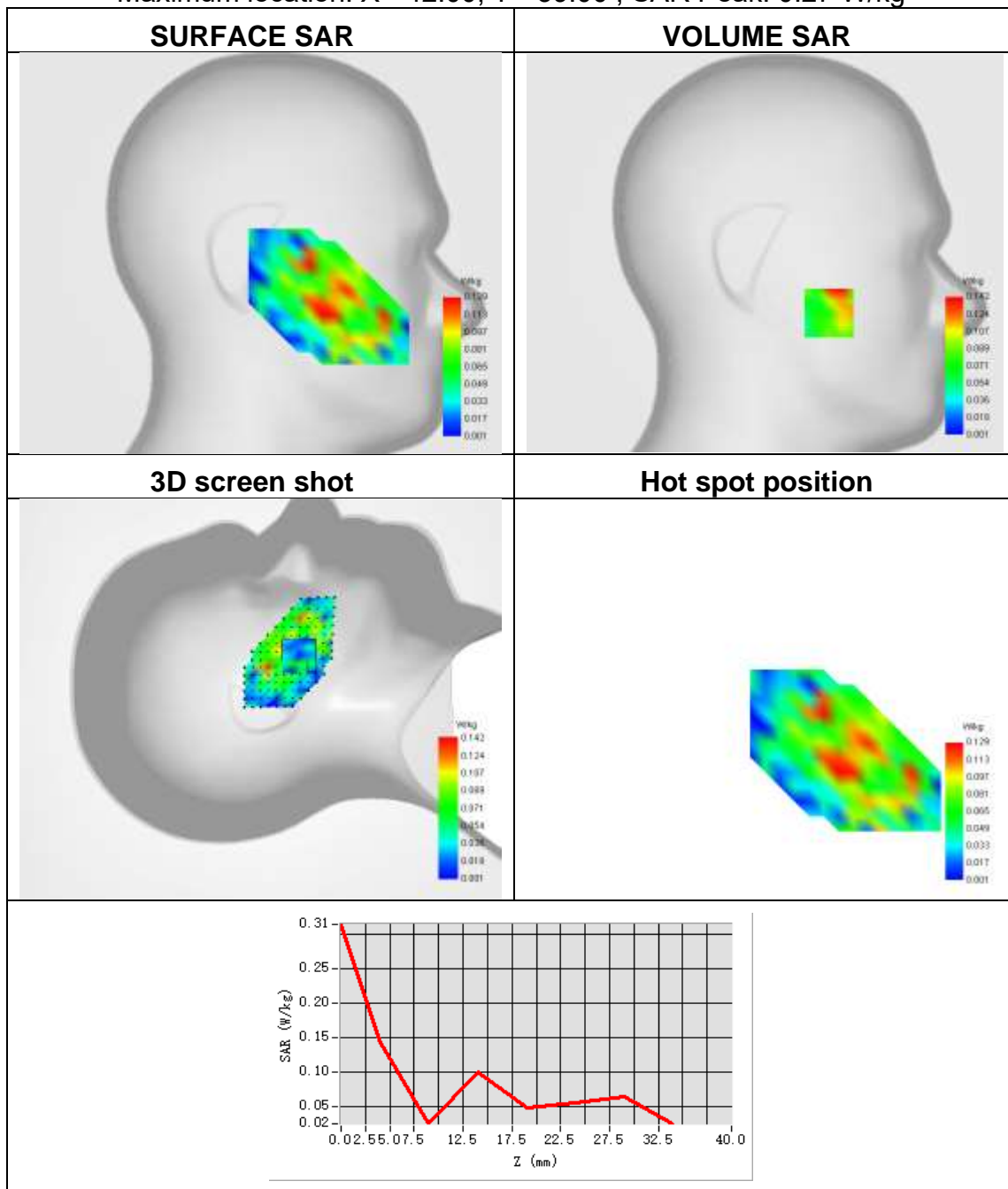




Plot 71:

Test Date	2023-07-17
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	NR N5
Signal	NR FDD
Frequency	844
SAR 10g (W/Kg)	0.061
SAR 1g (W/Kg)	0.138

Maximum location: X=-42.00, Y=-39.00 ; SAR Peak: 0.27 W/kg

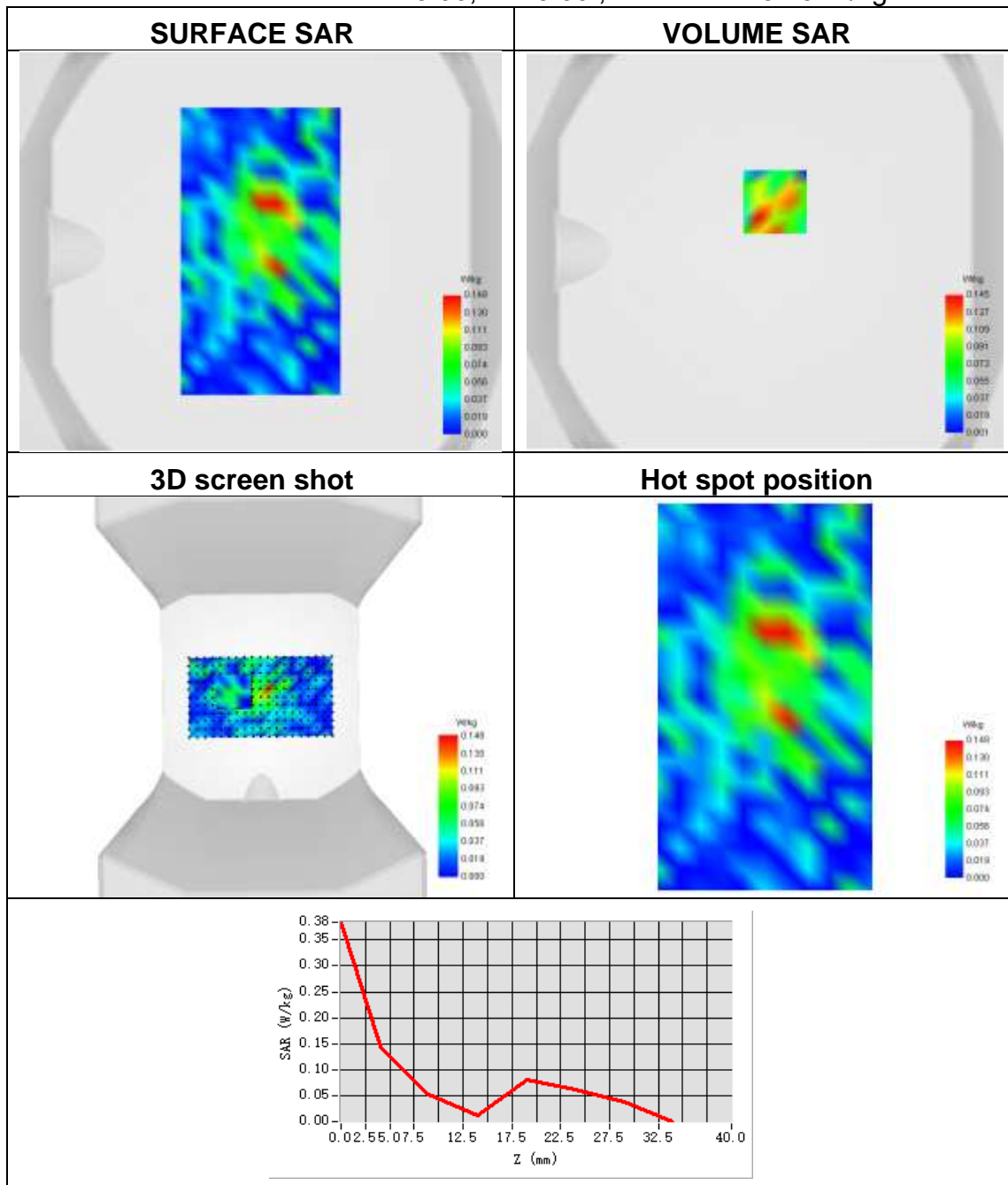




Plot 72:

Test Date	2023-07-17
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	NR N5
Signal	NR FDD
Frequency	844
SAR 10g (W/Kg)	0.076
SAR 1g (W/Kg)	0.172

Maximum location: X=5.00, Y=25.00 ; SAR Peak: 0.40 W/kg

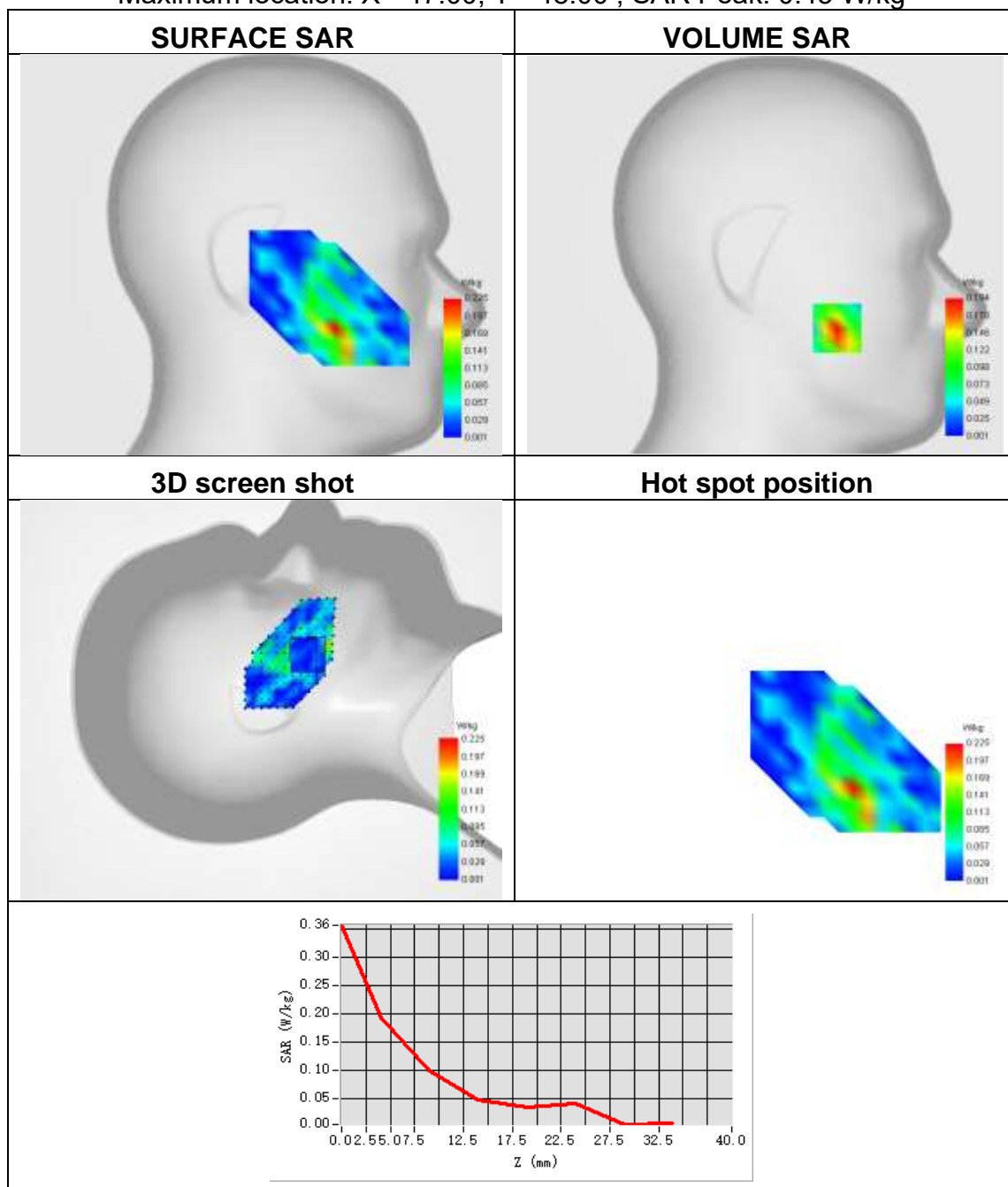




Plot 73:

Test Date	2023-07-13
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Cheek
Band	NR N41
Signal	NR TDD
Frequency	2592.99
SAR 10g (W/Kg)	0.083
SAR 1g (W/Kg)	0.196

Maximum location: X=-47.00, Y=-48.00 ; SAR Peak: 0.43 W/kg

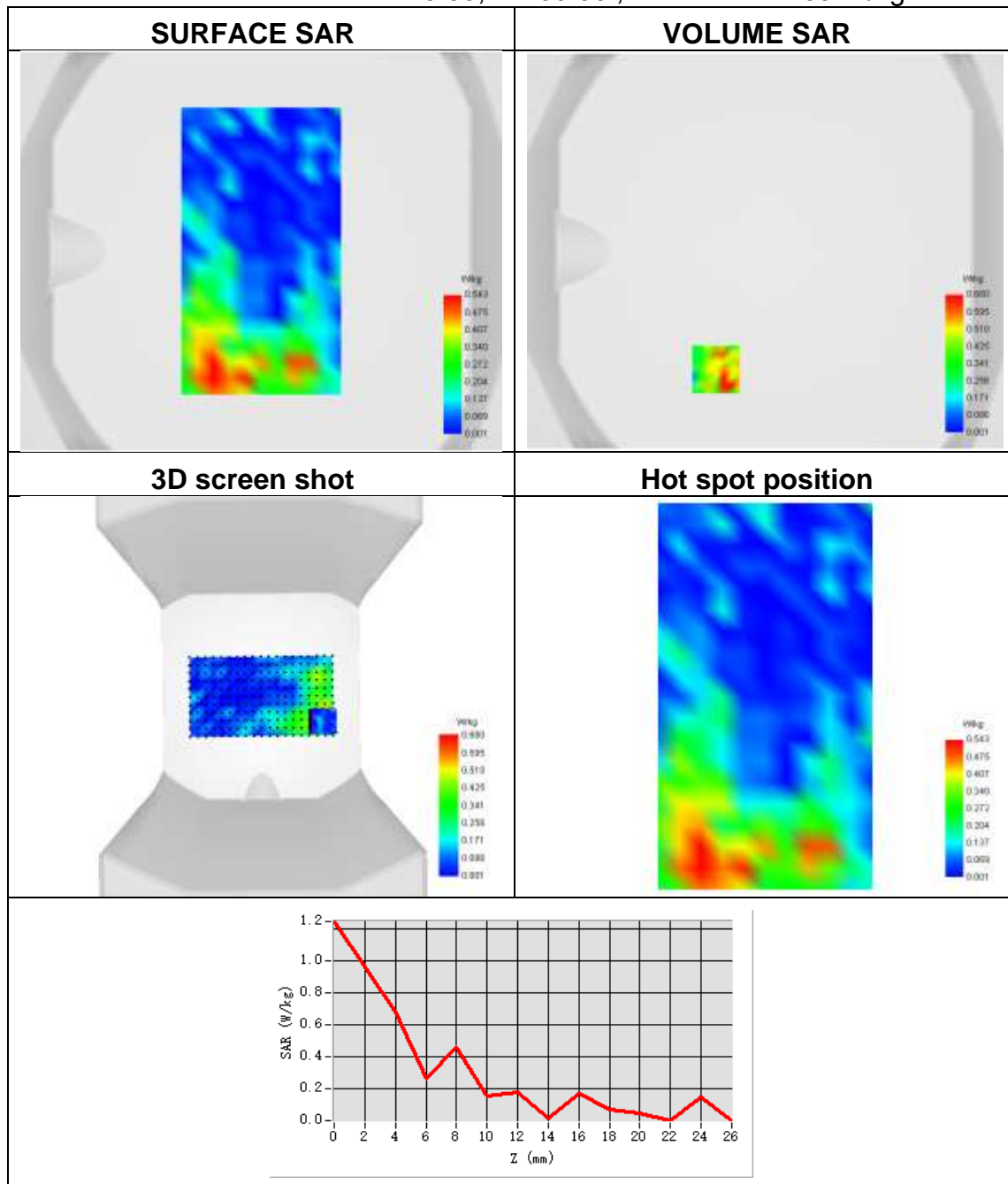




Plot 74:

Test Date	2023-07-13
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	NR N41
Signal	NR TDD
Frequency	2592.99
SAR 10g (W/Kg)	0.179
SAR 1g (W/Kg)	0.430

Maximum location: X=-25.00, Y=-60.00 ; SAR Peak: 1.63 W/kg

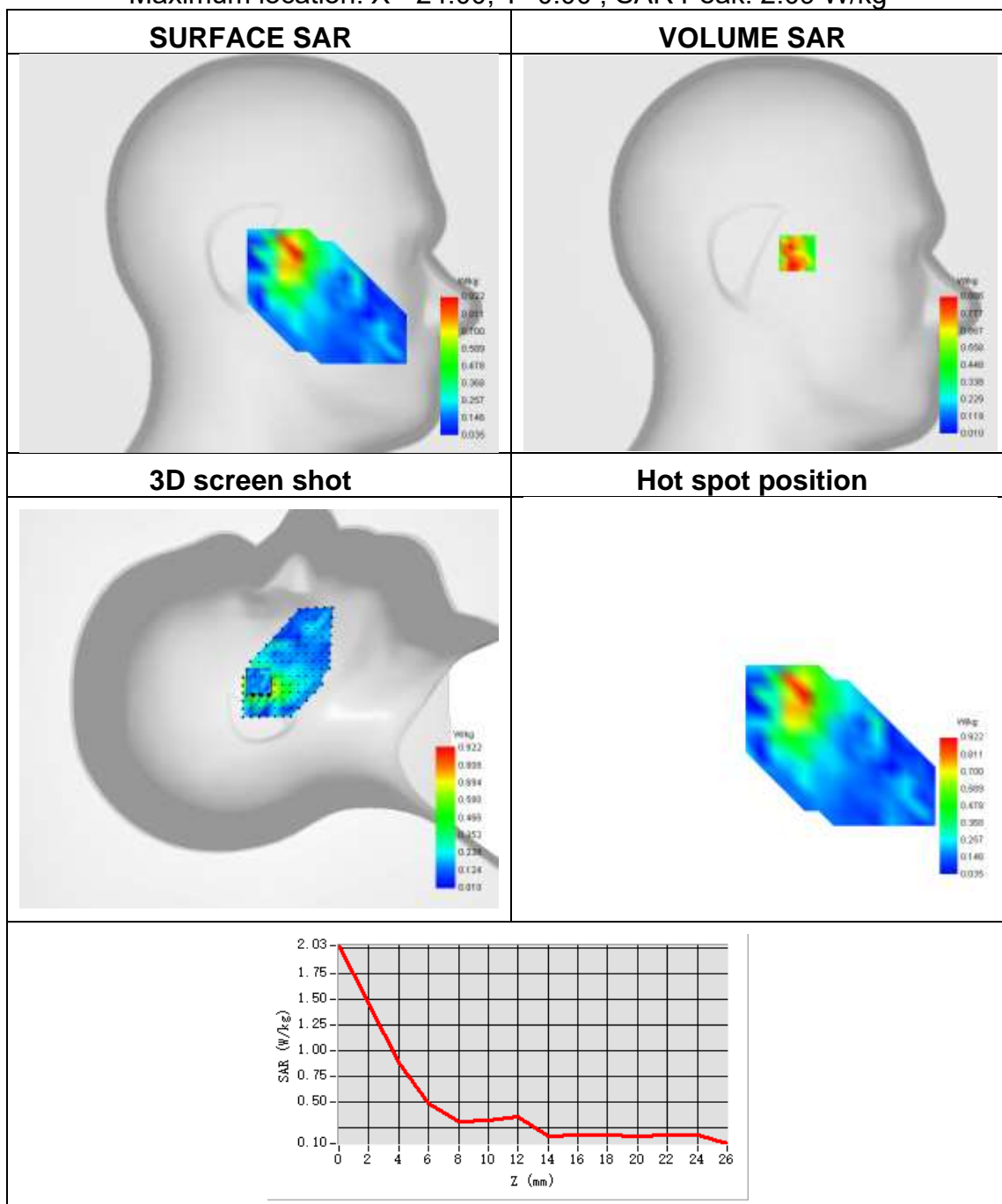




Plot 75:

Test Date	2023-07-15
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	NR N48
Signal	NR TDD
Frequency	3649.98
SAR 10g (W/Kg)	0.332
SAR 1g (W/Kg)	0.844

Maximum location: X=-24.00, Y=0.00 ; SAR Peak: 2.09 W/kg

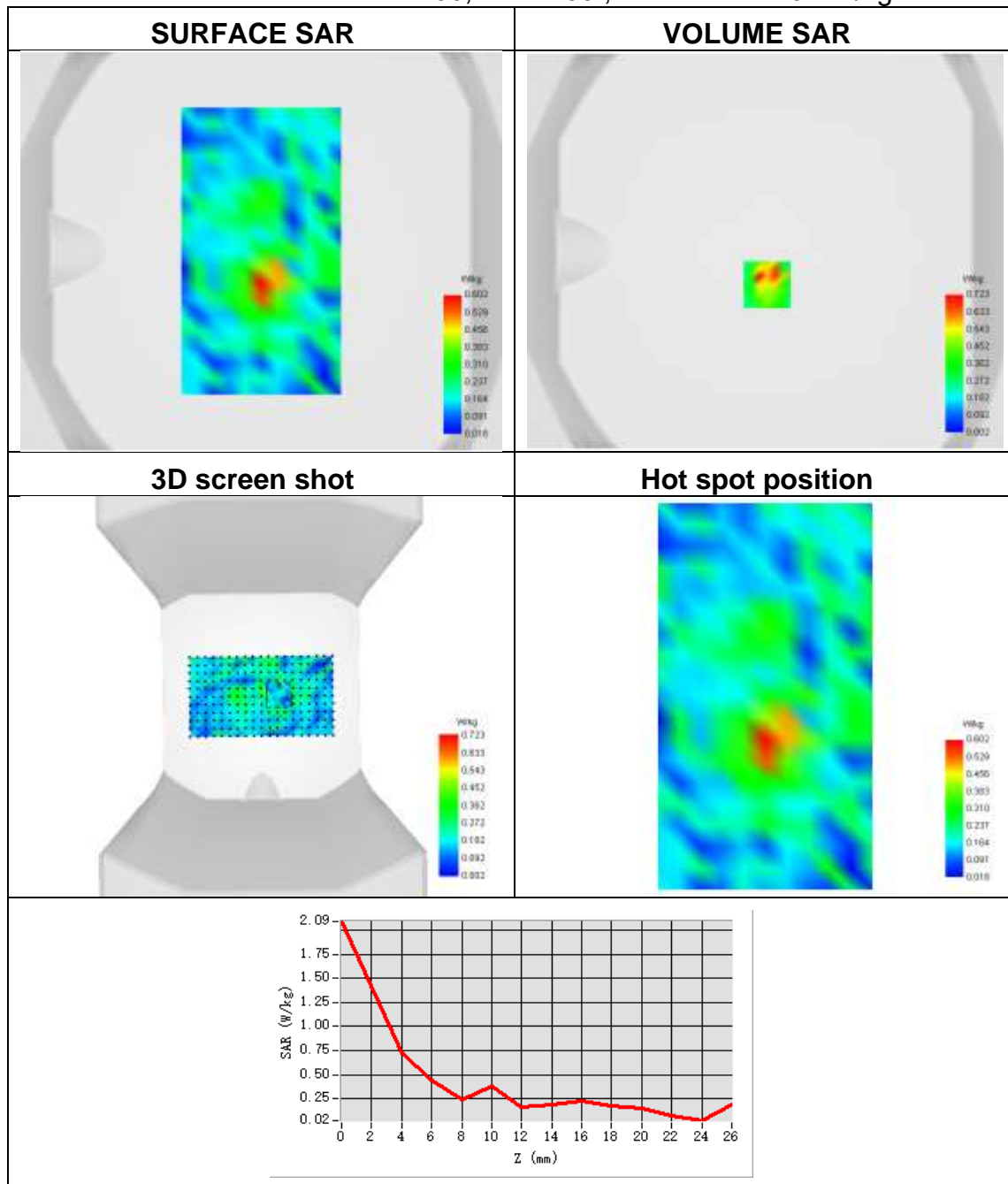




Plot 76:

Test Date	2023-07-15
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Top Side
Band	NR N48
Signal	NR TDD
Frequency	3649.98
SAR 10g (W/Kg)	0.247
SAR 1g (W/Kg)	0.574

Maximum location: X=1.00, Y=-17.00 ; SAR Peak: 2.04 W/kg

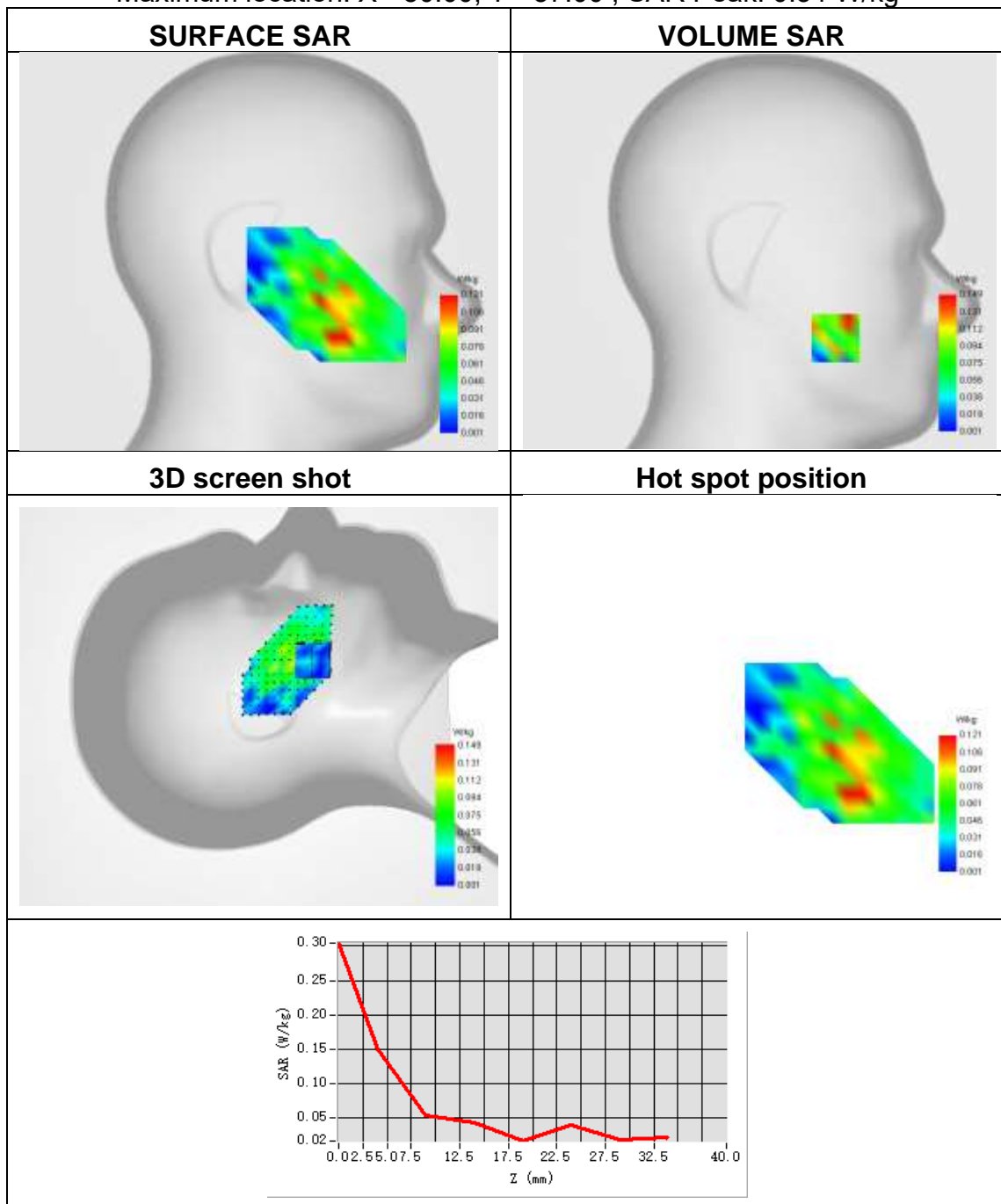




Plot 77:

Test Date	2023-06-21
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left Cheek
Device Position	Cheek
Band	NR N71
Signal	NR FDD
Frequency	688
SAR 10g (W/Kg)	0.065
SAR 1g (W/Kg)	0.143

Maximum location: X=-50.00, Y=-57.00 ; SAR Peak: 0.34 W/kg

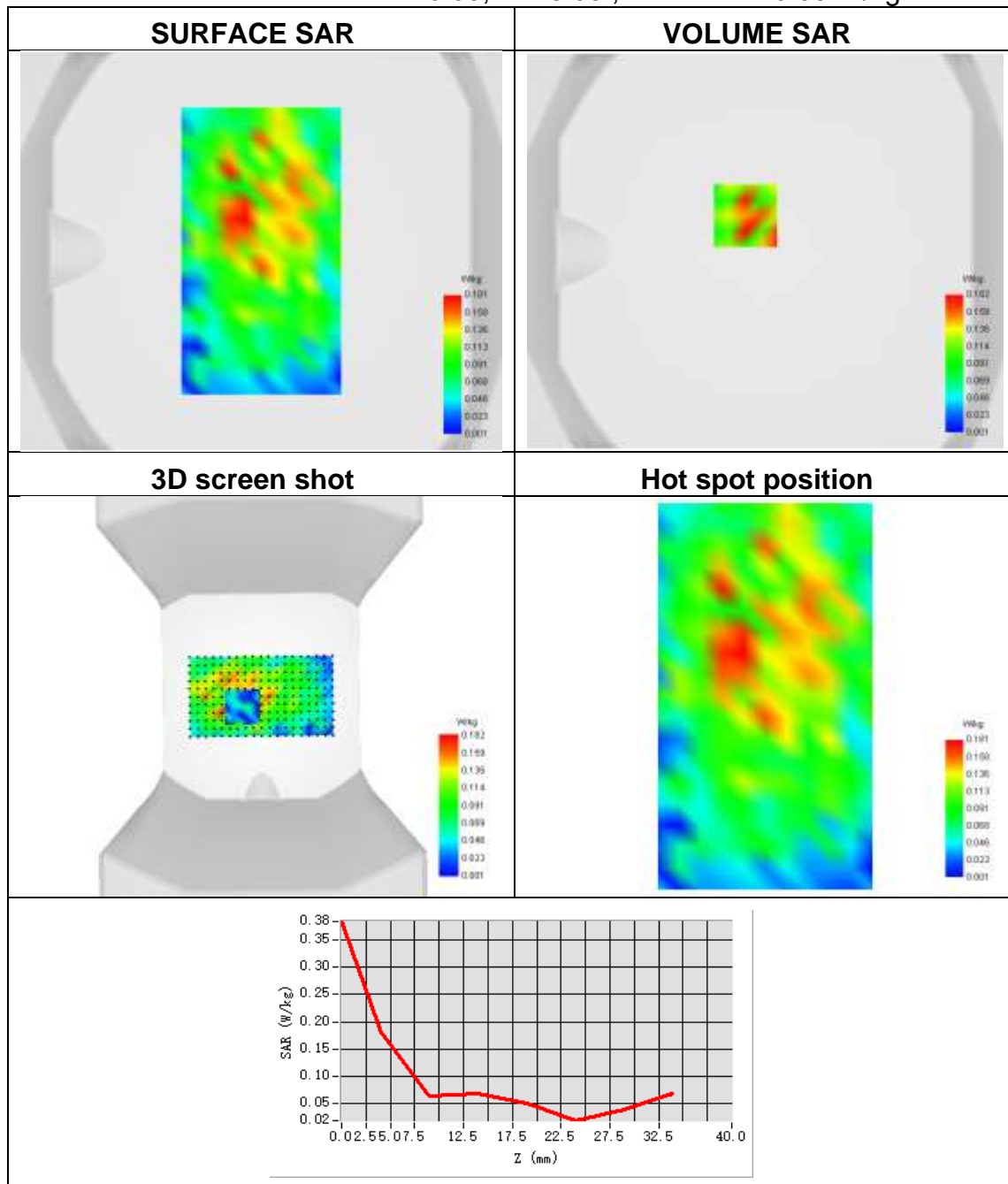




Plot 78:

Test Date	2023-06-21
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Back Side
Band	NR N71
Signal	NR FDD
Frequency	688
SAR 10g (W/Kg)	0.074
SAR 1g (W/Kg)	0.155

Maximum location: X=-10.00, Y=18.00 ; SAR Peak: 0.35 W/kg

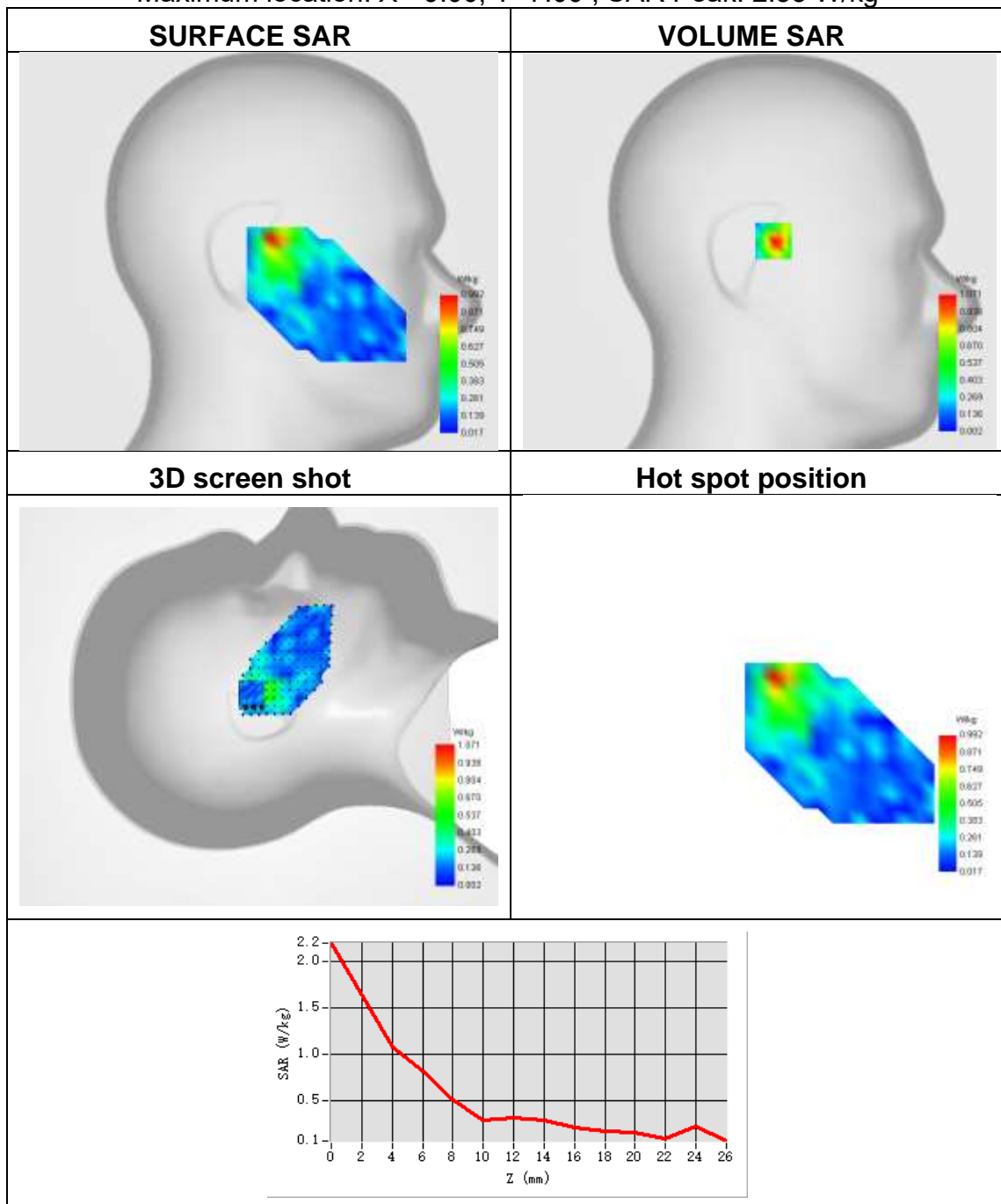




Plot 79:

Test Date	2023-07-17
Area Scan	sam_direct_droit2_surf8mm.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Left head
Device Position	Tilt
Band	NR N77
Signal	NR TDD
Frequency	3500
SAR 10g (W/Kg)	0.382
SAR 1g (W/Kg)	0.883

Maximum location: X=-9.00, Y=7.00 ; SAR Peak: 2.33 W/kg

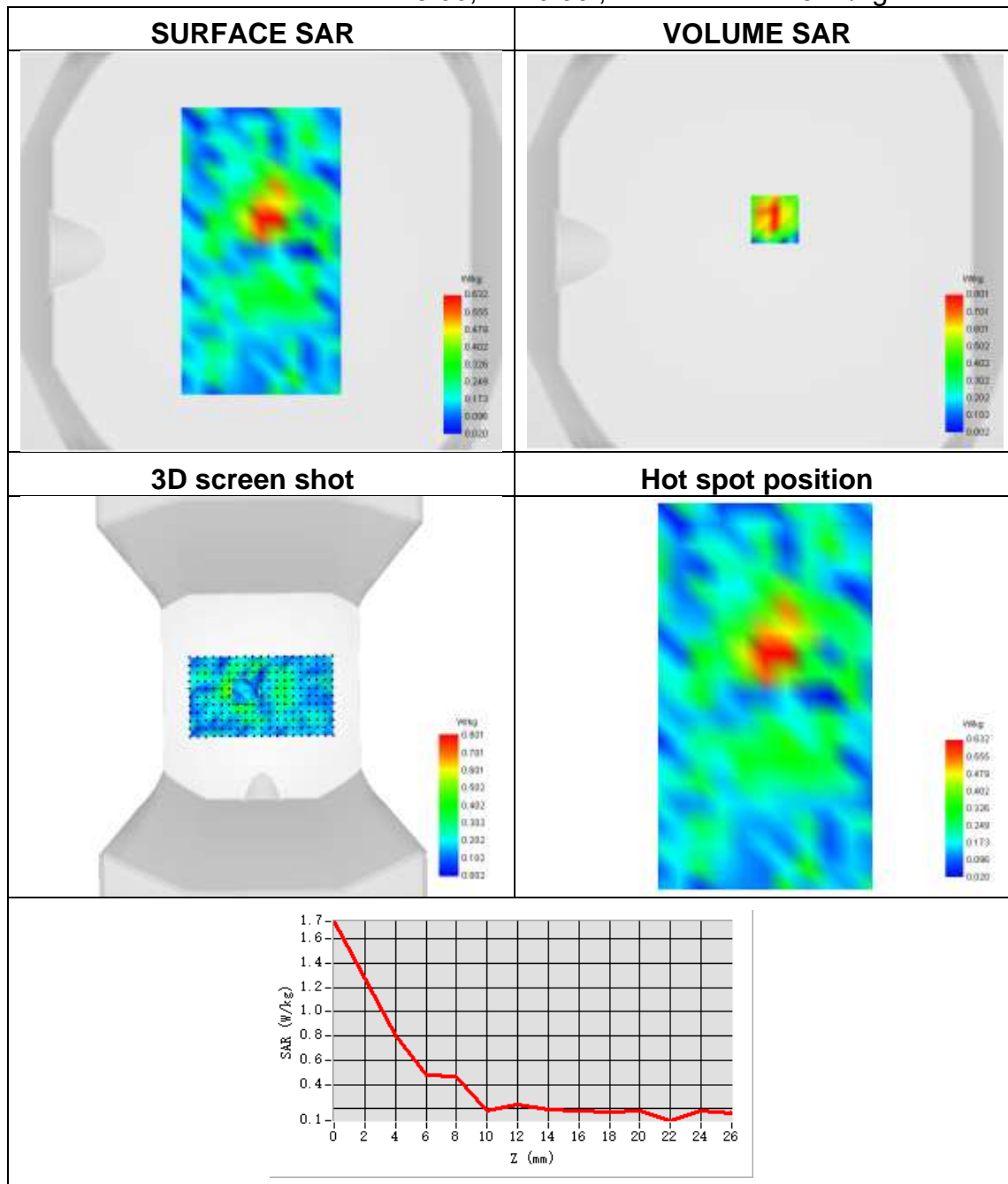




Plot 80:

Test Date	2023-07-17
Area Scan	surf_sam_plan.txt
Zoom Scan	5x5x7,dx=8mm dy=8mm dz=5mm
Phantom	Validation plane
Device Position	Top Edge
Band	NR N77
Signal	NR TDD
Frequency	3500
SAR 10g (W/Kg)	0.313
SAR 1g (W/Kg)	0.684

Maximum location: X=5.00, Y=16.00 ; SAR Peak: 1.78 W/kg





Appendix C. Probe Calibration and Dipole Calibration Report

Refer the appendix Calibration Report.

※※※※END OF THE REPORT※※※※